EXHIBIT H – Frequency Block Diagram

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Ultraview Digital Telemetry

JP3 3 ECG/SPO2 PWM buffer Ш MUX 4 PWM data PWM formatter Comparator 5 JP4 **PWM** SPO₂ 1 ramp control generator control vcxo JP2 pacer flag RA ON status data 150.5 MHz PWM RL ON 2 PAL 155.0 MHz CON 3 control LL ON LA ON first frequency doubler 8 kHz -ID0-7 switch bank 1 clock 301.0 MHz generator to 310.0 MHz ID8-15 switch bank 2 32 KHz Status MUX second RF out frequency doubler to RA battery lead detector

Transmitter Board Block Diagram

Transmitter Board Description

Status Mux

record switch

Receives the following inputs:

- · Lead Status indicators from the automatic lead switching PAL (ECG board)
- Transmitter ID switches
- · Low battery flag from the low battery detector
- · Record signal from the patient remote record switch

602.0 MHz to 620.0 MHz