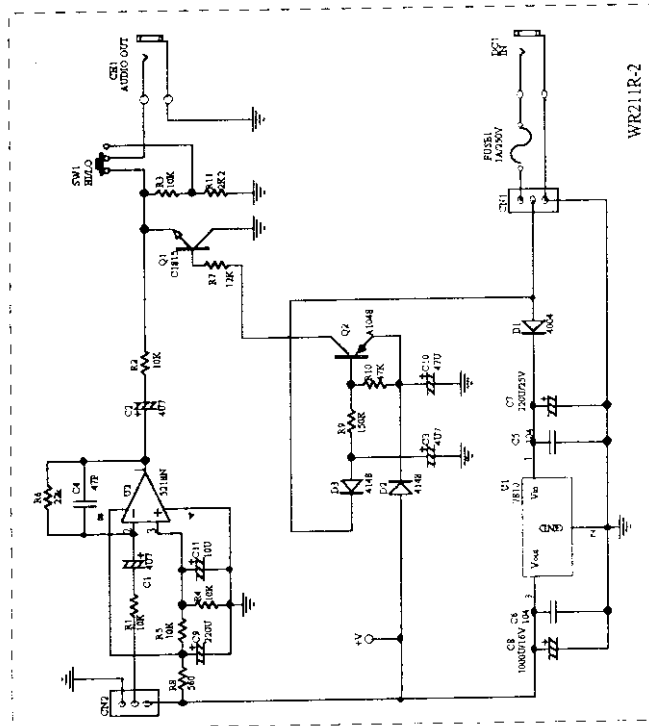
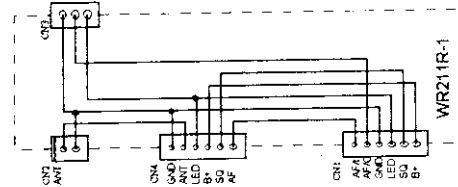


EXHIBIT D

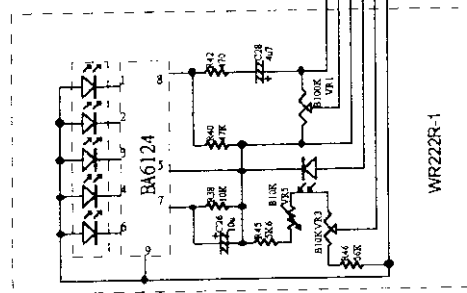
Circuit Diagram



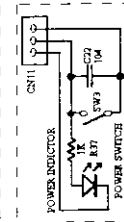
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WR222R-1



WR222R-4

WR 211R SCHEMATIC DIAGRAM			
Rev	Number	Revision	
A3	06.07.97		
Date	31 Dec 1997	Sheet of	708
By	CIA VERNER, JINRIR, SKE	Drawn By	