EXHIBIT 2-1   CFS8DL5804BD-1 / 573F-5804BD1

CIRCUIT DESCRIPTION

The Transmitter is a amplitude keyed oscillator (Q4,. etc.,) who's FDE is a saw (YL1), it is keyed on and off by Q3. The messages are generated by MICROPROCESSOR (U1). OUTPUT NETWORK (C13, L3, ETC) feeds the RF into the TX Antenna (ANT2).The receiver is a single chip receiver (U3) with an internal LO. The LO is phase locked to YL2 (20.89375 X 18 = 334.3 MHz LO) TheRX Antenna (ANT1) couples though a matching / band-pass circuit (C42, L15,ETC) into the SINGLE CHIP RECEIVER (U3) and then is applied to SHAPING CKT (U4 ETC.) and then into MICROPROCESSOR (U1) for decoding.