

EXHIBIT 2-1

Circuit Description.

**CFS8DL5800USM**

The 5800USM was previously known as the 5800GDO. (CFS8DL5800GDO) The reason for this change is the original receiver chip (U1) had gone obsolete. It is still constructed on a single circuit board, the same circuit board as used on the 5800RL(FCC ID: CFS8DL5800RL), except the transmitter portion is not populated. The 5800USM functions as a stand-alone receiver.

The receiver is a single conversion superhet with a intermediate frequency at 10.7MHz. Q4 etc. is the low noise amplifier which is connected to the PCB mounted antenna. The IC U1 includes a balanced mixer which converts the incoming signal down from 345MHz to 10.7MHz. This IC also includes the required IF gain and detector output. YL1 and YL2 are IF filters. IC U2 and U3 perform video filtering and processing to provide a data signal to the microcontroller U6. The local oscillator is part of the receiver IC U1 SAW device YL3 is the frequency-determining component, 334.3 MHz (low side injection)is fed into the mixer.