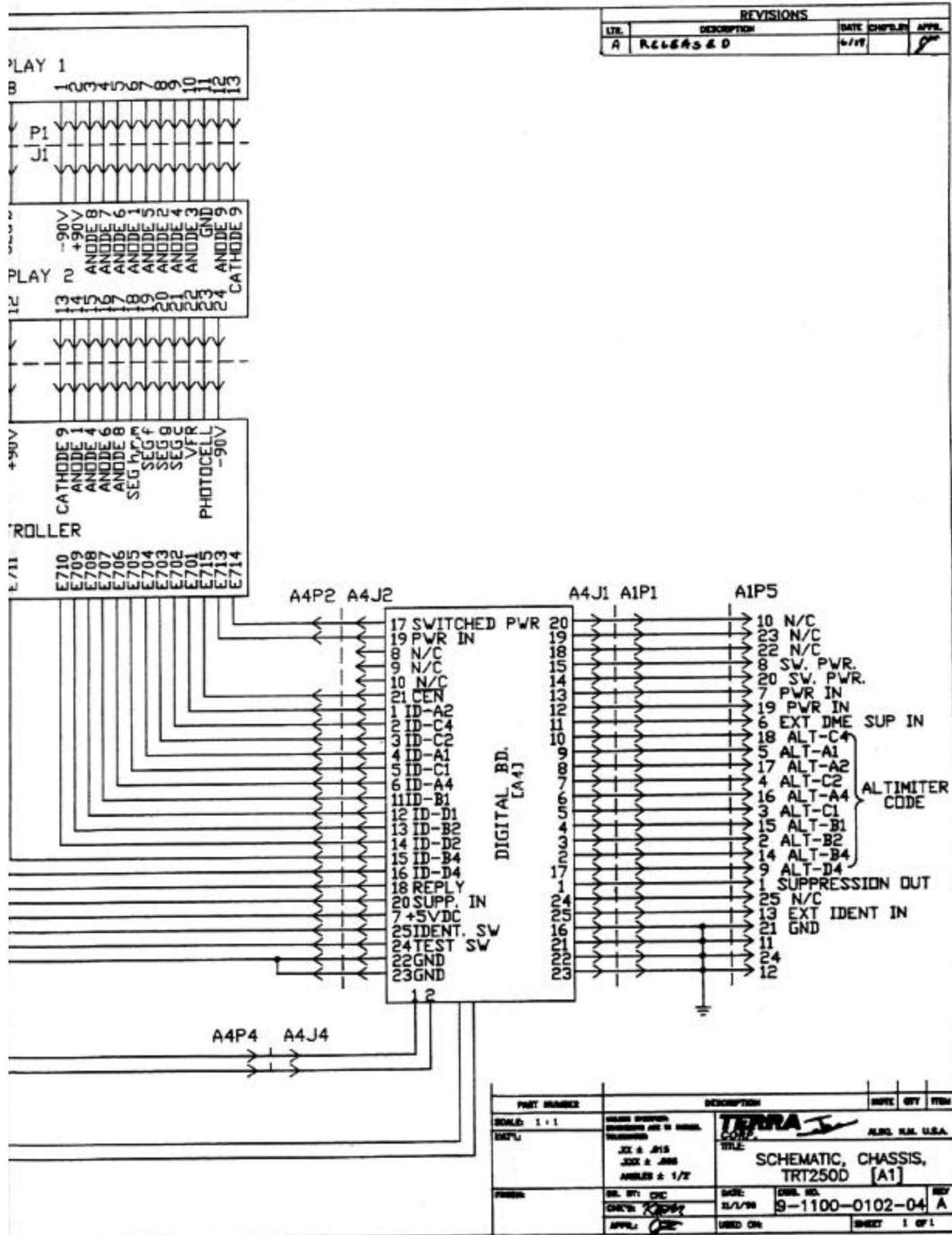
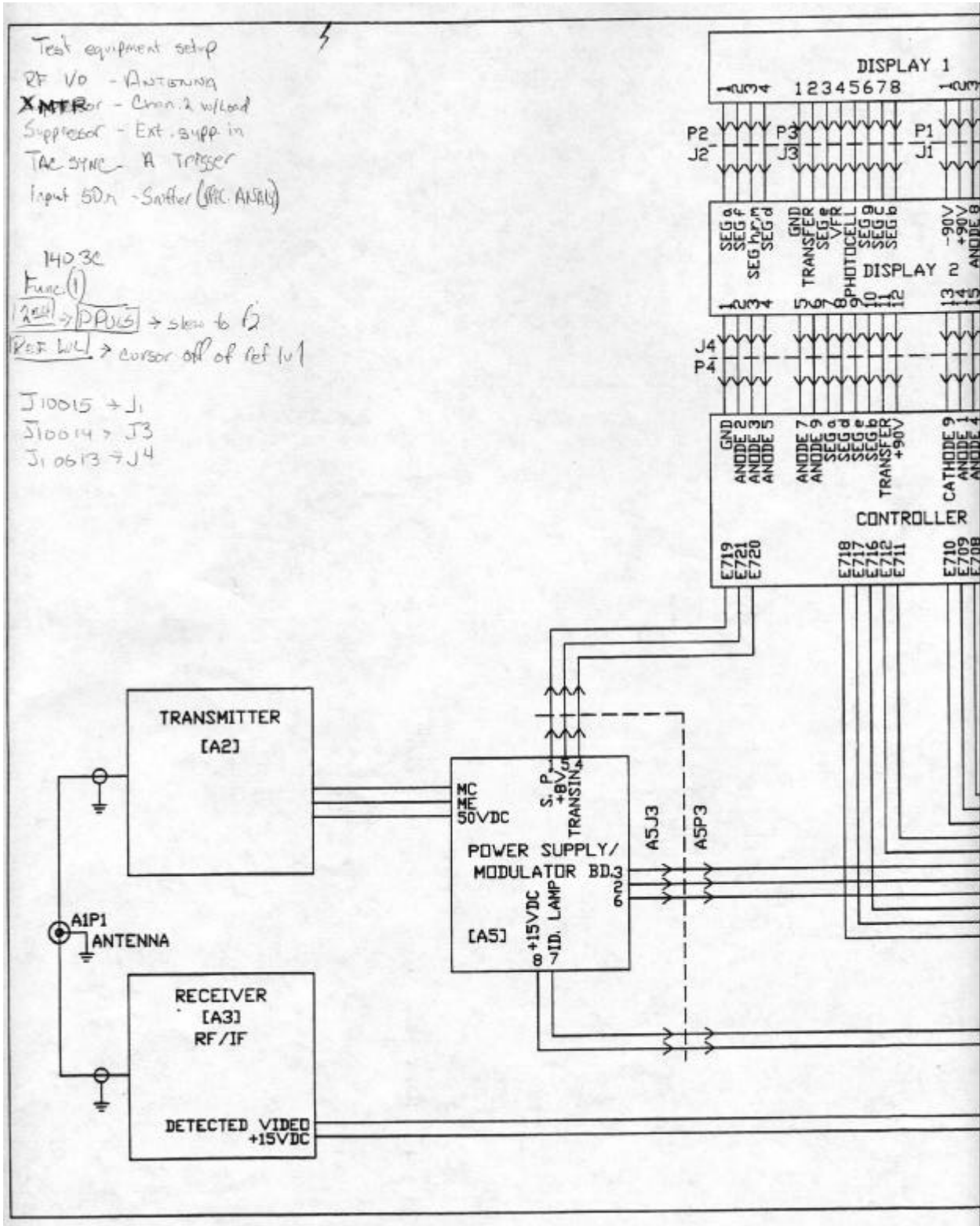


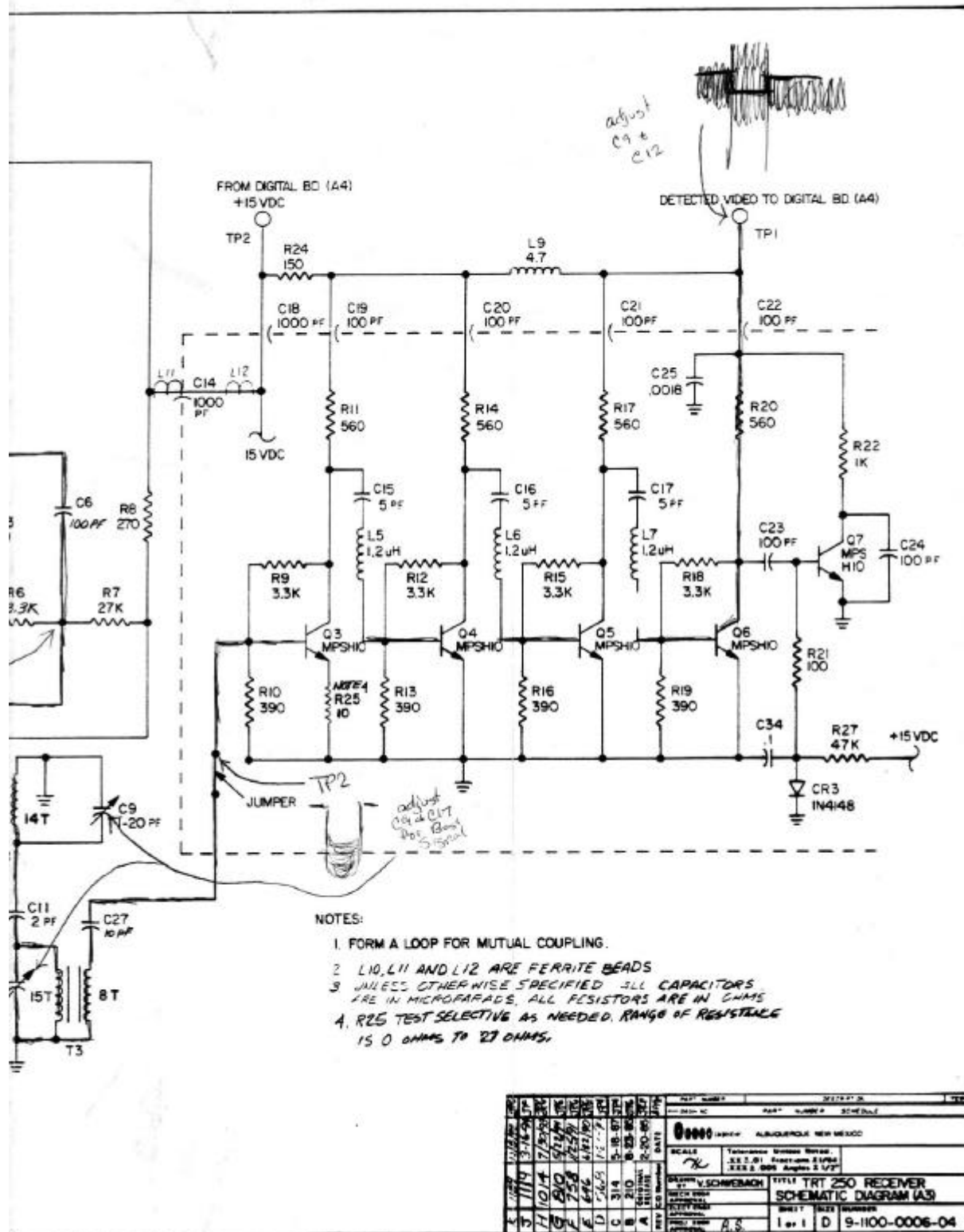
Chassis, Page 1 of 2



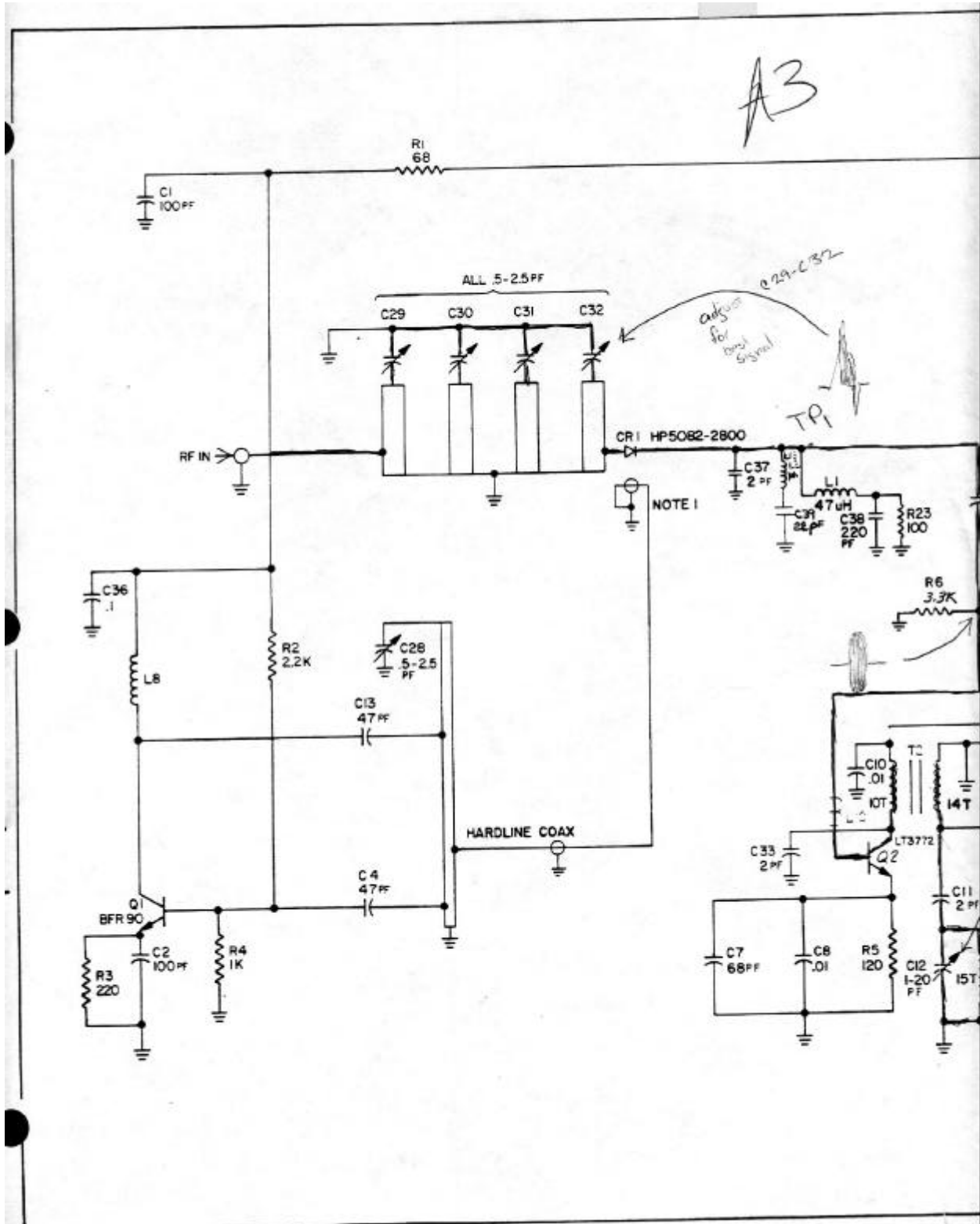
Chassis, Page 2 of 2



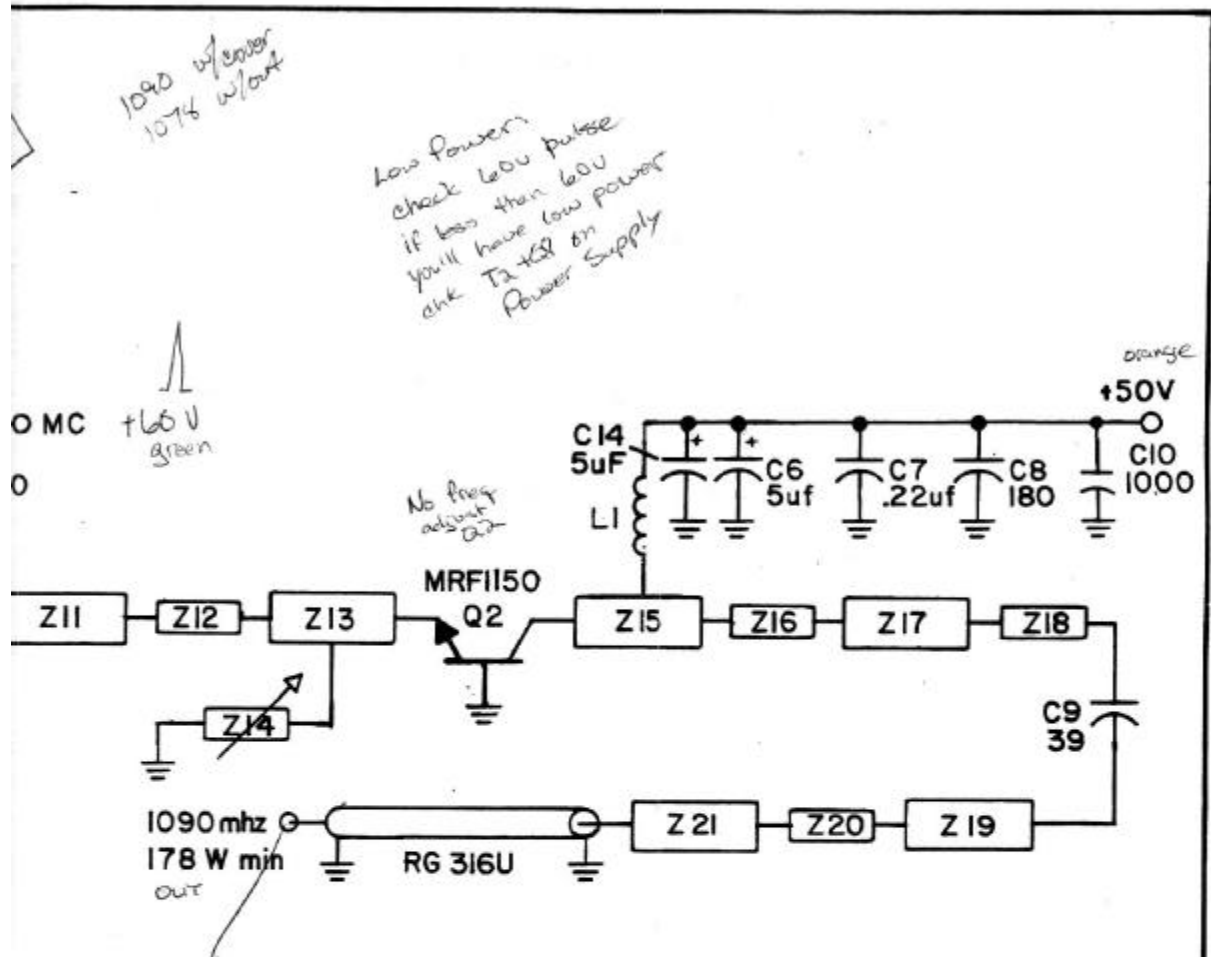
Receiver, Page 1 of 2



Receiver, Page 1 of 2



Transmitter, Page 1 of 2

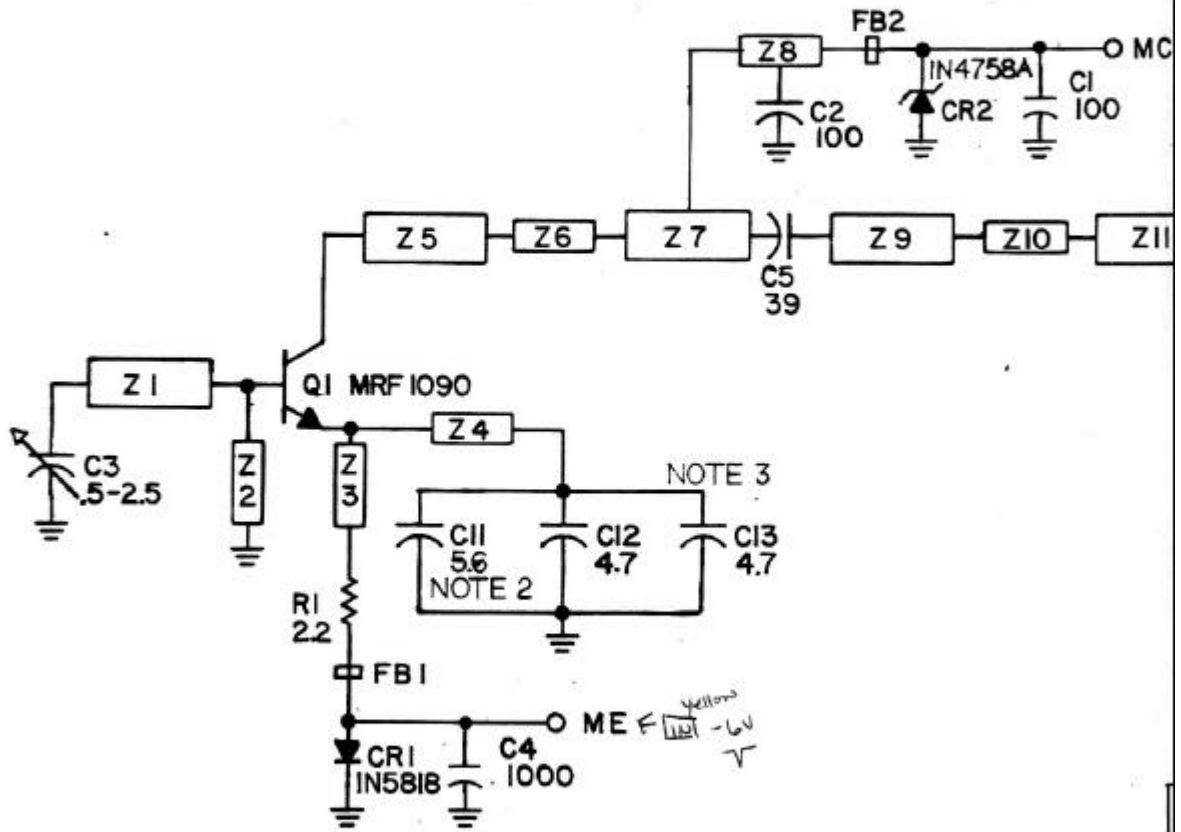


REV	CO Number	DATE	APPR.	PART NUMBER	DESCRIPTION	ITEM
H	1227	1/31/96	JPK			
G	1203	8/9/95	JPK			
F	669	8/6/90	JPK			
E	644	6/22/90	JPK			
D	314	5-14-87	JPK			
C	198	5-28-85	-			
B	189	4-29-85	-			
A	ORIGINAL RELEASE	2-20-85	AS			
				DASH NO. PART NUMBER SCHEDULE 00000 corporation ALBUQUERQUE, NEW MEXICO		
				SCALE	Tolerance Unless Noted; .XX ± .01 Fractions ± 1/64 .XXX ± .005 Angles ± 1/2°	
				DRAWN BY A.S.		TITLE TRT 250 TRANSMITTER SCHEMATIC DIAGRAM
				MECH ENGR APPROVAL ELECT ENGR APPROVAL PROJ ENGR APPROVAL A.S.		
				SHEET 1 of 1	SIZE B	NUMBER 9 1100 0007 02

191-0021-000

Transmitter, Page 2 of 2

A2

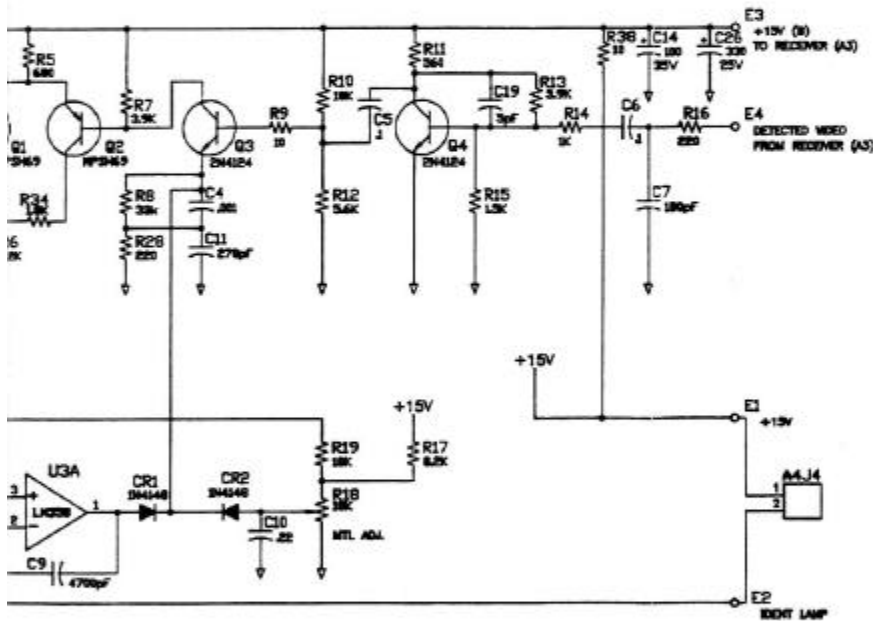
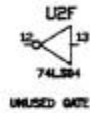
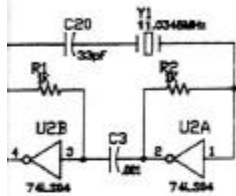


NOTES:

1. ALL CAPACITANCE IN pf's UNLESS NOTED.
2. C11, C12, & C13 ARE TEST SELECT FROM 4.7 TO 5.6 pF
3. C12 & C13 ARE INSTALLED IN TEST AS REQUIRED.

Digital Board, Page 1 of 2

REVISIONS					
LTR.	DESCRIPTION	DATE	CHK'D BY	APP.	
A	RELEASE FOR MANUFACTURE	6/18/84	RMN	JPF	
B	ECO #1145	7/18/84	EJA	JPG	
C	ECO #1150	8/17/84	EJA	JPG	
D	ECO #1181	7/7/95	JPF	JTR	

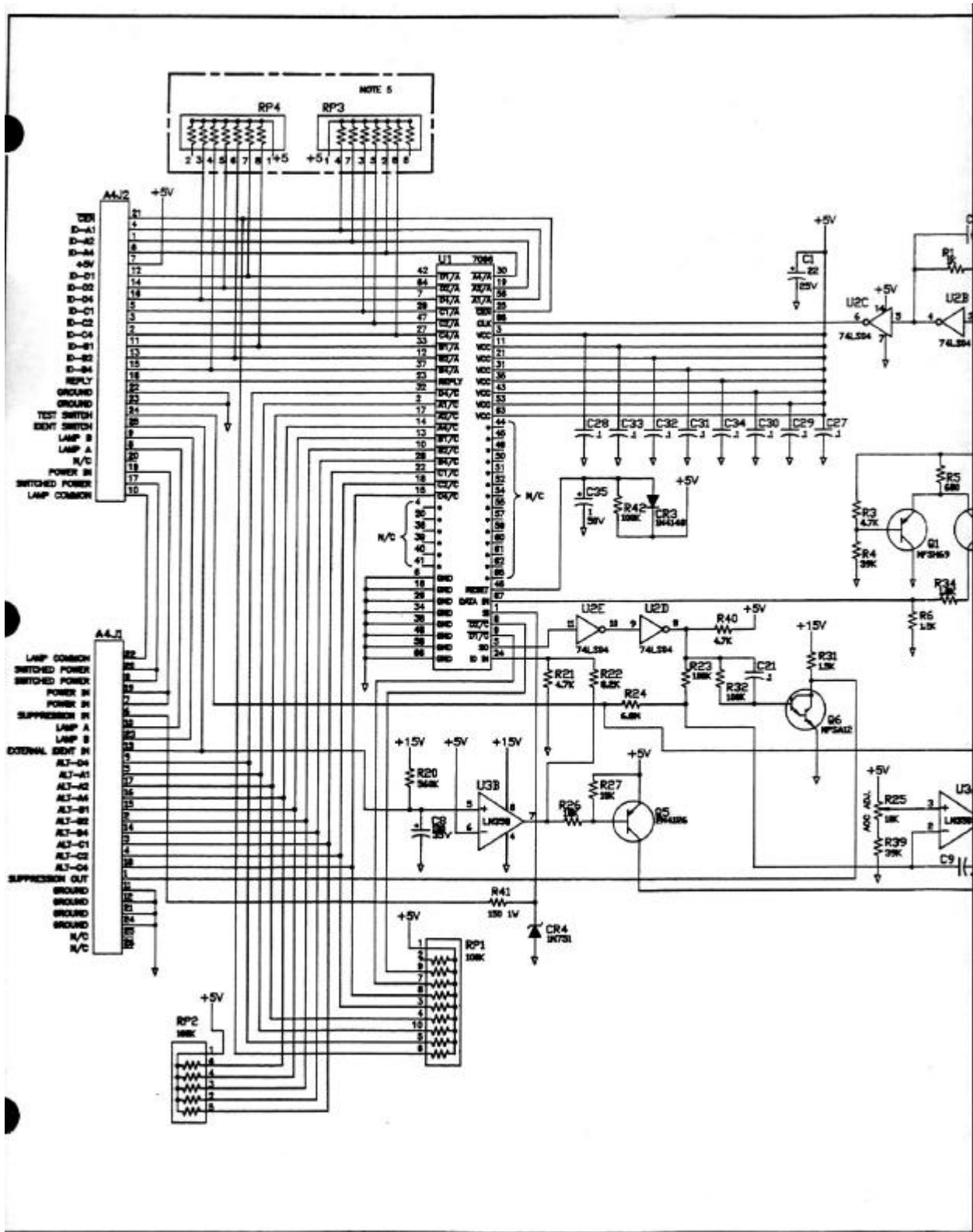


- NOTES: (UNLESS OTHERWISE SPECIFIED)
1. RESISTANCE VALUES ARE IN OHMS.
 2. ALL RESISTORS ARE 1/4W, 5%. *
 3. CAPACITANCE VALUES ARE IN MICROFARADS.
 4. * INDICATES RESERVED PINS ON U1.
 5. THIS DRAWING IS USED FOR UNITS S/N 4203 AND ABOVE.

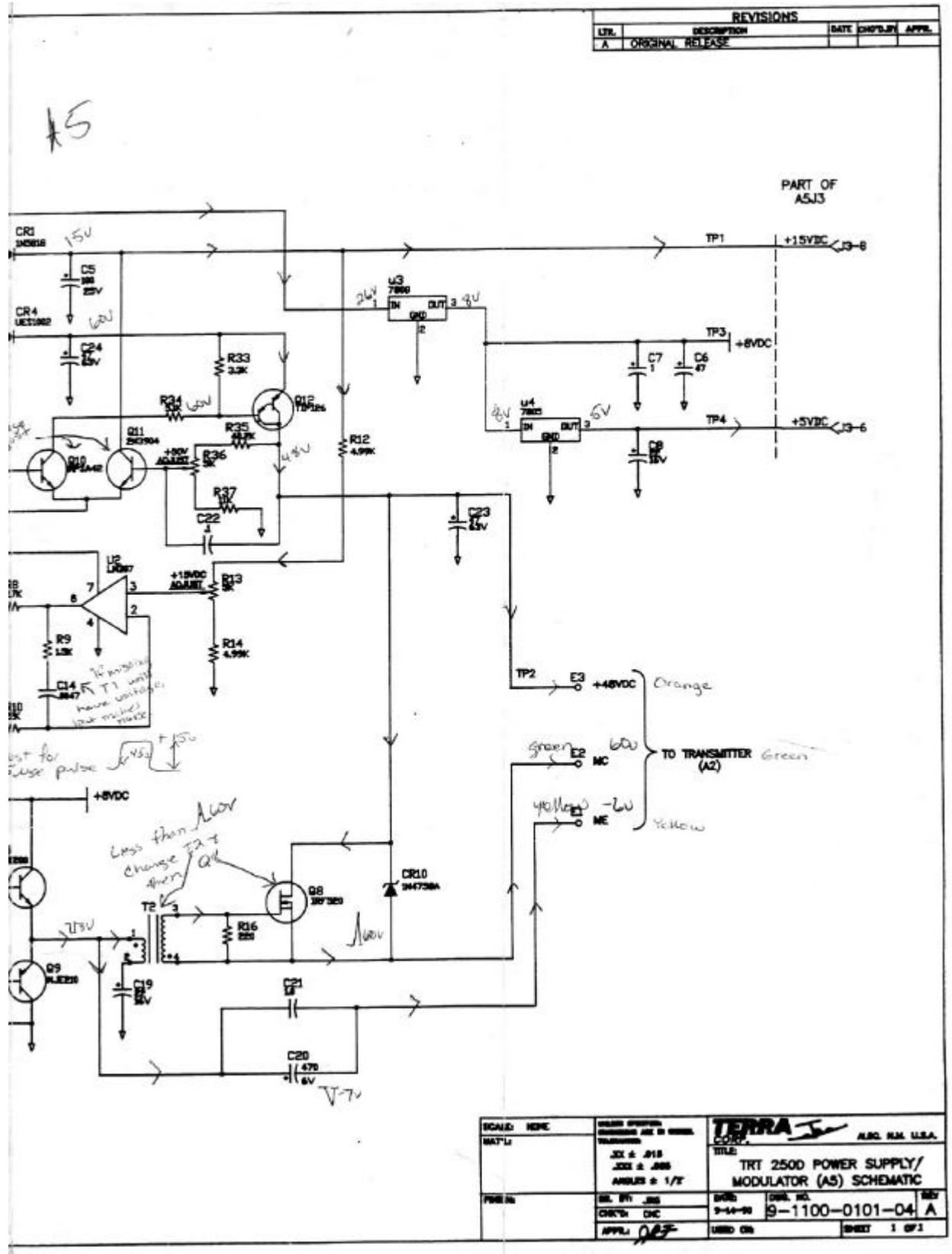
LAST USED	NOT USED
C20	Q1
Q2	Q12-C13
Q3	C16-C18
R42	Q14-Q15
U3	U2B-U2D
Y1	R20-R27

UNLESS SPECIFIED DIMENSIONS ARE IN DECIMAL INCHES.		ALBU, N.M. U.S.A.	
JX & J18 JCC & ANN APRILS & 1/7		TITLE: SCHEMATIC, TR1250 DIGITAL BO., (ALTERA)	
DR. BY: JPF CHK'D: JPF	DATE: 8/16/84	DRG. NO.: 9-1100-0129-04 D	REV: 1 OF 1
APPR.: JPF	USED ON:	SHEET:	1 OF 1

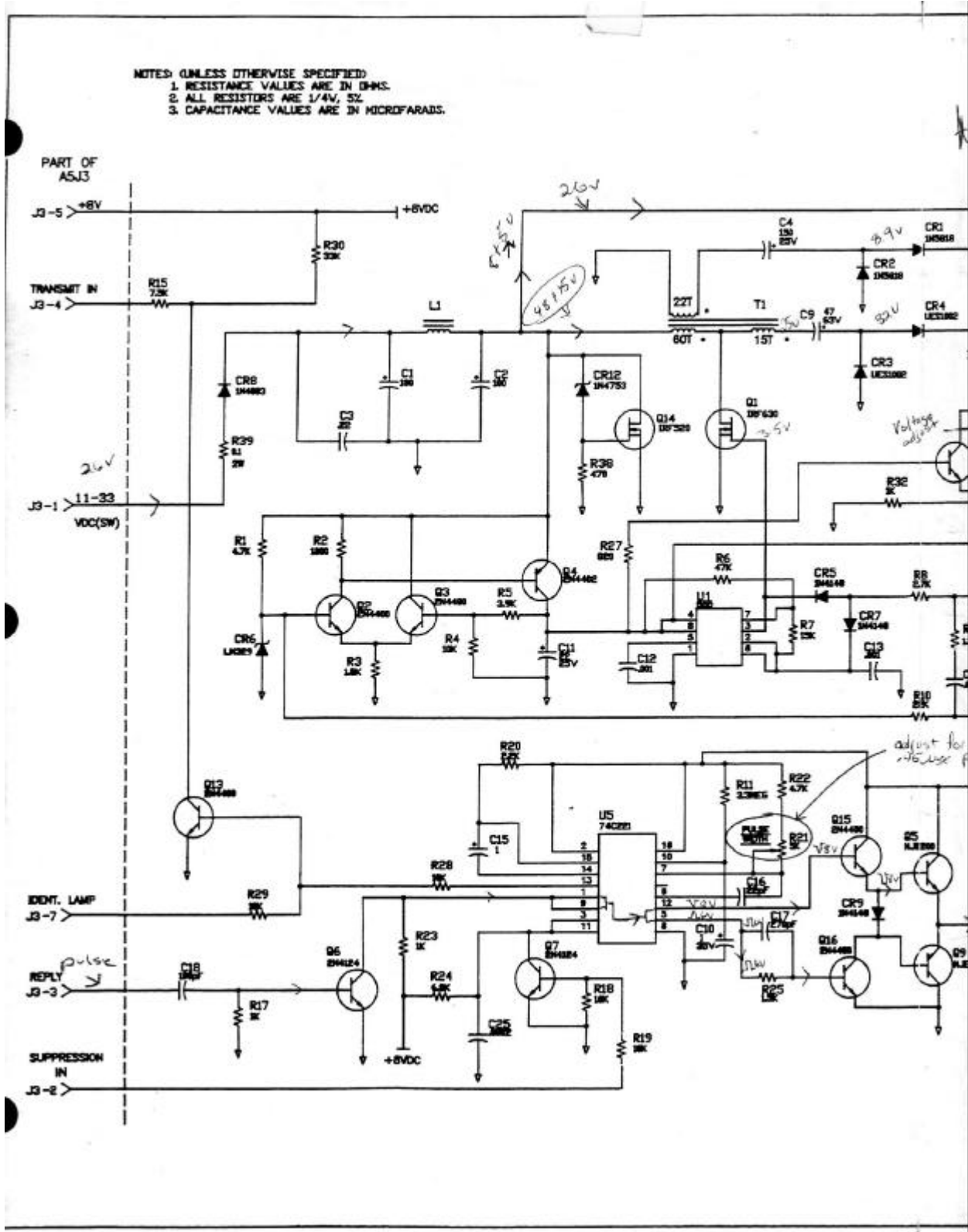
Digital Board, Page 2 of 2



Power Supply/Modulator, Page 1 of 2

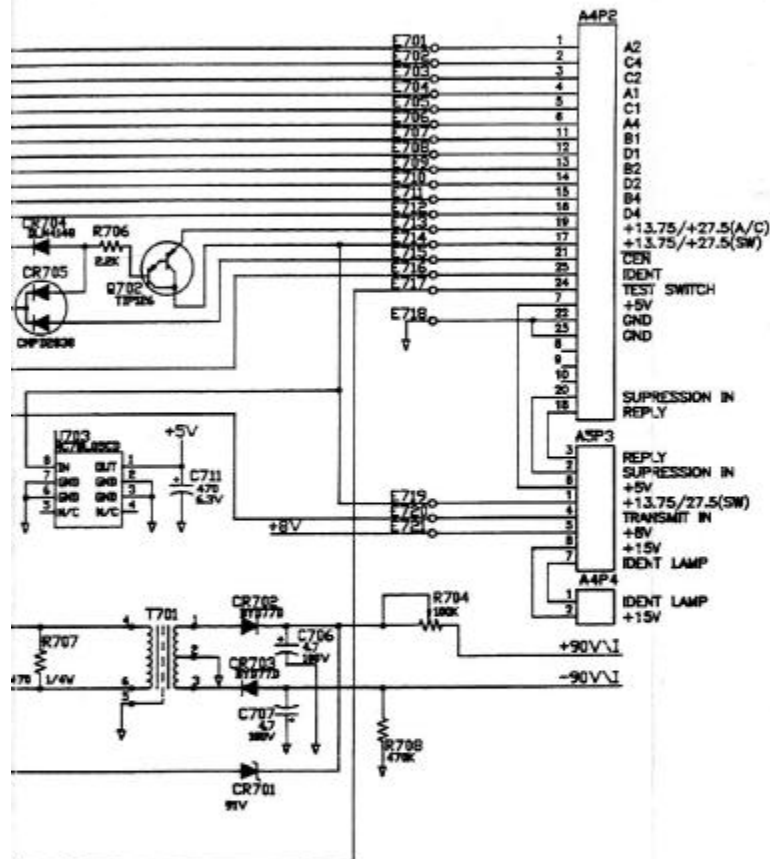


Power Supply/Modulator, Page 1 of 2



Smart Panel Controller, Page 1 of 2

REVISIONS				
LTR.	DESCRIPTION	DATE	CHG'D.BY	APPR.
A	RELEASE FOR MANUFACTURE	10/9/90	RMM	JRF
B	REVISED, ECO # 713	11/5/90	RMM	JRF
C	REVISED, ECO # 738	12/14/90	RMM	JRF
D	REVISED, ECO #1147	10/31/94	RMM	JPG



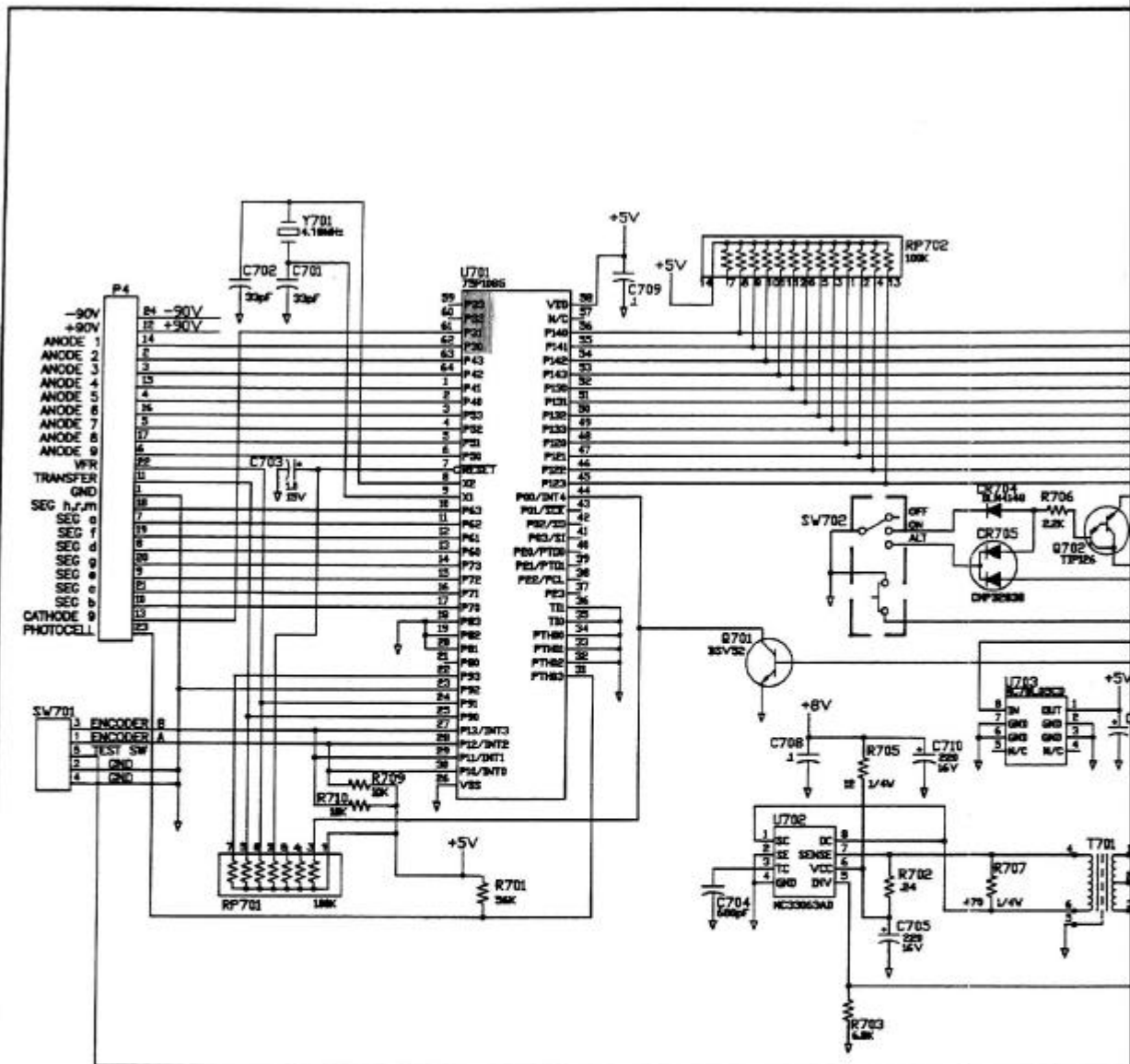
LAST USED	
CR11	Y01
CR208	
CR209	
CR710	
CR702	
CR703	
Y01	
U703	

UNLESS SPECIFIED:
DIMENSIONS ARE IN INCHES.
TOLERANCES:
.XX ± .015
.XXX ± .005
ANGLES ± 1/2

TERRA CORP. ALBQ. N.M. U.S.A.
TITLE:
CONTROLLER, SMART PANEL, TRT250D

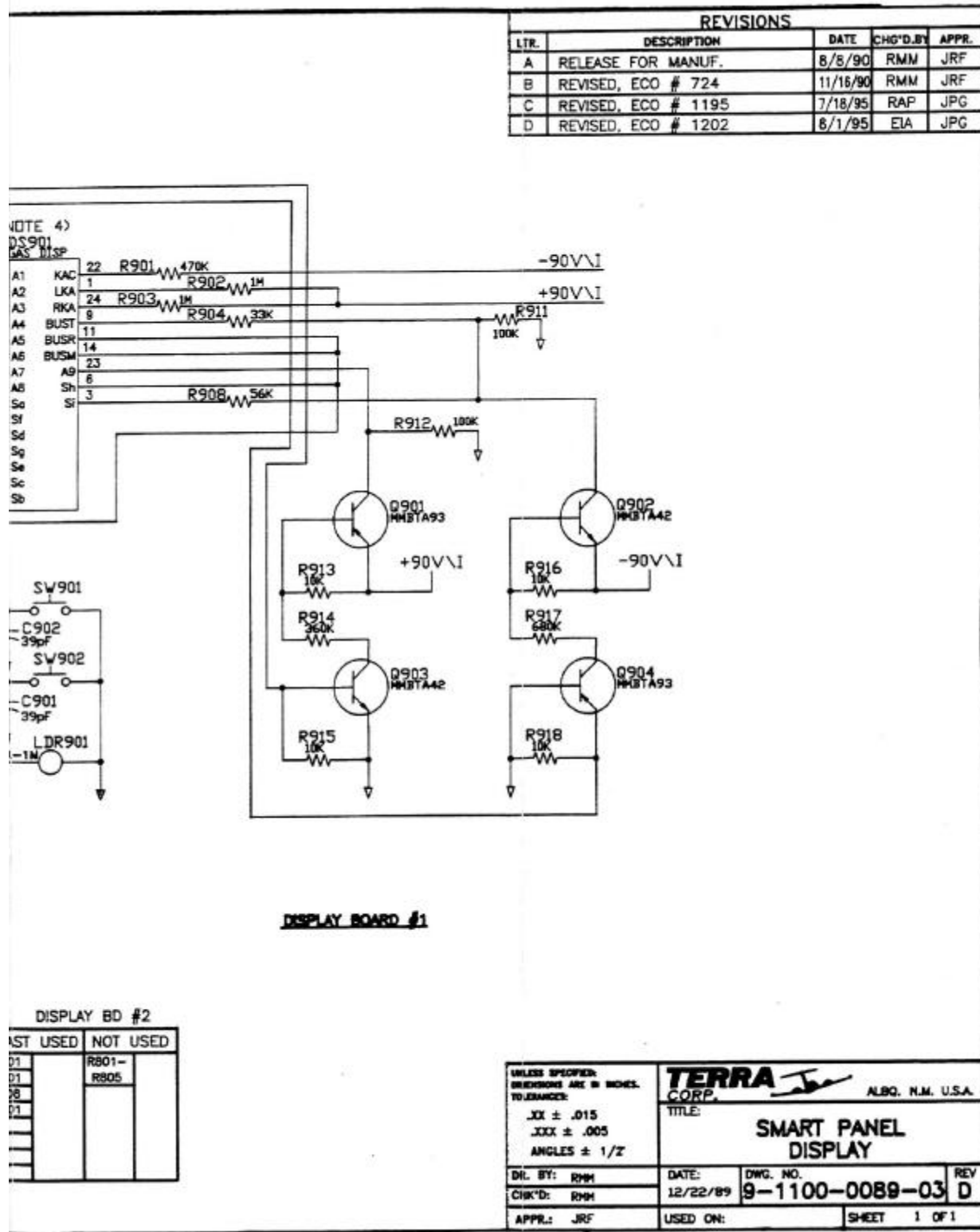
DR. BY: RMM	DATE: 9/26/90	DWG. NO. 9-1100-0091-04	REV D
CHK'D: RAM	USED ON:	SHEET 1 OF 1	
APPR.: JRF			

Smart Panel Controller, Page 2 of 2

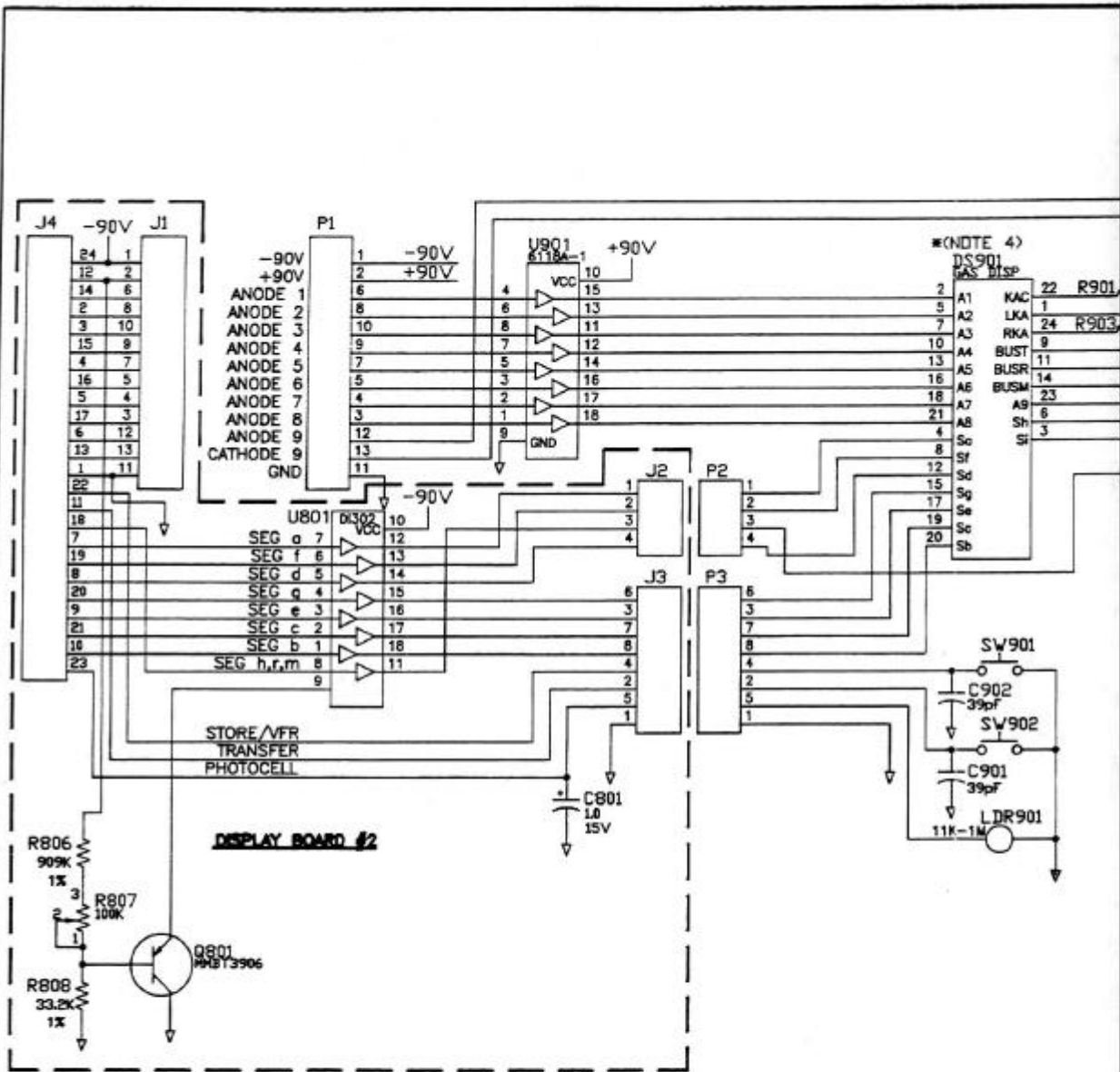


NOTES: UNLESS OTHERWISE SPECIFIED:
 1. RESISTANCE VALUES ARE IN OHMS.
 2. ALL RESISTORS ARE 1/8W, 5%.
 3. CAPACITANCE VALUES ARE IN MICROFARADS.

Smart Panel Display, Page 1 of 2



Smart Panel Display, Page 2 of 2



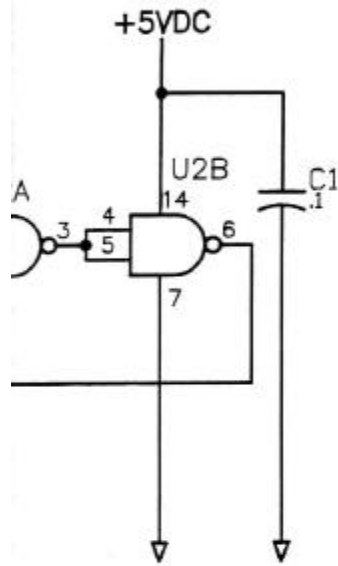
- NOTES: (UNLESS OTHERWISE SPECIFIED)
1. RESISTANCE VALUES ARE IN OHMS.
 2. ALL RESISTORS ARE 1/8W, 5%.
 3. CAPACITANCE VALUES ARE IN MICROFARADS.
 4. DS901 MATES WITH J901. (PIN NUMBERS ARE THE SAME)
 5. PLUGS AND JACKS ARE NOT CALLED OUT IN THE LAST USED TABLE. USE ALL SCHEMATICS IN COMPLETE UNIT TO DETERMINE NUMBER USAGE.

DISPLAY BD #1	
*LAST USED	NOT USED
DS801	R905-
LDR901	R907
Q904	R909-
R918	R910
SW902	
U901	

DISPLAY BD #2	
*LAST USED	NOT USED
C801	R801-
Q801	R805
R808	
U801	

Mode S Mod Board, Page 1 of 2

REVISIONS				
LTR.	DESCRIPTION	DATE	CHG'D.BY	APPR.
A	RELEASE FOR MANUFACTURE	3/16/94	RMM	<i>g/f</i>



NOTES: (UNLESS OTHERWISE SPECIFIED)

1. RESISTANCE VALUES ARE IN OHMS.
2. ALL RESISTORS ARE 1/4W, 5%.
3. CAPACITANCE VALUES ARE IN MICROFARADS.
4. REFER TO SCHEMATIC 9-1100-0005-04 FOR 'E' POINT CONNECTIONS.

UNLESS SPECIFIED: DIMENSIONS ARE IN INCHES. TOLERANCES: .XX ± .015 .XXX ± .005 ANGLES ± 1/2	TERRA CORP. ALBQ. N.M. U.S.A.		
	TITLE: SCHEMATIC, MODE 'S' MOD BOARD		
DR. BY: RMM <i>RMM</i>	DATE: 3/15/94	DWG. NO. 9-1100-0128-02	REV A
CHK'D: <i>JTW</i>	USED ON:		SHEET 1 OF 1
APPR.: <i>ABFala</i>			

Mode S Mod Board, Page 2 of 2

