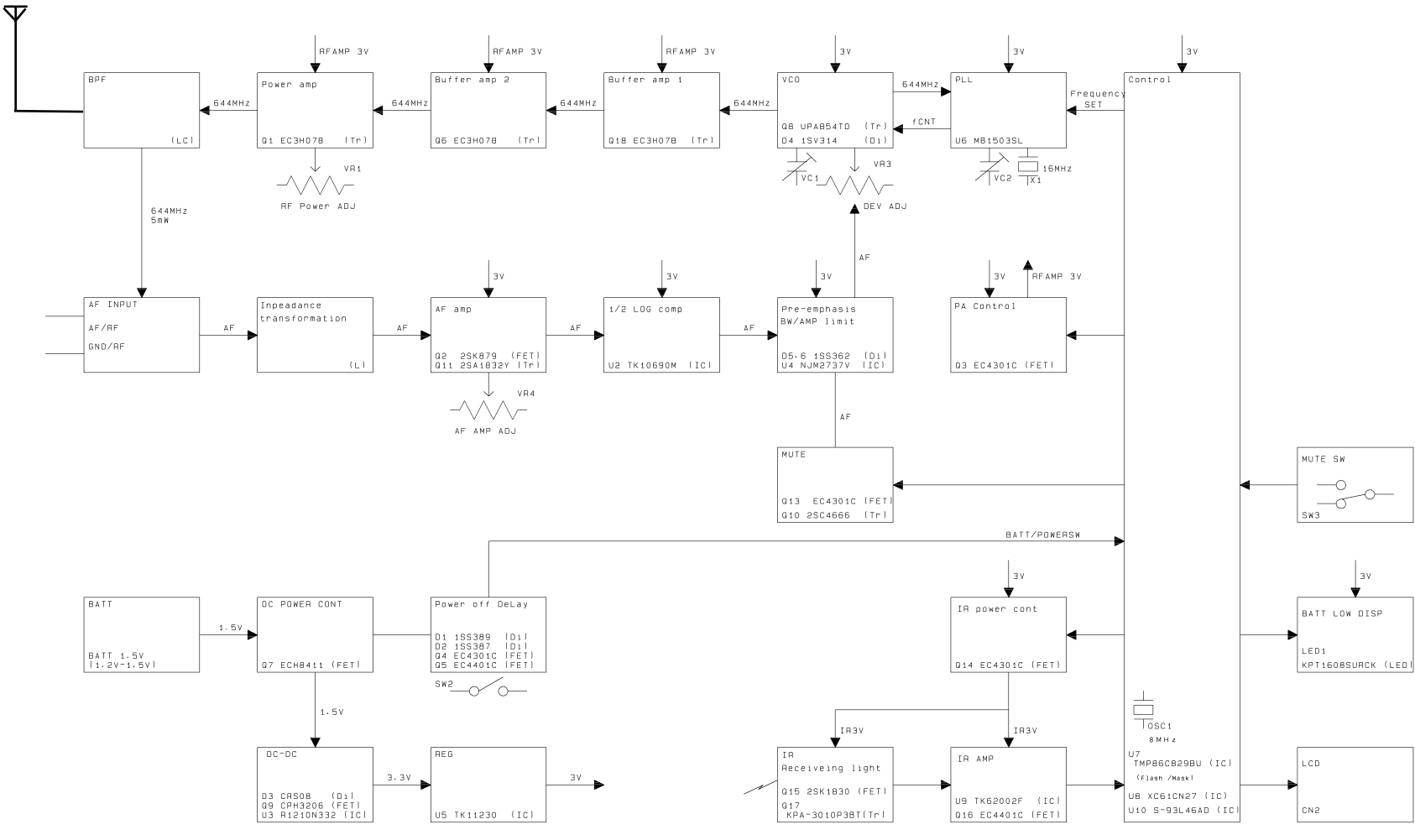


643.025MHz to 647.000MHz



DESIGN	Shi ya	DATE	2009 / 02 / 06	SHEET	1 / 1	VER	1.00
DRAWN	Shi ya	TITLE	A X 3 0 0 N B a n d			DRAWING NO.	W069C02B01-00
CHECK		NAME	Block Diagram				
APPROV.			CIRCUIT DESIGN, INC				

FCC ID: CCRA300

BLOCK DIAGRAM

