

VERNAL REV C
02 2780-001943

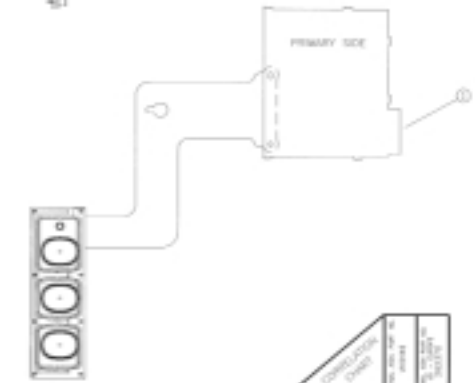
REVISED PART

LOCATION	DESCRIPTION	REV	BY	DATE
1	REVISION	1		
2	REVISION	2		
3	REVISION	3		
4	REVISION	4		
5	REVISION	5		
6	REVISION	6		
7	REVISION	7		
8	REVISION	8		

- NOTES:
1. REFERENCE FIGS. A-4-810 STANDARDS FOR ASSEMBLY PROCEDURES AND PROCEDURES, UNLESS OTHERWISE SPECIFIED.
 2. WINDY ITEM 2 TO ITEM 1 USING LAY SOLDER PROCEDURE.
 3. ENCLOSE ELECTRONICS IN HOUSING ITEM 2.
 4. AFTER LAY SOLDER, HOUSING MUST BE FUNCTIONALLY TESTED.
 5. PLACE BAR CODE / PATENT LABEL (ITEM 4) IN AREA INDICATED. DO NOT COVER OPERATOR IN HOUSING OR MARKING ON HOUSING.
 6. FORM ITEM 53 MAY NOT PROTRUDE BEYOND CODES OF HOUSING. DO NOT COVER MARKING IN HOUSING.
 7. ASSEMBLY IS TO PRINT LABEL INFORMATION AS SHOWN, USING APPROXIMATE LABEL ITEM 40.
 8. REFER TO LATEST REVISION OF DRAWING LISTINGS FOR ACTUAL PATENT NUMBERS FOR THE PATENT GROUP IDENTIFIED.
 9. LAY OUT PATENT NUMBERS TO THE RIGHT AVAILABLE. ONE NUMBER / COMBINATION OF OTHER NUMBERS MUST BE MARKED FOR CORRESPONDING PATENT NUMBERS AS SHOWN ON LATEST REVISION OF DRAWING LISTINGS.
 10. THE MARK ON EACH PART WILL BE PROGRAMMED WITH THE FOLLOWING POWER LEVEL, DEVIATED FOR FCC COMPLIANCE.

POWER	HWL (WATT)	HWL (WATT)	HWL (WATT)	HWL (WATT)	HWL (WATT)	HWL (WATT)
1	0.01-0.02	0.001	0.001	0.001	0.001	0.001
2	0.02-0.05	0.002	0.002	0.002	0.002	0.002
3	0.05-0.10	0.005	0.005	0.005	0.005	0.005
4	0.10-0.20	0.010	0.010	0.010	0.010	0.010
5	0.20-0.50	0.020	0.020	0.020	0.020	0.020
6	0.50-1.00	0.050	0.050	0.050	0.050	0.050
7	1.00-2.00	0.100	0.100	0.100	0.100	0.100
8	2.00-5.00	0.200	0.200	0.200	0.200	0.200
9	5.00-10.00	0.500	0.500	0.500	0.500	0.500
10	10.00-20.00	1.000	1.000	1.000	1.000	1.000

- NOTE: THE MARK VALUE REFLECTS A SELF CHECK. HIGH BYTE IS THE ACTUAL VALUE, LOW BYTE IS COMPLEMENT OF 184H (0101 + 2).
- NOTE: THESE MARK VALUES ARE TO BE USED ONLY IN VEHICLES WITHOUT METALLIC PAINT.
11. MARKING IDENTIFICATION:
 - A. THE RECEIVER THRESHOLD IS PROGRAMMED IN HIGH ADDRESS BYTE. READING HIGH BYE WITH 0 - 10000000 00000000 0000 0000.
 - B. THE RECEIVER THRESHOLD = 12.22 + 0.119 * POWER.
 - C. THE RECEIVER THRESHOLD IS PLACED IN THE UPPER BYTE. THE LOWER BYTE IS THE UPPER BYTE * 10. THIS INDICATES.



POWER	HWL (WATT)	HWL (WATT)	HWL (WATT)
1	0.01	0.01	0.01
2	0.02	0.02	0.02
3	0.05	0.05	0.05
4	0.10	0.10	0.10
5	0.20	0.20	0.20
6	0.50	0.50	0.50
7	1.00	1.00	1.00
8	2.00	2.00	2.00
9	5.00	5.00	5.00
10	10.00	10.00	10.00

ELECTRONICS TAG - UNLESS OTHERWISE SPECIFIED

DO NOT SEAL

DATE: 1-17-17

BY: [Signature]

REVISION: 0

REV	DATE	BY	CHK	APP	DESCRIPTION
1	02/27/17	0000	00	000000	ISSUE FOR PRODUCTION
2	03/01/17	0000	00	000000	REVISED PART
3	03/01/17	0000	00	000000	REVISED PART
4	03/01/17	0000	00	000000	REVISED PART
5	03/01/17	0000	00	000000	REVISED PART
6	03/01/17	0000	00	000000	REVISED PART
7	03/01/17	0000	00	000000	REVISED PART
8	03/01/17	0000	00	000000	REVISED PART
9	03/01/17	0000	00	000000	REVISED PART
10	03/01/17	0000	00	000000	REVISED PART

TRIMSON CONTROLS

ELECTRONIC ASSEMBLY

REV: 0

DATE: 02/27/17

BY: [Signature]

CHK: [Signature]

APP: [Signature]

DESCRIPTION: VK9-446

8 7 6 5 4 3 2 1