

**NOTES:**

- REFERENCE IPC-A-610 STANDARDS FOR ASSEMBLY PRACTICES, AND PROCEDURES, UNLESS OTHERWISE SPECIFIED.
- EACH ASSEMBLY SHALL BE LABELED WITH A KAPTON LABEL ON CIRCUIT SIDE OF PCB. DO NOT COVER TEST POINTS, OR BOARD PART NUMBER/REV. LABEL MUST REFERENCE FINAL ASSEMBLY PRODUCT CODE AND FINAL ASSEMBLY REVISION LEVEL.  
 LABEL DEFINITION  
 PRODUCT CODE      REV. LEVEL  
 ALPHANUMERIC SERIAL #
- MAXIMUM ALLOWABLE PROTRUSION OF LEADS FROM BOTTOM SIDE OF BOARD IS .100 [2.54], EXCEPT DS1.
- MAXIMUM ALLOWABLE GAP BETWEEN MATING SURFACES OF PC BOARD AND J1, J2, J3, AND J4 IS TO IPC STANDARD.
- AFTER DEPANELLING ALL CARRIER TABS MAY NOT PROTRUDE BEYOND EDGE OF PCB. MAXIMUM ROUTER CUT INTO PCB SHALL BE LESS THAN .010 [.25] COPPER FOIL PATTERN MUST NOT BE TOUCHED BY ROUTER TOOLING.
- COMPASS SHALL BE ACCURATE WITHIN THE FOLLOWING LIMITS:  
 HEADING      ANGLE      VALUE(HEX)  
 N      0 ±10      0x00 ±0x07  
 E      90 ±10      0x40 ±0x07  
 S      180 ±10      0x80 ±0x07  
 W      270 ±10      0xC0 ±0x07  
 AT A 200 MILLIGAUSS FIELD STRENGTH.
- WITH THE UNIT IN COMPASS DISPLAY MODE ADJUST THE MODULE INPUT VOLTAGE TO 13.5 VDC ± .1 VDC AND THE DIMMING LEVEL TO FULL BRIGHTNESS. VERIFY THAT THE FILAMENT VOLTAGE MEASURES BETWEEN 3.50VAC +400/-250mVAC AND 10.0VDC ± 1.0VDC.
- L-SHAPE LEAD BEND REQUIRED FOR ELECTROLYTIC CAPACITORS (C5, C12, C31 AND C41) AT FLAGGED LOCATIONS.
- Y102 IS SENSITIVE TO TEMPERATURES ABOVE 275°C. FOR APPLICATION LONGER THAN 2.5 SECONDS. DO NOT EXCEED THESE CONDITIONS DURING REWORK.
- THE NVM ON EACH PART WILL BE PROGRAMMED WITH THE FOLLOWING POWER LEVEL SETTINGS FOR FCC COMPLIANCE.

REGION	FREQ. RANGE (MHz)	ROM POWER LEVEL (u101)	NVM ADDRESS	FCC CERTIFIED POWER LEVEL (NVM DATA)	NEEDS TO BE PROGRAMMED IN NVM
0	285-303	0x0CF1	0x0D	0x0FEE	YES
1	303-322	0x0EEF	0x0E	0x0FEE	YES
2	335-360	0x0FEE	0x0F	0x0EEF	YES
3	360-380	0x0FEE	0x10	0x0CF1	YES
4	380-400	0x15E8	0x11	0x0DF0	YES
5	410-420	0x19E4	0x12	0x0FEE	YES
DEFAULT CODE POWER		0x3BC2	0x0A	0x3FBE	YES
VEHICLE ID		0x00FD	0x13	-	NO
MAX TX POWER		0xFEFF	0x14	-	NO

- NOTE: THE NVM VALUE INCLUDES A SELF CHECK. HIGH BYTE IS THE ACTUAL VALUE. LOW BYTE IS COMPLEMENT OF HIGH BYTE + 2.
- TRAINING SENSITIVITY:
    - THE RECEIVER THRESHOLD IS PROGRAMMED IN NVM ADDRESS 0x04. MEASURE RSSI AT NODAL WITH A -55dBm 280MHz SIGNAL. THE RECEIVER THRESHOLD IS 10ED.
    - THE RECEIVER THRESHOLD IS PUT IN THE UPPER BYTE, THE LOWER BYTE IS THE UPPER BYTE +2, THEN INVERTED.
  - LED COLOR TYPE AND LOCATION - VISION SYSTEM.
  - OUTPUT POWER MEASUREMENTS (RF) SHALL BE PERFORMED DURING NODAL TEST:
 

FREQ(MHz)	POWER (P1) NO ATTENUATOR(dBm)	POWER ATTENUATOR #1 (dBm)	POWER ATTENUATOR #2 (dBm)
280	-1.6 ±3.5	(P1-4.5) ±1.0	(P1-9.5) ±3.0
340	-1.6 ±3.5	(P1-4.5) ±1.0	(P1-9.5) ±3.0
390	-1.6 ±3.5	(P1-4.5) ±1.0	(P1-9.5) ±3.0
  - TRAINING SENSITIVITY SHALL BE PERFORMED DURING NODAL TEST:
 

POWER(dBm)	FREQUENCY(MHz)	VCO FREQUENCY(MHz)
-56.0 ±5.0	283.2	280
-58.0 ±6.0	393.2	390
  - REFERENCE TEST DEVELOPMENT SPEC # TD-ELE-002.
  - PART MUST BE 100% ELECTRICALLY TESTED.
  - APPLY SCREWS (ITEM 3 AND 4) WITH TORQUE OF 2.5 (± 1.0) in lb, FROM THE SECONDARY SIDE OF PCB TO SECURE CONNECTOR (J3).

REV	SH/ZONE	DESCRIPTION	P.C.N.	BY	DATE	BY
A		SEE DRAWING VJ4005 FOR HISTORY RELEASE FOR PRODUCTION	59929	HP	30MAR01	EDICT
B1		REV CORR CHART VJ0803 PLACED WAS V53925 NP (C32, C33)	61064	KVL	27JUN01	EDICT
B3		VE3111 WAS V97532 (R32, R200)				
B4		VL8329 WAS VJ4186 (U1)				
B5		V98739 PLACED WAS NP (C71, C128)				
B6		V53925 NP WAS PLACED (C81, C92)				
B7		V53925 PLACED WAS NP (C82)				
B8		V98739 NP WAS PLACED (C83, C95)				
B9		VG1106 PLACED WAS NP (C142)				
B10		VF5913 PLACED WAS NP (CR44)				
B11		VB4277 NP WAS PLACED (Q20, Q26)				
B12		VA2501 NP WAS PLACED (Q21, Q23)				
B13		V98736 NP WAS PLACED (R1)				
B14		VE3111 NP WAS PLACED (R53, R65)				
B15		V98737 NP WAS PLACED (R62, R67)				
B16		VB4271 PLACED WAS NP (R129)				
B17		VB4268 PLACED WAS NP (R131)				
B18		V98736 PLACED WAS NP (R137)				
B19		VE8699 PLACED WAS NP (U104) NEW STENCIL REQUIRED				

REV	SH/ZONE	DESCRIPTION	P.C.N.	BY	DATE	BY
C1		REV CORR CHART	61311	JR	10OCT01	EDICT
C2		DEL V98741 PLACED (C69)				
C3		DEL V98739 PLACED (C71)				
C4		VG1106 WAS V68100 (C9, C40)				
C5		V68469 WAS V6452 (C132)				
C6		VG1110 WAS V56967 (CR12)				
C7		VJ0059 WAS VK2765 (L130, L131)				
C8		VAS609 WAS V98736 (R1, R7, R63, R81, R83, R98, R105, R128, R130, R137, R145)				
C9		V55507 WAS V27868 (R85)				
C10		VM3601 WAS VL8329 (U1)				
C11		V53925 NP WAS PLACED (C82)				
C12		VH5052 NP WAS PLACED (CR14, CR15, CR24, CR26, CR27, CR31, CR32, CR37, CR38)				
C13		VH5052 PLACED WAS NP (CR25, CR54)				
C14		VJ4184 NP WAS PLACED (CR35)				
C15		VF5913 NP WAS PLACED (CR44)				
C16		V97532 NP WAS PLACED (R14, R26, R29, R60)				
C17		V97532 PLACED WAS NP (R66)				
C18		VB6959 NP WAS PLACED (R68, R74)				
C19		VAS607 NP WAS PLACED (R96)				
C20		VAS609 NP WAS PLACED (R98)				
C21		ADD VL6631 PLACED (2, 3)				
C22		ADD VA1511 PLACED (R15)				
C23		ADD VM0790 PLACED (R16)				
C24		REV NOTE 7, 10 AND 11				
C25		ADD NOTE 17 ND STENCIL CHANGE REQUIRED				

**CORRELATION CHART**

DRAWING REV	DATE	REV	REV
A	30MAR01	A	A
B	27JUN01	B	B
C	10OCT01	C	C

PCB PART NO. (DWG. NO. VJ4004)	PCB PART NO. (DWG. NO. VJ0998)	PRODUCT CODE
		NF

ELECTRONICS STD. UNLESS OTHERWISE SPECIFIED

THIRD ANGLE PROJECTION

DO NOT SCALE

X.XX DECIMAL ± 0.040 [± 1.0] ANGULAR ± 2° 0'

X.XXX DECIMAL ± 0.020 [± .50] DRAFT ANGLES NOTED

X.XXXX DECIMAL ± 0.006 [± .15]

SPECIFIES FEATURES REQUIRING SPECIAL CONSIDERATIONS. SEE PRODUCT CONTROL PLAN.

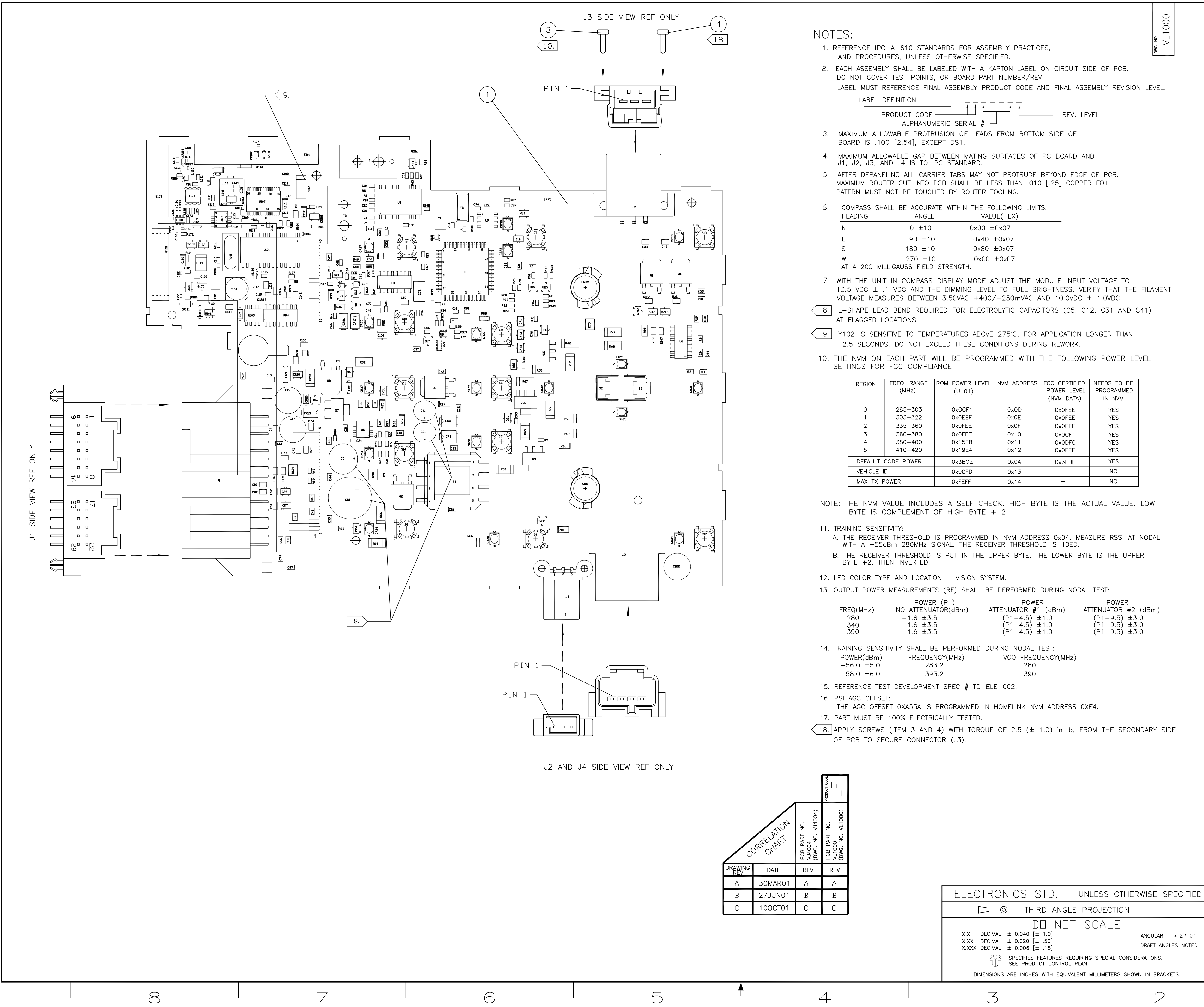
DIMENSIONS ARE INCHES WITH EQUIVALENT MILLIMETERS SHOWN IN BRACKETS.

**JOHNSON CONTROLS**  
AUTOMOTIVE SYSTEMS GROUP INTERIORS

PCB ASSEMBLY

PROJECT	02.5 FN145 OHC	DRWN. BY	H. PHAM	DATE	19APR01
CUSTOMER	FORD	CHKD. BY	B. SUNNERVILLE	DATE	19APR01
SIZE	D	ENGR. APPR	P. TARNOW	DATE	19APR01
TOOL NO. REF.		SCALE	2/1	SHEET	1 OF 5
DATA FORMAT	ACAD	DWG. NO.	VL0998	REV.	C





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- EACH ASSEMBLY SHALL BE LABELED WITH A KAPTON LABEL ON CIRCUIT SIDE OF PCB. DO NOT COVER TEST POINTS, OR BOARD PART NUMBER/REV. LABEL MUST REFERENCE FINAL ASSEMBLY PRODUCT CODE AND FINAL ASSEMBLY REVISION LEVEL.  

LABEL DEFINITION	
PRODUCT CODE	REV. LEVEL
ALPHANUMERIC SERIAL #	
- MAXIMUM ALLOWABLE PROTRUSION OF LEADS FROM BOTTOM SIDE OF BOARD IS .100 [2.54], EXCEPT DS1.
- MAXIMUM ALLOWABLE GAP BETWEEN MATING SURFACES OF PC BOARD AND J1, J2, J3, AND J4 IS TO IPC STANDARD.
- AFTER DEPANELLING ALL CARRIER TABS MAY NOT PROTRUDE BEYOND EDGE OF PCB. MAXIMUM ROUTER CUT INTO PCB SHALL BE LESS THAN .010 [.25] COPPER FOIL PATTERN MUST NOT BE TOUCHED BY ROUTER TOOLING.
- COMPASS SHALL BE ACCURATE WITHIN THE FOLLOWING LIMITS:

HEADING	ANGLE	VALUE(HEX)
N	0 ±10	0x00 ±0x07
E	90 ±10	0x40 ±0x07
S	180 ±10	0x80 ±0x07
W	270 ±10	0xC0 ±0x07

AT A 200 MILLIGAUSS FIELD STRENGTH.
- WITH THE UNIT IN COMPASS DISPLAY MODE ADJUST THE MODULE INPUT VOLTAGE TO 13.5 VDC ± .1 VDC AND THE DIMMING LEVEL TO FULL BRIGHTNESS. VERIFY THAT THE FILAMENT VOLTAGE MEASURES BETWEEN 3.50VAC +400/-250mVAC AND 10.0VDC ± 1.0VDC.
- L-SHAPE LEAD BEND REQUIRED FOR ELECTROLYTIC CAPACITORS (C5, C12, C31 AND C41) AT FLAGGED LOCATIONS.
- Y102 IS SENSITIVE TO TEMPERATURES ABOVE 275°C. FOR APPLICATION LONGER THAN 2.5 SECONDS. DO NOT EXCEED THESE CONDITIONS DURING REWORK.
- THE NVM ON EACH PART WILL BE PROGRAMMED WITH THE FOLLOWING POWER LEVEL SETTINGS FOR FCC COMPLIANCE.

REGION	FREQ. RANGE (MHz)	ROM POWER LEVEL (U101)	NVM ADDRESS	FCC CERTIFIED POWER LEVEL (NVM DATA)	NEEDS TO BE PROGRAMMED IN NVM
0	285-303	0x0CF1	0x0D	0x0FEE	YES
1	303-322	0x0EEF	0x0E	0x0FEE	YES
2	335-360	0x0FEE	0x0F	0x0EEF	YES
3	360-380	0x0FEE	0x10	0x0CF1	YES
4	380-400	0x15EB	0x11	0x0DF0	YES
5	410-420	0x19E4	0x12	0x0FEE	YES
DEFAULT CODE POWER		0x3BC2	0x0A	0x3FBE	YES
VEHICLE ID		0x00FD	0x13	-	NO
MAX TX POWER		0xFEFF	0x14	-	NO

- NOTE: THE NVM VALUE INCLUDES A SELF CHECK. HIGH BYTE IS THE ACTUAL VALUE. LOW BYTE IS COMPLEMENT OF HIGH BYTE + 2.
- TRAINING SENSITIVITY:
    - THE RECEIVER THRESHOLD IS PROGRAMMED IN NVM ADDRESS 0x04. MEASURE RSSI AT NODAL WITH A -55dBm 280MHz SIGNAL. THE RECEIVER THRESHOLD IS 10ED.
    - THE RECEIVER THRESHOLD IS PUT IN THE UPPER BYTE, THE LOWER BYTE IS THE UPPER BYTE +2, THEN INVERTED.
  - LED COLOR TYPE AND LOCATION - VISION SYSTEM.
  - OUTPUT POWER MEASUREMENTS (RF) SHALL BE PERFORMED DURING NODAL TEST:

FREQ(MHz)	POWER (P1)		POWER	
	NO ATTENUATOR(dBm)	ATTENUATOR #1 (dBm)	ATTENUATOR #2 (dBm)	ATTENUATOR #3 (dBm)
280	-1.6 ±3.5	(P1-4.5) ±1.0	(P1-9.5) ±3.0	(P1-9.5) ±3.0
340	-1.6 ±3.5	(P1-4.5) ±1.0	(P1-9.5) ±3.0	(P1-9.5) ±3.0
390	-1.6 ±3.5	(P1-4.5) ±1.0	(P1-9.5) ±3.0	(P1-9.5) ±3.0
  - TRAINING SENSITIVITY SHALL BE PERFORMED DURING NODAL TEST:

POWER(dBm)	FREQUENCY(MHz)	VCO FREQUENCY(MHz)
-56.0 ±5.0	283.2	280
-58.0 ±6.0	393.2	390
  - REFERENCE TEST DEVELOPMENT SPEC # TD-ELE-002.
  - PSI AGC OFFSET: THE AGC OFFSET 0xA55A IS PROGRAMMED IN HOMELINK NVM ADDRESS 0XF4.
  - PART MUST BE 100% ELECTRICALLY TESTED.
  - APPLY SCREWS (ITEM 3 AND 4) WITH TORQUE OF 2.5 (± 1.0) IN LB, FROM THE SECONDARY SIDE OF PCB TO SECURE CONNECTOR (J3).

REV	BY	DATE	DESCRIPTION	P.C.N.	BY	DATE	BY
A			SEE DRAWING VJ4005 FOR HISTORY RELEASE FOR PRODUCTION	59929	HP	30MAR01	EDICT
B1			REV CDRR CHART	61064	KVL	27JUN01	EDICT
B2			VJ0803 PLACED WAS V53925 NP (C32, C33)				
B3			VE3111 WAS V97532 (R32, R200)				
B4			VL8329 WAS VJ4186 (U1)				
B5			V98741 PLACED WAS NP (C69)				
B6			V98739 PLACED WAS NP (C71)				
B7			V53925 NP WAS PLACED (C81, C92)				
B8			V53925 PLACED WAS NP (C82)				
B9			V98739 NP WAS PLACED (C83, C95)				
B10			VJ2418 NP WAS PLACED (C136)				
B11			VJ2417 NP WAS PLACED (C137)				
B12			VF5069 NP WAS PLACED (C172, C191, C192, C193)				
B13			VJ7763 NP WAS PLACED (C173)				
B14			VF5913 PLACED WAS NP (CR44)				
B15			VJ0802 NP WAS PLACED (E102, E103)				
B16			VJ7762 NP WAS PLACED (L128, L129)				
B17			VB4277 NP WAS PLACED (Q20, Q26)				
B18			VA2501 NP WAS PLACED (Q21, Q23)				
B19			V61409 NP WAS PLACED (R10)				
B20			VE3111 NP WAS PLACED (R53, R65)				
B21			V98737 NP WAS PLACED (R62, R67)				
B22			VJ0061 NP WAS PLACED (R172)				
B23			VF2613 PLACED WAS NP (S1)				
B24			VF2613 NP WAS PLACED (S4)				
B25			VG1502 NP WAS PLACED (U108)				
B26			VJ5456 NP WAS PLACED (Y103)				
			NEW STENCIL REQUIRED				

REV	BY	DATE	DESCRIPTION	P.C.N.	BY	DATE	BY
C1			REV CDRR CHART	61311	JR	10OCT01	EDICT
C2			DEL V98741 PLACED (C69)				
C3			DEL V98739 PLACED (C71)				
C4			VG1106 WAS V68100 (C9, C40)				
C5			V68469 WAS V64452 (C132)				
C6			VG1110 WAS V56967 (CR12)				
C7			VJ0059 WAS VK2765 (L130, L131)				
C8			VA5609 WAS V98736 (R1, R7, R63, R81, R83, R98, R105, R128, R130, R137, R145)				
C9			V55507 WAS V27858 (R85)				
C10			VM3601 WAS VL8329 (U1)				
C11			V53925 NP WAS PLACED (C82)				
C12			VH5052 NP WAS PLACED (CR24, CR26, CR27, CR37)				
C13			VH5052 PLACED WAS NP (CR25, CR54)				
C14			VF5913 NP WAS PLACED (CR44)				
C15			V97532 NP WAS PLACED (R14, R26)				
C16			V97532 PLACED WAS NP (R66)				
C17			VA5607 NP WAS PLACED (R96)				
C18			VA5609 NP WAS PLACED (R98)				
C19			ADD VL6631 PLACED (2, 3)				
C20			ADD VA1511 PLACED (R15)				
C21			ADD VM0790 PLACED (R16)				
C22			REVISE NOTE 7, 10 AND 11				
C23			ADD NOTE 18				
			NO STENCIL CHANGE REQUIRED				

CORRELATION CHART			
DRAWING REV	DATE	PCB PART NO. (DWS. NO. VJ4005)	PRODUCT CODE
A	30MAR01	A	A
B	27JUN01	B	B
C	10OCT01	C	C

ELECTRONICS STD. UNLESS OTHERWISE SPECIFIED

THIRD ANGLE PROJECTION

DO NOT SCALE

X.X DECIMAL ± 0.040 [± 1.0]  
X.XX DECIMAL ± 0.020 [± .50]  
X.XXX DECIMAL ± 0.006 [± .15]

ANGULAR ± 2° 0'  
DRAFT ANGLES NOTED

SPECIFIES FEATURES REQUIRING SPECIAL CONSIDERATIONS. SEE PRODUCT CONTROL PLAN.

DIMENSIONS ARE INCHES WITH EQUIVALENT MILLIMETERS SHOWN IN BRACKETS.

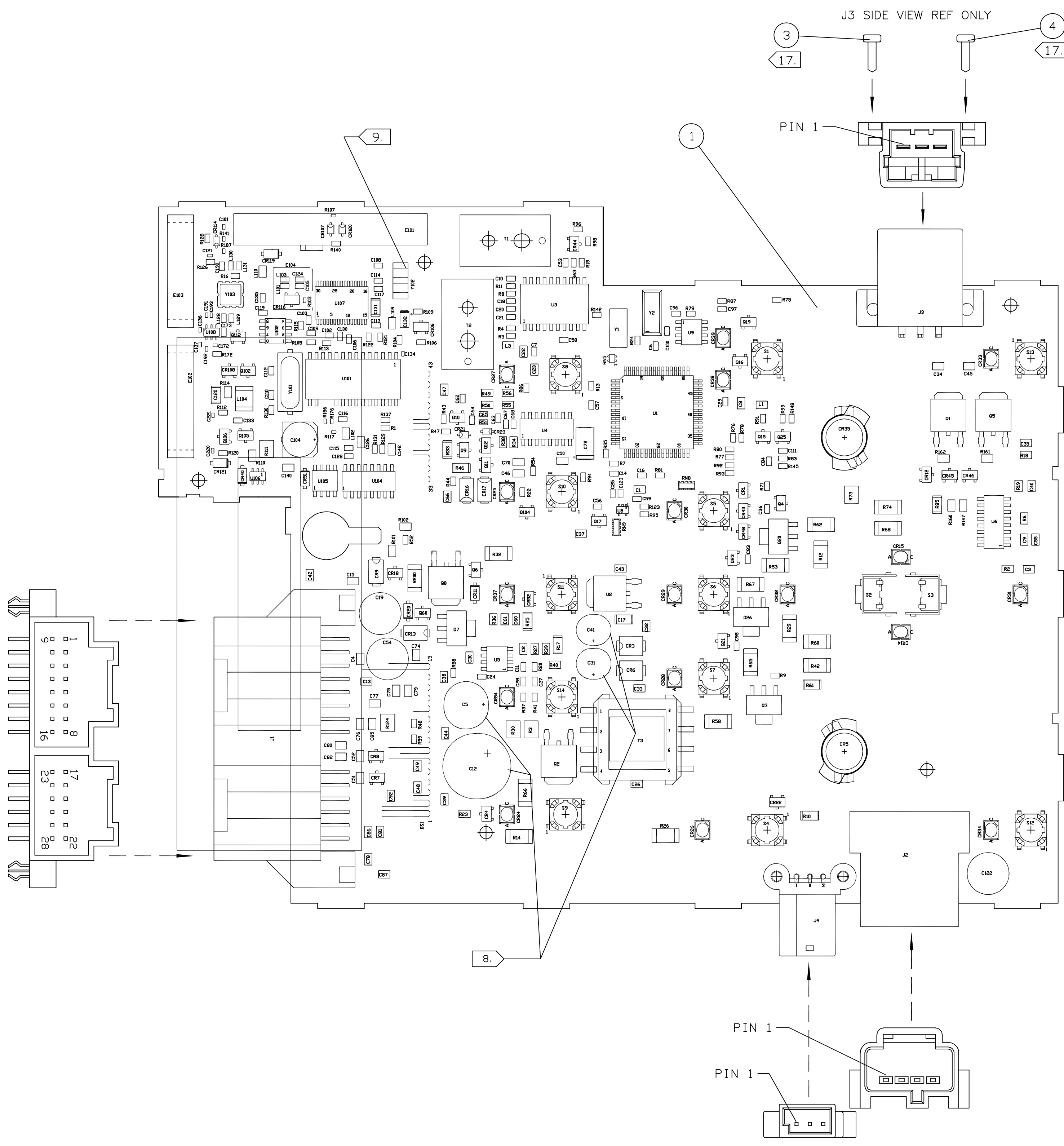
**JOHNSON CONTROLS**  
AUTOMOTIVE SYSTEMS GROUP INTERIORS

PCB ASSEMBLY

PROJECT	02.5 FN145 OHC	DRWN. BY	H. PHAM	DATE	19APR01
CUSTOMER	FORD	CHKD. BY	B. SUNNERVILLE	DATE	19APR01
SIZE	D	ENGR. APPR	P. TARNOW	DATE	19APR01
TOOL NO. REF.		SCALE	2/1	SHEET	1 OF 5
DATA FORMAT	ACAD	DWG. NO.	VL1000	REV.	C







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 LABEL DEFINITION  
 PRODUCT CODE \_\_\_\_\_ REV. LEVEL \_\_\_\_\_  
 ALPHANUMERIC SERIAL # \_\_\_\_\_
- MAXIMUM ALLOWABLE PROTRUSION OF LEADS FROM BOTTOM SIDE OF BOARD IS .100 [2.54], EXCEPT DS1.
- MAXIMUM ALLOWABLE GAP BETWEEN MATING SURFACES OF PC BOARD AND J1, J2, J3, AND J4 IS TO IPC STANDARD.
- AFTER DEPANELLING ALL CARRIER TABS MAY NOT PROTRUDE BEYOND EDGE OF PCB. MAXIMUM ROUTER CUT INTO PCB SHALL BE LESS THAN .010 [.25] COPPER FOIL PATTERN MUST NOT BE TOUCHED BY ROUTER TOOLING.
- COMPASS SHALL BE ACCURATE WITHIN THE FOLLOWING LIMITS:  

HEADING	ANGLE	VALUE(HEX)
N	0 ±10	0x00 ±0x07
E	90 ±10	0x40 ±0x07
S	180 ±10	0x80 ±0x07
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 AT A 200 MILLIGAUSS FIELD STRENGTH.
- WITH THE UNIT IN COMPASS DISPLAY MODE ADJUST THE MODULE INPUT VOLTAGE TO 13.5 VDC ± .1 VDC AND THE DIMMING LEVEL TO FULL BRIGHTNESS. VERIFY THAT THE FILAMENT VOLTAGE MEASURES BETWEEN 3.50VAC +400/-250mVAC AND 10.0VDC ± 1.0VDC.
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2	335-360	0x0FEE	0x0F	0x0EEF	YES
3	360-380	0x0FEE	0x10	0x0CF1	YES
4	380-400	0x15EB	0x11	0x0DF0	YES
5	410-420	0x19E4	0x12	0x0FEE	YES
DEFAULT CODE POWER		0x3BC2	0x0A	0x3FBE	YES
VEHICLE ID		0x00FD	0x13	-	NO
MAX TX POWER		0xFEFF	0x14	-	NO

- NOTE: THE NVM VALUE INCLUDES A SELF CHECK. HIGH BYTE IS THE ACTUAL VALUE. LOW BYTE IS COMPLEMENT OF HIGH BYTE + 2.
- TRAINING SENSITIVITY:
    - THE RECEIVER THRESHOLD IS PROGRAMMED IN NVM ADDRESS 0x04. MEASURE RSSI AT NODAL WITH A -55dBm 280MHz SIGNAL. THE RECEIVER THRESHOLD IS 10ED.
    - THE RECEIVER THRESHOLD IS PUT IN THE UPPER BYTE, THE LOWER BYTE IS THE UPPER BYTE +2, THEN INVERTED.
  - LED COLOR TYPE AND LOCATION - VISION SYSTEM.
  - OUTPUT POWER MEASUREMENTS (RF) SHALL BE PERFORMED DURING NODAL TEST:
 

FREQ(MHz)	POWER (P1) NO ATTENUATOR(dBm)	POWER ATTENUATOR #1 (dBm)	POWER ATTENUATOR #2 (dBm)
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340	-1.6 ±3.5	(P1-4.5) ±1.0	(P1-9.5) ±3.0
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  - TRAINING SENSITIVITY SHALL BE PERFORMED DURING NODAL TEST:
 

POWER(dBm)	FREQUENCY(MHz)	VCO FREQUENCY(MHz)
-56.0 ±5.0	283.2	280
-58.0 ±6.0	393.2	390
  - REFERENCE TEST DEVELOPMENT SPEC # TD-ELE-002.
  - PART MUST BE 100% ELECTRICALLY TESTED.
  - APPLY SCREWS (ITEM 3 AND 4) WITH TORQUE OF 2.5 (± 1.0) IN LB, FROM THE SECONDARY SIDE OF PCB TO SECURE CONNECTOR (J3).

**CORRELATION CHART**

DRAWING REV	DATE	REV	REV
A	30MAR01	A	A
B	27JUN01	B	B
C	10OCT01	C	C

PCB PART NO. (DWG. NO. VJ4004)  
 PCB PART NO. (DWG. NO. VL1001)  
 PRODUCT CODE: KF

REV. NO. VL1001

REV	SW/ZONE	DESCRIPTION	P.C.N.	BY	DATE	BY
A		SEE DRAWING VJ4005 FOR HISTORY RELEASE FOR PRODUCTION	59929	HP	30MAR01	EDICT
B1		REV CORR CHART	61064	KVL	27JUN01	EDICT
B2		VJ0803 PLACED WAS V53925 NP (C32, C33)				
B3		VE3111 WAS V97532 (R32, R200)				
B4		VL8329 WAS VJ4186 (U1)				
B5		V98739 PLACED WAS NP (C71, C128)				
B6		V53925 NP WAS PLACED (C81, C92)				
B7		V53925 PLACED WAS NP (C82)				
B8		V98739 NP WAS PLACED (C83, C95)				
B9		VG1106 PLACED WAS NP (C142)				
B10		VF5913 PLACED WAS NP (CR44)				
B11		VB4277 NP WAS PLACED (Q20, Q26)				
B12		VA2501 NP WAS PLACED (Q21, Q23)				
B13		V98736 NP WAS PLACED (R1)				
B14		VE3111 NP WAS PLACED (R53, R65)				
B15		V98737 NP WAS PLACED (R62, R67)				
B16		VB4271 PLACED WAS NP (R129)				
B17		VB4268 PLACED WAS NP (R131)				
B18		V98736 PLACED WAS NP (R137)				
B19		VE8699 PLACED WAS NP (U104) NEW STENCIL REQUIRED				
C1		REV CORR CHART	61311	JR	10OCT01	EDICT
C2		DEL V98741 PLACED (C69)				
C3		DEL V98739 PLACED (C71)				
C4		VG1106 WAS V68100 (C9, C40)				
C5		V68459 WAS V46452 (C132)				
C6		VG1110 WAS V56967 (CR12)				
C7		VJ0059 WAS VK2765 (L130, L131)				
C8		V45609 WAS V98736 (R1, R7, R63, R81, R83, R98, R105, R128, R130, R137, R145)				
C9		V55507 WAS V27868 (R85)				
C10		VM3601 WAS VL8329 (U1)				
C11		V53925 NP WAS PLACED (C82)				
C12		VF5913 NP WAS PLACED (CR44)				
C13		V45607 NP WAS PLACED (R96)				
C14		V45609 NP WAS PLACED (R98)				
C15		ADD VL6631 PLACED (2, 3)				
C16		ADD VA1511 PLACED (R15)				
C17		ADD VM0790 PLACED (R16)				
C18		REV NOTE 7, 11 AND 10				
C19		ADD NOTE 17				
		ND STENCIL CHANGE REQUIRED				

**JOHNSON CONTROLS**  
 AUTOMOTIVE SYSTEMS GROUP INTERIORS

**PCB ASSEMBLY**

ELECTRONICS STD. UNLESS OTHERWISE SPECIFIED		PROJECT	DRWN. BY	DATE
THIRD ANGLE PROJECTION		02.5 FN145 OHC	H. PHAM	19APR01
DO NOT SCALE		CUSTOMER	CHKD. BY	DATE
X.X DECIMAL ± 0.040 [± 1.0] ANGULAR ± 2° 0'		FORD	B. SUNNERVILLE	19APR01
X.XX DECIMAL ± 0.020 [± .50] DRAFT ANGLES NOTED		SIZE	ENGR. APPR	DATE
X.XXX DECIMAL ± 0.006 [± .15]		D	P. TARNOW	19APR01
SPECIFIES FEATURES REQUIRING SPECIAL CONSIDERATIONS. SEE PRODUCT CONTROL PLAN.		TOOL NO. REF.	SCALE	SHEET 1 OF 5
DIMENSIONS ARE INCHES WITH EQUIVALENT MILLIMETERS SHOWN IN BRACKETS.		DATA FORMAT	2/1	
		DWG. NO.	REV.	
		VL1001	C	

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