

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 106T – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/08

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6435	4.23	6.60	24.00	-1.00
Mid	6475	4.23	6.60	24.00	-1.00
High	6515	4.23	6.60	24.00	-1.00

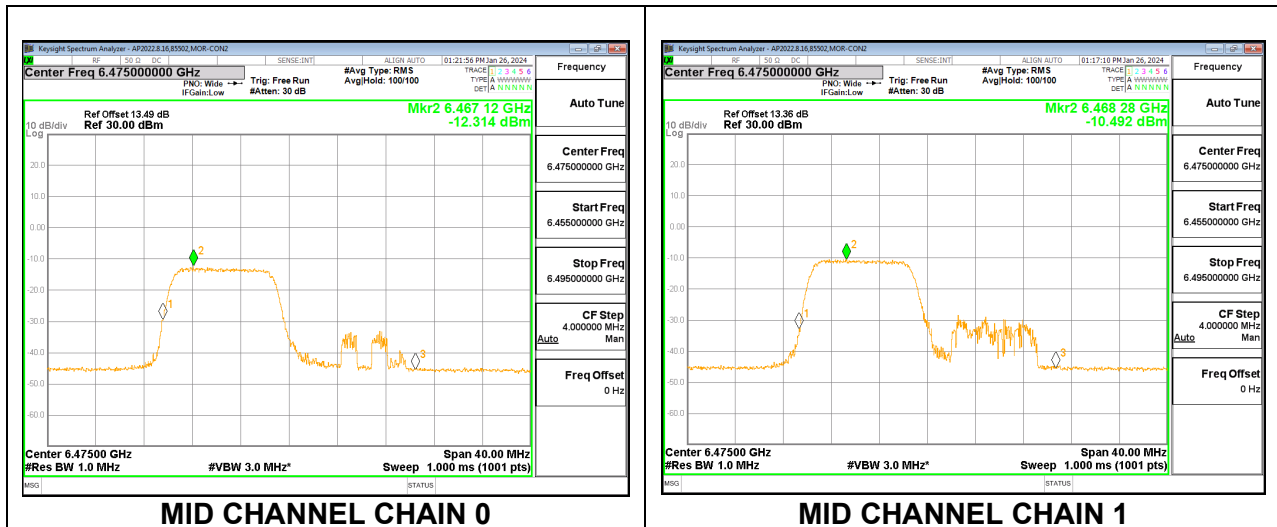
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6435	-3.00	-3.32	4.08	24.00	-19.92
Mid	6475	-4.45	-3.41	3.34	24.00	-20.66
High	6515	-4.04	-3.65	3.40	24.00	-20.60

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6435	-12.68	-10.53	-1.86	-1.00	-0.86
Mid	6475	-12.31	-10.49	-1.70	-1.00	-0.70
High	6515	-12.33	-10.78	-1.87	-1.00	-0.87



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 106T+26T – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/08-2024/01/09

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6435	4.23	6.60	24.00	-1.00
Mid	6475	4.23	6.60	24.00	-1.00
High	6515	4.23	6.60	24.00	-1.00

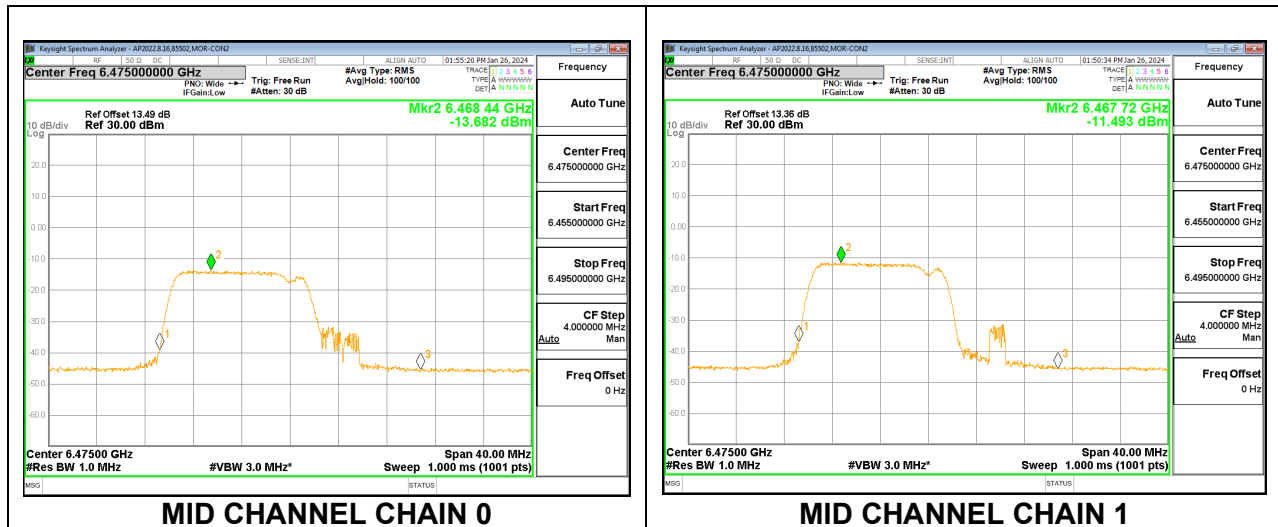
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6435	-3.83	-3.34	3.66	24.00	-20.34
Mid	6475	-4.06	-3.43	3.51	24.00	-20.49
High	6515	-4.03	-3.72	3.37	24.00	-20.63

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6435	-13.41	-11.81	-2.93	-1.00	-1.93
Mid	6475	-13.68	-11.49	-2.84	-1.00	-1.84
High	6515	-13.31	-11.85	-2.90	-1.00	-1.90



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 242T – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/09

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6435	4.23	6.60	24.00	-1.00
Mid	6475	4.23	6.60	24.00	-1.00
High	6515	4.23	6.60	24.00	-1.00

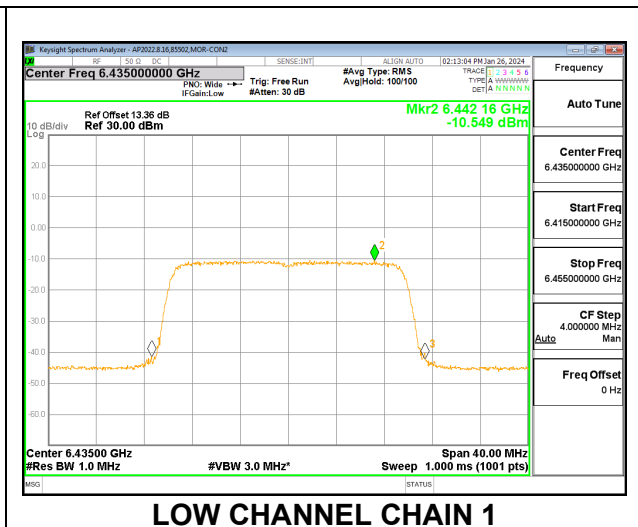
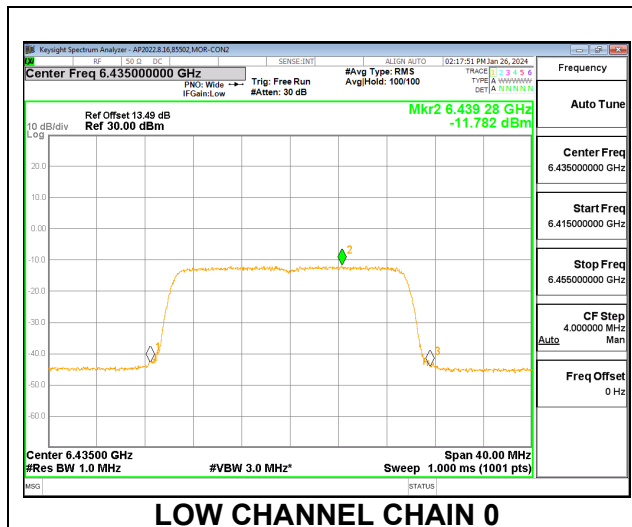
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6435	0.34	0.29	7.56	24.00	-16.44
Mid	6475	-0.32	0.47	7.33	24.00	-16.67
High	6515	-0.40	0.30	7.20	24.00	-16.80

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6435	-11.78	-10.55	-1.51	-1.00	-0.51
Mid	6475	-12.20	-10.48	-1.64	-1.00	-0.64
High	6515	-12.39	-10.93	-1.99	-1.00	-0.99



10.2.9. 802.11be EHT40 MODE 2TX IN THE UNII-6 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/11, 2024/02/16

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6445	4.23	6.60	24.00	-1.00
Mid	6485	4.23	6.60	24.00	-1.00
High	6525	5.50	7.72	24.00	-1.00

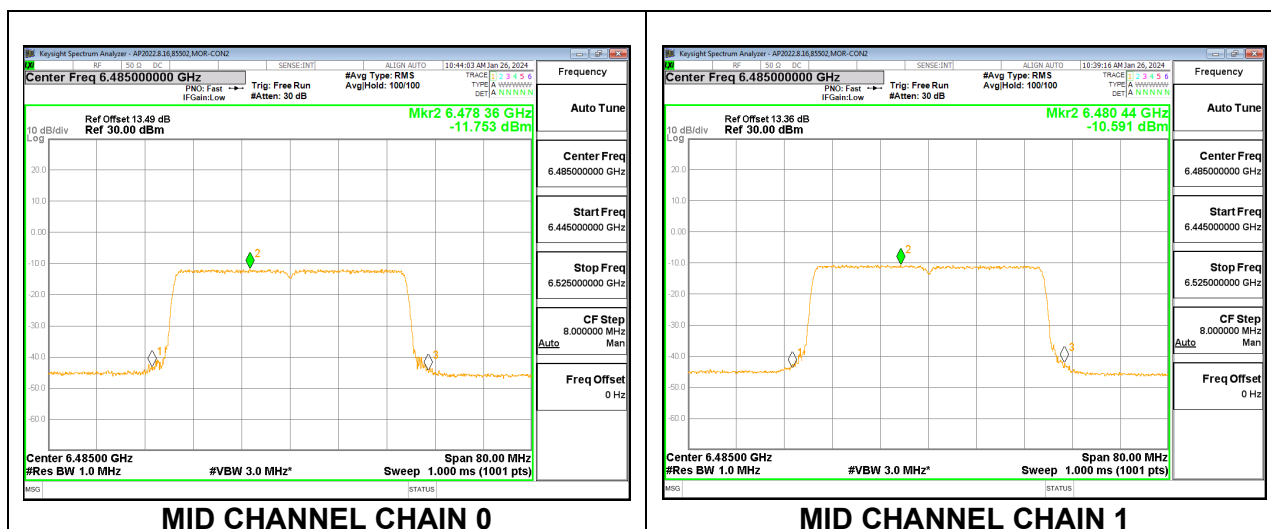
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6445	3.93	4.40	11.41	24.00	-12.59
Mid	6485	4.03	4.42	11.47	24.00	-12.53
High	6525	0.54	1.06	9.32	24.00	-14.68

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6445	-12.05	-10.46	-1.57	-1.00	-0.57
Mid	6485	-11.75	-10.59	-1.52	-1.00	-0.52
High	6525	-14.56	-12.67	-2.78	-1.00	-1.78



10.2.10. 802.11be EHT80 MODE 2TX IN THE UNII-6 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+242T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/11, 2024/02/16

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6465	4.23	6.60	24.00	-1.00
High	6545	5.50	7.72	24.00	-1.00

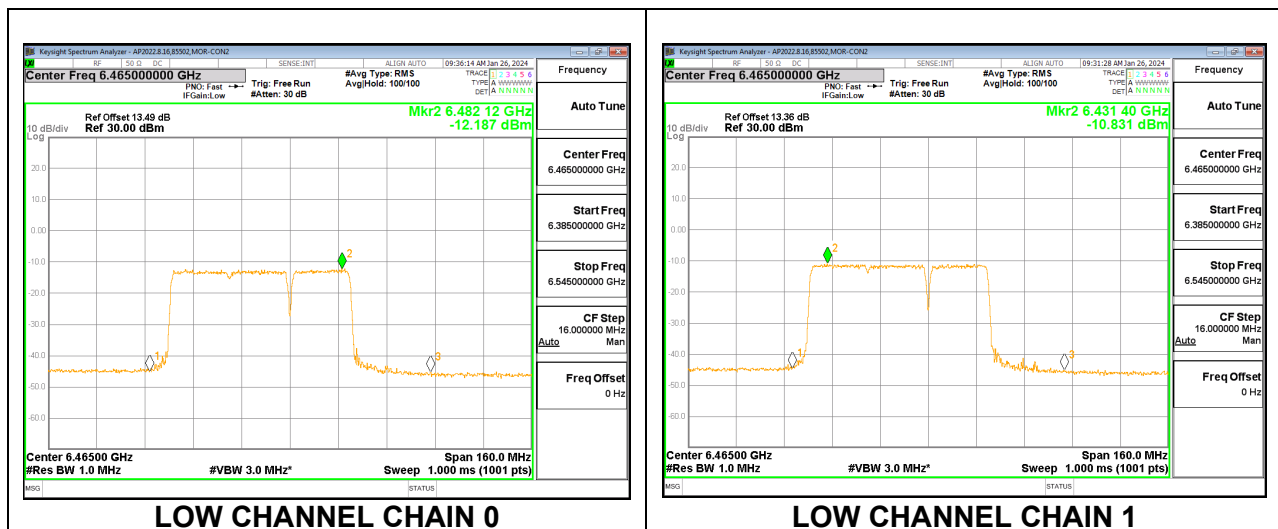
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6465	4.59	4.80	11.94	24.00	-12.06
High	6545	1.56	2.58	10.61	24.00	-13.39

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6465	-12.19	-10.83	-1.85	-1.00	-0.85
High	6545	-15.22	-12.71	-3.05	-1.00	-2.05



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+242T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6465	4.23	6.60	24.00	-1.00
High	6545	5.50	7.72	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6465	4.84	4.99	12.16	24.00	-11.84
High	6545	4.77	5.17	13.48	24.00	-10.52

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 242T+484T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6465	4.23	6.60	24.00	-1.00
High	6545	5.50	7.72	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6465	4.52	4.74	11.87	24.00	-12.13
High	6545	4.77	5.01	13.40	24.00	-10.60

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/11, 2024/02/16

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6465	4.23	6.60	24.00	-1.00
High	6545	5.50	7.72	24.00	-1.00

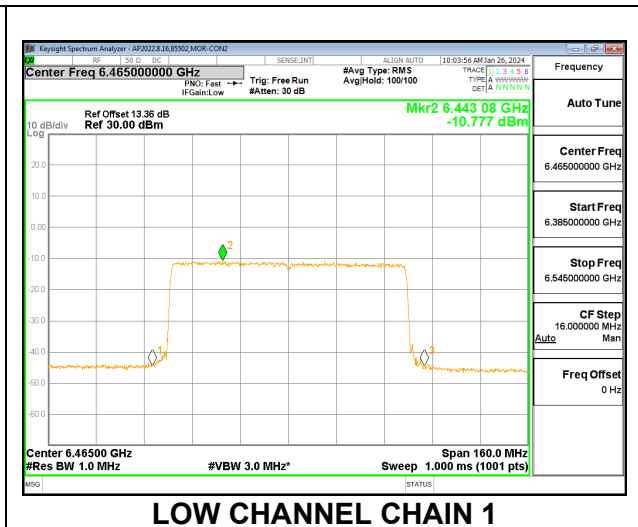
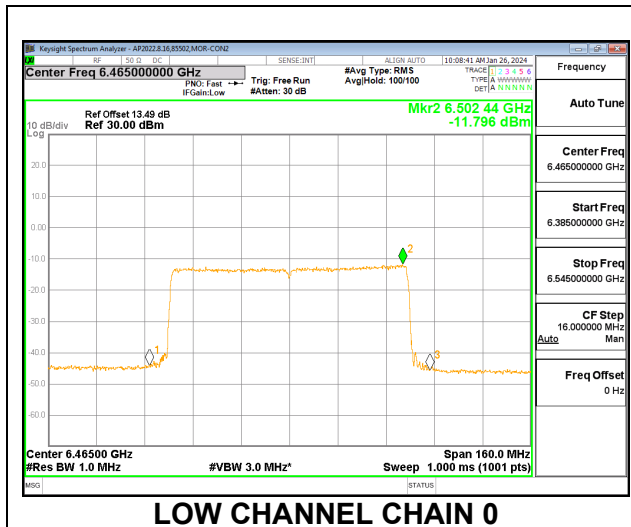
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6465	6.40	6.84	13.87	24.00	-10.13
High	6545	3.38	3.64	12.02	24.00	-11.98

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6465	-11.80	-10.78	-1.65	-1.00	-0.65
High	6545	-14.55	-13.19	-3.09	-1.00	-2.09



10.2.11. 802.11be EHT160 MODE 2TX IN THE UNII-6 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502,84740
Test Date:	2024/01/11,2024/02/16

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Mid	6505	5.50	7.72	24.00	-1.00

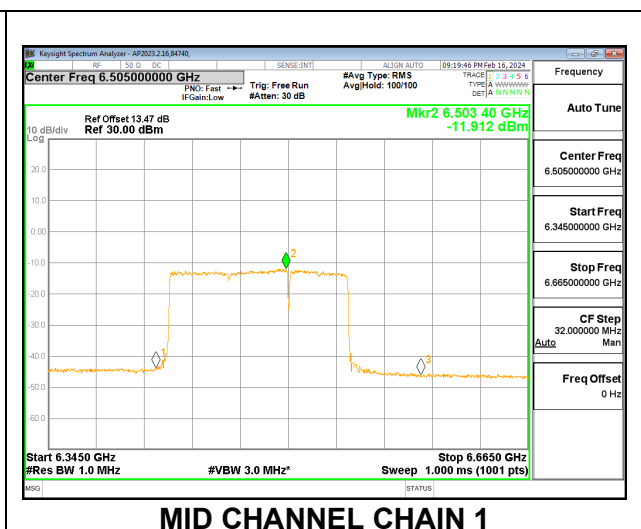
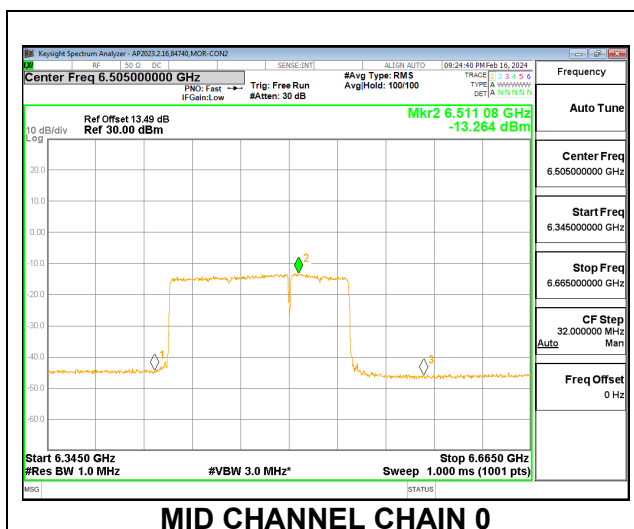
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6505	6.25	6.46	14.87	24.00	-9.13

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Mid	6505	-13.26	-11.91	-1.80	-1.00	-0.80



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Mid	6505	5.50	7.72	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6505	6.44	6.24	14.85	24.00	-9.15

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+996T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Mid	6505	5.50	7.72	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6505	6.39	6.24	14.83	24.00	-9.17

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Mid	6505	5.50	7.72	24.00	-1.00

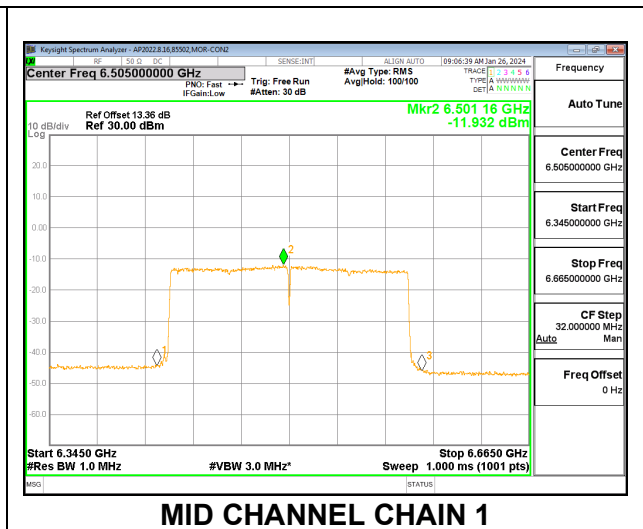
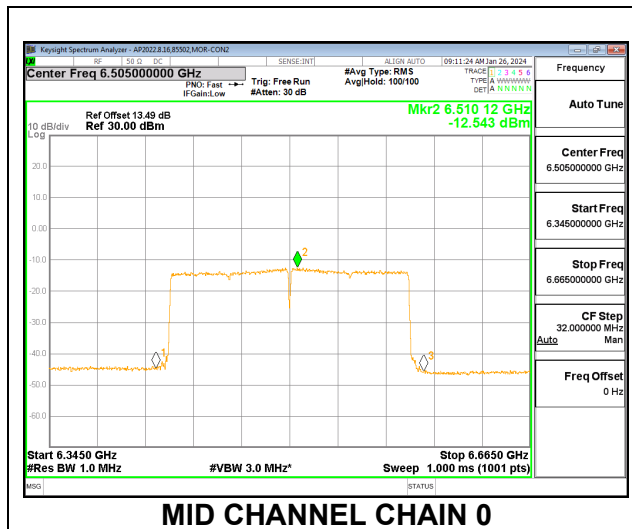
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6505	5.76	5.94	14.36	24.00	-9.64

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Mid	6505	-12.54	-11.93	-1.50	-1.00	-0.50



10.2.12. 802.11be EHT320 MODE 2TX IN THE UNII-6 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T+484T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	84740
Test Date:	2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.50	7.72	24.00	-1.00
High	6585	5.50	7.72	24.00	-1.00

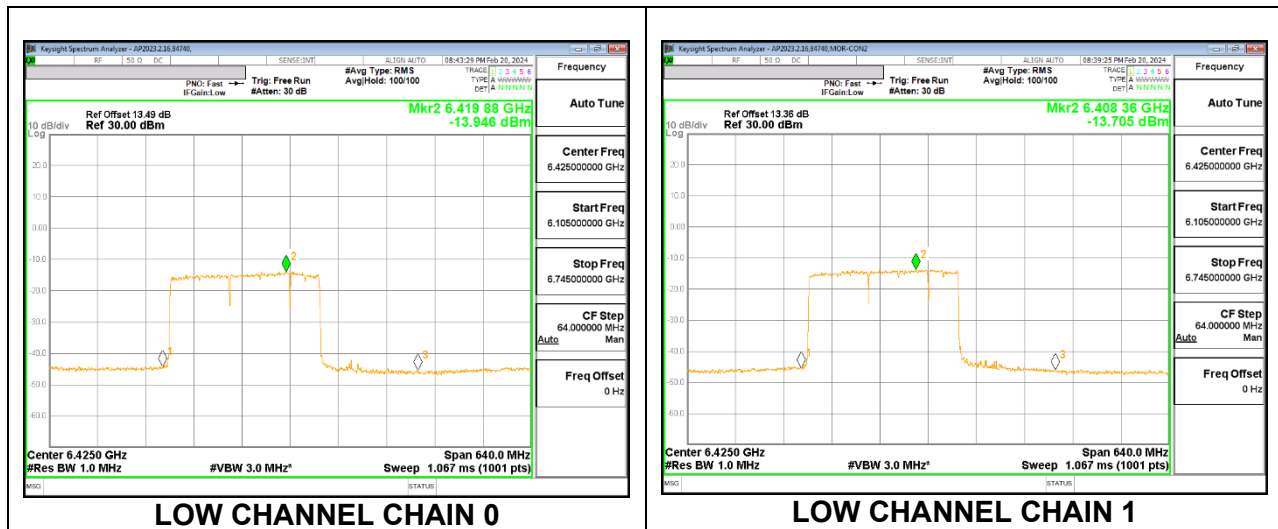
Duty Cycle CF (dB)	0.14	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	6.77	6.97	15.38	24.00	-8.62
High	6585	5.19	5.70	13.96	24.00	-10.04

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6425	-13.95	-13.71	-2.95	-1.00	-1.95
High	6585	-16.61	-15.89	-5.36	-1.00	-4.36



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T+484T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.50	7.72	24.00	-1.00
High	6585	5.50	7.72	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	6.57	6.94	15.27	24.00	-8.73
High	6585	6.08	6.74	14.93	24.00	-9.07

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T+996T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.50	7.72	24.00	-1.00
High	6585	5.50	7.72	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	6.90	6.81	15.37	24.00	-8.63
High	6585	6.75	6.93	15.35	24.00	-8.65

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T+996T (NON-CONTIGUOUS 2) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.50	7.72	24.00	-1.00
High	6585	5.50	7.72	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	6.97	6.78	15.39	24.00	-8.61
High	6585	6.80	6.99	15.41	24.00	-8.59

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 3x996T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/11, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.50	7.72	24.00	-1.00
High	6585	5.50	7.72	24.00	-1.00

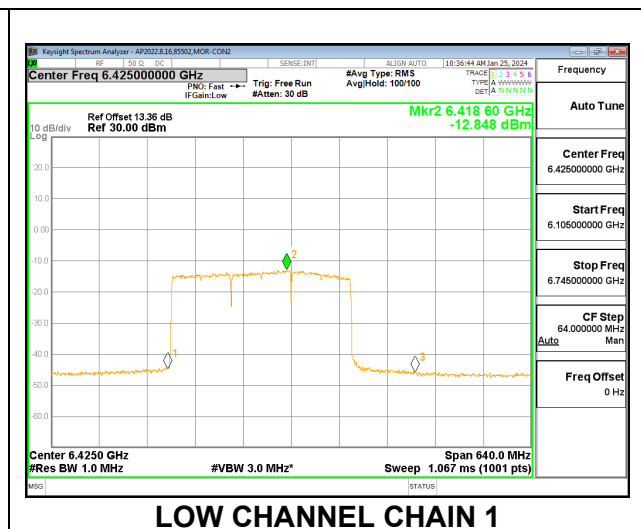
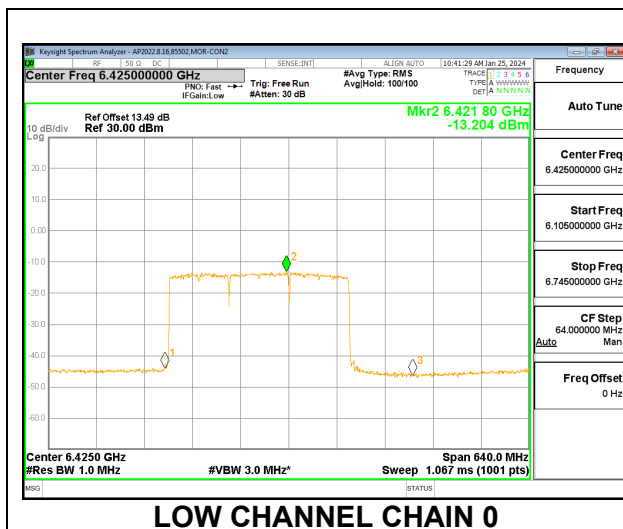
Duty Cycle CF (dB)	0.17	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	6.27	6.49	14.89	24.00	-9.11
High	6585	6.01	6.47	14.76	24.00	-9.24

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6425	-13.20	-12.85	-2.12	-1.00	-1.12
High	6585	-13.77	-12.83	-2.37	-1.00	-1.37



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 3x996T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.50	7.72	24.00	-1.00
High	6585	5.50	7.72	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	5.57	5.98	14.29	24.00	-9.71
High	6585	5.72	6.07	14.41	24.00	-9.59

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 3x996T (NON-CONTIGUOUS 2) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.50	7.72	24.00	-1.00
High	6585	5.50	7.72	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	5.81	6.17	14.50	24.00	-9.50
High	6585	5.78	6.36	14.59	24.00	-9.41

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 3x996T+484T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/11, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.50	7.72	24.00	-1.00
High	6585	5.50	7.72	24.00	-1.00

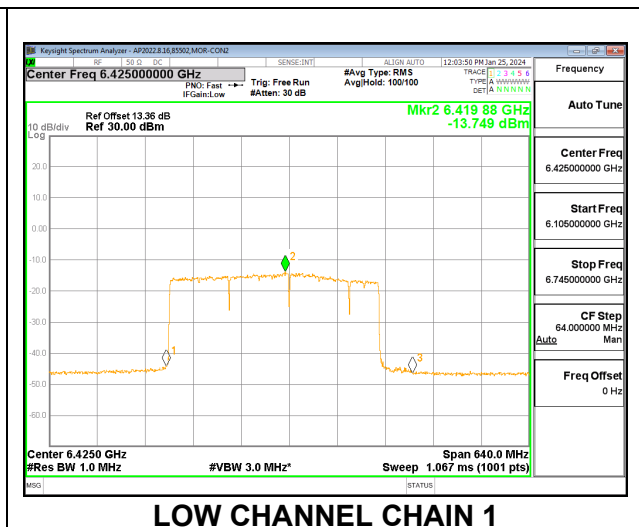
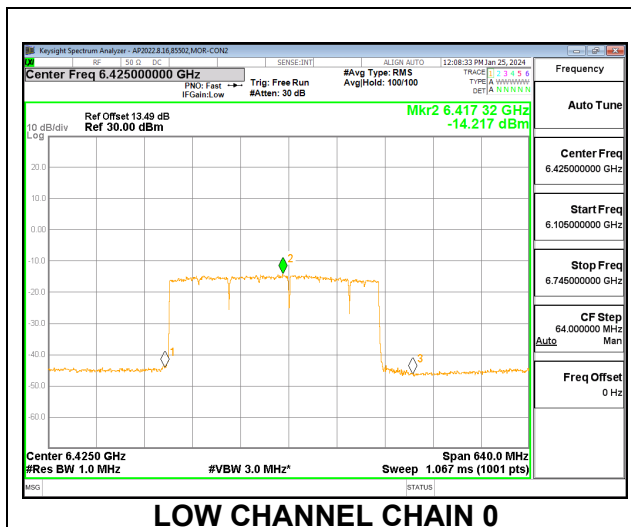
Duty Cycle CF (dB)	0.18	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	5.36	5.79	14.09	24.00	-9.91
High	6585	5.15	5.81	14.00	24.00	-10.00

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6425	-14.22	-13.75	-3.07	-1.00	-2.07
High	6585	-14.62	-14.10	-3.44	-1.00	-2.44



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 3x996T+484T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.50	7.72	24.00	-1.00
High	6585	5.50	7.72	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	5.40	5.72	14.07	24.00	-9.93
High	6585	5.22	5.88	14.07	24.00	-9.93

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T+2x996T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.50	7.72	24.00	-1.00
High	6585	5.50	7.72	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	4.97	5.41	13.71	24.00	-10.29
High	6585	4.96	5.57	13.79	24.00	-10.21

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T+2x996T (NON-CONTIGUOUS 2) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.50	7.72	24.00	-1.00
High	6585	5.50	7.72	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	5.09	5.40	13.76	24.00	-10.24
High	6585	5.03	5.57	13.82	24.00	-10.18

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T+484T+996T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.50	7.72	24.00	-1.00
High	6585	5.50	7.72	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	5.11	5.50	13.82	24.00	-10.18
High	6585	5.18	5.77	14.00	24.00	-10.00

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T+484T+996T (NON-CONTIGUOUS 2) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.50	7.72	24.00	-1.00
High	6585	5.50	7.72	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	5.08	5.55	13.83	24.00	-10.17
High	6585	5.02	5.61	13.84	24.00	-10.16

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 4x996T – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/11, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.50	7.72	24.00	-1.00
High	6585	5.50	7.72	24.00	-1.00

Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	11.73	11.41	20.08	24.00	-3.92
High	6585	8.81	9.40	17.63	24.00	-6.37

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: SU – LOW POWER INDOOR

Test Engineer:	84740
Test Date:	2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.50	7.72	24.00	-1.00
High	6585	5.50	7.72	24.00	-1.00

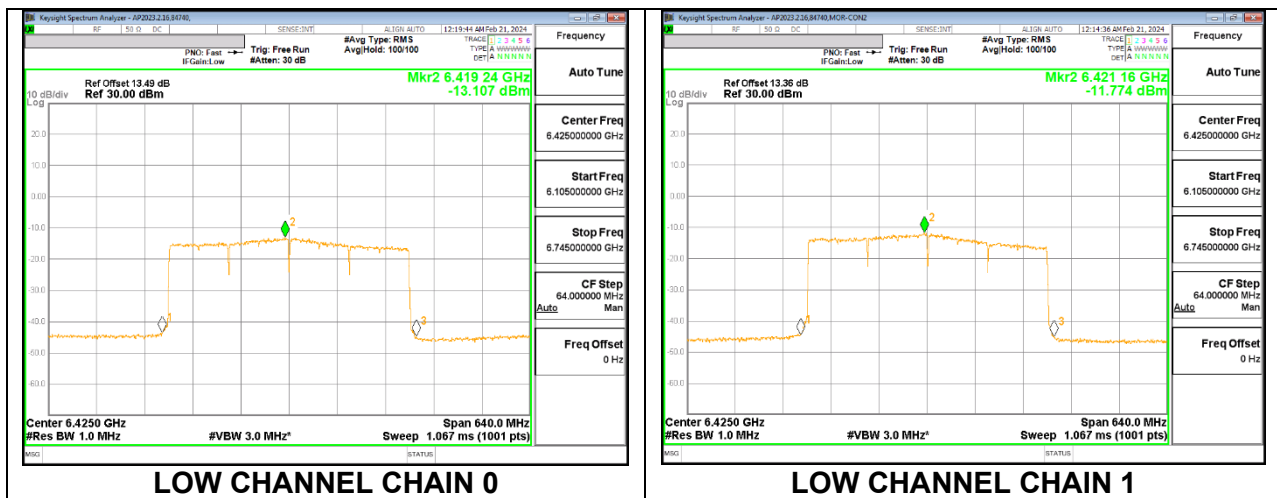
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	9.20	9.44	17.83	24.00	-6.17
High	6585	9.25	9.64	17.96	24.00	-6.04

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6425	-13.11	-11.77	-1.66	-1.00	-0.66
High	6585	-12.99	-12.12	-1.80	-1.00	-0.80



10.2.13. 802.11a MODE 2TX IN THE UNII-7 BAND

Note: These results leveraged from R14932101-E10a

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/09

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	30.00	17.00
Mid	6695	5.61	8.37	30.00	17.00
High	6855	5.61	8.37	30.00	17.00

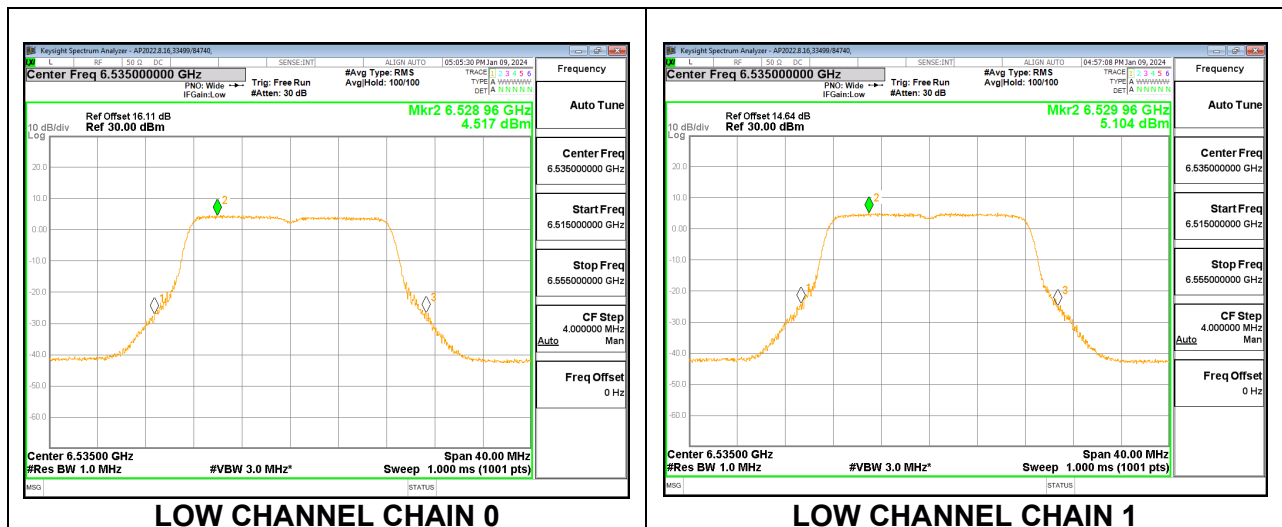
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	16.05	16.38	24.84	30.00	-5.16
Mid	6695	16.50	16.88	25.31	30.00	-4.69
High	6855	16.65	16.42	25.16	30.00	-4.84

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	4.52	5.10	16.20	17.00	-0.80
Mid	6695	4.67	4.60	16.02	17.00	-0.98
High	6855	4.52	4.74	16.01	17.00	-0.99



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	24.00	-1.00
Mid	6695	5.61	8.37	24.00	-1.00
High	6855	5.61	8.37	24.00	-1.00

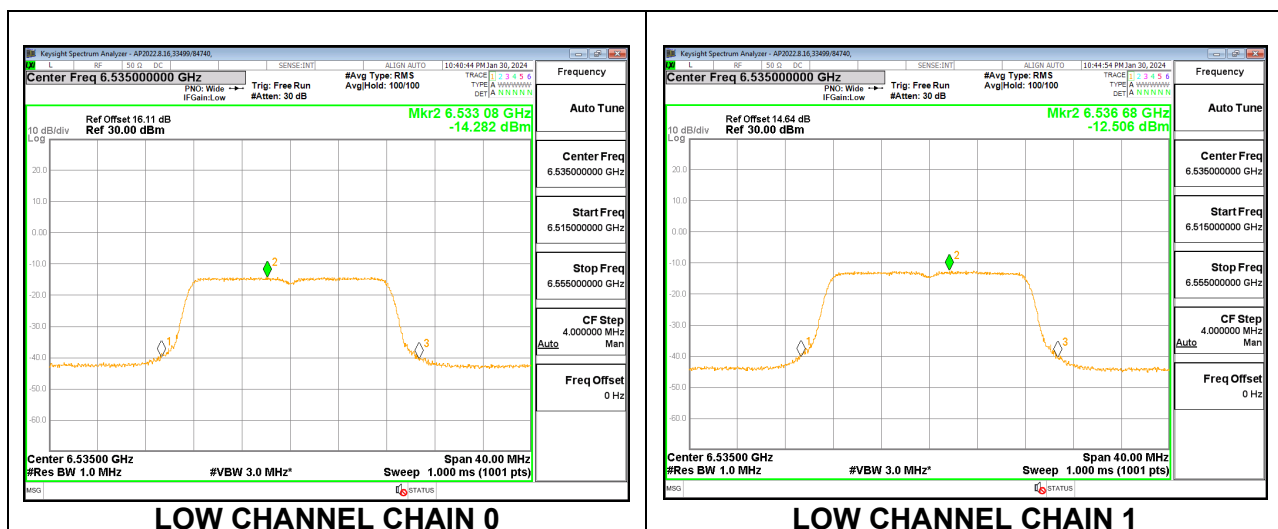
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	-3.69	-2.23	5.72	24.00	-18.28
Mid	6695	-2.24	-2.18	6.41	24.00	-17.59
High	6855	-1.66	-3.73	6.05	24.00	-17.95

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	-14.28	-12.51	-1.92	-1.00	-0.92
Mid	6695	-13.50	-13.31	-2.02	-1.00	-1.02
High	6855	-12.51	-14.46	-2.00	-1.00	-1.00



10.2.14. 802.11be EHT20 MODE 2TX IN THE UNII-7 BAND

Note: These results leveraged from R14932101-E10a

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 26T – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/09

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	30.00	17.00
Mid	6695	5.61	8.37	30.00	17.00
High	6855	5.61	8.37	30.00	17.00

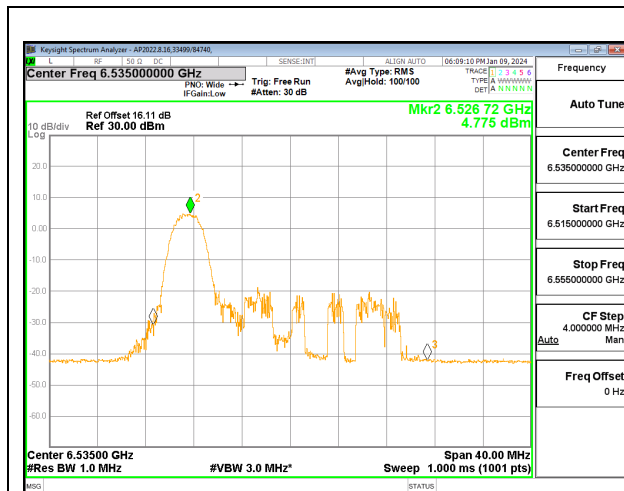
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

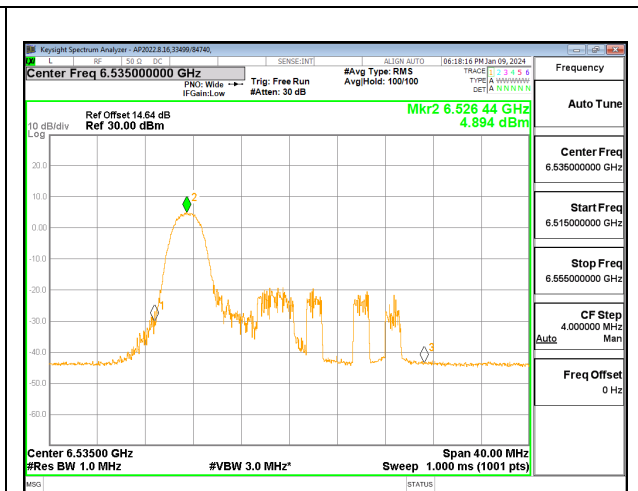
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	7.63	8.31	16.60	30.00	-13.40
Mid	6695	7.69	8.29	16.62	30.00	-13.38
High	6855	7.23	7.56	16.02	30.00	-13.98

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	4.78	4.89	16.22	17.00	-0.78
Mid	6695	4.02	4.94	15.89	17.00	-1.11
High	6855	4.69	4.71	16.08	17.00	-0.92



LOW CHANNEL CHAIN 0



LOW CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 26T – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	24.00	-1.00
Mid	6695	5.61	8.37	24.00	-1.00
High	6855	5.61	8.37	24.00	-1.00

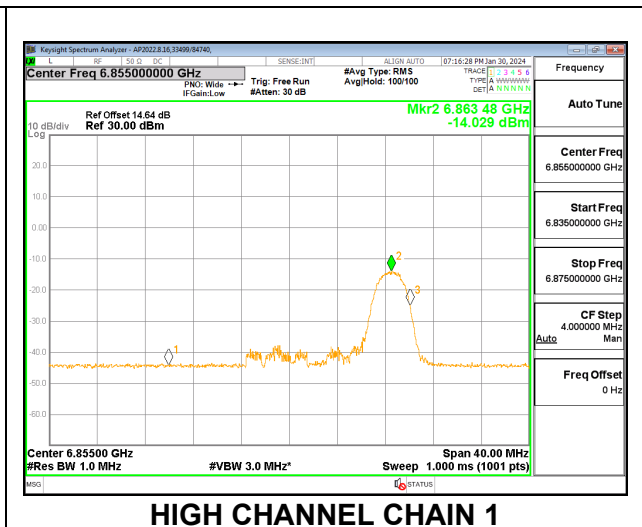
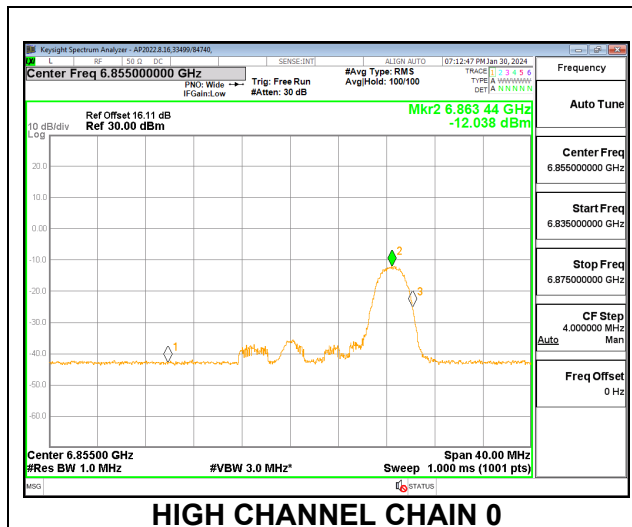
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	-11.59	-10.22	-2.23	24.00	-26.23
Mid	6695	-10.75	-10.54	-2.02	24.00	-26.02
High	6855	-9.62	-11.56	-1.86	24.00	-25.86

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	-13.77	-12.24	-1.56	-1.00	-0.56
Mid	6695	-13.73	-12.47	-1.67	-1.00	-0.67
High	6855	-12.04	-14.03	-1.54	-1.00	-0.54



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 52T – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/09

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	30.00	17.00
Mid	6695	5.61	8.37	30.00	17.00
High	6855	5.61	8.37	30.00	17.00

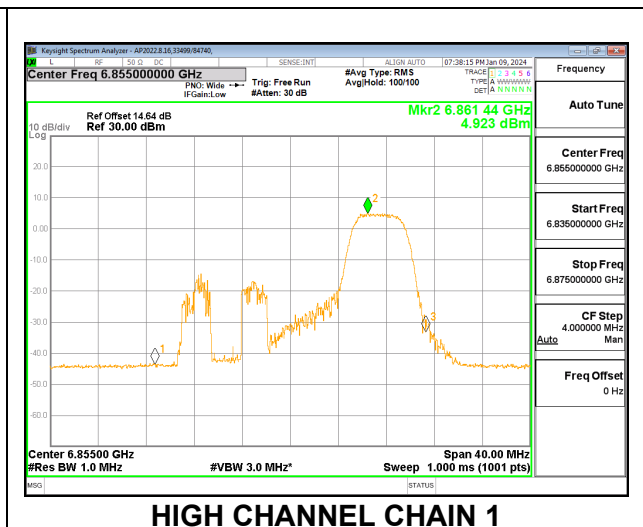
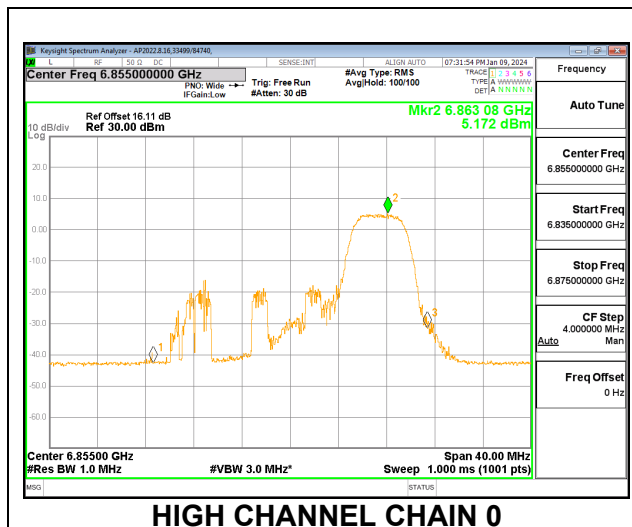
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	9.84	10.39	18.74	30.00	-11.26
Mid	6695	10.47	10.79	19.25	30.00	-10.75
High	6855	10.68	10.52	19.22	30.00	-10.78

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	4.43	4.74	15.97	17.00	-1.03
Mid	6695	4.24	5.08	16.06	17.00	-0.94
High	6855	5.17	4.92	16.43	17.00	-0.57



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 52T – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	24.00	-1.00
Mid	6695	5.61	8.37	24.00	-1.00
High	6855	5.61	8.37	24.00	-1.00

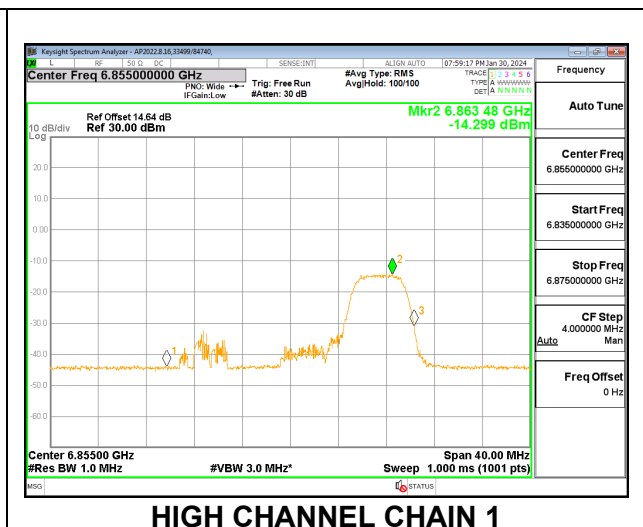
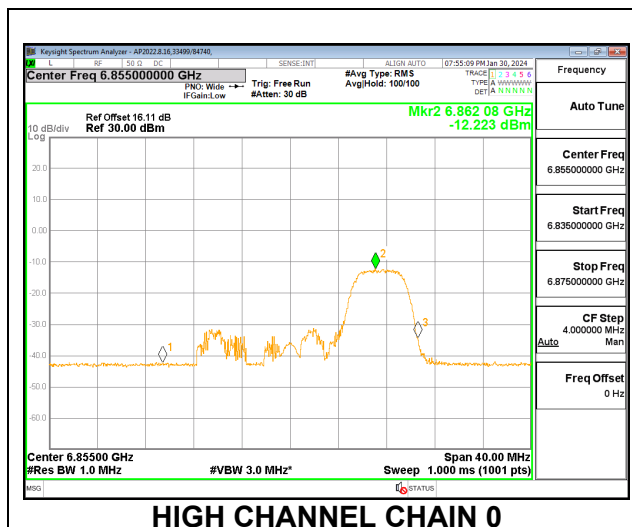
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	-9.08	-7.61	0.34	24.00	-23.66
Mid	6695	-7.74	-7.57	0.97	24.00	-23.03
High	6855	-7.20	-9.18	0.54	24.00	-23.46

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	-13.95	-12.64	-1.87	-1.00	-0.87
Mid	6695	-13.45	-13.15	-1.92	-1.00	-0.92
High	6855	-12.22	-14.30	-1.76	-1.00	-0.76



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 52T+26T – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/09

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	30.00	17.00
Mid	6695	5.61	8.37	30.00	17.00
High	6855	5.61	8.37	30.00	17.00

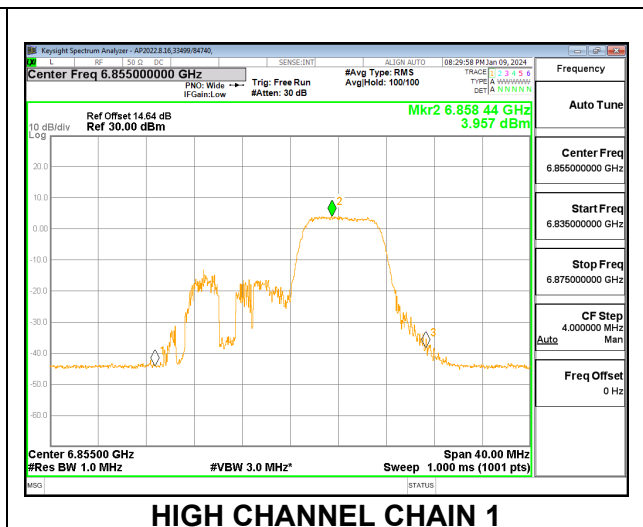
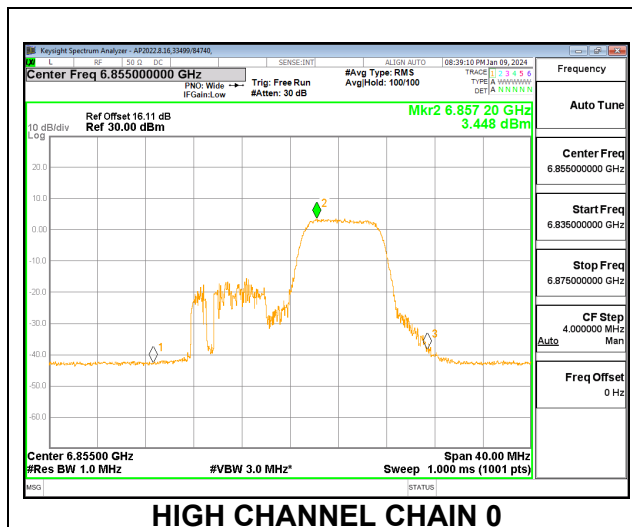
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	10.21	10.94	19.21	30.00	-10.79
Mid	6695	10.91	11.39	19.78	30.00	-10.22
High	6855	11.12	11.31	19.84	30.00	-10.16

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	2.81	3.91	14.77	17.00	-2.23
Mid	6695	3.22	3.99	15.01	17.00	-1.99
High	6855	3.45	3.96	15.09	17.00	-1.91



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 52T+26T – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	24.00	-1.00
Mid	6695	5.61	8.37	24.00	-1.00
High	6855	5.61	8.37	24.00	-1.00

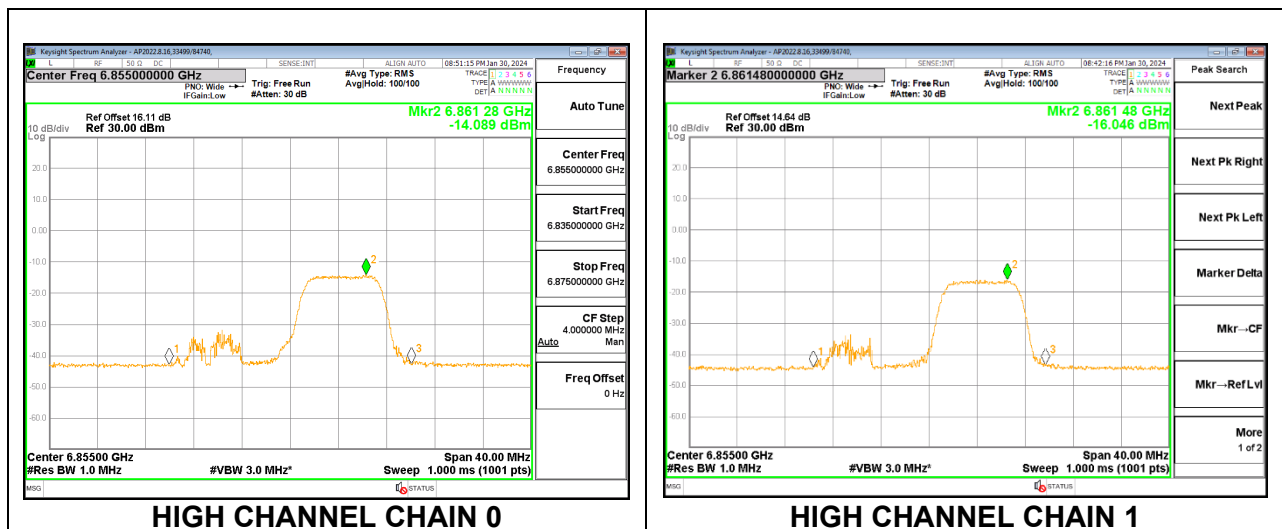
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	-9.34	-7.71	0.17	24.00	-23.83
Mid	6695	-7.84	-7.77	0.82	24.00	-23.18
High	6855	-7.38	-9.29	0.39	24.00	-23.61

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	-15.94	-14.70	-3.89	-1.00	-2.89
Mid	6695	-15.31	-14.89	-3.72	-1.00	-2.72
High	6855	-14.09	-16.05	-3.58	-1.00	-2.58



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 106T – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/09

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	30.00	17.00
Mid	6695	5.61	8.37	30.00	17.00
High	6855	5.61	8.37	30.00	17.00

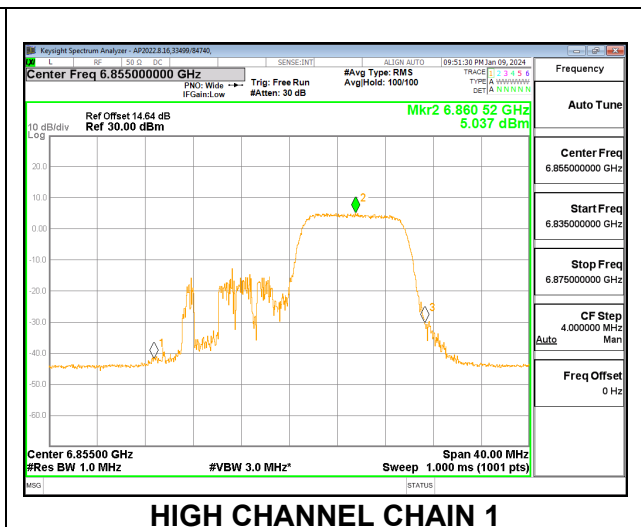
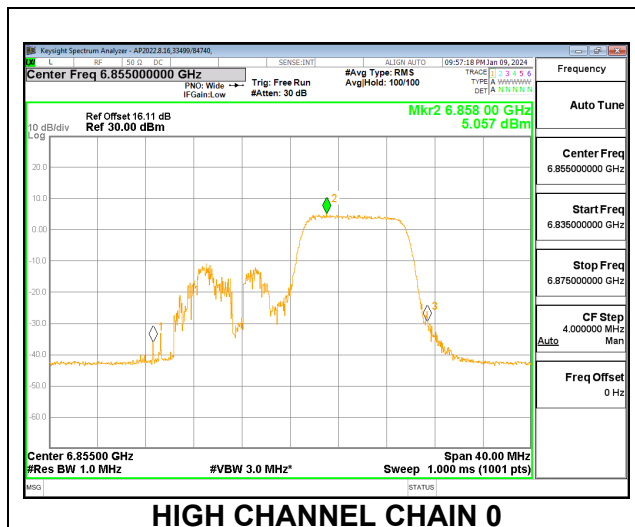
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	13.15	13.16	21.78	30.00	-8.22
Mid	6695	13.20	13.37	21.91	30.00	-8.09
High	6855	13.49	13.50	22.12	30.00	-7.88

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	4.68	5.22	16.34	17.00	-0.66
Mid	6695	4.51	4.97	16.13	17.00	-0.87
High	6855	5.06	5.04	16.43	17.00	-0.57



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 106T – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	24.00	-1.00
Mid	6695	5.61	8.37	24.00	-1.00
High	6855	5.61	8.37	24.00	-1.00

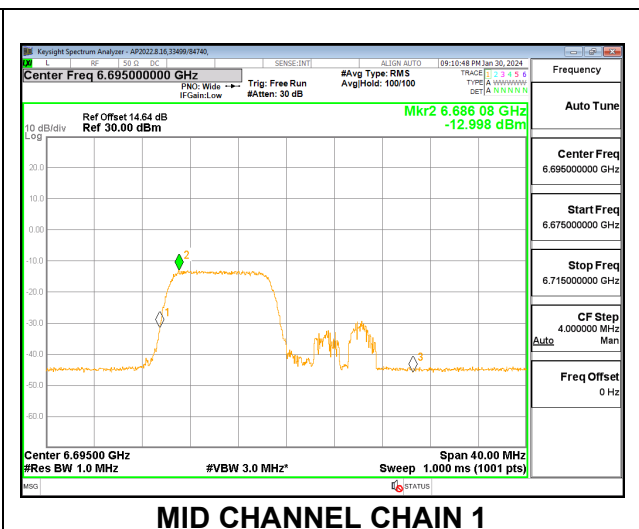
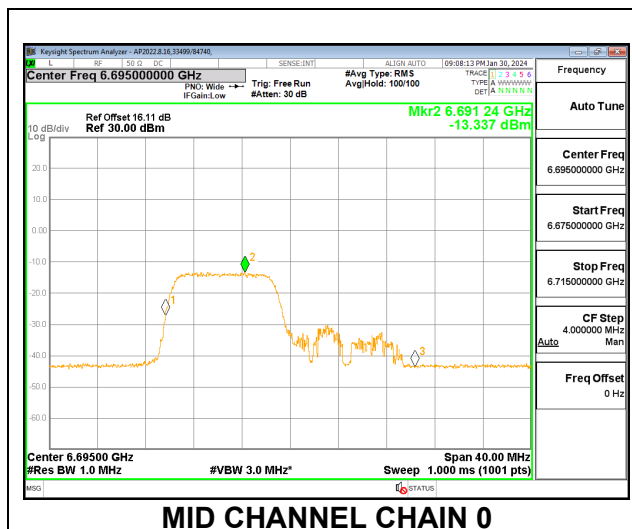
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	-6.35	-4.79	3.12	24.00	-20.88
Mid	6695	-4.87	-4.77	3.80	24.00	-20.20
High	6855	-4.44	-6.38	3.32	24.00	-20.68

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	-14.21	-12.67	-1.99	-1.00	-0.99
Mid	6695	-13.34	-13.00	-1.78	-1.00	-0.78
High	6855	-12.39	-14.45	-1.92	-1.00	-0.92



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 106T+26T – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/09

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	30.00	17.00
Mid	6695	5.61	8.37	30.00	17.00
High	6855	5.61	8.37	30.00	17.00

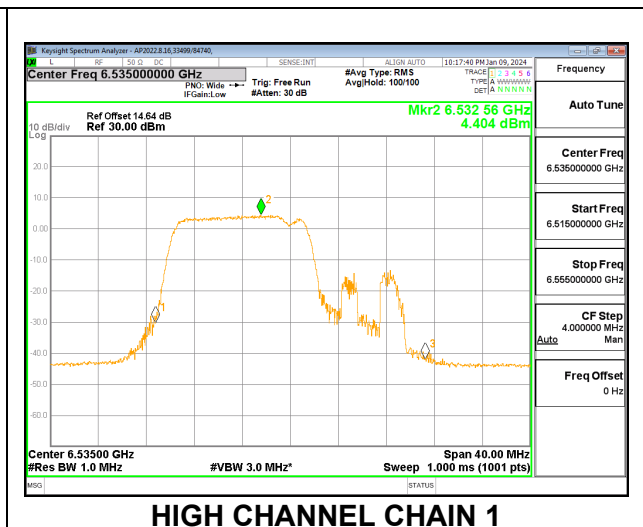
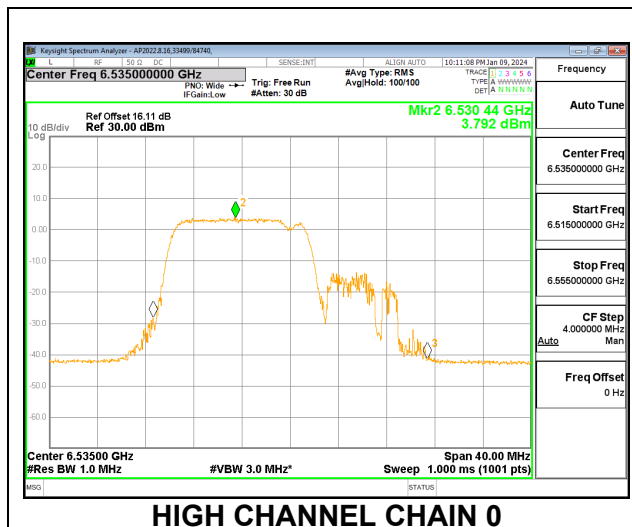
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	13.29	13.20	21.87	30.00	-8.13
Mid	6695	13.34	13.23	21.91	30.00	-8.09
High	6855	13.10	13.50	21.92	30.00	-8.08

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	3.79	4.40	15.49	17.00	-1.51
Mid	6695	3.48	4.42	15.36	17.00	-1.64
High	6855	4.48	4.25	15.74	17.00	-1.26



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 106T+26T – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	24.00	-1.00
Mid	6695	5.61	8.37	24.00	-1.00
High	6855	5.61	8.37	24.00	-1.00

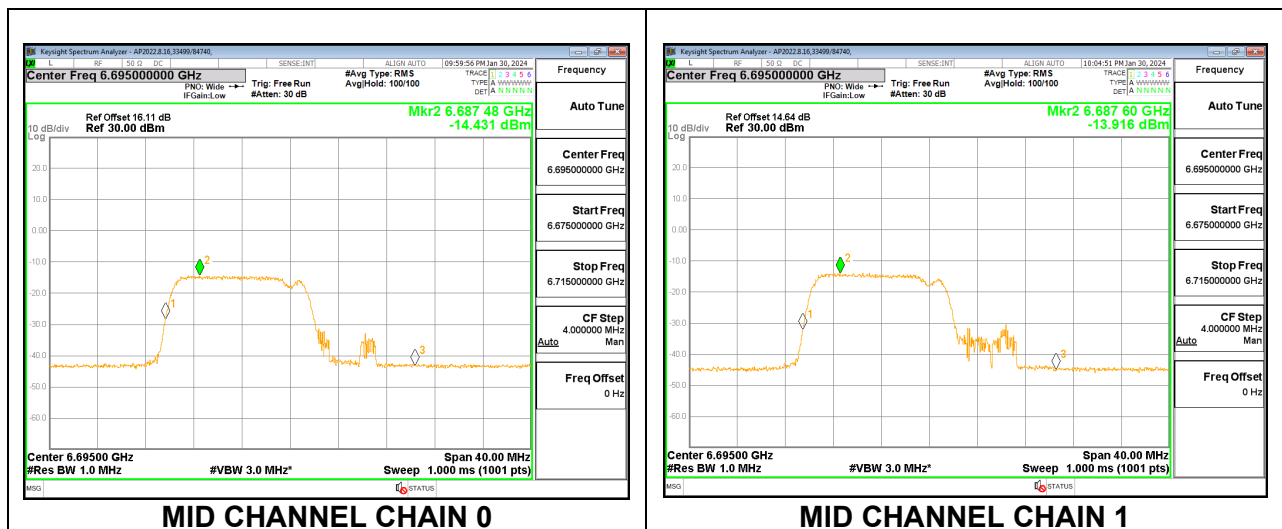
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	-6.35	-4.81	3.11	24.00	-20.89
Mid	6695	-4.89	-4.79	3.78	24.00	-20.22
High	6855	-4.47	-6.39	3.30	24.00	-20.70

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	-14.88	-13.70	-2.87	-1.00	-1.87
Mid	6695	-14.43	-13.92	-2.79	-1.00	-1.79
High	6855	-13.44	-15.24	-2.87	-1.00	-1.87



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 242T – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/09

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	30.00	17.00
Mid	6695	5.61	8.37	30.00	17.00
High	6855	5.61	8.37	30.00	17.00

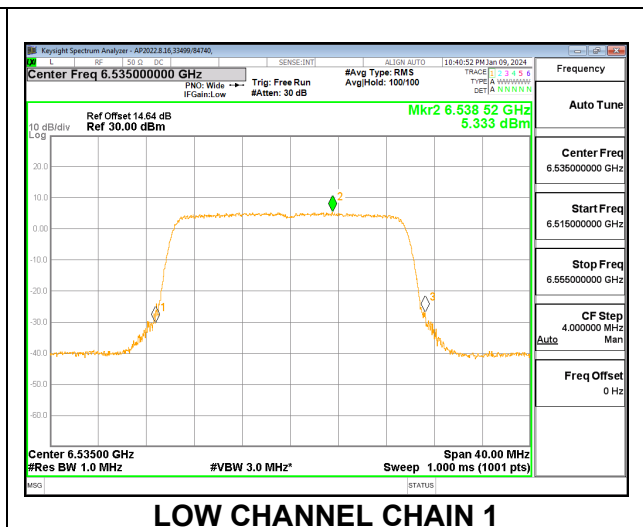
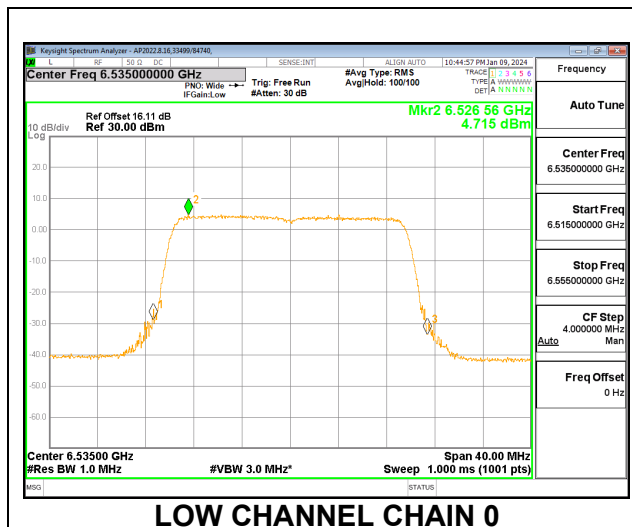
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	16.70	16.94	25.44	30.00	-4.56
Mid	6695	16.59	16.91	25.37	30.00	-4.63
High	6855	17.00	16.91	25.58	30.00	-4.42

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	4.72	5.33	16.42	17.00	-0.58
Mid	6695	4.60	5.07	16.22	17.00	-0.78
High	6855	5.12	4.90	16.39	17.00	-0.61



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 242T – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	24.00	-1.00
Mid	6695	5.61	8.37	24.00	-1.00
High	6855	5.61	8.37	24.00	-1.00

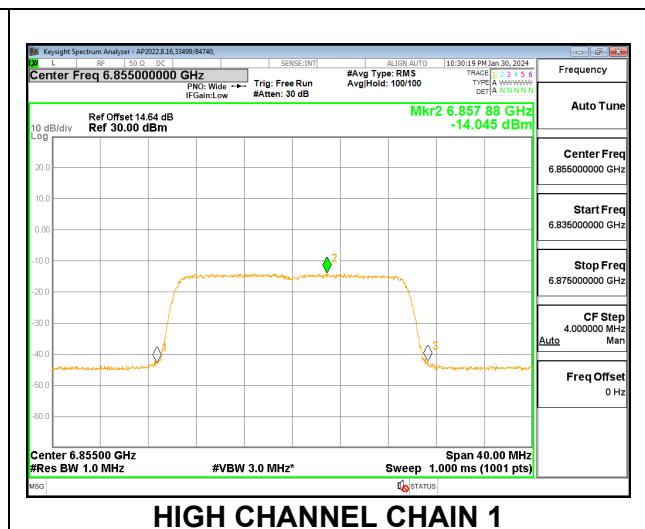
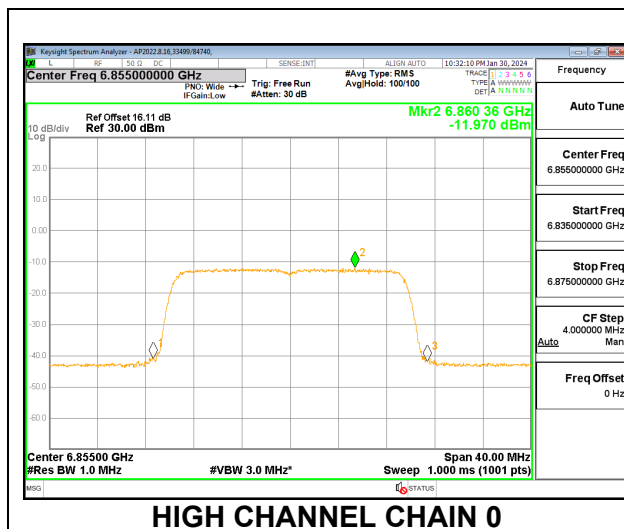
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	-2.36	-0.98	7.00	24.00	-17.00
Mid	6695	-1.95	-1.95	6.67	24.00	-17.33
High	6855	-0.48	-2.63	7.20	24.00	-16.80

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	-14.09	-12.38	-1.77	-1.00	-0.77
Mid	6695	-13.94	-12.24	-1.62	-1.00	-0.62
High	6855	-11.97	-14.05	-1.50	-1.00	-0.50



10.2.15. 802.11be EHT40 MODE 2TX IN THE UNII-7 BAND

Note: These results leveraged from R14932101-E10a

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/10

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6565	5.61	8.37	30.00	17.00
Mid	6685	5.61	8.37	30.00	17.00
High	6845	5.61	8.37	30.00	17.00

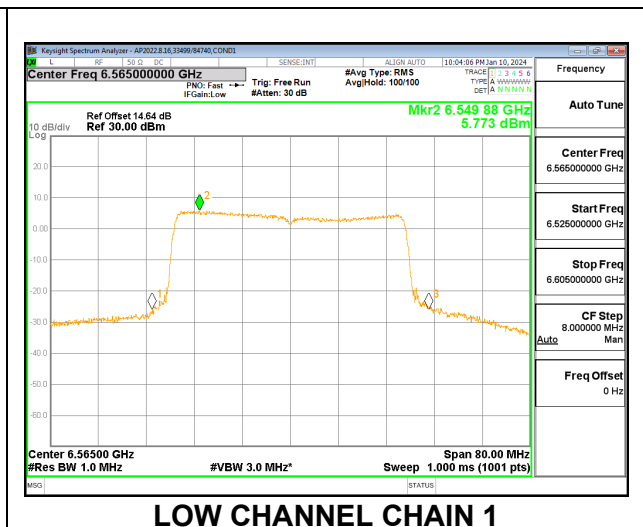
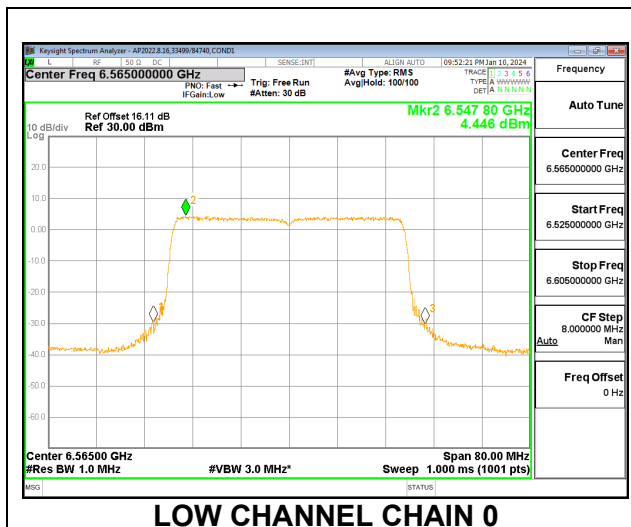
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas. Power (dBm)	Chain 1 Meas. Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6565	17.44	17.87	26.28	30.00	-3.72
Mid	6685	17.27	17.63	26.07	30.00	-3.93
High	6845	17.16	17.48	25.94	30.00	-4.06

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas. PSD (dBm)	Chain 1 Meas. PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6565	4.45	5.77	16.54	17.00	-0.46
Mid	6685	3.56	4.15	15.25	17.00	-1.75
High	6845	3.31	4.00	15.05	17.00	-1.95



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6565	5.61	8.37	24.00	-1.00
Mid	6685	5.61	8.37	24.00	-1.00
High	6845	5.61	8.37	24.00	-1.00

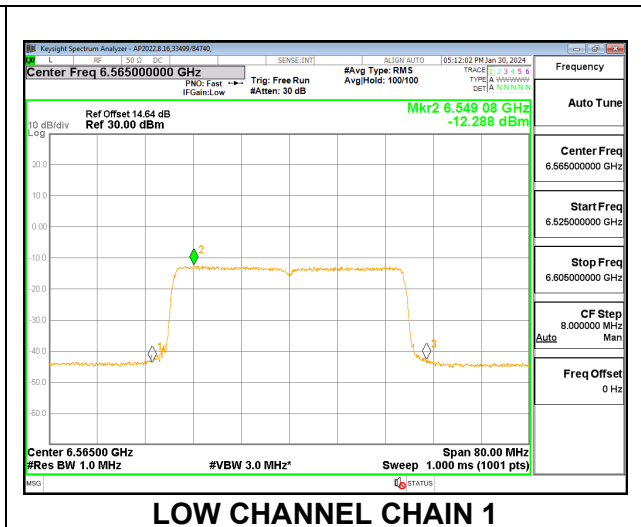
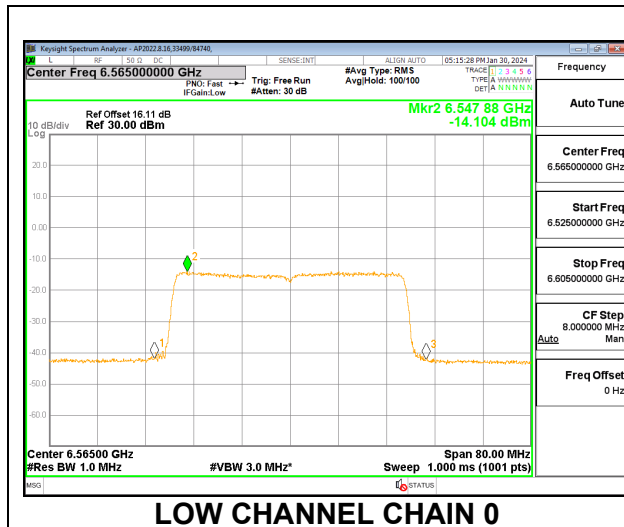
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6565	0.61	1.90	9.92	24.00	-14.08
Mid	6685	1.53	1.95	10.37	24.00	-13.63
High	6845	1.83	0.34	9.77	24.00	-14.23

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6565	-14.10	-12.29	-1.72	-1.00	-0.72
Mid	6685	-13.73	-12.80	-1.86	-1.00	-0.86
High	6845	-12.24	-14.35	-1.79	-1.00	-0.79



10.2.16. 802.11be EHT80 MODE 2TX IN THE UNII-7 BAND

Note: These results leveraged from R14932101-E10a

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+242T (CONTIGUOUS) – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/10

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6625	5.61	8.37	30.00	17.00
Mid	6705	5.61	8.37	30.00	17.00
High	6785	5.61	8.37	30.00	17.00

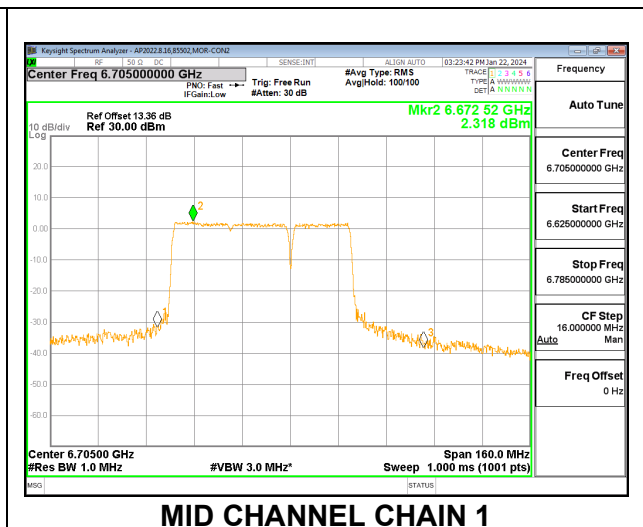
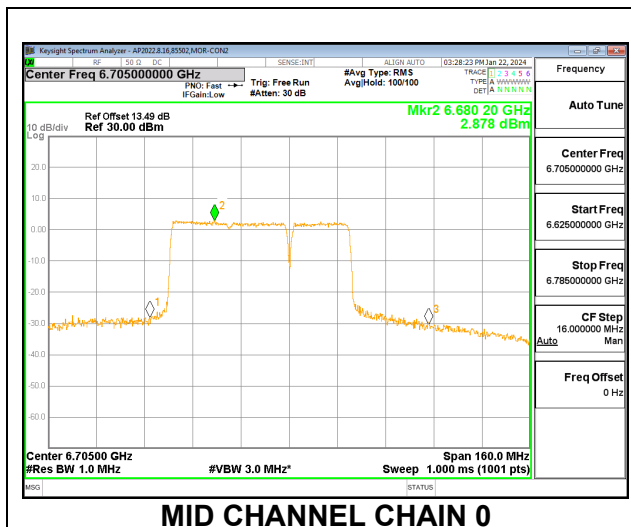
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6625	17.36	17.86	26.24	30.00	-3.76
Mid	6705	17.40	17.89	26.27	30.00	-3.73
High	6785	17.71	17.85	26.40	30.00	-3.60

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6625	0.10	0.27	11.57	17.00	-5.43
Mid	6705	2.88	2.32	13.99	17.00	-3.01
High	6785	1.87	2.34	13.49	17.00	-3.51



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+242T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6625	5.61	8.37	24.00	-1.00
Mid	6705	5.61	8.37	24.00	-1.00
High	6785	5.61	8.37	24.00	-1.00

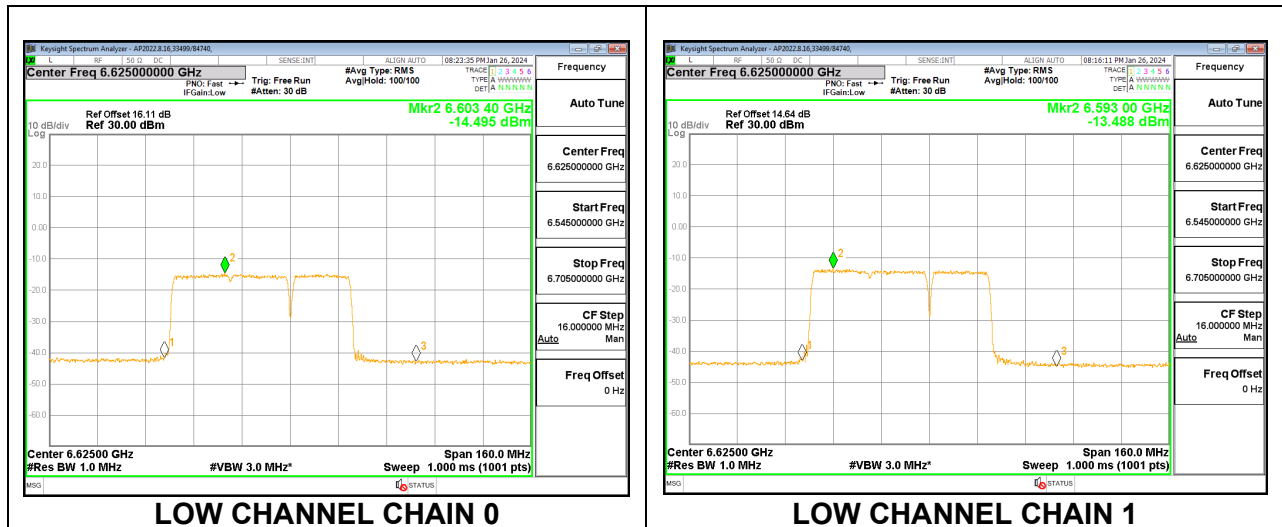
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6625	1.11	2.09	10.25	24.00	-13.75
Mid	6705	2.49	1.66	10.72	24.00	-13.28
High	6785	2.80	0.81	10.54	24.00	-13.46

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6625	-14.50	-13.49	-2.58	-1.00	-1.58
Mid	6705	-14.43	-14.72	-3.19	-1.00	-2.19
High	6785	-13.48	-15.15	-2.85	-1.00	-1.85



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+242T (NON-CONTIGUOUS) – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/10

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6625	5.61	8.37	30.00	17.00
Mid	6705	5.61	8.37	30.00	17.00
High	6785	5.61	8.37	30.00	17.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6625	17.29	17.66	26.10	30.00	-3.90
Mid	6705	17.33	17.67	26.12	30.00	-3.88
High	6785	17.68	17.80	26.36	30.00	-3.64

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+242T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6625	5.61	8.37	24.00	-1.00
Mid	6705	5.61	8.37	24.00	-1.00
High	6785	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6625	0.89	1.78	9.98	24.00	-14.02
Mid	6705	2.23	1.41	10.46	24.00	-13.54
High	6785	2.79	0.81	10.53	24.00	-13.47

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 242T+484T (NON-CONTIGUOUS) – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/10

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6625	5.61	8.37	30.00	17.00
Mid	6705	5.61	8.37	30.00	17.00
High	6785	5.61	8.37	30.00	17.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6625	17.16	17.64	26.03	30.00	-3.97
Mid	6705	17.34	17.61	26.10	30.00	-3.90
High	6785	17.62	17.74	26.30	30.00	-3.70

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 242T+484T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6625	5.61	8.37	24.00	-1.00
Mid	6705	5.61	8.37	24.00	-1.00
High	6785	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6625	0.92	1.80	10.00	24.00	-14.00
Mid	6705	2.17	1.27	10.36	24.00	-13.64
High	6785	2.88	0.86	10.61	24.00	-13.39

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/10

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6625	5.61	8.37	30.00	17.00
Mid	6705	5.61	8.37	30.00	17.00
High	6785	5.61	8.37	30.00	17.00

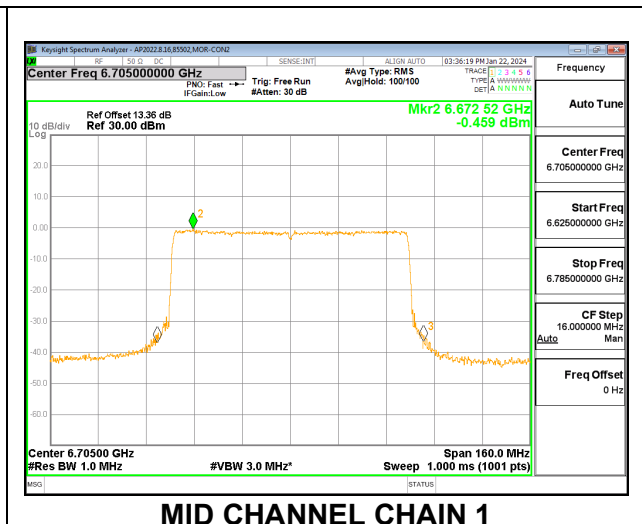
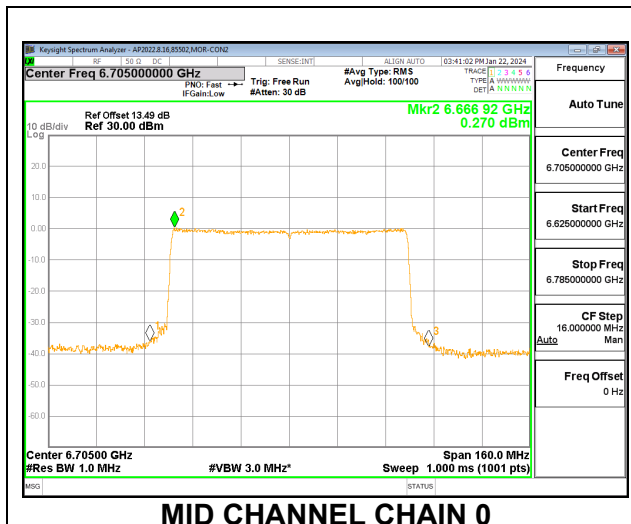
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6625	14.69	15.13	23.54	30.00	-6.46
Mid	6705	14.31	14.65	23.10	30.00	-6.90
High	6785	14.04	14.10	22.69	30.00	-7.31

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6625	-2.53	-2.83	8.71	17.00	-8.29
Mid	6705	0.27	-0.46	11.30	17.00	-5.70
High	6785	-0.58	-0.57	10.81	17.00	-6.19



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6625	5.61	8.37	24.00	-1.00
Mid	6705	5.61	8.37	24.00	-1.00
High	6785	5.61	8.37	24.00	-1.00

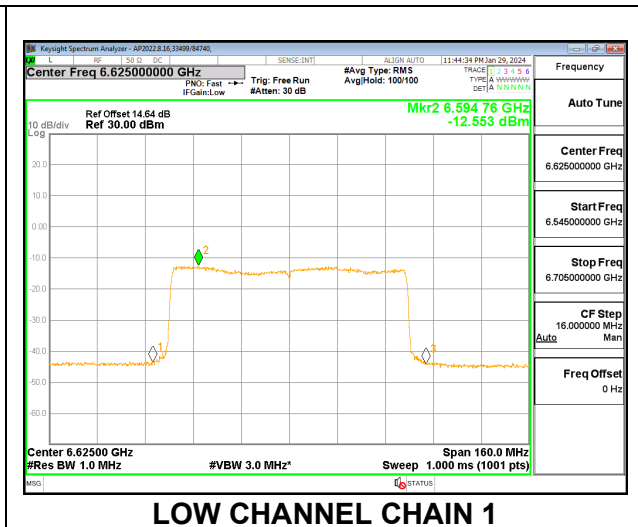
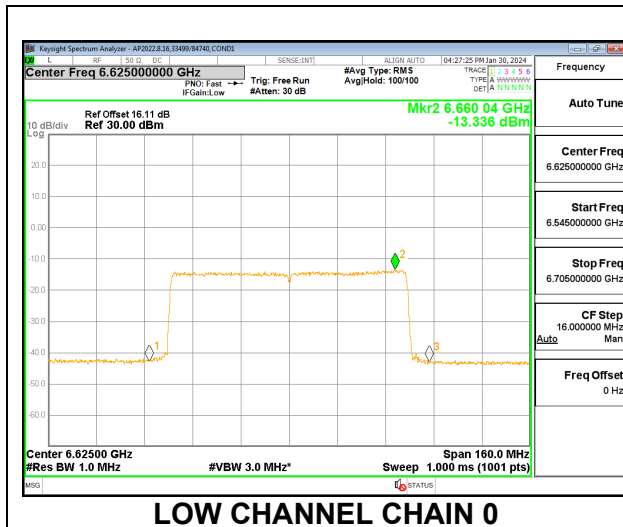
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6625	4.02	4.23	12.75	24.00	-11.25
Mid	6705	4.10	4.29	12.82	24.00	-11.18
High	6785	3.23	3.19	11.83	24.00	-12.17

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6625	-13.34	-12.55	-1.55	-1.00	-0.55
Mid	6705	-13.69	-12.84	-1.86	-1.00	-0.86
High	6785	-13.56	-12.99	-1.88	-1.00	-0.88



10.2.17. 802.11be EHT160 MODE 2TX IN THE UNII-7 BAND

Note: These results leveraged from R14932101-E10a

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T (CONTIGUOUS) – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Mid	6665	5.61	8.37	30.00	17.00

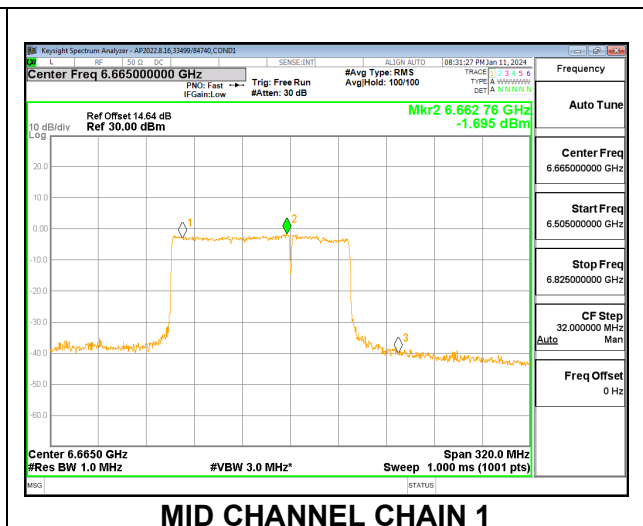
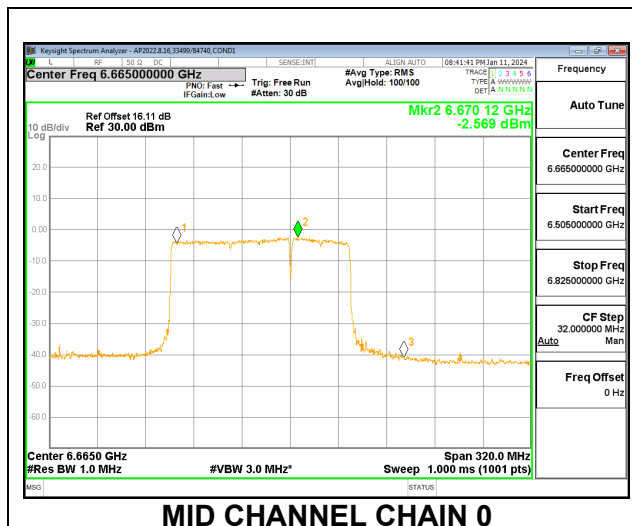
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6665	14.58	15.21	23.53	30.00	-6.47

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Mid	6665	-2.57	-1.70	9.27	17.00	-7.73



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Mid	6665	5.61	8.37	24.00	-1.00

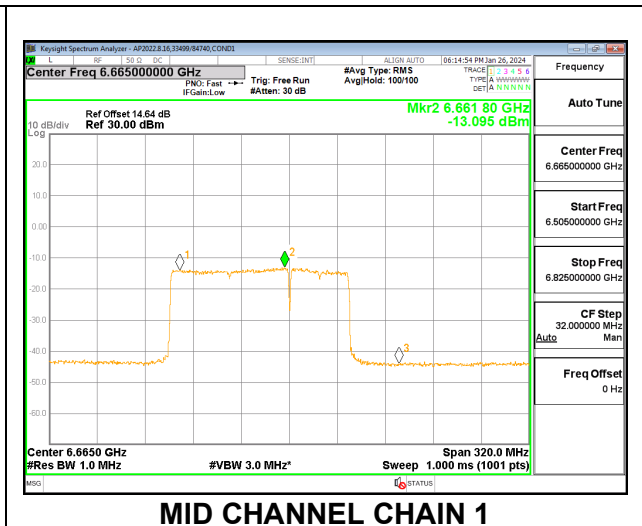
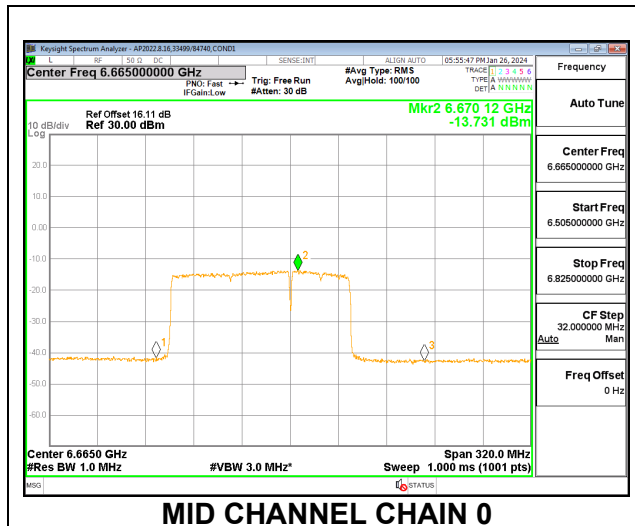
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6665	4.47	4.98	13.35	24.00	-10.65

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Mid	6665	-13.37	-13.10	-1.85	-1.00	-0.85



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T (NON-CONTIGUOUS) – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Mid	6665	5.61	8.37	30.00	17.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6665	15.45	15.48	24.09	30.00	-5.91

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Mid	6665	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6665	4.27	4.65	13.08	24.00	-10.92

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+996T (NON-CONTIGUOUS) – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Mid	6665	5.61	8.37	30.00	17.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6665	15.36	15.37	23.99	30.00	-6.01

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+996T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Mid	6665	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6665	4.71	4.89	13.42	24.00	-10.58

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/10

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Mid	6665	5.61	8.37	30.00	17.00

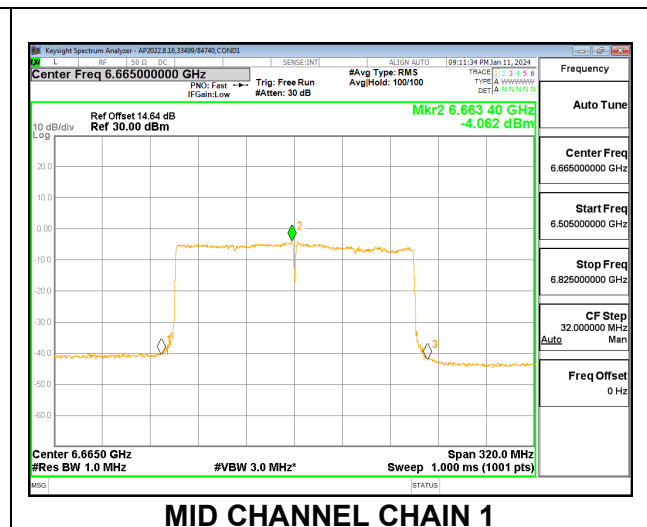
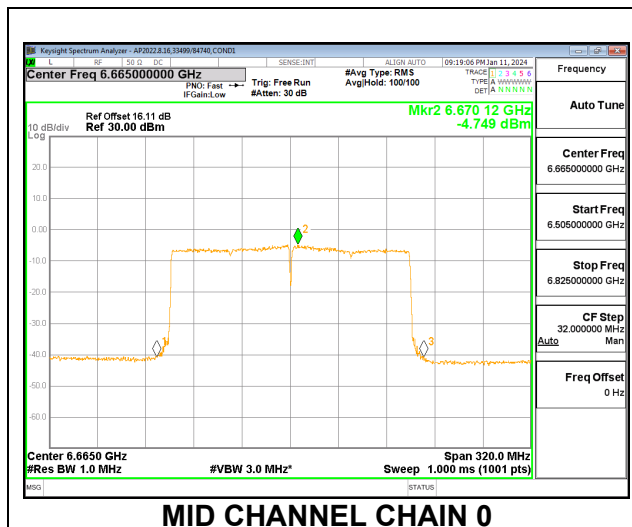
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6665	15.52	16.08	24.43	30.00	-5.57

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Mid	6665	-4.75	-4.06	6.99	17.00	-10.01



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Mid	6665	5.61	8.37	24.00	-1.00

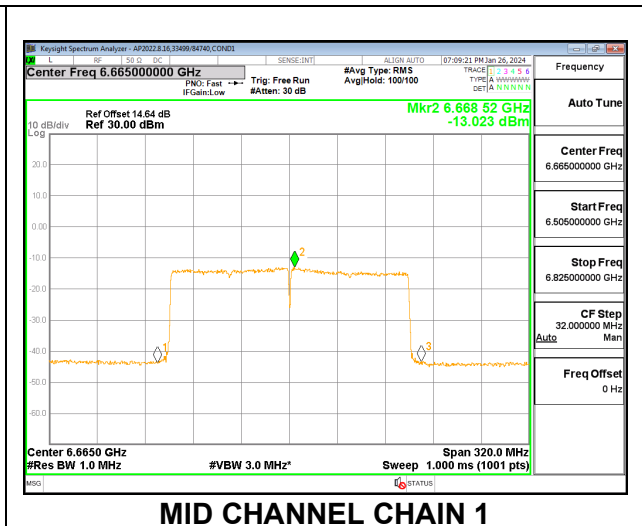
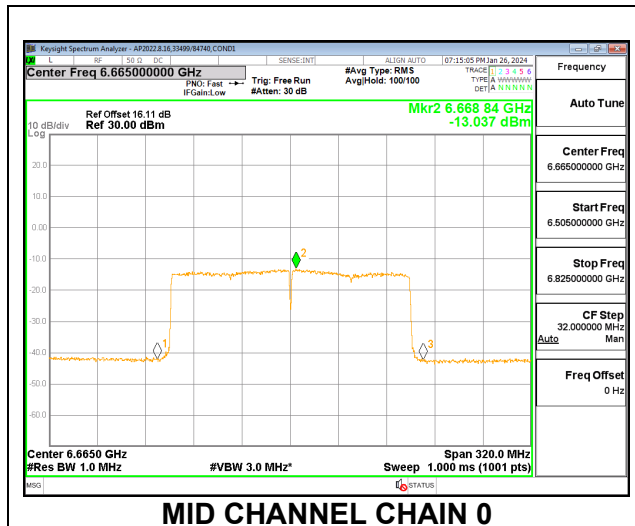
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6665	6.47	6.72	15.22	24.00	-8.78

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Mid	6665	-13.04	-13.02	-1.65	-1.00	-0.65



10.2.18. 802.11a MODE 2TX IN THE UNII-8 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE – LOW POWER INDOOR

Test Engineer:	33499/44389, 85502
Test Date:	2024/01/11-2024/01/12

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6875	5.50	7.72	24.00	-1.00
Mid	6995	5.28	7.54	24.00	-1.00
High	7115	5.28	7.54	24.00	-1.00

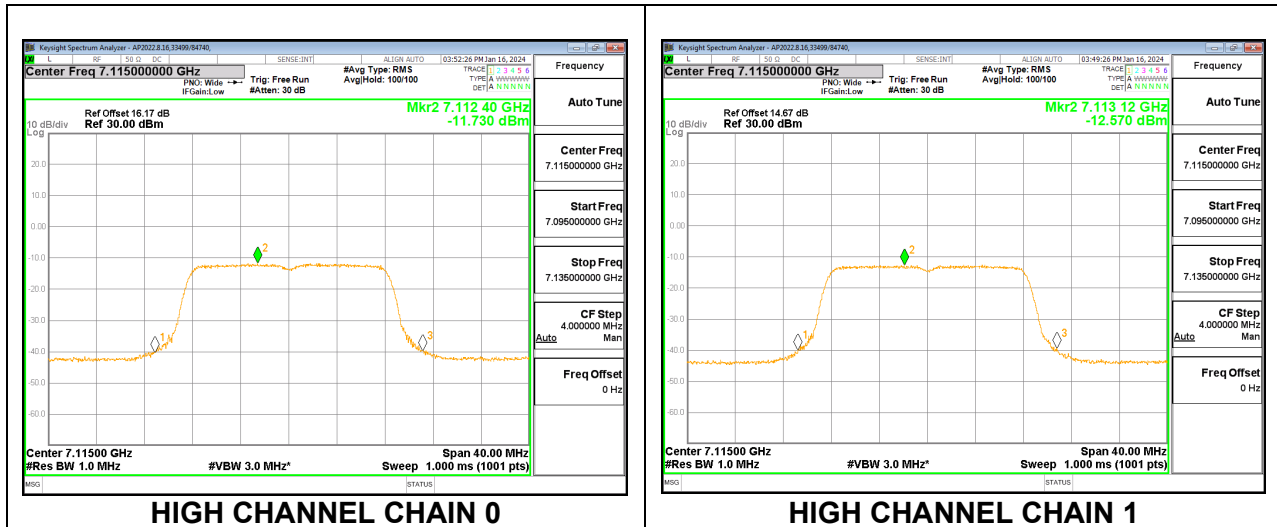
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6875	-1.59	-3.58	6.04	24.00	-17.96
Mid	6995	-0.90	-1.89	6.92	24.00	-17.08
High	7115	-0.82	-1.32	7.23	24.00	-16.77

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6875	-11.93	-13.41	-1.88	-1.00	-0.88
Mid	6995	-11.67	-12.88	-1.68	-1.00	-0.68
High	7115	-11.73	-12.57	-1.58	-1.00	-0.58



10.2.19. 802.11be EHT20 MODE 2TX IN THE UNII-8 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 26T – LOW POWER INDOOR

Test Engineer:	33499/44389, 85502
Test Date:	2024/01/11-2024/01/12

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6875	5.50	7.72	24.00	-1.00
Mid	6995	5.28	7.54	24.00	-1.00
High	7115	5.28	7.54	24.00	-1.00

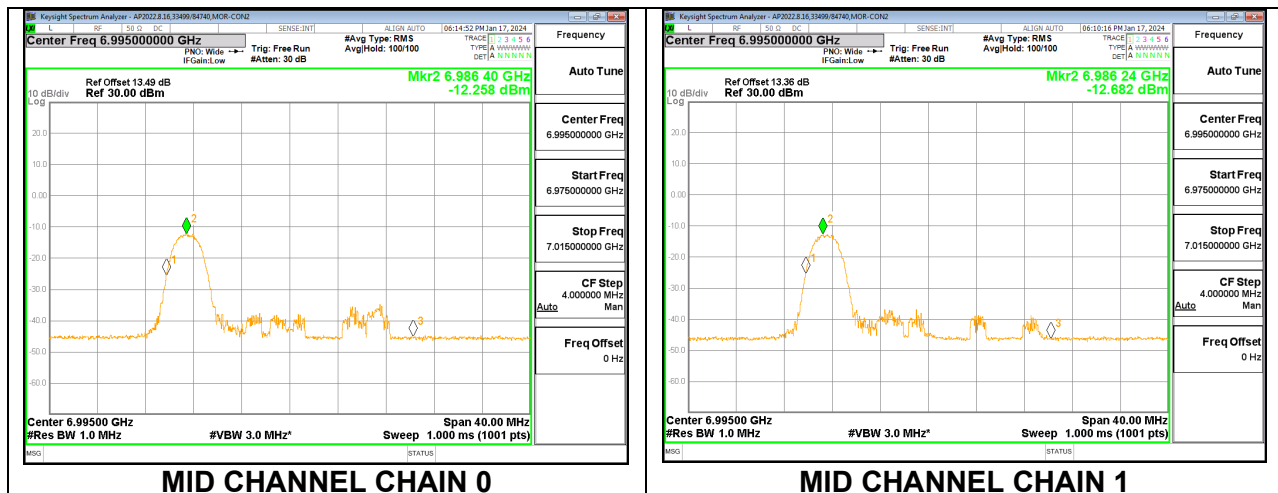
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6875	-10.81	-12.69	-3.14	24.00	-27.14
Mid	6995	-8.96	-10.07	-1.19	24.00	-25.19
High	7115	-9.53	-9.81	-1.38	24.00	-25.38

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6875	-11.85	-13.69	-1.94	-1.00	-0.94
Mid	6995	-12.26	-12.68	-1.91	-1.00	-0.91
High	7115	-12.71	-12.27	-1.94	-1.00	-0.94



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 52T – LOW POWER INDOOR

Test Engineer:	33499/44389, 85502
Test Date:	2024/01/11-2024/01/12

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6875	5.50	7.72	24.00	-1.00
Mid	6995	5.28	7.54	24.00	-1.00
High	7115	5.28	7.54	24.00	-1.00

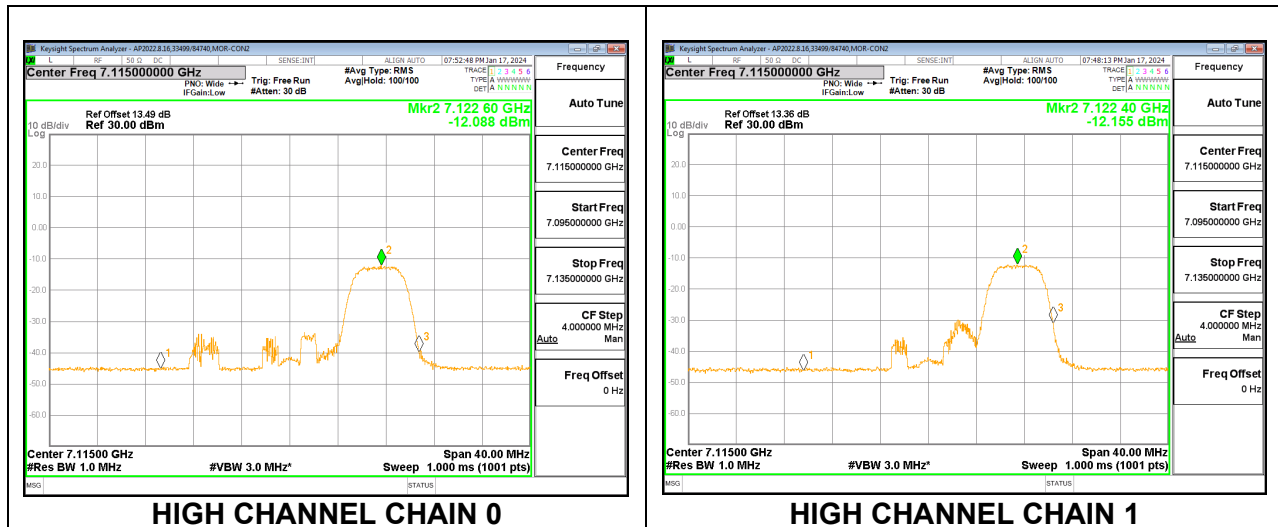
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6875	-7.87	-9.77	-0.21	24.00	-24.21
Mid	6995	-6.57	-7.67	1.21	24.00	-22.79
High	7115	-6.65	-6.92	1.51	24.00	-22.49

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6875	-11.78	-13.84	-1.96	-1.00	-0.96
Mid	6995	-12.32	-12.76	-1.99	-1.00	-0.99
High	7115	-12.09	-12.16	-1.57	-1.00	-0.57



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 52T+26T – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/17

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6875	5.50	7.72	24.00	-1.00
Mid	6995	5.28	7.54	24.00	-1.00
High	7115	5.28	7.54	24.00	-1.00

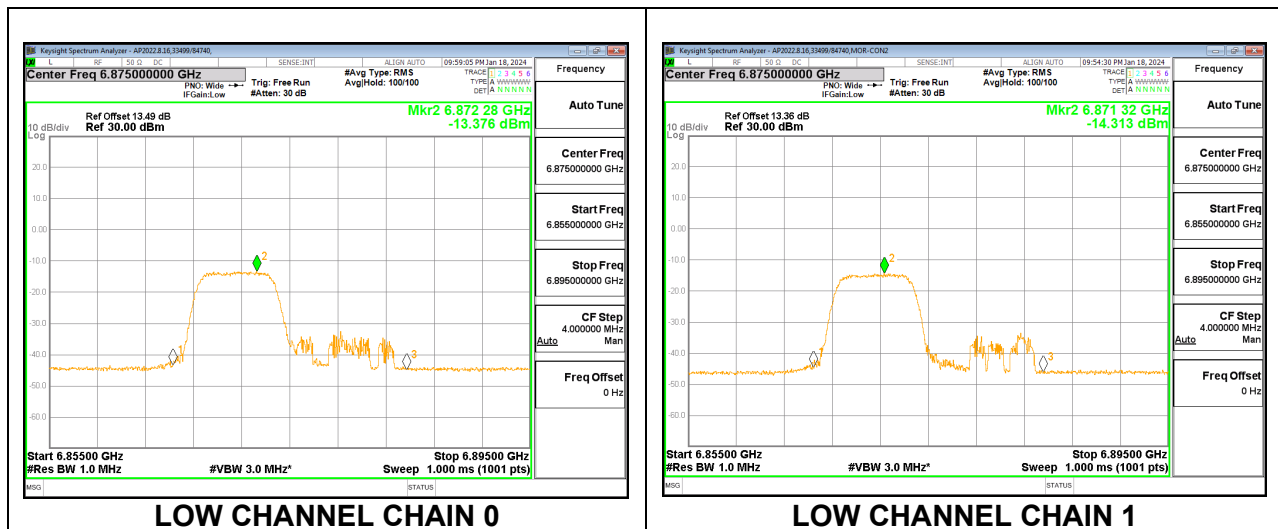
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6875	-7.96	-9.86	-0.30	24.00	-24.30
Mid	6995	-6.69	-7.76	1.10	24.00	-22.90
High	7115	-6.74	-7.04	1.40	24.00	-22.60

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6875	-13.38	-14.31	-3.09	-1.00	-2.09
Mid	6995	-14.04	-14.22	-3.58	-1.00	-2.58
High	7115	-14.42	-13.80	-3.55	-1.00	-2.55



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 106T – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/17, 2024/02/16

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6875	5.50	7.72	24.00	-1.00
Mid	6995	5.28	7.54	24.00	-1.00
High	7115	5.28	7.54	24.00	-1.00

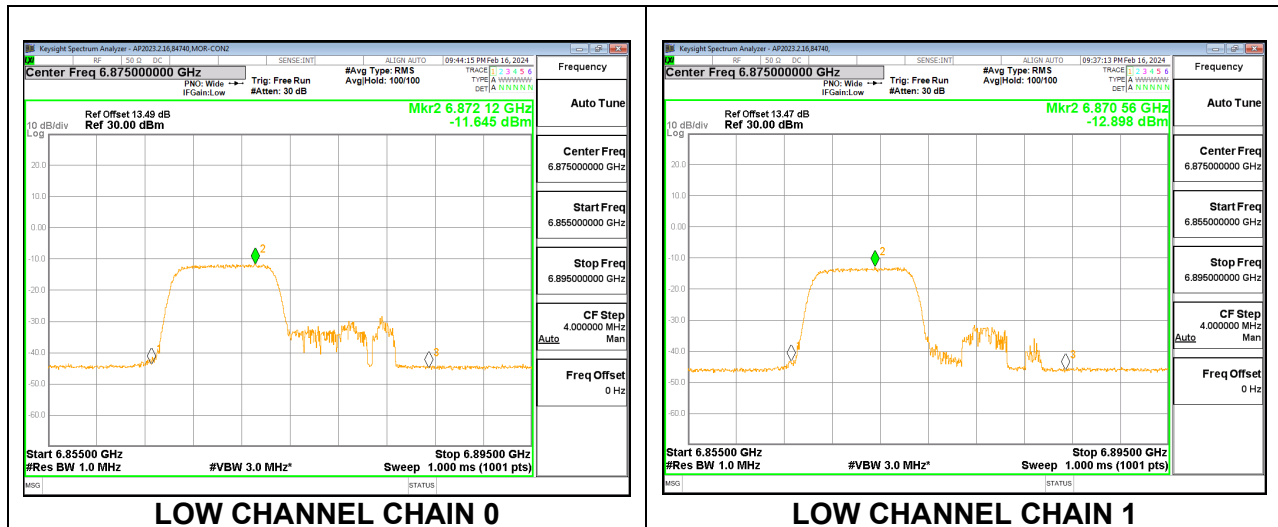
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6875	-4.94	-6.86	2.72	24.00	-21.28
Mid	6995	-3.64	-4.71	4.15	24.00	-19.85
High	7115	-3.66	-3.97	4.48	24.00	-19.52

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6875	-11.65	-12.90	-1.50	-1.00	-0.50
Mid	6995	-12.48	-12.37	-1.87	-1.00	-0.87
High	7115	-12.47	-12.09	-1.73	-1.00	-0.73



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 106T+26T – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/17

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6875	5.50	7.72	24.00	-1.00
Mid	6995	5.28	7.54	24.00	-1.00
High	7115	5.28	7.54	24.00	-1.00

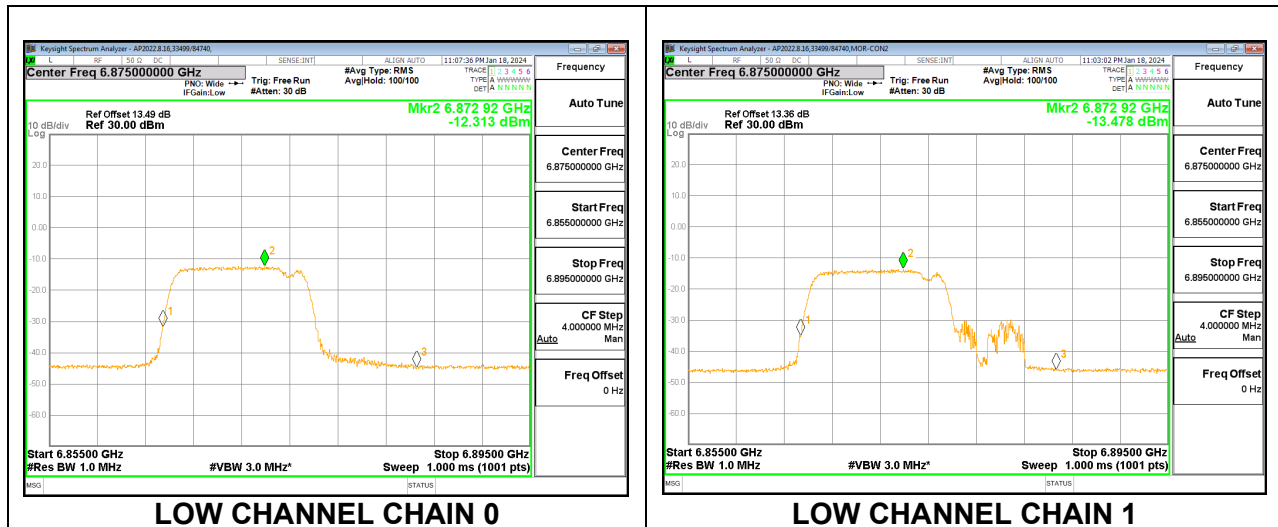
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6875	-4.96	-6.88	2.70	24.00	-21.30
Mid	6995	-3.77	-4.71	4.08	24.00	-19.92
High	7115	-3.84	-4.00	4.37	24.00	-19.63

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6875	-12.31	-13.48	-2.13	-1.00	-1.13
Mid	6995	-13.23	-13.33	-2.73	-1.00	-1.73
High	7115	-13.53	-13.14	-2.78	-1.00	-1.78



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 242T – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/17

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6875	5.50	7.72	24.00	-1.00
Mid	6995	5.28	7.54	24.00	-1.00
High	7115	5.28	7.54	24.00	-1.00

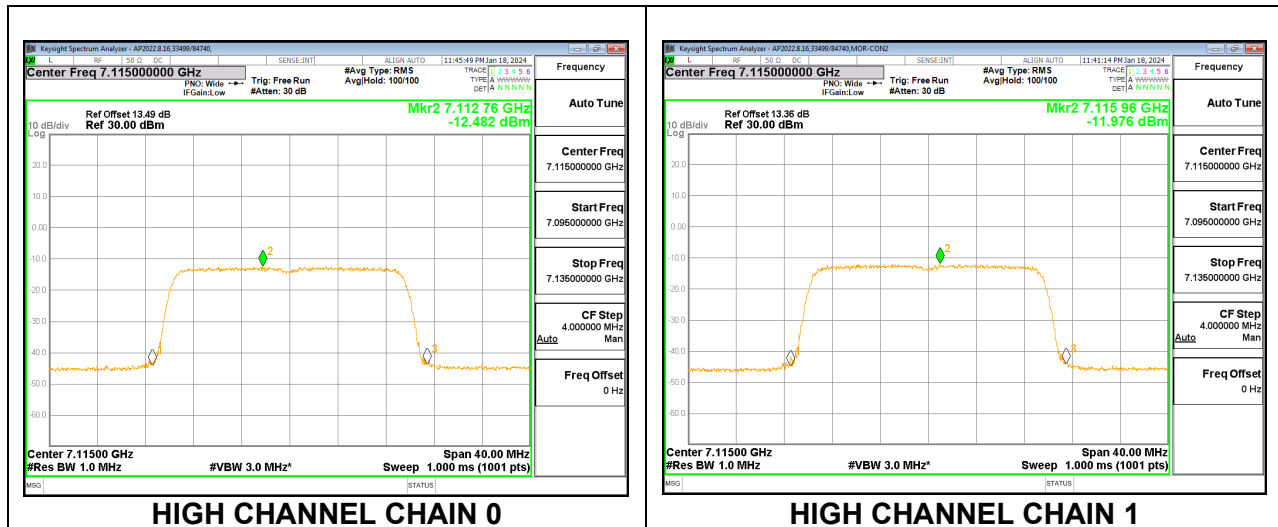
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6875	-2.02	-3.95	5.63	24.00	-18.37
Mid	6995	-0.24	-1.36	7.53	24.00	-16.47
High	7115	-0.24	-0.72	7.82	24.00	-16.18

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6875	-11.77	-13.80	-1.94	-1.00	-0.94
Mid	6995	-12.27	-12.46	-1.81	-1.00	-0.81
High	7115	-12.48	-11.98	-1.67	-1.00	-0.67



10.2.20. 802.11be EHT40 MODE 2TX IN THE UNII-8 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/17

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6885	5.50	7.72	24.00	-1.00
Mid	7005	5.28	7.54	24.00	-1.00
High	7085	5.28	7.54	24.00	-1.00

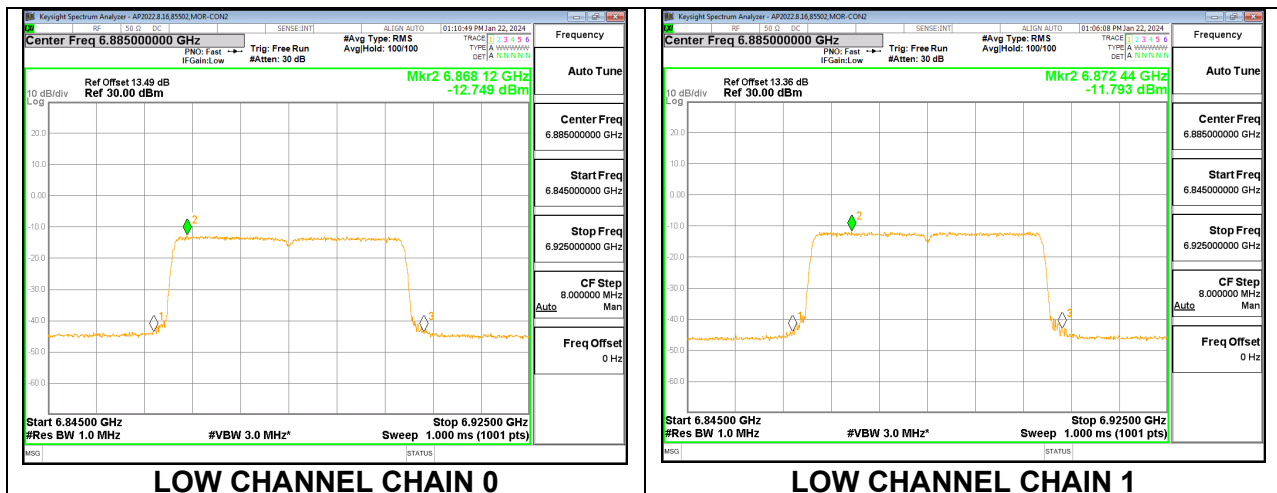
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6885	1.29	-0.56	8.97	24.00	-15.03
Mid	7005	2.86	2.12	10.80	24.00	-13.20
High	7085	2.83	1.71	10.60	24.00	-13.40

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6885	-12.75	-11.79	-1.51	-1.00	-0.51
Mid	6965	-12.66	-11.64	-1.57	-1.00	-0.57
High	7085	-12.91	-11.50	-1.59	-1.00	-0.59



10.2.21. 802.11be EHT80 MODE 2TX IN THE UNII-8 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+242T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502,84740
Test Date:	2024/01/17,2024/02/16

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6865	5.50	7.72	24.00	-1.00
Mid	6945	5.28	7.54	24.00	-1.00
High	7025	5.28	7.54	24.00	-1.00

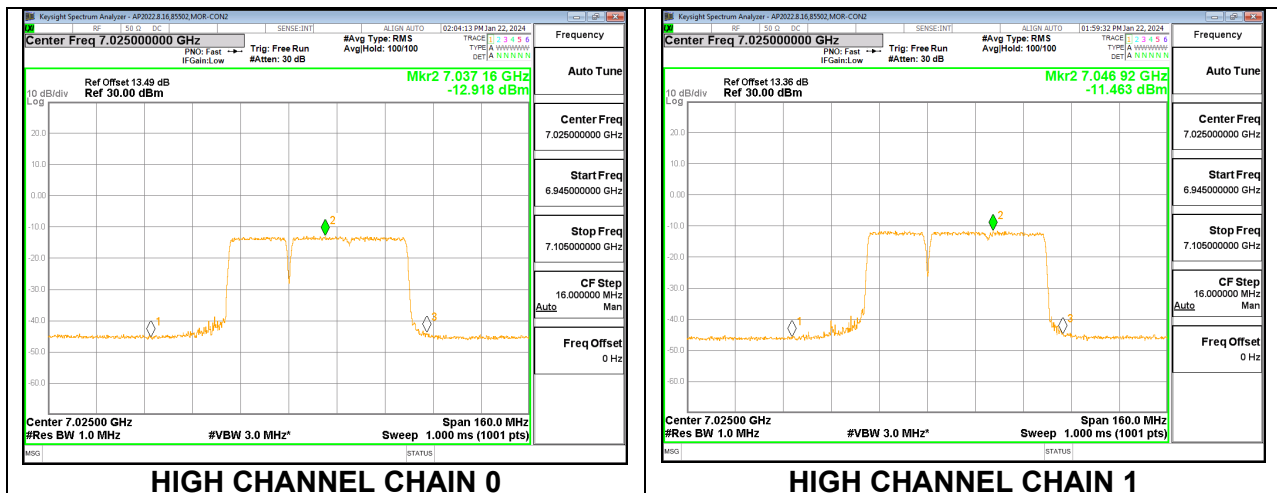
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6865	2.84	1.39	10.69	24.00	-13.31
Mid	6945	2.27	1.29	10.10	24.00	-13.90
High	7025	2.70	1.77	10.55	24.00	-13.45

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6865	-12.45	-12.64	-1.82	-1.00	-0.82
Mid	6945	-12.91	-11.66	-1.69	-1.00	-0.69
High	7025	-12.92	-11.46	-1.58	-1.00	-0.58



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+242T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/17

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6865	5.50	7.72	24.00	-1.00
Mid	6945	5.28	7.54	24.00	-1.00
High	7025	5.28	7.54	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6865	2.65	1.18	10.49	24.00	-13.51
Mid	6945	2.10	1.10	9.92	24.00	-14.08
High	7025	2.67	1.54	10.43	24.00	-13.57

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 242T+484T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/17

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6865	5.50	7.72	24.00	-1.00
Mid	6945	5.28	7.54	24.00	-1.00
High	7025	5.28	7.54	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6865	2.62	1.15	10.46	24.00	-13.54
Mid	6945	2.15	1.16	9.97	24.00	-14.03
High	7025	2.53	1.56	10.36	24.00	-13.64

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/17

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6865	5.50	7.72	24.00	-1.00
Mid	6945	5.28	7.54	24.00	-1.00
High	7025	5.28	7.54	24.00	-1.00

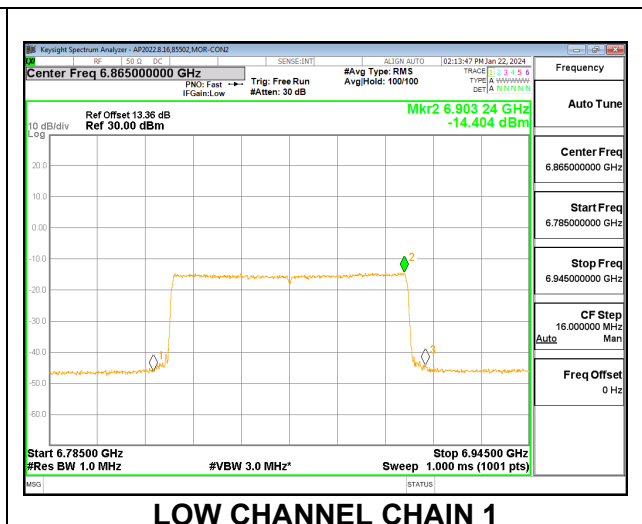
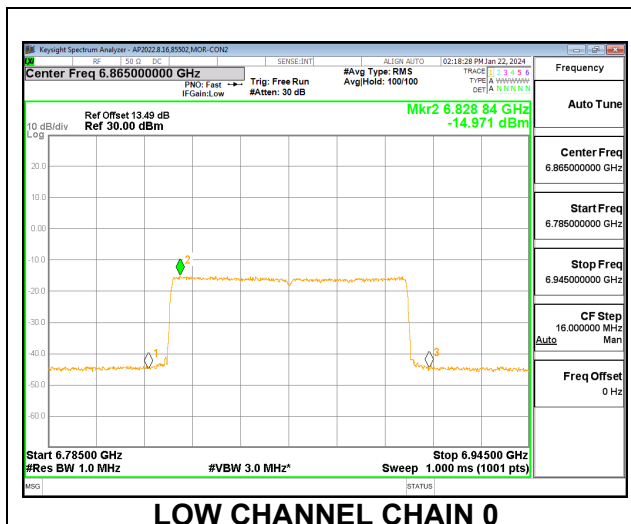
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6865	3.13	2.71	11.44	24.00	-12.56
Mid	6945	6.10	5.67	14.18	24.00	-9.82
High	7025	6.20	5.59	14.20	24.00	-9.80

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6865	-14.97	-14.40	-3.95	-1.00	-2.95
Mid	6945	-15.56	-14.15	-4.25	-1.00	-3.25
High	7025	-15.45	-14.30	-4.28	-1.00	-3.28



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: SU – LOW POWER INDOOR

Test Engineer:	85502,84740
Test Date:	2024/01/17,2024/02/16

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6865	5.50	7.72	24.00	-1.00
Mid	6945	5.28	7.54	24.00	-1.00
High	7025	5.28	7.54	24.00	-1.00

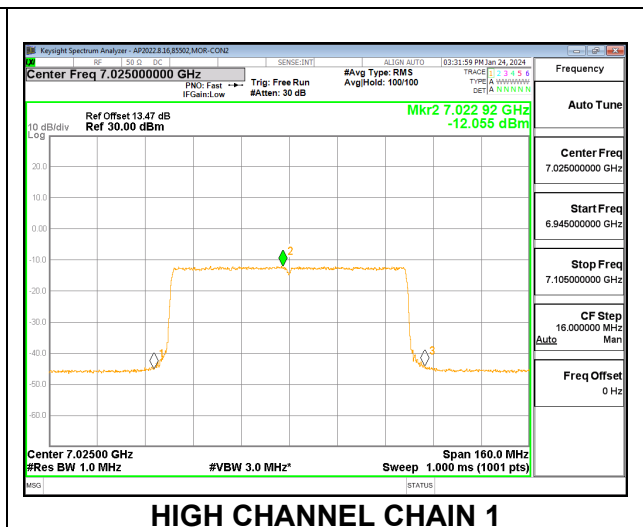
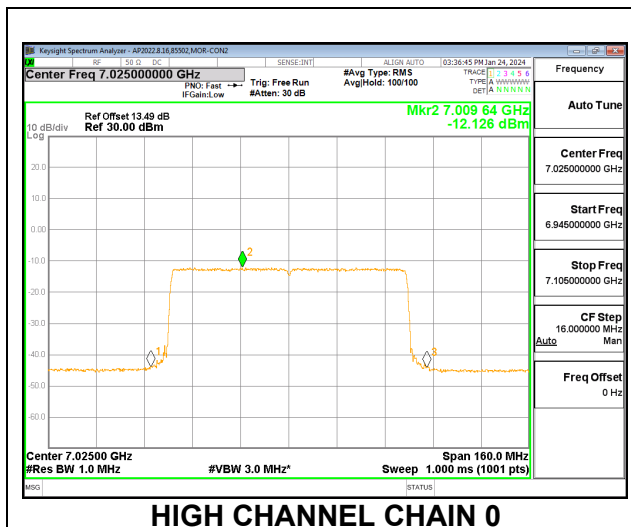
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6865	3.28	2.69	11.51	24.00	-12.49
Mid	6945	6.81	6.08	14.75	24.00	-9.25
High	7025	6.99	6.07	14.84	24.00	-9.16

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6865	-14.84	-14.86	-4.12	-1.00	-3.12
Mid	6945	-12.14	-12.07	-1.56	-1.00	-0.56
High	7025	-12.13	-12.06	-1.54	-1.00	-0.54



10.2.22. 802.11be EHT160 MODE 2TX IN THE UNII-8 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502,84740
Test Date:	2024/01/17,2024/02/16

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIPR Power Limit (dBm)	PSD Limit (dBm)
Low	6825	5.50	7.72	24.00	-1.00
High	6985	5.28	7.54	24.00	-1.00

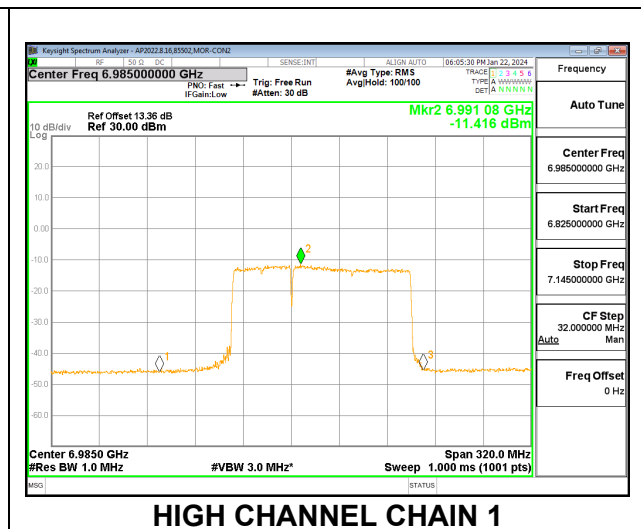
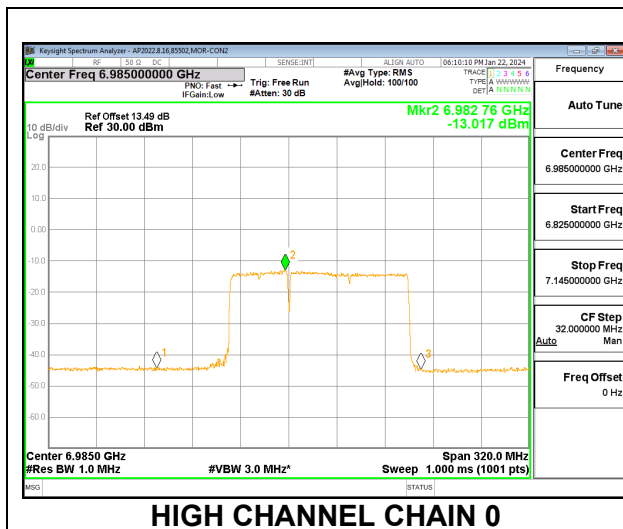
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6825	4.16	3.86	12.52	24.00	-11.48
High	6985	6.46	6.18	14.61	24.00	-9.39

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6825	-12.35	-12.63	-1.76	-1.00	-0.76
High	6985	-13.02	-11.42	-1.59	-1.00	-0.59



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/17

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIPR Power Limit (dBm)	PSD Limit (dBm)
Low	6825	5.50	7.72	24.00	-1.00
High	6985	5.28	7.54	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6825	6.47	6.48	14.99	24.00	-9.01
High	6985	6.43	6.11	14.56	24.00	-9.44

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+996T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/17

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIPR Power Limit (dBm)	PSD Limit (dBm)
Low	6825	5.50	7.72	24.00	-1.00
High	6985	5.28	7.54	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6825	6.42	6.33	14.89	24.00	-9.11
High	6985	6.35	6.08	14.51	24.00	-9.49

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/17

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIPR Power Limit (dBm)	PSD Limit (dBm)
Low	6825	5.50	7.72	24.00	-1.00
High	6985	5.28	7.54	24.00	-1.00

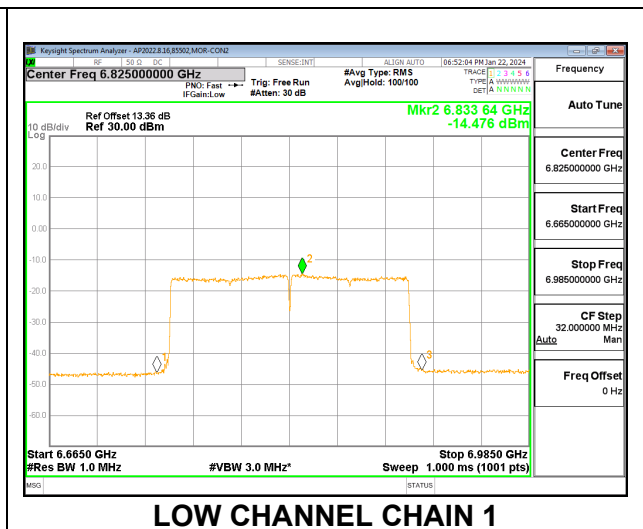
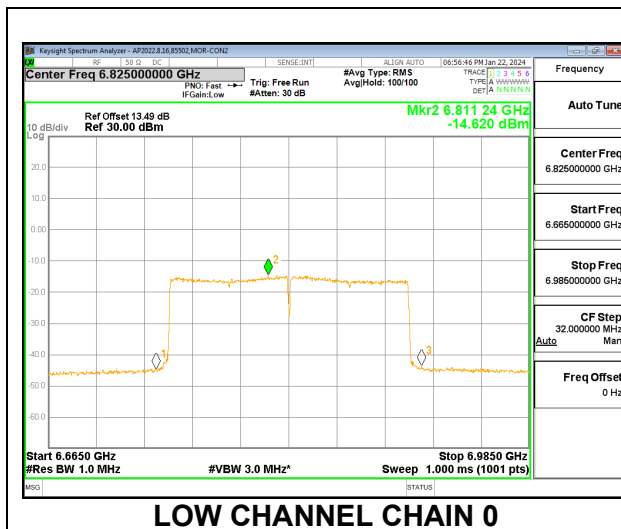
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6825	6.57	6.09	14.85	24.00	-9.15
High	6985	7.04	6.54	15.09	24.00	-8.91

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6825	-14.62	-14.48	-3.82	-1.00	-2.82
High	6985	-15.35	-14.02	-4.08	-1.00	-3.08



10.2.23. 802.11be EHT320 MODE 2TX IN THE UNII-8 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T+484T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.50	7.72	24.00	-1.00
High	6905	5.50	7.72	24.00	-1.00

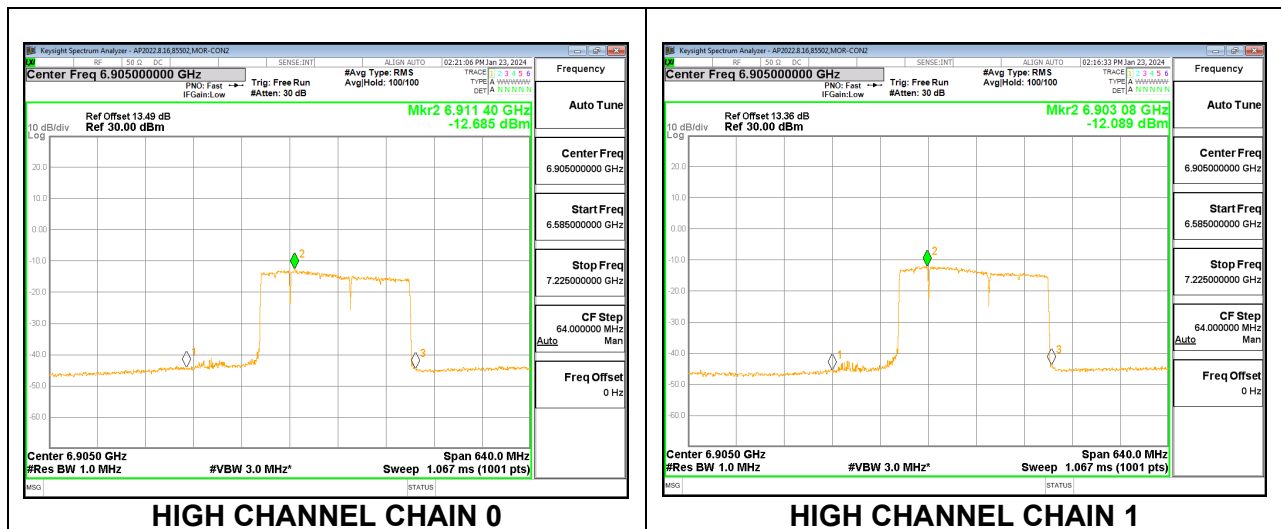
Duty Cycle CF (dB)	0.14	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	7.03	7.48	15.77	24.00	-8.23
High	6905	6.78	6.10	14.96	24.00	-9.04

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6745	-12.34	-13.22	-1.89	-1.00	-0.89
High	6905	-12.69	-12.09	-1.51	-1.00	-0.51



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T+484T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.50	7.72	24.00	-1.00
High	6905	5.50	7.72	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	6.88	7.49	15.71	24.00	-8.29
High	6905	7.12	6.69	15.42	24.00	-8.58

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T+996T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.50	7.72	24.00	-1.00
High	6905	5.50	7.72	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	6.76	7.25	15.52	24.00	-8.48
High	6905	7.42	7.14	15.79	24.00	-8.21

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T+996T (NON-CONTIGUOUS 2) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.50	7.72	24.00	-1.00
High	6905	5.50	7.72	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	6.69	7.12	15.42	24.00	-8.58
High	6905	7.44	7.19	15.83	24.00	-8.17

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 3x996T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.50	7.72	24.00	-1.00
High	6905	5.50	7.72	24.00	-1.00

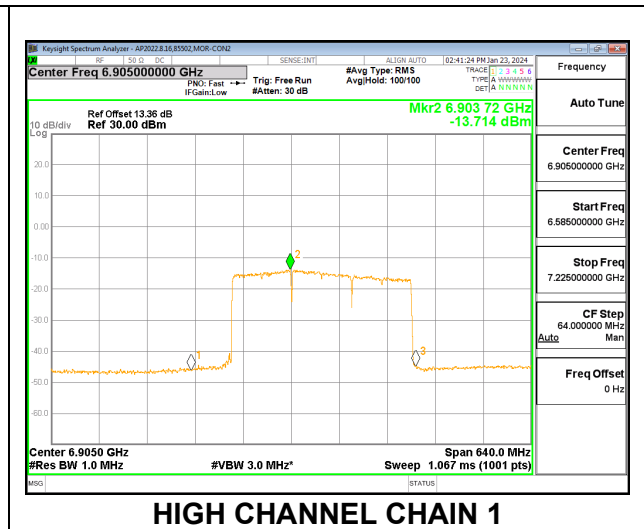
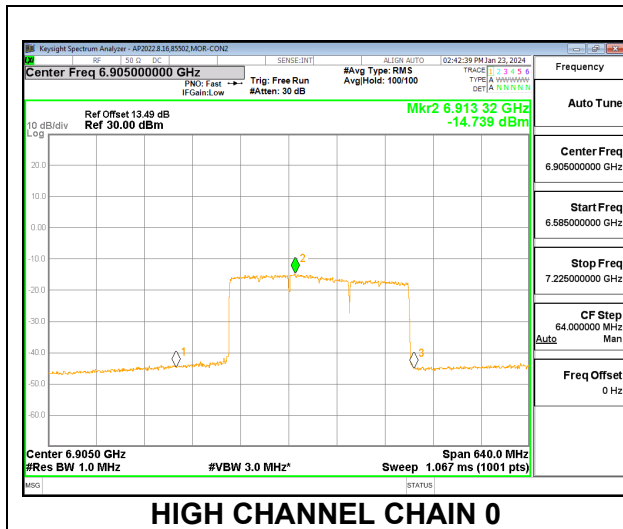
Duty Cycle CF (dB)	0.17	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	5.72	6.47	14.62	24.00	-9.38
High	6905	7.26	6.26	15.30	24.00	-8.70

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6745	-13.90	-14.87	-3.46	-1.00	-2.46
High	6905	-14.74	-13.71	-3.30	-1.00	-2.30



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 3x996T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.50	7.72	24.00	-1.00
High	6905	5.50	7.72	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	5.41	5.61	14.02	24.00	-9.98
High	6905	7.38	6.62	15.53	24.00	-8.47

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 3x996T (NON-CONTIGUOUS 2) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.50	7.72	24.00	-1.00
High	6905	5.50	7.72	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	5.44	6.07	14.28	24.00	-9.72
High	6905	7.46	6.77	15.64	24.00	-8.36