2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Mid	6505	5.61	8.37	24.00	-1.00

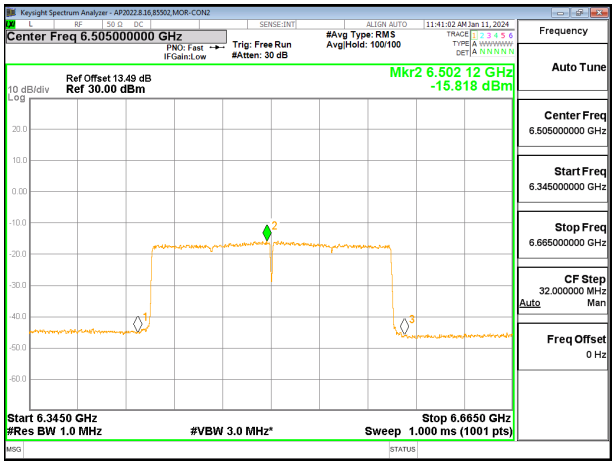
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

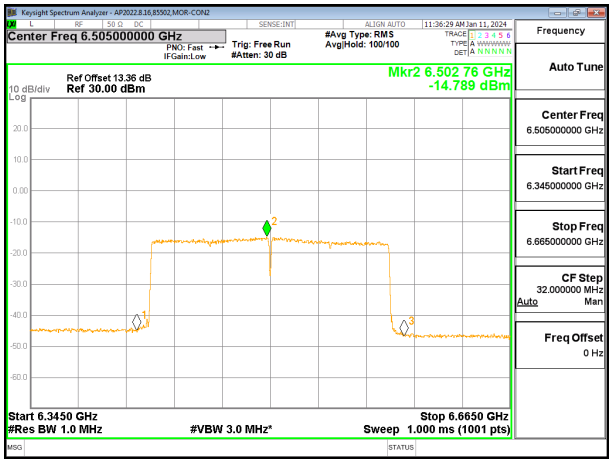
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6505	4.47	5.13	13.43	24.00	-10.57

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Mid	6505	-15.82	-14.79	-3.89	-1.00	-2.89



MID CHANNEL CHAIN 0



MID CHANNEL CHAIN 1

9.2.12. 802.11be EHT320 MODE 2TX IN THE UNII-6 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T+484T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/11, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.61	8.37	24.00	-1.00
High	6585	5.61	8.37	24.00	-1.00

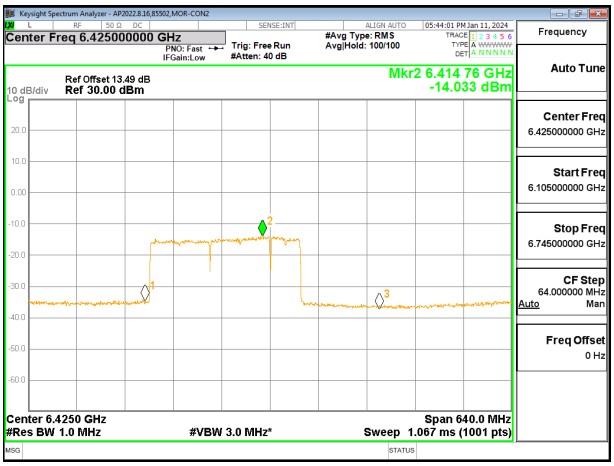
Duty Cycle CF (dB)	0.14	**Included in Calculations of Corr'd PSD**

Output Power Results

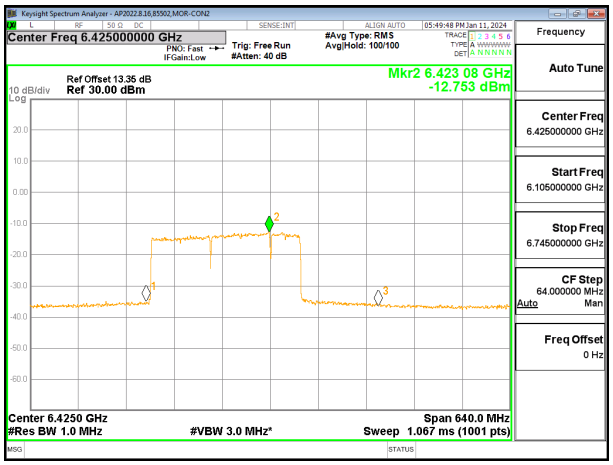
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	6.97	6.97	15.59	24.00	-8.41
High	6585	5.97	6.54	14.88	24.00	-9.12

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6425	-14.03	-12.75	-1.83	-1.00	-0.83
High	6585	-14.06	-13.08	-2.02	-1.00	-1.02



LOW CHANNEL CHAIN 0



LOW CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T+484T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/11, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.61	8.37	24.00	-1.00
High	6585	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	6.89	6.82	15.48	24.00	-8.52
High	6585	5.75	6.57	14.80	24.00	-9.20

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T+996T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/11, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.61	8.37	24.00	-1.00
High	6585	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	6.62	6.68	15.27	24.00	-8.73
High	6585	5.70	6.51	14.74	24.00	-9.26

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T+996T (NON-CONTIGUOUS 2) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/11, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.61	8.37	24.00	-1.00
High	6585	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	6.57	6.71	15.26	24.00	-8.74
High	6585	5.62	6.54	14.72	24.00	-9.28

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 3x996T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/11, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.61	8.37	24.00	-1.00
High	6585	5.61	8.37	24.00	-1.00

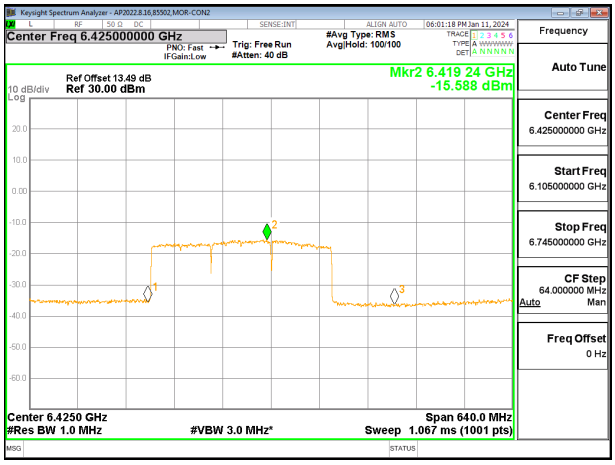
Duty Cycle CF (dB)	0.17	**Included in Calculations of Corr'd PSD**

Output Power Results

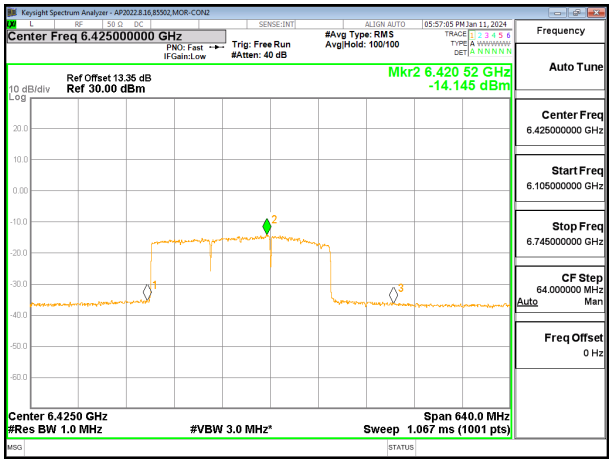
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	6.41	6.86	15.26	24.00	-8.74
High	6585	4.87	5.63	13.89	24.00	-10.11

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6425	-15.59	-14.15	-3.26	-1.00	-2.26
High	6585	-15.98	-15.09	-3.96	-1.00	-2.96



LOW CHANNEL CHAIN 0



LOW CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 3x996T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/11, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.61	8.37	24.00	-1.00
High	6585	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	6.14	6.55	14.97	24.00	-9.03
High	6585	4.47	5.25	13.50	24.00	-10.50

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 3x996T (NON-CONTIGUOUS 2) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/11, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.61	8.37	24.00	-1.00
High	6585	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	6.26	6.59	15.05	24.00	-8.95
High	6585	4.50	5.46	13.63	24.00	-10.37

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 3x996T+484T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.61	8.37	24.00	-1.00
High	6585	5.61	8.37	24.00	-1.00

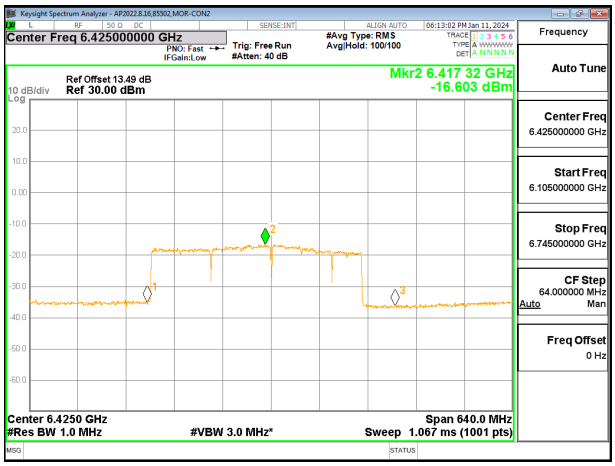
Duty Cycle CF (dB)	0.18	**Included in Calculations of Corr'd PSD**

Output Power Results

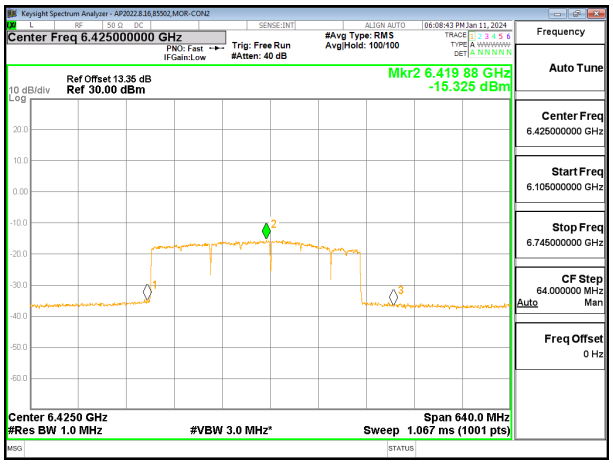
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	6.82	6.66	15.36	24.00	-8.64
High	6585	6.44	6.64	15.16	24.00	-8.84

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6425	-16.60	-15.33	-4.36	-1.00	-3.36
High	6585	-16.59	-15.98	-4.71	-1.00	-3.71



LOW CHANNEL CHAIN 0



LOW CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 3x996T+484T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.61	8.37	24.00	-1.00
High	6585	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	6.11	6.51	14.93	24.00	-9.07
High	6585	6.49	6.91	15.33	24.00	-8.67

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T+2x996T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.61	8.37	24.00	-1.00
High	6585	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	5.90	6.51	14.84	24.00	-9.16
High	6585	6.47	6.87	15.29	24.00	-8.71

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T+2x996T (NON-CONTIGUOUS 2) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.61	8.37	24.00	-1.00
High	6585	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	6.51	6.77	15.26	24.00	-8.74
High	6585	6.55	6.47	15.13	24.00	-8.87

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T+484T+996T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.61	8.37	24.00	-1.00
High	6585	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	6.96	6.45	15.33	24.00	-8.67
High	6585	6.61	6.89	15.37	24.00	-8.63

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T+484T+996T (NON-CONTIGUOUS 2) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.61	8.37	24.00	-1.00
High	6585	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	6.95	6.52	15.36	24.00	-8.64
High	6585	6.51	6.73	15.24	24.00	-8.76

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 4x996T – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.61	8.37	24.00	-1.00
High	6585	5.61	8.37	24.00	-1.00

Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	9.91	9.40	18.28	24.00	-5.72
High	6585	9.94	9.27	18.24	24.00	-5.76

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: SU – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6425	5.61	8.37	24.00	-1.00
High	6585	5.61	8.37	24.00	-1.00

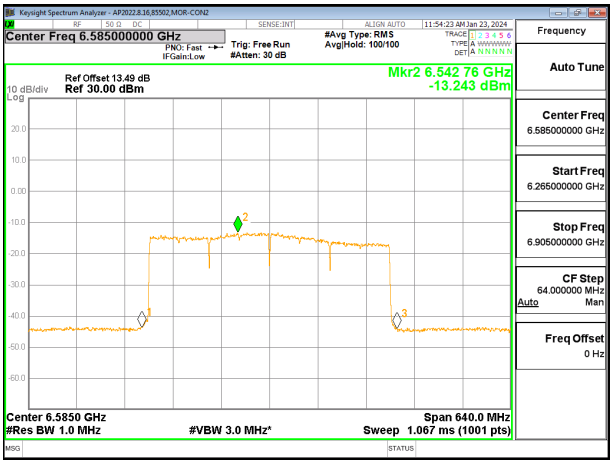
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

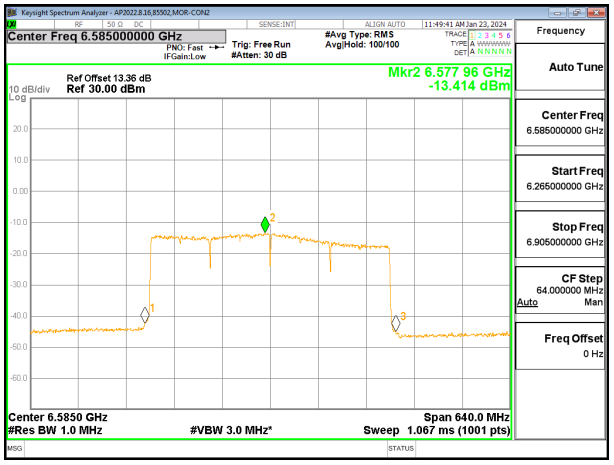
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6425	9.35	8.77	17.69	24.00	-6.31
High	6585	8.50	9.08	17.42	24.00	-6.58

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6425	-13.77	-13.08	-2.03	-1.00	-1.03
High	6585	-13.24	-13.41	-1.95	-1.00	-0.95



HIGH CHANNEL CHAIN 0



HIGH CHANNEL CHAIN 1

9.2.13. 802.11a MODE 2TX IN THE UNII-7 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/09

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	30.00	17.00
Mid	6695	5.61	8.37	30.00	17.00
High	6855	5.61	8.37	30.00	17.00

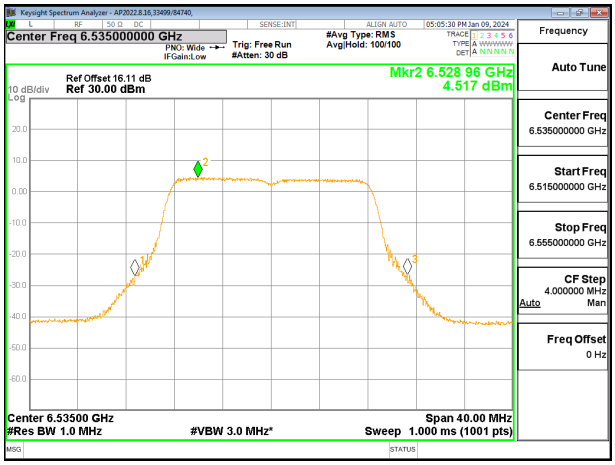
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

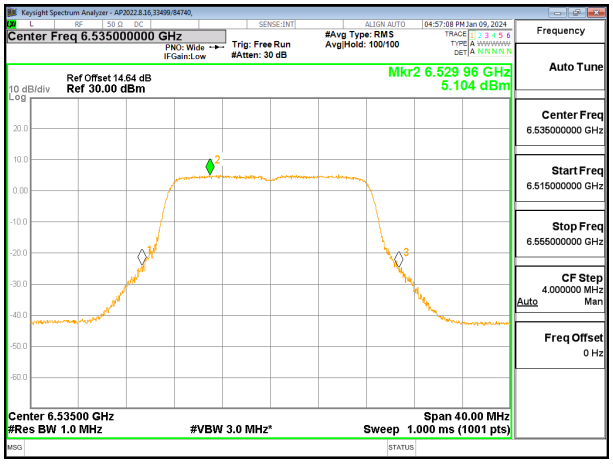
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	16.05	16.38	24.84	30.00	-5.16
Mid	6695	16.50	16.88	25.31	30.00	-4.69
High	6855	16.65	16.42	25.16	30.00	-4.84

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	4.52	5.10	16.20	17.00	-0.80
Mid	6695	4.67	4.60	16.02	17.00	-0.98
High	6855	4.52	4.74	16.01	17.00	-0.99



LOW CHANNEL CHAIN 0



LOW CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	24.00	-1.00
Mid	6695	5.61	8.37	24.00	-1.00
High	6855	5.61	8.37	24.00	-1.00

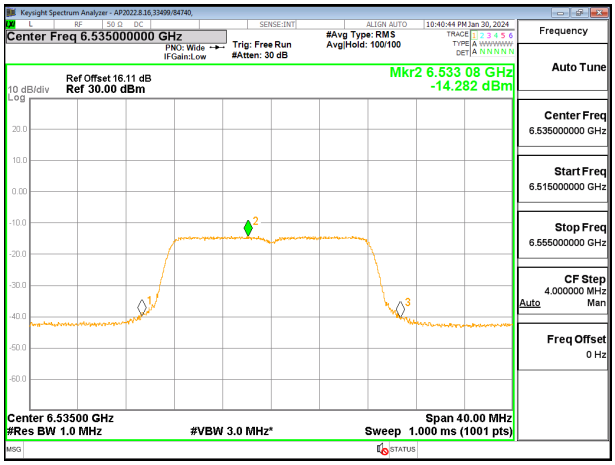
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

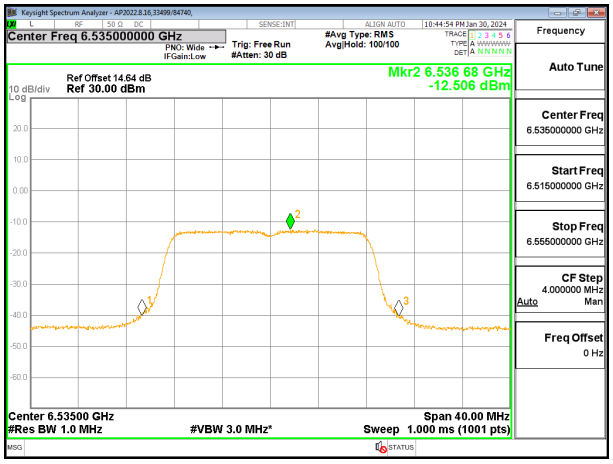
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	-3.69	-2.23	5.72	24.00	-18.28
Mid	6695	-2.24	-2.18	6.41	24.00	-17.59
High	6855	-1.66	-3.73	6.05	24.00	-17.95

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	-14.28	-12.51	-1.92	-1.00	-0.92
Mid	6695	-13.50	-13.31	-2.02	-1.00	-1.02
High	6855	-12.51	-14.46	-2.00	-1.00	-1.00



LOW CHANNEL CHAIN 0



LOW CHANNEL CHAIN 1

9.2.14. 802.11be EHT20 MODE 2TX IN THE UNII-7 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 26T – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/09

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	30.00	17.00
Mid	6695	5.61	8.37	30.00	17.00
High	6855	5.61	8.37	30.00	17.00

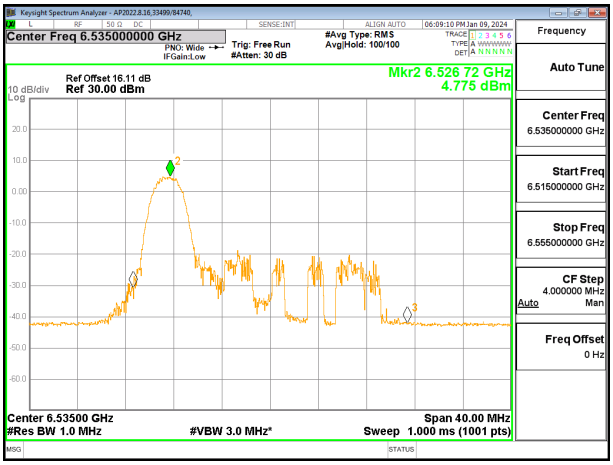
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	7.63	8.31	16.60	30.00	-13.40
Mid	6695	7.69	8.29	16.62	30.00	-13.38
High	6855	7.23	7.56	16.02	30.00	-13.98

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	4.78	4.89	16.22	17.00	-0.78
Mid	6695	4.02	4.94	15.89	17.00	-1.11
High	6855	4.69	4.71	16.08	17.00	-0.92



LOW CHANNEL CHAIN 0

LOW CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 26T – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	24.00	-1.00
Mid	6695	5.61	8.37	24.00	-1.00
High	6855	5.61	8.37	24.00	-1.00

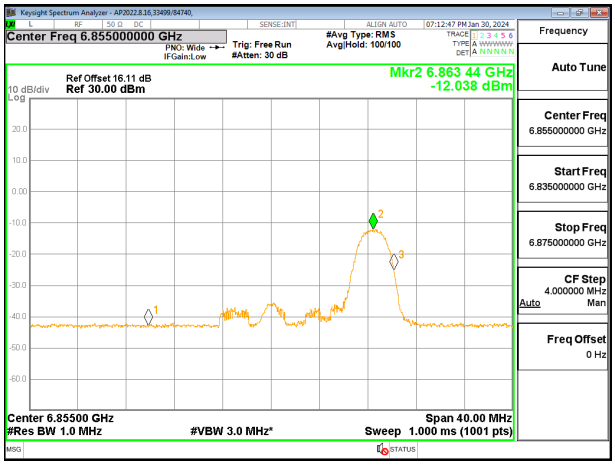
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

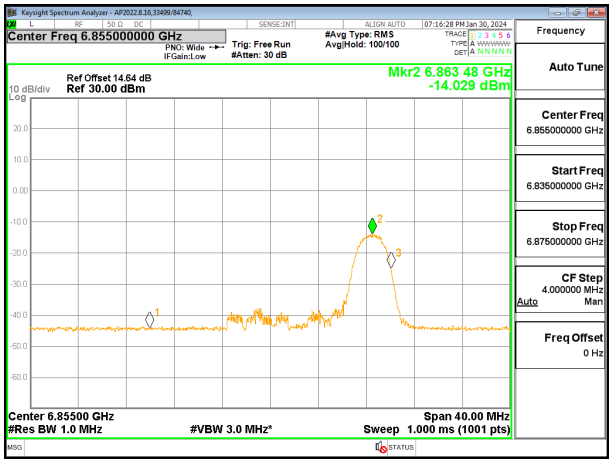
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	-11.59	-10.22	-2.23	24.00	-26.23
Mid	6695	-10.75	-10.54	-2.02	24.00	-26.02
High	6855	-9.62	-11.56	-1.86	24.00	-25.86

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	-13.77	-12.24	-1.56	-1.00	-0.56
Mid	6695	-13.73	-12.47	-1.67	-1.00	-0.67
High	6855	-12.04	-14.03	-1.54	-1.00	-0.54



HIGH CHANNEL CHAIN 0



HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 52T – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/09

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	30.00	17.00
Mid	6695	5.61	8.37	30.00	17.00
High	6855	5.61	8.37	30.00	17.00

Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

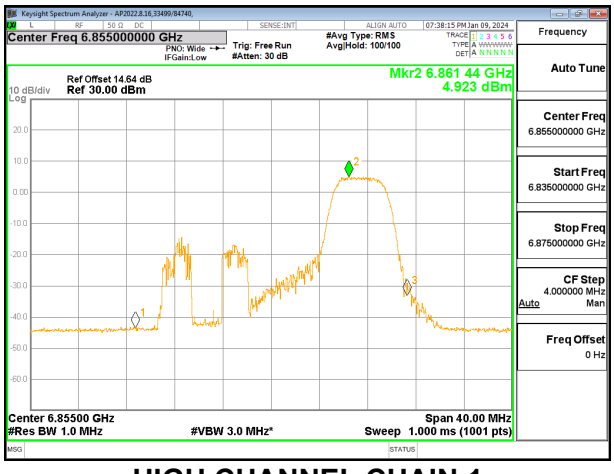
Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	9.84	10.39	18.74	30.00	-11.26
Mid	6695	10.47	10.79	19.25	30.00	-10.75
High	6855	10.68	10.52	19.22	30.00	-10.78

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	4.43	4.74	15.97	17.00	-1.03
Mid	6695	4.24	5.08	16.06	17.00	-0.94
High	6855	5.17	4.92	16.43	17.00	-0.57

HIGH CHANNEL CHAIN 0



HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 52T – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	24.00	-1.00
Mid	6695	5.61	8.37	24.00	-1.00
High	6855	5.61	8.37	24.00	-1.00

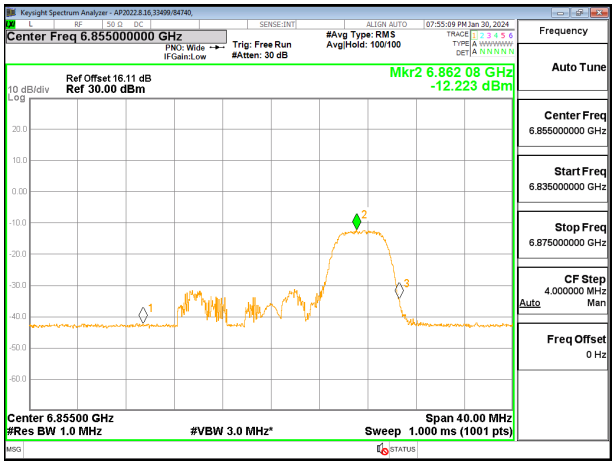
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

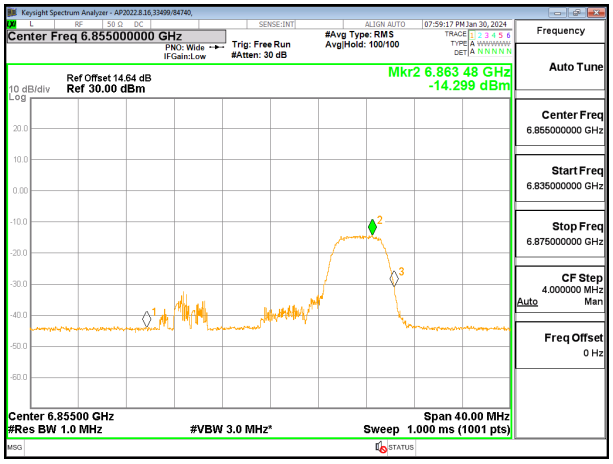
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	-9.08	-7.61	0.34	24.00	-23.66
Mid	6695	-7.74	-7.57	0.97	24.00	-23.03
High	6855	-7.20	-9.18	0.54	24.00	-23.46

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	-13.95	-12.64	-1.87	-1.00	-0.87
Mid	6695	-13.45	-13.15	-1.92	-1.00	-0.92
High	6855	-12.22	-14.30	-1.76	-1.00	-0.76



HIGH CHANNEL CHAIN 0



HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 52T+26T – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/09

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	30.00	17.00
Mid	6695	5.61	8.37	30.00	17.00
High	6855	5.61	8.37	30.00	17.00

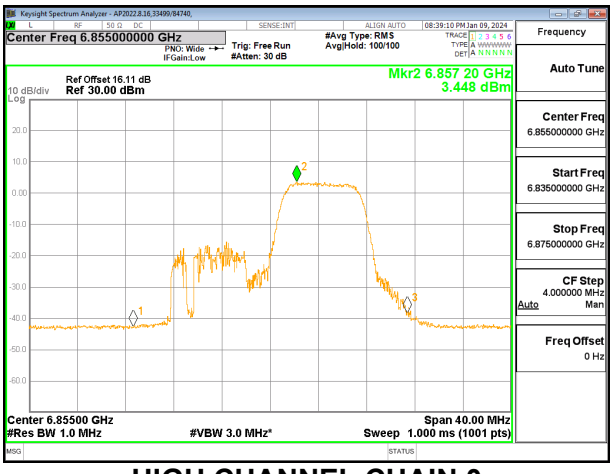
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

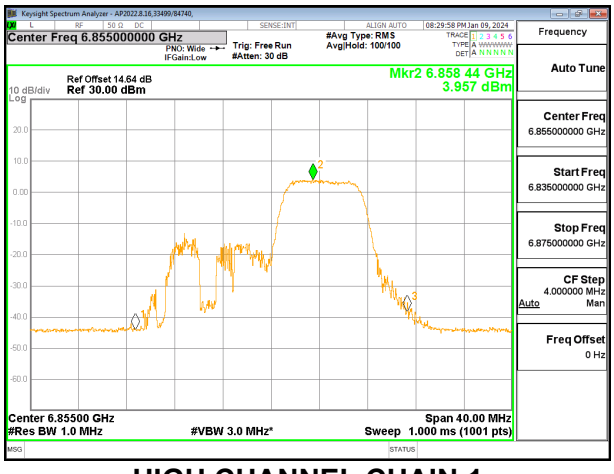
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	10.21	10.94	19.21	30.00	-10.79
Mid	6695	10.91	11.39	19.78	30.00	-10.22
High	6855	11.12	11.31	19.84	30.00	-10.16

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	2.81	3.91	14.77	17.00	-2.23
Mid	6695	3.22	3.99	15.01	17.00	-1.99
High	6855	3.45	3.96	15.09	17.00	-1.91



HIGH CHANNEL CHAIN 0



HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 52T+26T – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	24.00	-1.00
Mid	6695	5.61	8.37	24.00	-1.00
High	6855	5.61	8.37	24.00	-1.00

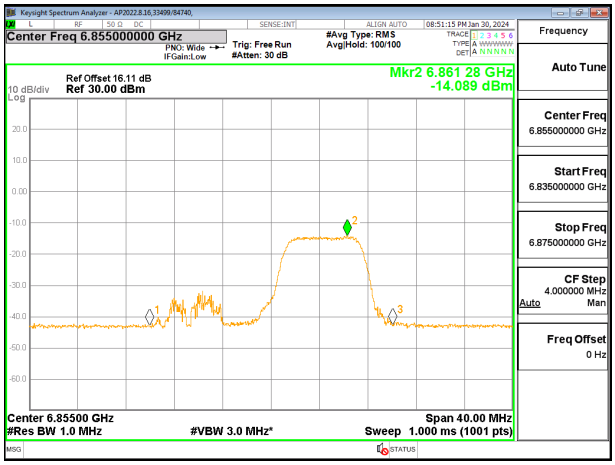
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

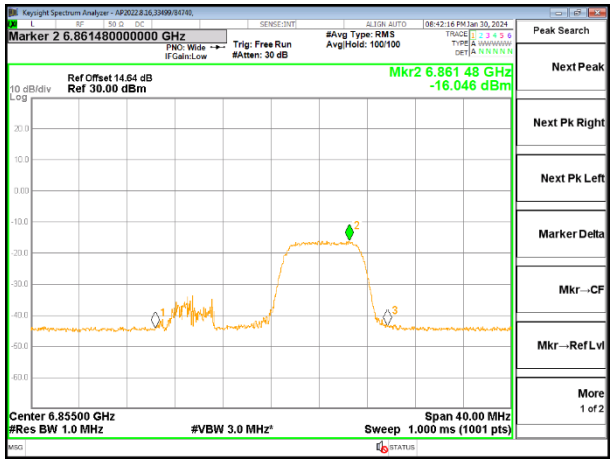
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	-9.34	-7.71	0.17	24.00	-23.83
Mid	6695	-7.84	-7.77	0.82	24.00	-23.18
High	6855	-7.38	-9.29	0.39	24.00	-23.61

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	-15.94	-14.70	-3.89	-1.00	-2.89
Mid	6695	-15.31	-14.89	-3.72	-1.00	-2.72
High	6855	-14.09	-16.05	-3.58	-1.00	-2.58



HIGH CHANNEL CHAIN 0



HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 106T – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/09

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	30.00	17.00
Mid	6695	5.61	8.37	30.00	17.00
High	6855	5.61	8.37	30.00	17.00

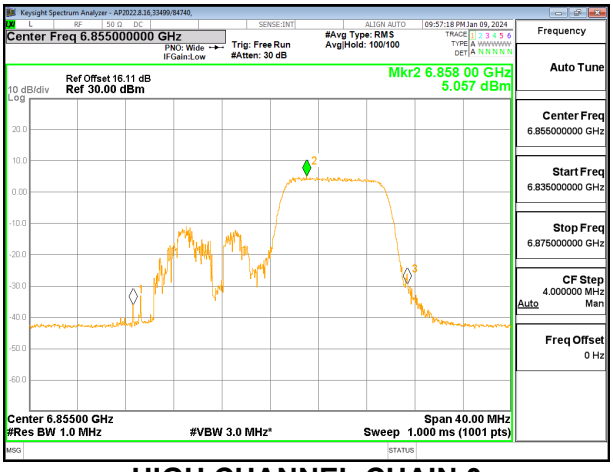
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

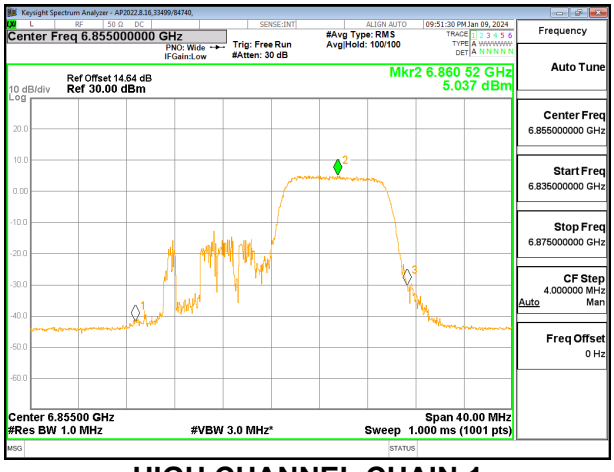
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	13.15	13.16	21.78	30.00	-8.22
Mid	6695	13.20	13.37	21.91	30.00	-8.09
High	6855	13.49	13.50	22.12	30.00	-7.88

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	4.68	5.22	16.34	17.00	-0.66
Mid	6695	4.51	4.97	16.13	17.00	-0.87
High	6855	5.06	5.04	16.43	17.00	-0.57



HIGH CHANNEL CHAIN 0



HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 106T – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	24.00	-1.00
Mid	6695	5.61	8.37	24.00	-1.00
High	6855	5.61	8.37	24.00	-1.00

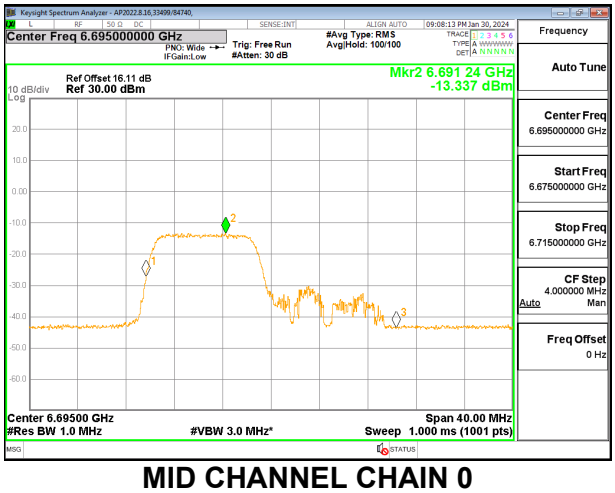
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD

Output Power Results

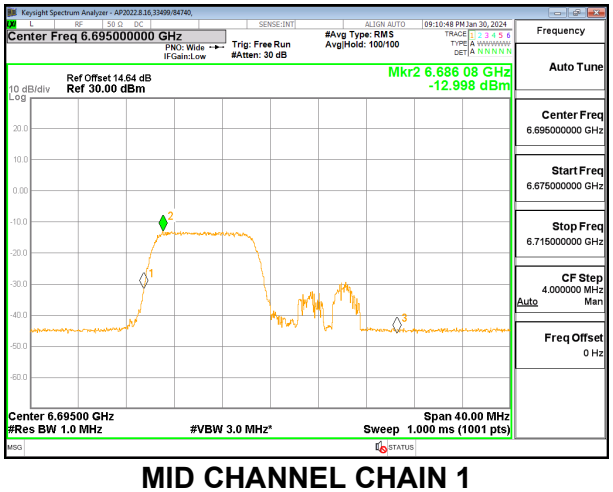
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	-6.35	-4.79	3.12	24.00	-20.88
Mid	6695	-4.87	-4.77	3.80	24.00	-20.20
High	6855	-4.44	-6.38	3.32	24.00	-20.68

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	-14.21	-12.67	-1.99	-1.00	-0.99
Mid	6695	-13.34	-13.00	-1.78	-1.00	-0.78
High	6855	-12.39	-14.45	-1.92	-1.00	-0.92



MID CHANNEL CHAIN 0



MID CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 106T+26T – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/09

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	30.00	17.00
Mid	6695	5.61	8.37	30.00	17.00
High	6855	5.61	8.37	30.00	17.00

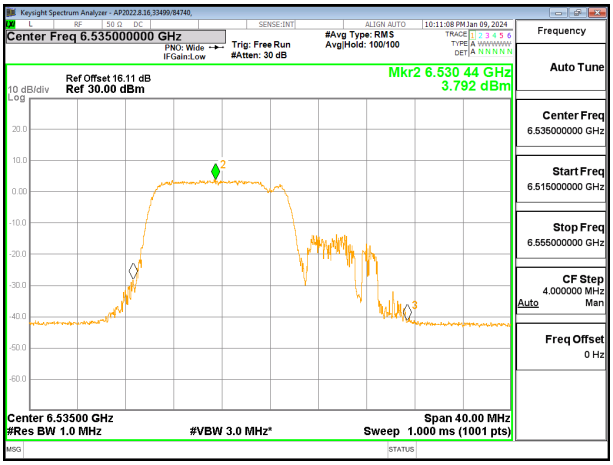
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	13.29	13.20	21.87	30.00	-8.13
Mid	6695	13.34	13.23	21.91	30.00	-8.09
High	6855	13.10	13.50	21.92	30.00	-8.08

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	3.79	4.40	15.49	17.00	-1.51
Mid	6695	3.48	4.42	15.36	17.00	-1.64
High	6855	4.48	4.25	15.74	17.00	-1.26



HIGH CHANNEL CHAIN 0

HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 106T+26T – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	24.00	-1.00
Mid	6695	5.61	8.37	24.00	-1.00
High	6855	5.61	8.37	24.00	-1.00

Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	-6.35	-4.81	3.11	24.00	-20.89
Mid	6695	-4.89	-4.79	3.78	24.00	-20.22
High	6855	-4.47	-6.39	3.30	24.00	-20.70

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	-14.88	-13.70	-2.87	-1.00	-1.87
Mid	6695	-14.43	-13.92	-2.79	-1.00	-1.79
High	6855	-13.44	-15.24	-2.87	-1.00	-1.87

MID CHANNEL CHAIN 0

MID CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 242T – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/09

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	30.00	17.00
Mid	6695	5.61	8.37	30.00	17.00
High	6855	5.61	8.37	30.00	17.00

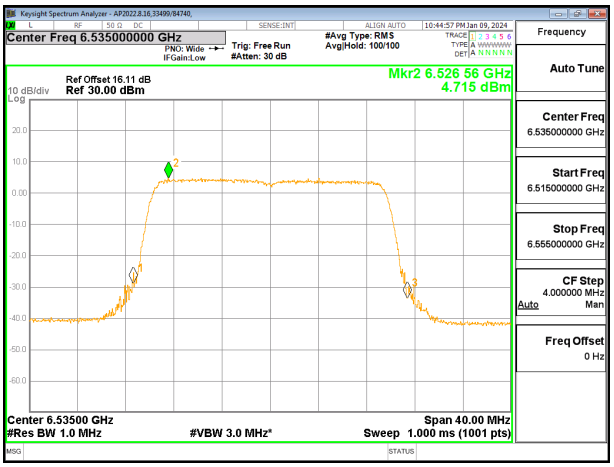
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

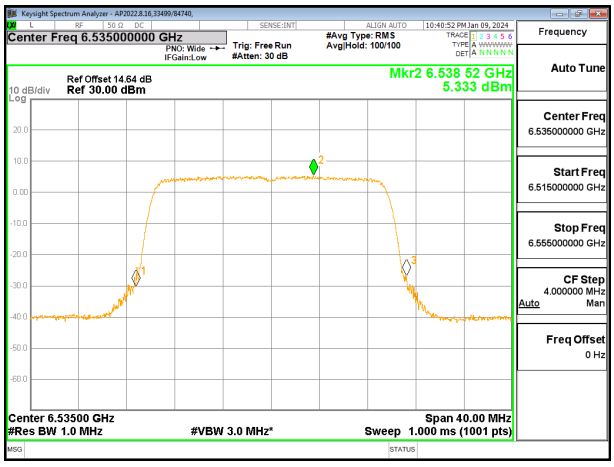
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	16.70	16.94	25.44	30.00	-4.56
Mid	6695	16.59	16.91	25.37	30.00	-4.63
High	6855	17.00	16.91	25.58	30.00	-4.42

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	4.72	5.33	16.42	17.00	-0.58
Mid	6695	4.60	5.07	16.22	17.00	-0.78
High	6855	5.12	4.90	16.39	17.00	-0.61



LOW CHANNEL CHAIN 0



LOW CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 242T – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6535	5.61	8.37	24.00	-1.00
Mid	6695	5.61	8.37	24.00	-1.00
High	6855	5.61	8.37	24.00	-1.00

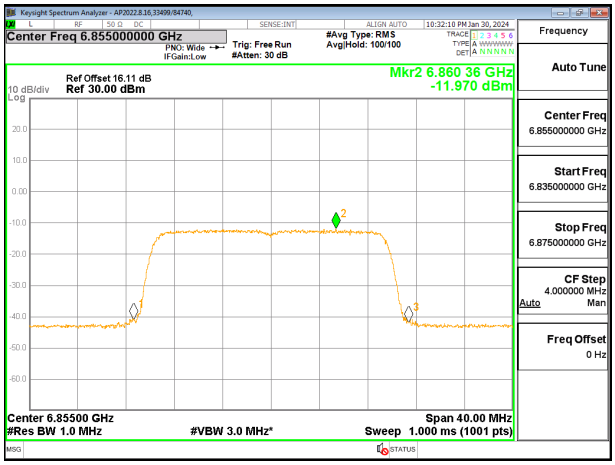
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

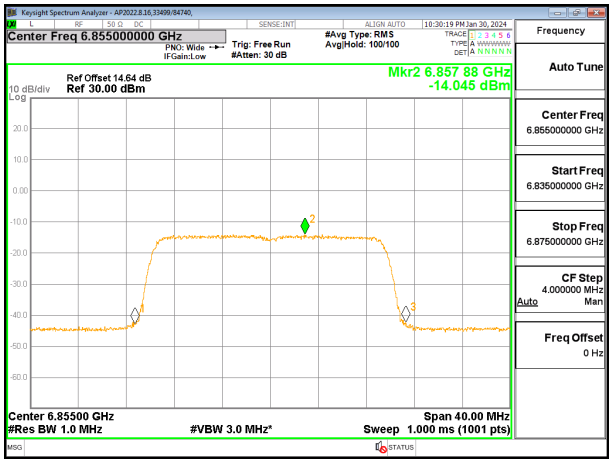
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6535	-2.36	-0.98	7.00	24.00	-17.00
Mid	6695	-1.95	-1.95	6.67	24.00	-17.33
High	6855	-0.48	-2.63	7.20	24.00	-16.80

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6535	-14.09	-12.38	-1.77	-1.00	-0.77
Mid	6695	-13.94	-12.24	-1.62	-1.00	-0.62
High	6855	-11.97	-14.05	-1.50	-1.00	-0.50



HIGH CHANNEL CHAIN 0



HIGH CHANNEL CHAIN 1

9.2.15. 802.11be EHT40 MODE 2TX IN THE UNII-7 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/10

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6565	5.61	8.37	30.00	17.00
Mid	6685	5.61	8.37	30.00	17.00
High	6845	5.61	8.37	30.00	17.00

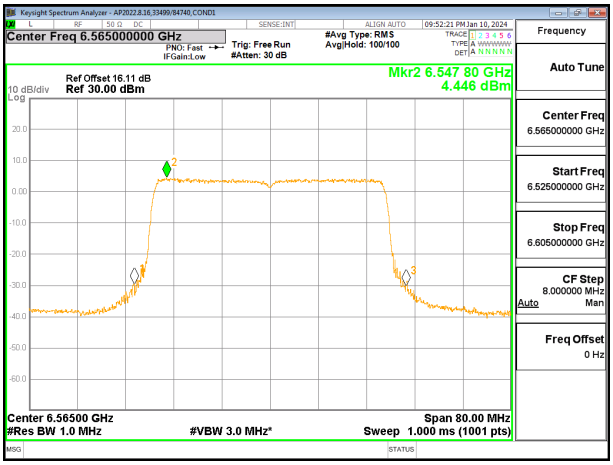
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

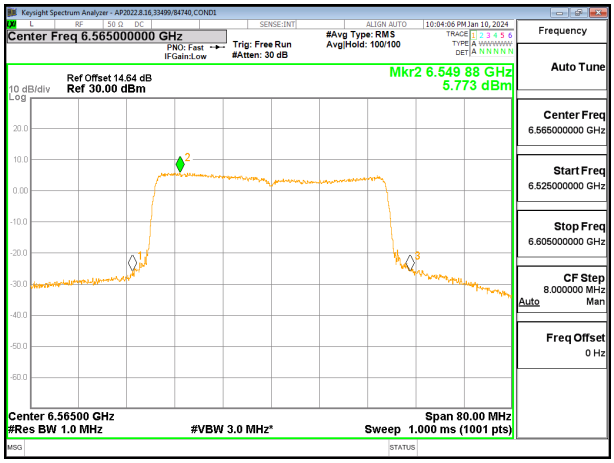
Channel	Frequency (MHz)	Chain 0 Meas. Power (dBm)	Chain 1 Meas. Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6565	17.44	17.87	26.28	30.00	-3.72
Mid	6685	17.27	17.63	26.07	30.00	-3.93
High	6845	17.16	17.48	25.94	30.00	-4.06

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas. PSD (dBm)	Chain 1 Meas. PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6565	4.45	5.77	16.54	17.00	-0.46
Mid	6685	3.56	4.15	15.25	17.00	-1.75
High	6845	3.31	4.00	15.05	17.00	-1.95



LOW CHANNEL CHAIN 0



LOW CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6565	5.61	8.37	24.00	-1.00
Mid	6685	5.61	8.37	24.00	-1.00
High	6845	5.61	8.37	24.00	-1.00

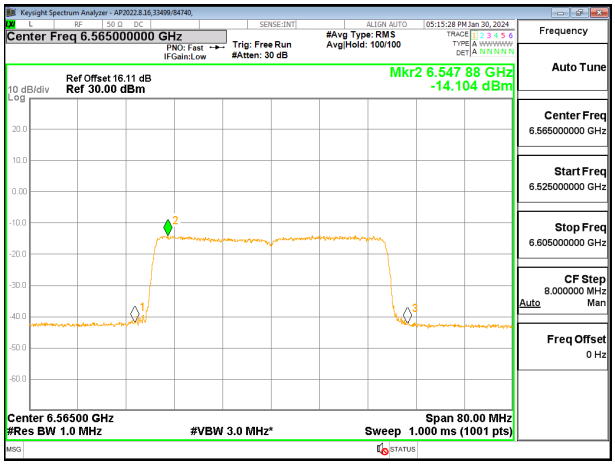
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

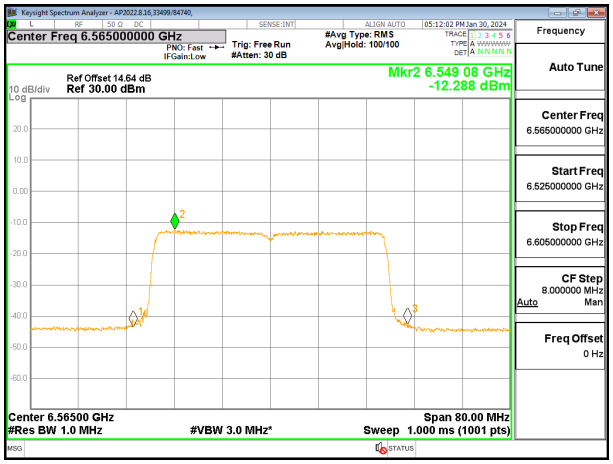
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6565	0.61	1.90	9.92	24.00	-14.08
Mid	6685	1.53	1.95	10.37	24.00	-13.63
High	6845	1.83	0.34	9.77	24.00	-14.23

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6565	-14.10	-12.29	-1.72	-1.00	-0.72
Mid	6685	-13.73	-12.80	-1.86	-1.00	-0.86
High	6845	-12.24	-14.35	-1.79	-1.00	-0.79



LOW CHANNEL CHAIN 0



LOW CHANNEL CHAIN 1

9.2.16. 802.11be EHT80 MODE 2TX IN THE UNII-7 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+242T (CONTIGUOUS) – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/10

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6625	5.61	8.37	30.00	17.00
Mid	6705	5.61	8.37	30.00	17.00
High	6785	5.61	8.37	30.00	17.00

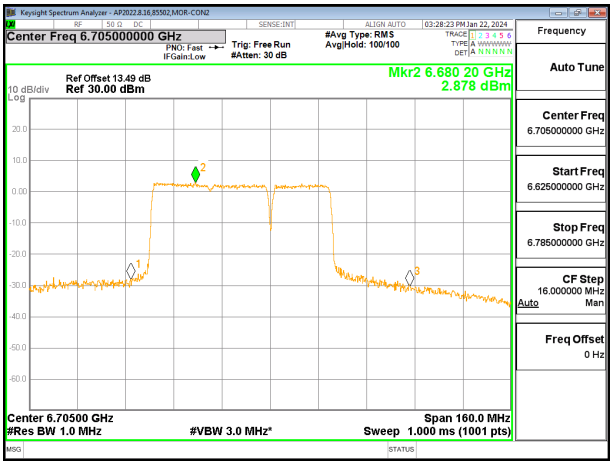
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**	

Output Power Results

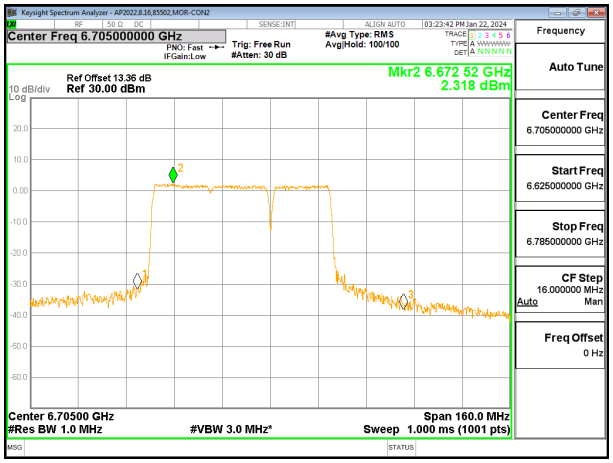
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6625	17.36	17.86	26.24	30.00	-3.76
Mid	6705	17.40	17.89	26.27	30.00	-3.73
High	6785	17.71	17.85	26.40	30.00	-3.60

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6625	0.10	0.27	11.57	17.00	-5.43
Mid	6705	2.88	2.32	13.99	17.00	-3.01
High	6785	1.87	2.34	13.49	17.00	-3.51



MID CHANNEL CHAIN 0



MID CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+242T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6625	5.61	8.37	24.00	-1.00
Mid	6705	5.61	8.37	24.00	-1.00
High	6785	5.61	8.37	24.00	-1.00

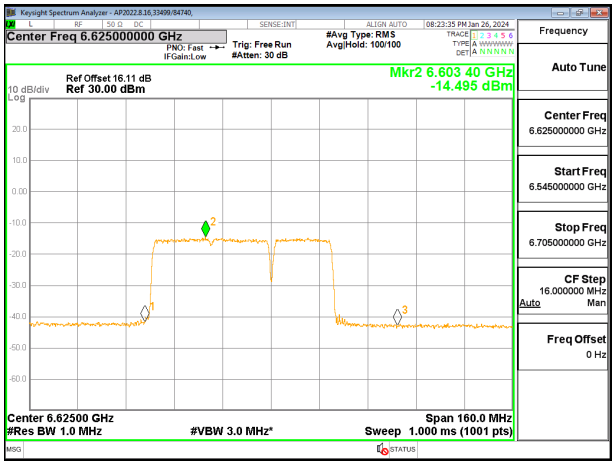
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

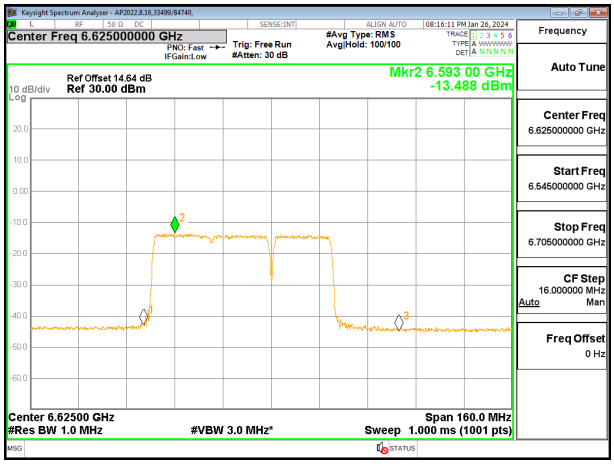
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6625	1.11	2.09	10.25	24.00	-13.75
Mid	6705	2.49	1.66	10.72	24.00	-13.28
High	6785	2.80	0.81	10.54	24.00	-13.46

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6625	-14.50	-13.49	-2.58	-1.00	-1.58
Mid	6705	-14.43	-14.72	-3.19	-1.00	-2.19
High	6785	-13.48	-15.15	-2.85	-1.00	-1.85



LOW CHANNEL CHAIN 0



LOW CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+242T (NON-CONTIGUOUS) – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/10

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6625	5.61	8.37	30.00	17.00
Mid	6705	5.61	8.37	30.00	17.00
High	6785	5.61	8.37	30.00	17.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6625	17.29	17.66	26.10	30.00	-3.90
Mid	6705	17.33	17.67	26.12	30.00	-3.88
High	6785	17.68	17.80	26.36	30.00	-3.64

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+242T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6625	5.61	8.37	24.00	-1.00
Mid	6705	5.61	8.37	24.00	-1.00
High	6785	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6625	0.89	1.78	9.98	24.00	-14.02
Mid	6705	2.23	1.41	10.46	24.00	-13.54
High	6785	2.79	0.81	10.53	24.00	-13.47

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 242T+484T (NON-CONTIGUOUS) – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/10

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6625	5.61	8.37	30.00	17.00
Mid	6705	5.61	8.37	30.00	17.00
High	6785	5.61	8.37	30.00	17.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6625	17.16	17.64	26.03	30.00	-3.97
Mid	6705	17.34	17.61	26.10	30.00	-3.90
High	6785	17.62	17.74	26.30	30.00	-3.70

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 242T+484T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6625	5.61	8.37	24.00	-1.00
Mid	6705	5.61	8.37	24.00	-1.00
High	6785	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6625	0.92	1.80	10.00	24.00	-14.00
Mid	6705	2.17	1.27	10.36	24.00	-13.64
High	6785	2.88	0.86	10.61	24.00	-13.39

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/10

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6625	5.61	8.37	30.00	17.00
Mid	6705	5.61	8.37	30.00	17.00
High	6785	5.61	8.37	30.00	17.00

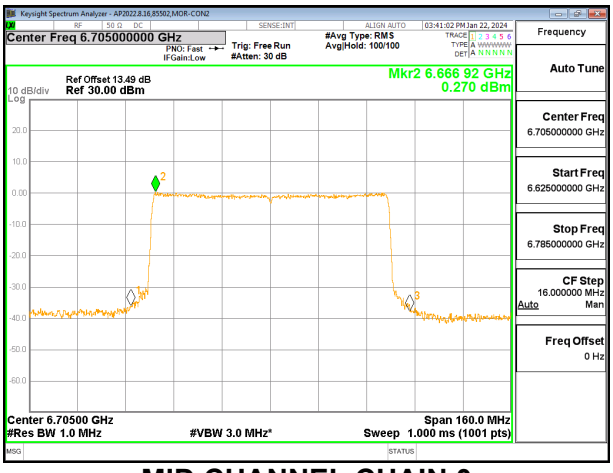
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

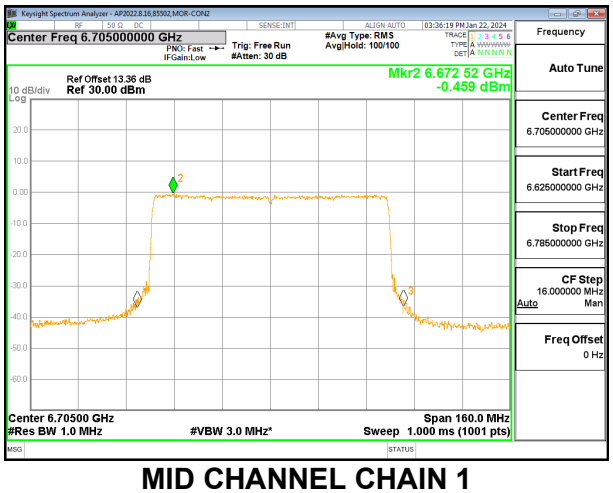
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6625	14.69	15.13	23.54	30.00	-6.46
Mid	6705	14.31	14.65	23.10	30.00	-6.90
High	6785	14.04	14.10	22.69	30.00	-7.31

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6625	-2.53	-2.83	8.71	17.00	-8.29
Mid	6705	0.27	-0.46	11.30	17.00	-5.70
High	6785	-0.58	-0.57	10.81	17.00	-6.19



MID CHANNEL CHAIN 0



MID CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6625	5.61	8.37	24.00	-1.00
Mid	6705	5.61	8.37	24.00	-1.00
High	6785	5.61	8.37	24.00	-1.00

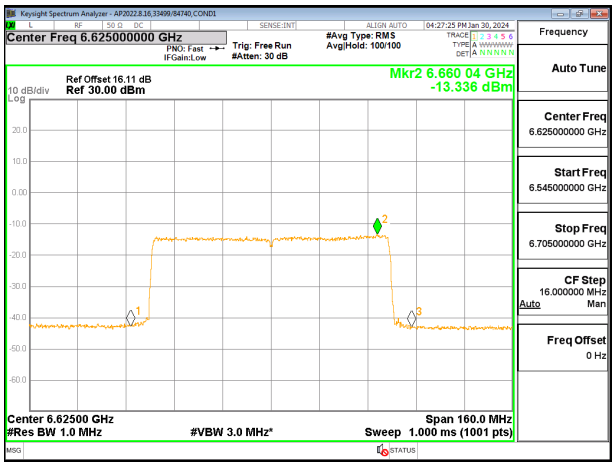
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

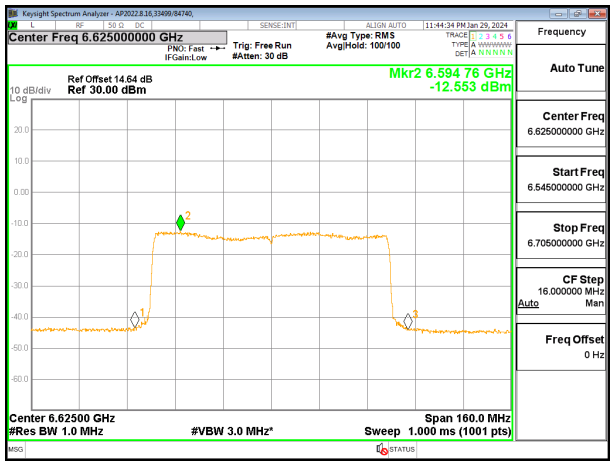
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6625	4.02	4.23	12.75	24.00	-11.25
Mid	6705	4.10	4.29	12.82	24.00	-11.18
High	6785	3.23	3.19	11.83	24.00	-12.17

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6625	-13.34	-12.55	-1.55	-1.00	-0.55
Mid	6705	-13.69	-12.84	-1.86	-1.00	-0.86
High	6785	-13.56	-12.99	-1.88	-1.00	-0.88



LOW CHANNEL CHAIN 0



LOW CHANNEL CHAIN 1

9.2.17. 802.11be EHT160 MODE 2TX IN THE UNII-7 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T (CONTIGUOUS) – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Mid	6665	5.61	8.37	30.00	17.00

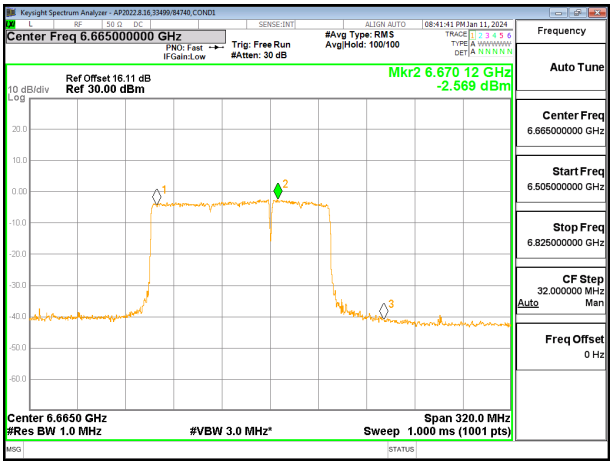
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

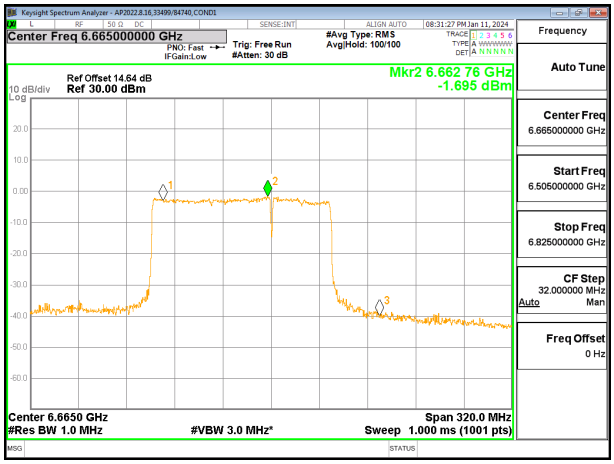
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6665	14.58	15.21	23.53	30.00	-6.47

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Mid	6665	-2.57	-1.70	9.27	17.00	-7.73



MID CHANNEL CHAIN 0



MID CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Mid	6665	5.61	8.37	24.00	-1.00

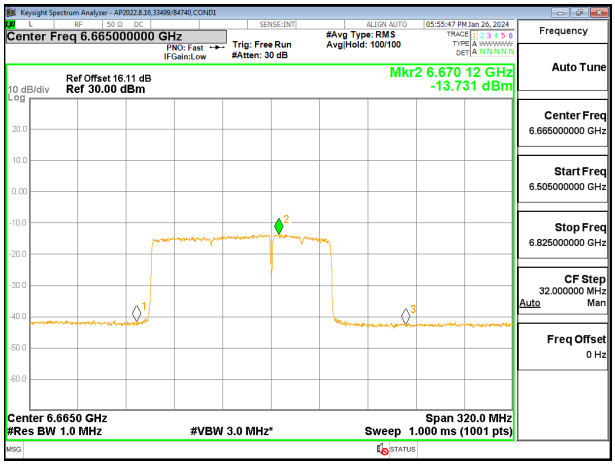
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

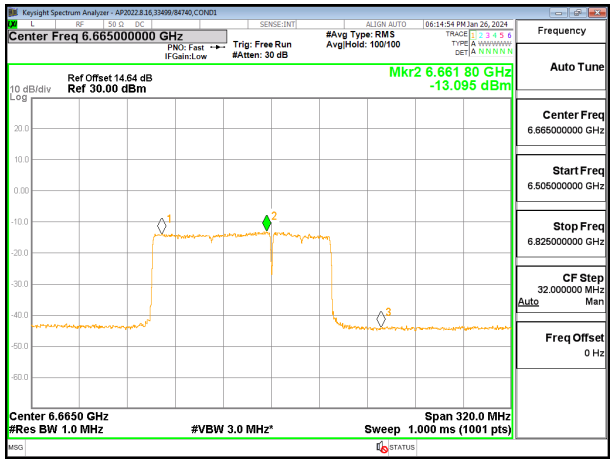
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6665	4.47	4.98	13.35	24.00	-10.65

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Mid	6665	-13.37	-13.10	-1.85	-1.00	-0.85



MID CHANNEL CHAIN 0



MID CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T (NON-CONTIGUOUS) – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Mid	6665	5.61	8.37	30.00	17.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6665	15.45	15.48	24.09	30.00	-5.91

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Mid	6665	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6665	4.27	4.65	13.08	24.00	-10.92

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+996T (NON-CONTIGUOUS) – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/11

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Mid	6665	5.61	8.37	30.00	17.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6665	15.36	15.37	23.99	30.00	-6.01

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+996T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Mid	6665	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6665	4.71	4.89	13.42	24.00	-10.58

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T – STANDARD POWER

Test Engineer:	33499/44389
Test Date:	2024/01/10

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Mid	6665	5.61	8.37	30.00	17.00

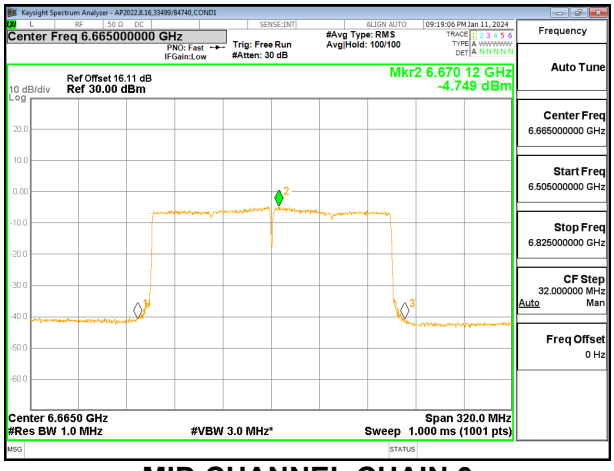
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

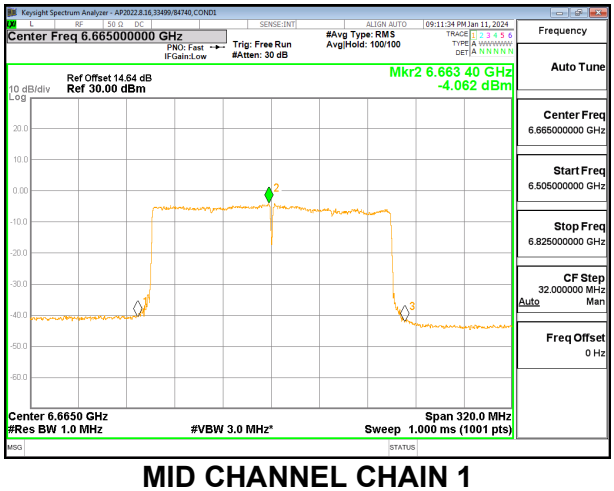
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6665	15.52	16.08	24.43	30.00	-5.57

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Mid	6665	-4.75	-4.06	6.99	17.00	-10.01



MID CHANNEL CHAIN 0



MID CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T – LOW POWER INDOOR

Test Engineer:	33499/44389
Test Date:	2024-01-30

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Mid	6665	5.61	8.37	24.00	-1.00

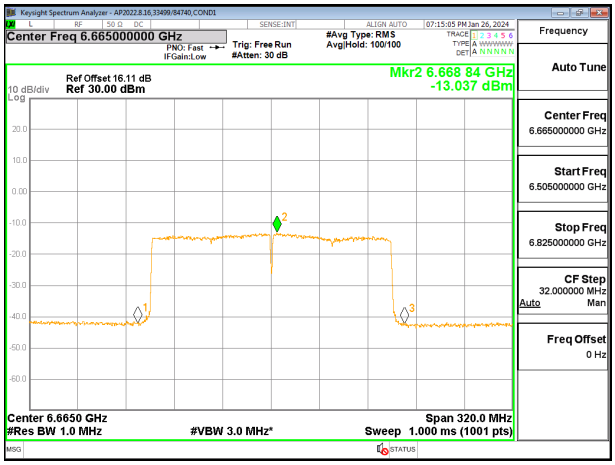
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

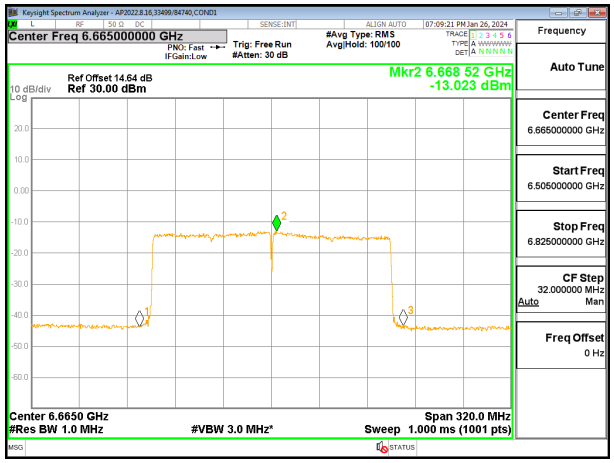
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Mid	6665	6.47	6.72	15.22	24.00	-8.78

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Mid	6665	-13.04	-13.02	-1.65	-1.00	-0.65



MID CHANNEL CHAIN 0



MID CHANNEL CHAIN 1

9.2.18. 802.11a MODE 2TX IN THE UNII-8 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE – LOW POWER INDOOR

Test Engineer:	33499/44389, 85502, 84740
Test Date:	2024/01/11-2024/01/12, 2024/02/19

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6875	5.61	8.37	24.00	-1.00
Mid	6995	4.21	6.65	24.00	-1.00
High	7115	4.21	6.65	24.00	-1.00

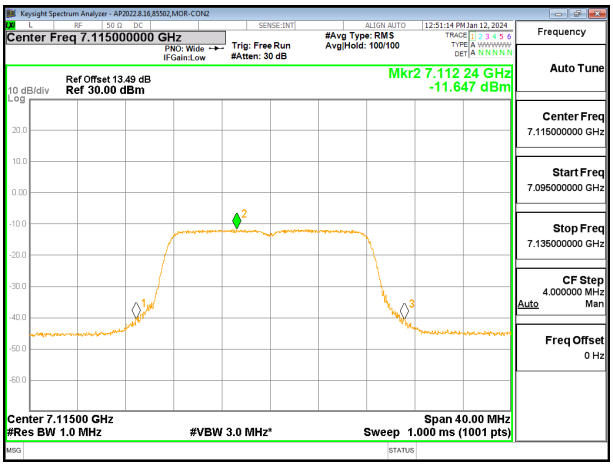
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

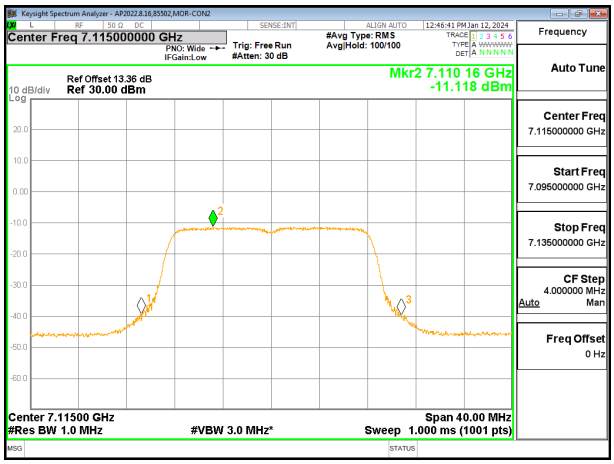
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6875	-1.21	-1.84	7.11	24.00	-16.89
Mid	6995	0.91	-0.47	7.49	24.00	-16.51
High	7115	0.39	-0.20	7.33	24.00	-16.67

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6875	-13.11	-15.08	-2.60	-1.00	-1.60
Mid	6995	-11.81	-11.69	-2.09	-1.00	-1.09
High	7115	-11.65	-11.12	-1.71	-1.00	-0.71



HIGH CHANNEL CHAIN 0



HIGH CHANNEL CHAIN 1

9.2.19. 802.11be EHT20 MODE 2TX IN THE UNII-8 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 26T – LOW POWER INDOOR

Test Engineer:	33499/44389, 85502, 84740
Test Date:	2024/01/11-2024/01/12, 2024/02/19

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6875	5.61	8.37	24.00	-1.00
Mid	6995	4.21	6.65	24.00	-1.00
High	7115	4.21	6.65	24.00	-1.00

Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

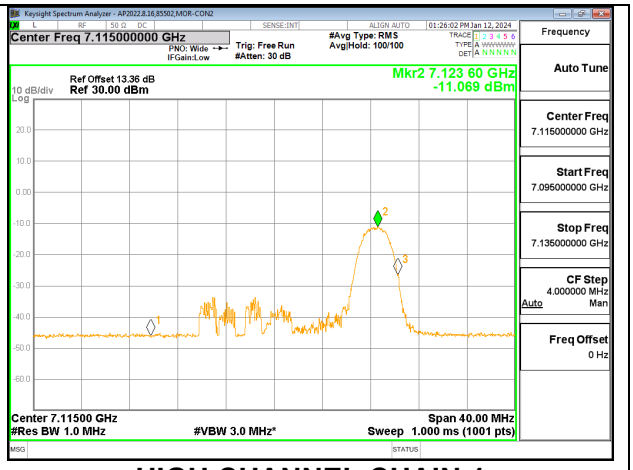
Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6875	-9.81	-10.92	-1.71	24.00	-25.71
Mid	6995	-7.86	-9.23	-1.27	24.00	-25.27
High	7115	-8.54	-8.93	-1.51	24.00	-25.51

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6875	-13.11	-15.06	-2.59	-1.00	-1.59
Mid	6995	-11.93	-11.60	-2.10	-1.00	-1.10
High	7115	-11.52	-11.07	-1.63	-1.00	-0.63

HIGH CHANNEL CHAIN 0



HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 52T – LOW POWER INDOOR

Test Engineer:	33499/44389, 85502, 84740
Test Date:	2024/01/11-2024/01/12, 2024/02/19

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6875	5.61	8.37	24.00	-1.00
Mid	6995	4.21	6.65	24.00	-1.00
High	7115	4.21	6.65	24.00	-1.00

Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6875	-7.45	-7.55	1.12	24.00	-22.88
Mid	6995	-5.56	-6.78	1.09	24.00	-22.91
High	7115	-6.02	-6.51	0.96	24.00	-23.04

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6875	-13.39	-15.28	-2.85	-1.00	-1.85
Mid	6995	-12.08	-11.80	-2.28	-1.00	-1.28
High	7115	-12.01	-11.73	-2.21	-1.00	-1.21

HIGH CHANNEL CHAIN 0

HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 52T+26T – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/17

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6875	5.61	8.37	24.00	-1.00
Mid	6995	4.21	6.65	24.00	-1.00
High	7115	4.21	6.65	24.00	-1.00

Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6875	-7.58	-7.60	1.03	24.00	-22.97
Mid	6995	-5.69	-6.88	0.98	24.00	-23.02
High	7115	-6.05	-6.57	0.92	24.00	-23.08

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6875	-13.25	-14.69	-2.53	-1.00	-1.53
Mid	6995	-13.44	-13.99	-4.04	-1.00	-3.04
High	7115	-13.73	-13.36	-3.88	-1.00	-2.88

LOW CHANNEL CHAIN 0

LOW CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 106T – LOW POWER INDOOR

Test Engineer:	85502, 84470
Test Date:	2024/01/17, 2024/02/19

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6875	5.61	8.37	24.00	-1.00
Mid	6995	4.21	6.65	24.00	-1.00
High	7115	4.21	6.65	24.00	-1.00

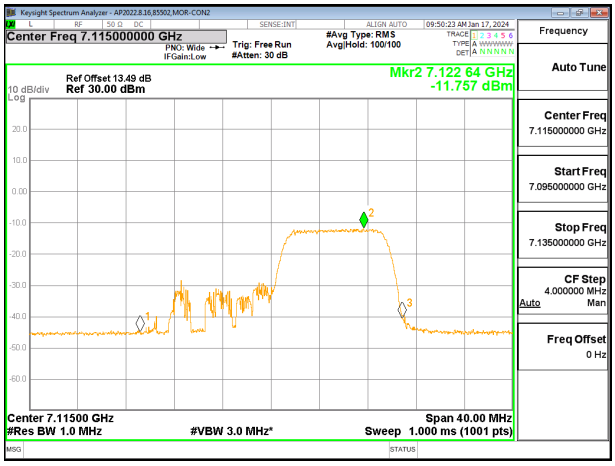
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

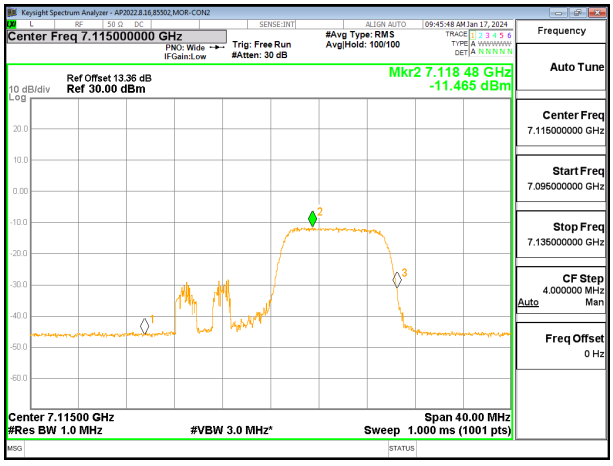
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6875	-4.58	-4.60	4.03	24.00	-19.97
Mid	6995	-2.63	-3.83	4.03	24.00	-19.97
High	7115	-3.05	-3.57	3.92	24.00	-20.08

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6875	-13.54	-15.34	-2.97	-1.00	-1.97
Mid	6995	-11.78	-11.84	-2.15	-1.00	-1.15
High	7115	-11.76	-11.47	-1.95	-1.00	-0.95



HIGH CHANNEL CHAIN 0



HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 106T+26T – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/17

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6875	5.61	8.37	24.00	-1.00
Mid	6995	4.21	6.65	24.00	-1.00
High	7115	4.21	6.65	24.00	-1.00

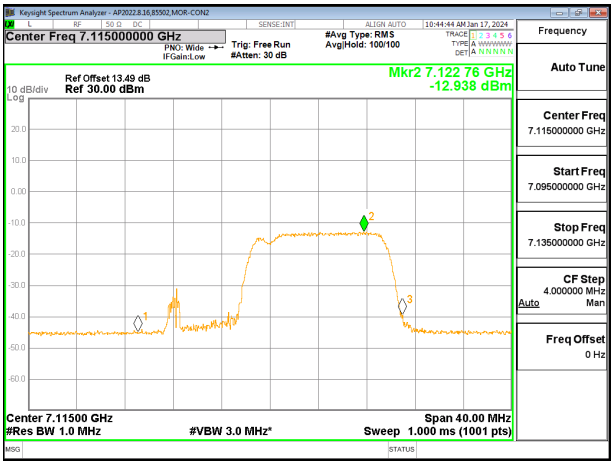
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6875	-4.60	-4.52	4.06	24.00	-19.94
Mid	6995	-2.60	-3.79	4.07	24.00	-19.93
High	7115	-2.93	-3.47	4.03	24.00	-19.97

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6875	-12.44	-13.81	-1.69	-1.00	-0.69
Mid	6995	-12.85	-12.88	-3.20	-1.00	-2.20
High	7115	-12.94	-13.35	-2.98	-1.00	-1.98



HIGH CHANNEL CHAIN 0

HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 242T – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/17, 2024/02/19

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6875	5.61	8.37	24.00	-1.00
Mid	6995	4.21	6.65	24.00	-1.00
High	7115	4.21	6.65	24.00	-1.00

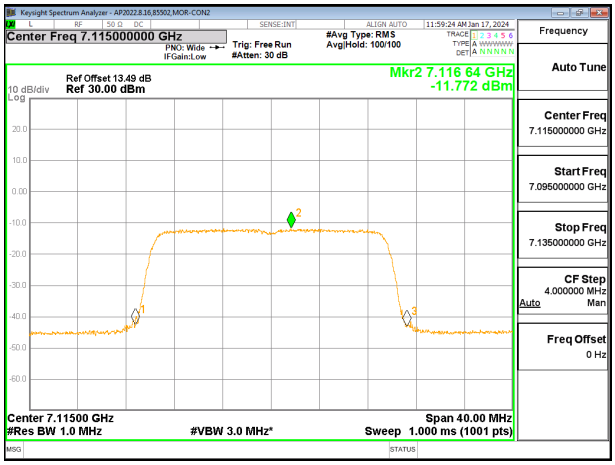
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD

Output Power Results

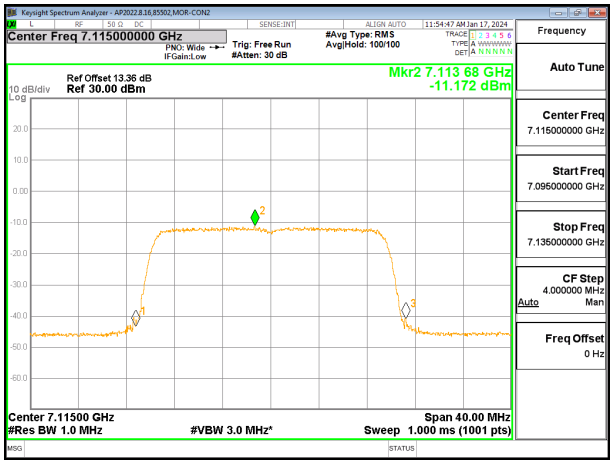
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6875	-1.59	-1.60	7.03	24.00	-16.97
Mid	6995	0.99	-0.40	7.57	24.00	-16.43
High	7115	0.36	-0.20	7.31	24.00	-16.69

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6875	-13.71	-15.69	-3.21	-1.00	-2.21
Mid	6995	-11.58	-12.11	-2.18	-1.00	-1.18
High	7115	-11.77	-11.17	-1.80	-1.00	-0.80



HIGH CHANNEL CHAIN 0



HIGH CHANNEL CHAIN 1

9.2.20. 802.11be EHT40 MODE 2TX IN THE UNII-8 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/17, 2024/02/19

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	e.i.r.p. Power Limit (dBm)	PSD Limit (dBm)
Low	6885	5.61	8.37	24.00	-1.00
Mid	7005	4.21	6.65	24.00	-1.00
High	7085	4.21	6.65	24.00	-1.00

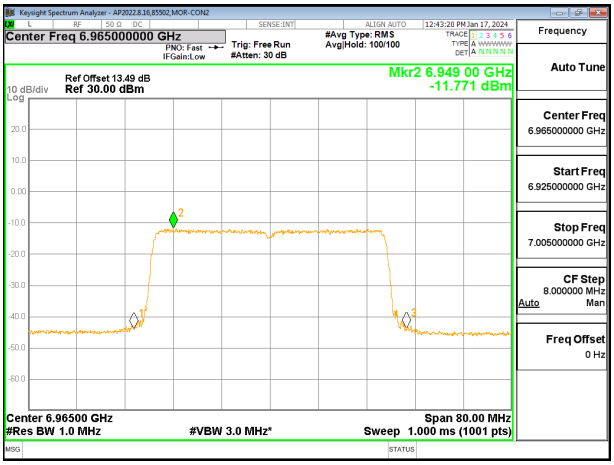
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

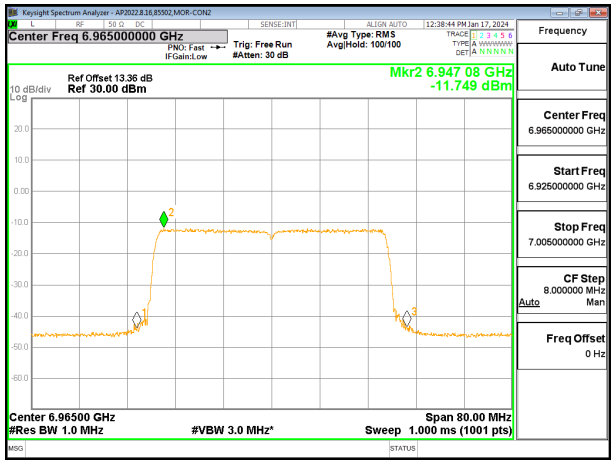
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6885	1.57	-0.91	9.12	24.00	-14.88
Mid	7005	4.04	2.43	10.53	24.00	-13.47
High	7085	3.81	2.93	10.61	24.00	-13.39

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6885	-13.87	-15.47	-3.22	-1.00	-2.22
Mid	6965	-11.77	-11.75	-2.10	-1.00	-1.10
High	7085	-11.87	-12.18	-2.36	-1.00	-1.36



MID CHANNEL CHAIN 0



MID CHANNEL CHAIN 1

9.2.21. 802.11be EHT80 MODE 2TX IN THE UNII-8 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+242T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/17, 2024/02/19

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6865	5.61	8.37	24.00	-1.00
Mid	6945	4.21	6.65	24.00	-1.00
High	7025	4.21	6.65	24.00	-1.00

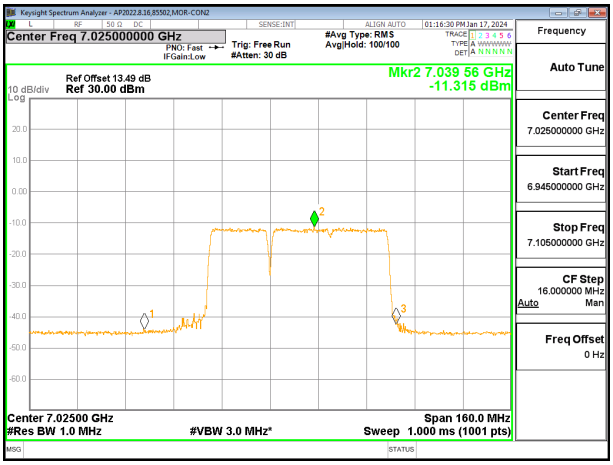
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

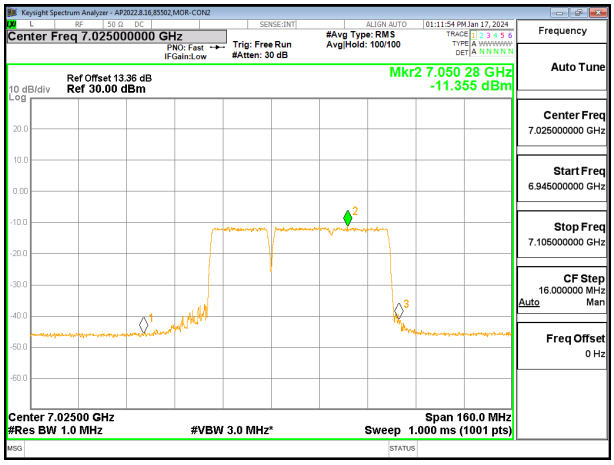
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6865	3.33	0.94	10.92	24.00	-13.08
Mid	6945	6.20	4.51	12.66	24.00	-11.34
High	7025	5.90	4.82	12.61	24.00	-11.39

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6865	-13.62	-15.87	-3.22	-1.00	-2.22
Mid	6945	-11.61	-11.18	-1.73	-1.00	-0.73
High	7025	-11.32	-11.36	-1.67	-1.00	-0.67



HIGH CHANNEL CHAIN 0



HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+242T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/17

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6865	5.61	8.37	24.00	-1.00
Mid	6945	4.21	6.65	24.00	-1.00
High	7025	4.21	6.65	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6865	4.90	4.89	13.52	24.00	-10.48
Mid	6945	4.71	4.38	11.77	24.00	-12.23
High	7025	4.86	4.33	11.82	24.00	-12.18

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 242T+484T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/17

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6865	5.61	8.37	24.00	-1.00
Mid	6945	4.21	6.65	24.00	-1.00
High	7025	4.21	6.65	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6865	4.80	4.82	13.43	24.00	-10.57
Mid	6945	4.77	4.34	11.78	24.00	-12.22
High	7025	4.91	4.33	11.85	24.00	-12.15

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/17

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6865	5.61	8.37	24.00	-1.00
Mid	6945	4.21	6.65	24.00	-1.00
High	7025	4.21	6.65	24.00	-1.00

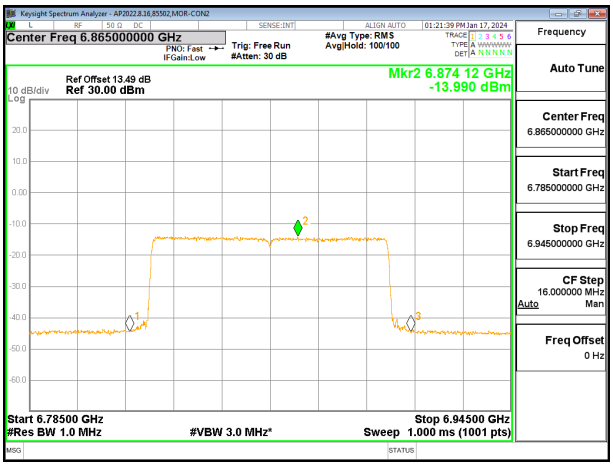
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

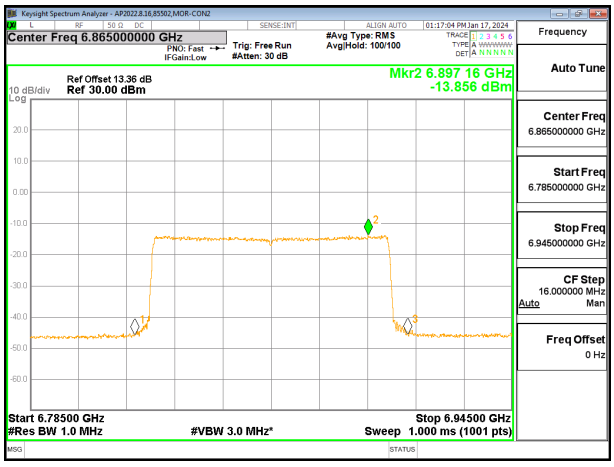
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6865	3.00	2.73	11.49	24.00	-12.51
Mid	6945	7.38	6.65	14.25	24.00	-9.75
High	7025	6.95	6.04	13.74	24.00	-10.26

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6865	-13.99	-13.86	-2.54	-1.00	-1.54
Mid	6945	-14.02	-14.04	-4.37	-1.00	-3.37
High	7025	-14.21	-14.02	-4.45	-1.00	-3.45



LOW CHANNEL CHAIN 0



LOW CHANNEL CHAIN 1

9.2.22. 802.11be EHT160 MODE 2TX IN THE UNII-8 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/17, 2024/02/19

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIPR Power Limit (dBm)	PSD Limit (dBm)
Low	6825	5.61	8.37	24.00	-1.00
High	6985	4.21	6.65	24.00	-1.00

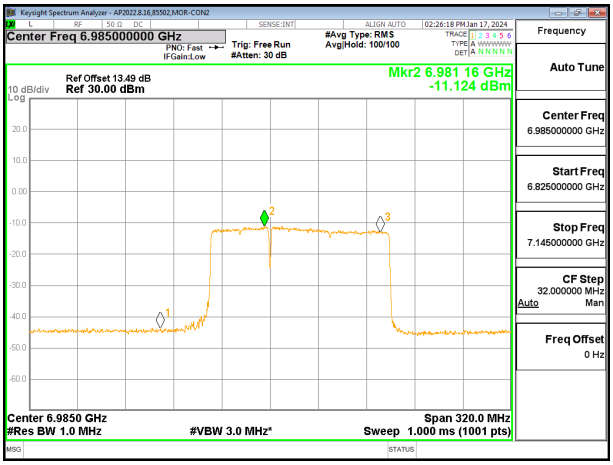
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

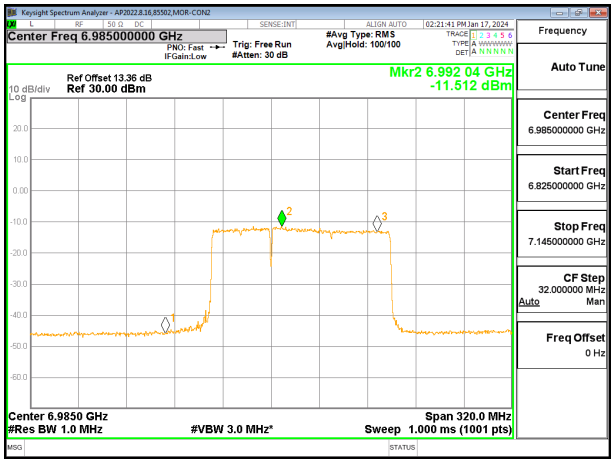
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6825	4.96	4.70	13.45	24.00	-10.55
High	6985	7.07	7.04	14.28	24.00	-9.72

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6825	-13.09	-13.05	-1.69	-1.00	-0.69
High	6985	-11.12	-11.51	-1.65	-1.00	-0.65



HIGH CHANNEL CHAIN 0



HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/17

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIPR Power Limit (dBm)	PSD Limit (dBm)
Low	6825	5.61	8.37	24.00	-1.00
High	6985	4.21	6.65	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6825	4.87	4.49	13.30	24.00	-10.70
High	6985	7.47	6.94	14.43	24.00	-9.57

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+996T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/17

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIPR Power Limit (dBm)	PSD Limit (dBm)
Low	6825	5.61	8.37	24.00	-1.00
High	6985	4.21	6.65	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6825	4.56	4.22	13.01	24.00	-10.99
High	6985	7.36	6.80	14.31	24.00	-9.69

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T – LOW POWER INDOOR

Test Engineer:	85502
Test Date:	2024/01/17

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIPR Power Limit (dBm)	PSD Limit (dBm)
Low	6825	5.61	8.37	24.00	-1.00
High	6985	4.21	6.65	24.00	-1.00

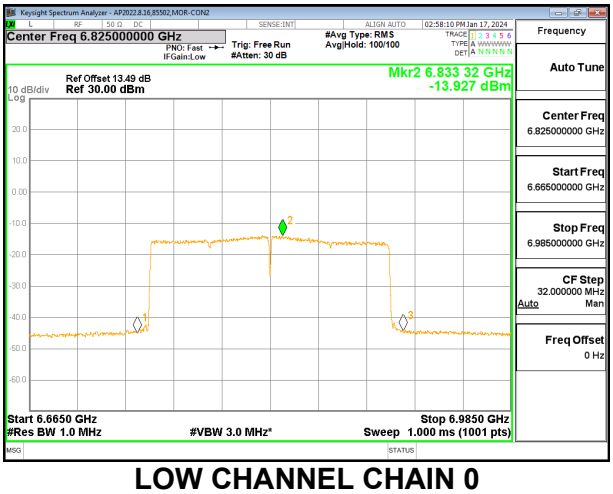
Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

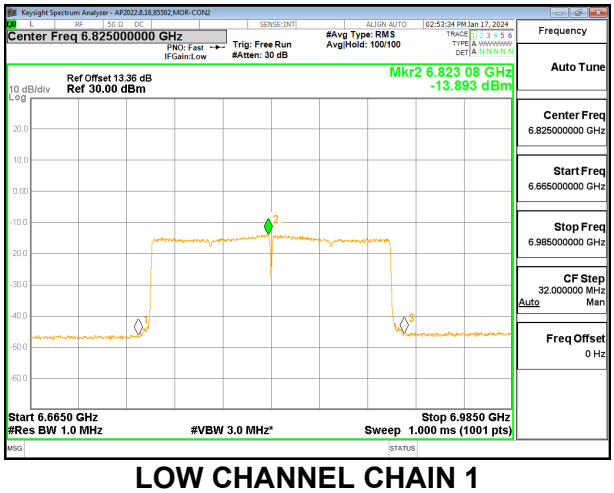
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6825	6.74	6.09	15.05	24.00	-8.95
High	6985	7.92	7.56	14.96	24.00	-9.04

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6825	-13.93	-13.89	-2.53	-1.00	-1.53
High	6985	-13.45	-13.71	-3.92	-1.00	-2.92



LOW CHANNEL CHAIN 0



LOW CHANNEL CHAIN 1

9.2.23. 802.11be EHT320 MODE 2TX IN THE UNII-8 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T+484T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	84740
Test Date:	2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.61	8.37	24.00	-1.00
High	6905	5.61	8.37	24.00	-1.00

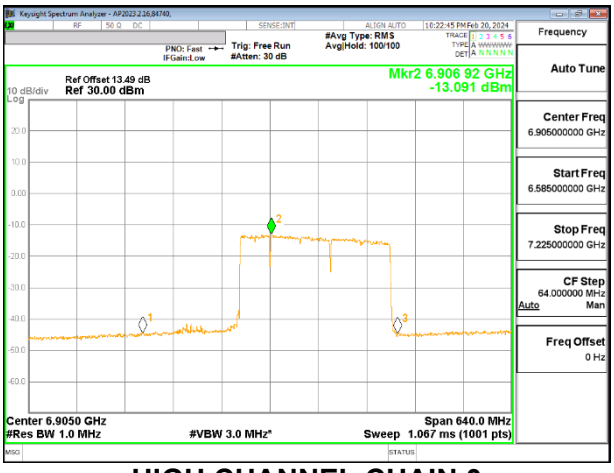
Duty Cycle CF (dB)	0.14	**Included in Calculations of Corr'd PSD**

Output Power Results

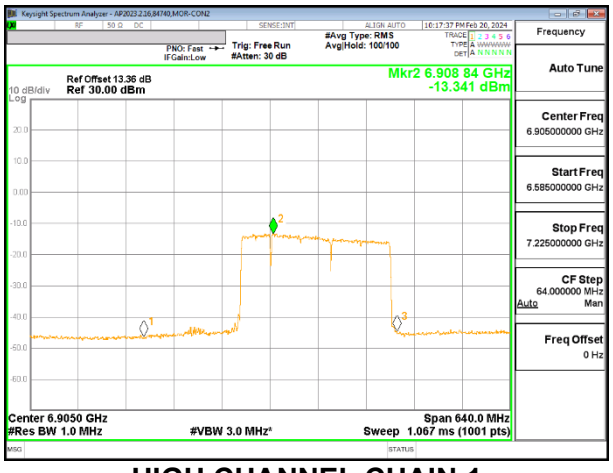
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	6.33	6.76	15.17	24.00	-8.83
High	6905	8.02	7.60	16.44	24.00	-7.56

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6745	-14.69	-13.64	-2.61	-1.00	-1.61
High	6905	-13.09	-13.34	-1.69	-1.00	-0.69



HIGH CHANNEL CHAIN 0



HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T+484T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.61	8.37	24.00	-1.00
High	6905	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	6.28	6.67	15.10	24.00	-8.90
High	6905	8.49	7.61	16.69	24.00	-7.31

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T+996T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.61	8.37	24.00	-1.00
High	6905	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	6.12	6.27	14.82	24.00	-9.18
High	6905	8.44	8.17	16.93	24.00	-7.07

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T+996T (NON-CONTIGUOUS 2) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.61	8.37	24.00	-1.00
High	6905	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	6.13	6.35	14.86	24.00	-9.14
High	6905	8.47	8.19	16.95	24.00	-7.05

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 3x996T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.61	8.37	24.00	-1.00
High	6905	5.61	8.37	24.00	-1.00

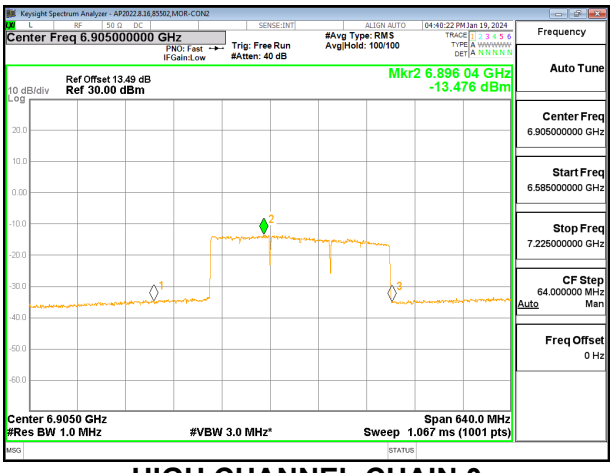
Duty Cycle CF (dB)	0.17	**Included in Calculations of Corr'd PSD**

Output Power Results

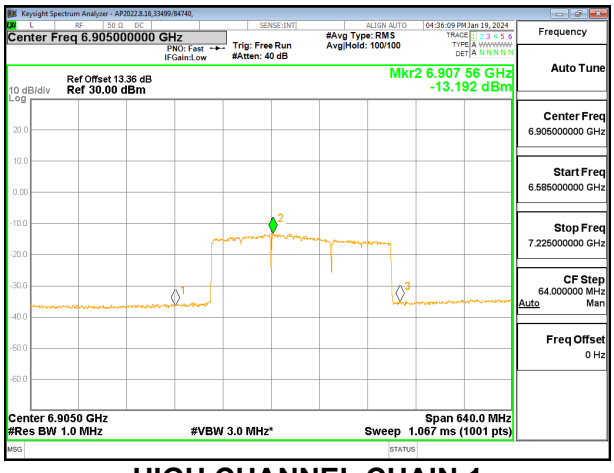
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	5.23	5.59	14.03	24.00	-9.97
High	6905	8.32	8.22	16.89	24.00	-7.11

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6745	-13.95	-13.94	-2.40	-1.00	-1.40
High	6905	-13.48	-13.19	-1.78	-1.00	-0.78



HIGH CHANNEL CHAIN 0



HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 3x996T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.61	8.37	24.00	-1.00
High	6905	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	4.75	4.65	13.32	24.00	-10.68
High	6905	8.37	8.07	16.84	24.00	-7.16

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 3x996T (NON-CONTIGUOUS 2) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.61	8.37	24.00	-1.00
High	6905	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	4.84	5.16	13.62	24.00	-10.38
High	6905	8.14	7.82	16.60	24.00	-7.40

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 3x996T+484T (CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.61	8.37	24.00	-1.00
High	6905	5.61	8.37	24.00	-1.00

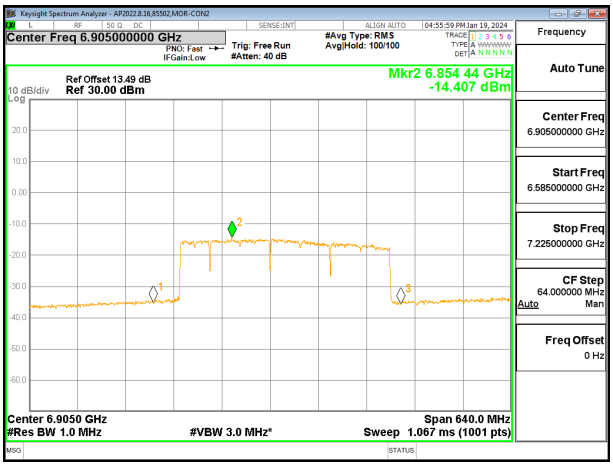
Duty Cycle CF (dB)	0.18	**Included in Calculations of Corr'd PSD**

Output Power Results

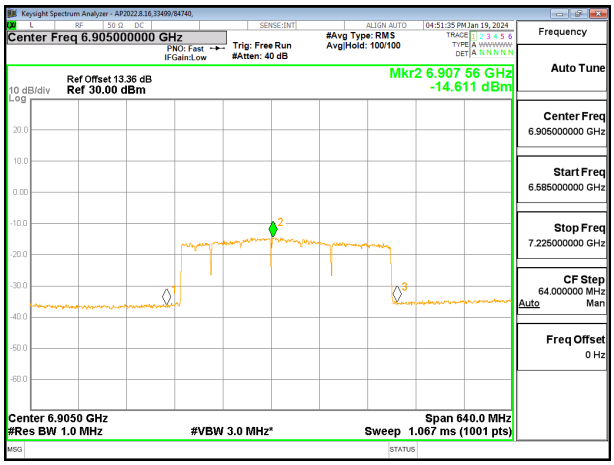
Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	4.42	4.63	13.15	24.00	-10.85
High	6905	7.99	8.23	16.73	24.00	-7.27

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm)	Chain 1 Meas PSD (dBm)	Total Corr'd EIRP PSD (dBm)	PSD Limit (dBm)	PSD Margin (dB)
Low	6745	-15.23	-14.64	-3.36	-1.00	-2.36
High	6905	-14.41	-14.61	-2.95	-1.00	-1.95



HIGH CHANNEL CHAIN 0



HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 3x996T+484T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.61	8.37	24.00	-1.00
High	6905	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	4.46	4.56	13.13	24.00	-10.87
High	6905	7.94	8.42	16.81	24.00	-7.19

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T+2x996T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.61	8.37	24.00	-1.00
High	6905	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	4.31	4.36	12.96	24.00	-11.04
High	6905	8.06	8.38	16.84	24.00	-7.16

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T+2x996T (NON-CONTIGUOUS 2) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.61	8.37	24.00	-1.00
High	6905	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	4.25	4.19	12.84	24.00	-11.16
High	6905	8.13	8.47	16.92	24.00	-7.08

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T+484T+996T (NON-CONTIGUOUS) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.61	8.37	24.00	-1.00
High	6905	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	4.35	4.44	13.02	24.00	-10.98
High	6905	8.08	8.41	16.87	24.00	-7.13

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 2x996T+484T+996T (NON-CONTIGUOUS 2) – LOW POWER INDOOR

Test Engineer:	85502, 84740
Test Date:	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.61	8.37	24.00	-1.00
High	6905	5.61	8.37	24.00	-1.00

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	4.26	4.40	12.95	24.00	-11.05
High	6905	8.18	8.44	16.93	24.00	-7.07

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 4x996T – LOW POWER INDOOR

85502, 84740	85502, 84740
2024/01/19, 2024/02/20	2024/01/19, 2024/02/20

Bandwidth, Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	PSD Limit (dBm)
Low	6745	5.61	8.37	24.00	-1.00
High	6905	5.61	8.37	24.00	-1.00

Duty Cycle CF (dB)	0.00	**Included in Calculations of Corr'd PSD**

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd EIRP (dBm)	Power Limit EIRP (dBm)	Power Margin (dB)
Low	6745	7.51	7.43	16.09	24.00	-7.91
High	6905	10.17	9.77	18.59	24.00	-5.41

9.3. 26 dB BANDWIDTH

LIMITS

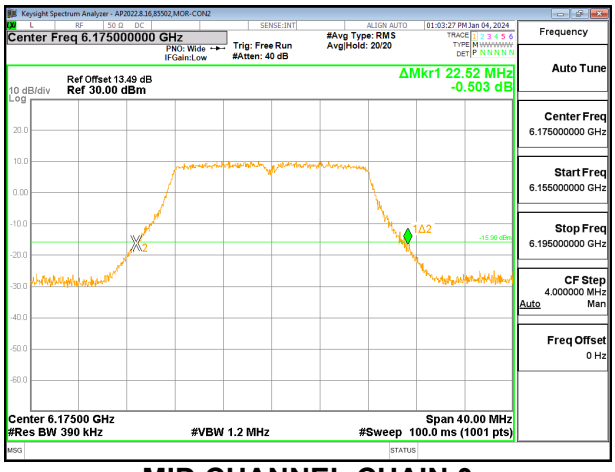
FCC. §15.407 (a) (10)
Less than 320 MHz
Note: for 320MHz BW measurements, 99% OBW will be used to show compliance.

RESULTS

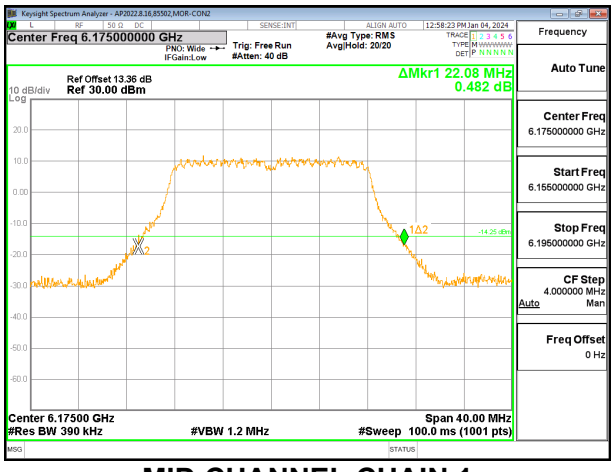
9.3.1. 802.11a MODE 2TX IN THE UNII-5 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE

Channel	Frequency (MHz)	26 dB Bandwidth Chain 0 (MHz)	26 dB Bandwidth Chain 1 (MHz)	Limit (MHz)	Worst-Case Margin (MHz)
Low	5955	22.16	21.80	320	-297.84
Mid	6175	22.52	22.08	320	-297.48
High	6415	22.60	21.88	320	-297.40



MID CHANNEL CHAIN 0

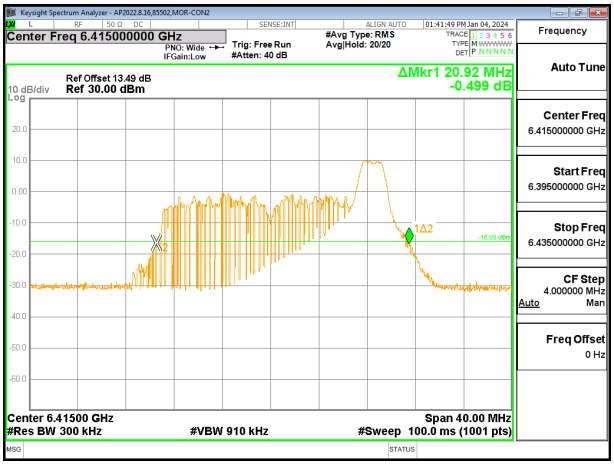


MID CHANNEL CHAIN 1

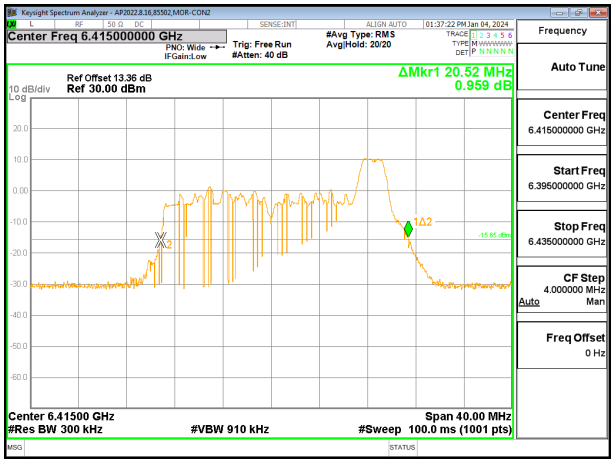
9.3.2. 802.11be EHT20 MODE 2TX IN THE UNII-5 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 26T

Channel	Frequency (MHz)	26 dB Bandwidth Chain 0 (MHz)	26 dB Bandwidth Chain 1 (MHz)	Limit (MHz)	Worst-Case Margin (MHz)
Low	5955	20.48	20.28	320	-299.52
Mid	6175	20.44	20.28	320	-299.56
High	6415	20.92	20.52	320	-299.08



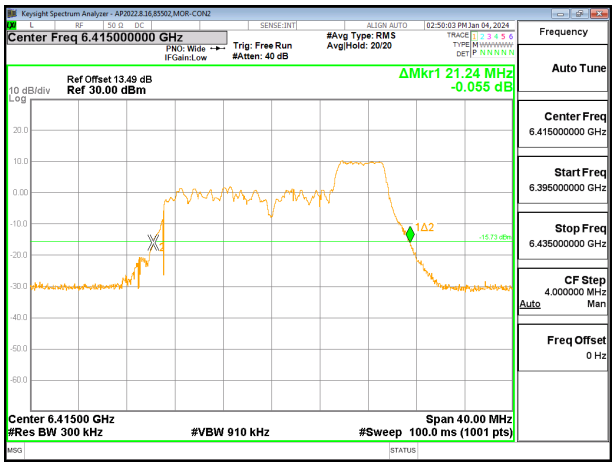
HIGH CHANNEL CHAIN 0



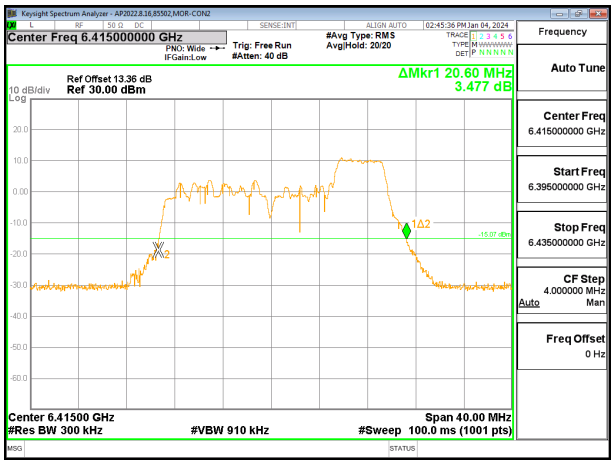
HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 52T

Channel	Frequency (MHz)	26 dB Bandwidth Chain 0 (MHz)	26 dB Bandwidth Chain 1 (MHz)	Limit (MHz)	Worst-Case Margin (MHz)
Low	5955	20.48	20.56	320	-299.44
Mid	6175	20.60	20.60	320	-299.40
High	6415	21.24	20.60	320	-298.76



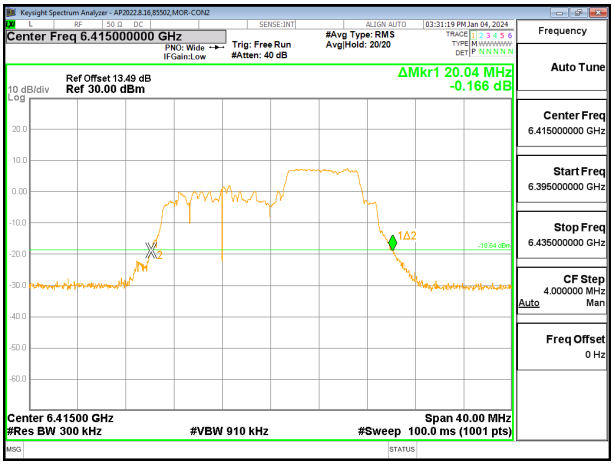
HIGH CHANNEL CHAIN 0



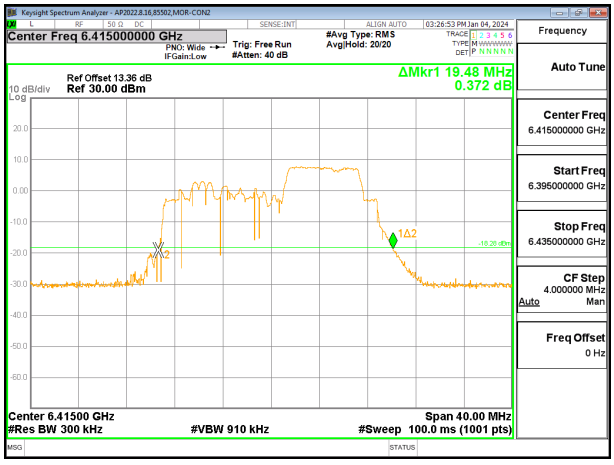
HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 52T+26T

Channel	Frequency (MHz)	26 dB Bandwidth Chain 0 (MHz)	26 dB Bandwidth Chain 1 (MHz)	Limit (MHz)	Worst-Case Margin (MHz)
Low	5955	19.24	19.40	320	-300.60
Mid	6175	19.20	19.24	320	-300.76
High	6415	20.04	19.48	320	-299.96



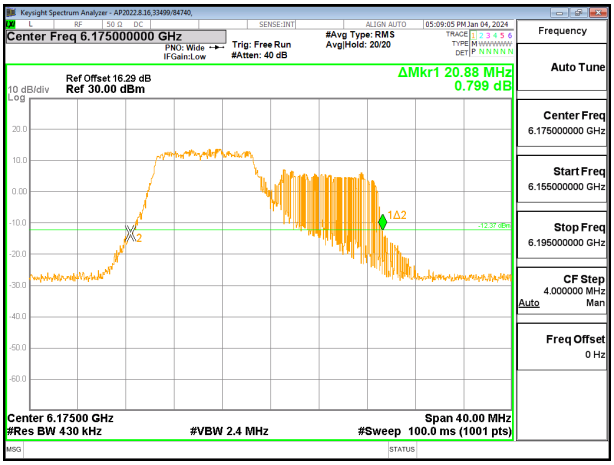
HIGH CHANNEL CHAIN 0



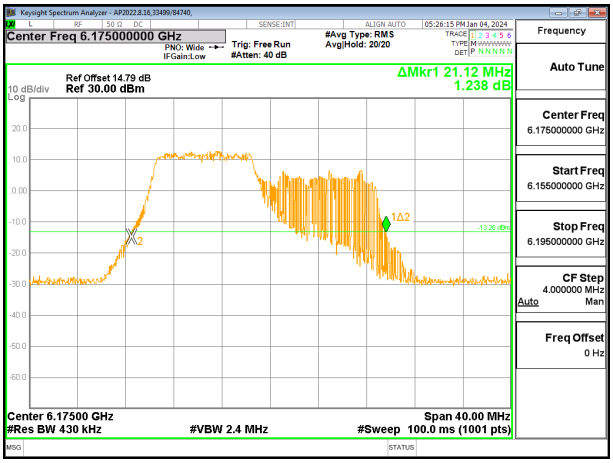
HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 106T

Channel	Frequency (MHz)	26 dB Bandwidth Chain 0 (MHz)	26 dB Bandwidth Chain 1 (MHz)	Limit (MHz)	Worst-Case Margin (MHz)
Low	5955	20.56	20.88	320	-299.12
Mid	6175	20.88	21.12	320	-298.88
High	6415	21.48	20.60	320	-298.52



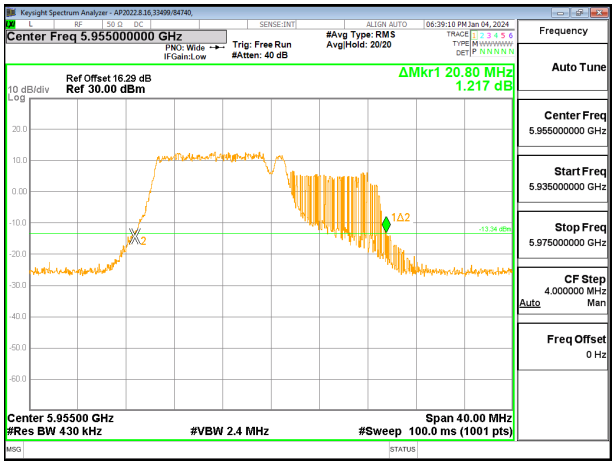
MID CHANNEL CHAIN 0



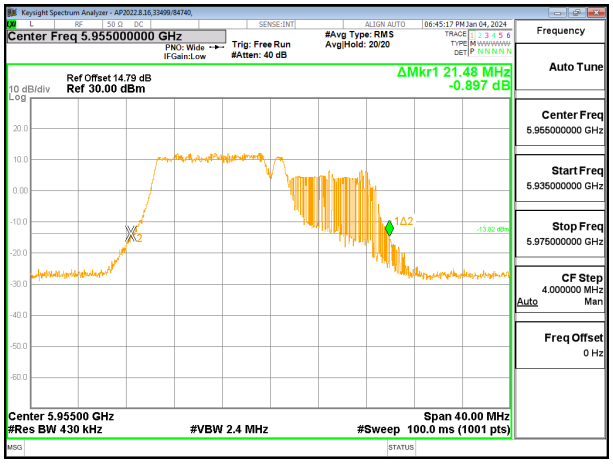
MID CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 106T+26T

Channel	Frequency (MHz)	26 dB Bandwidth Chain 0 (MHz)	26 dB Bandwidth Chain 1 (MHz)	Limit (MHz)	Worst-Case Margin (MHz)
Low	5955	20.80	21.48	320	-298.52
Mid	6175	20.96	21.28	320	-298.72
High	6415	21.32	21.12	320	-298.68



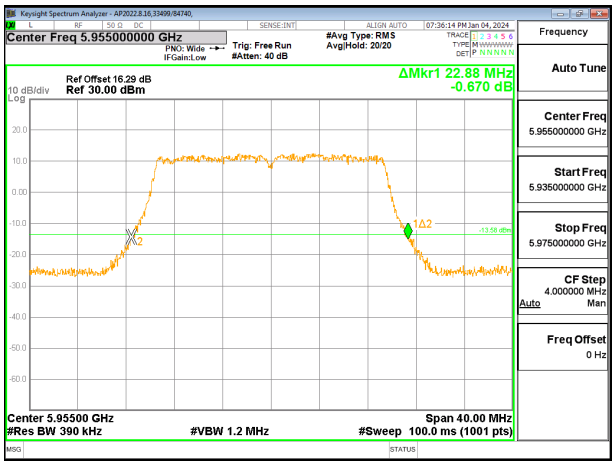
LOW CHANNEL CHAIN 0



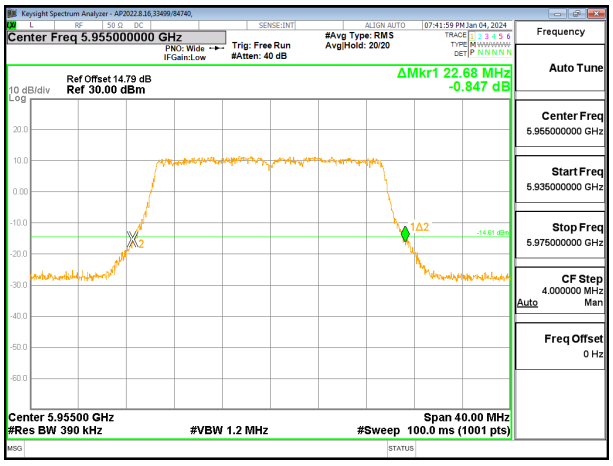
LOW CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 242T

Channel	Frequency (MHz)	26 dB Bandwidth Chain 0 (MHz)	26 dB Bandwidth Chain 1 (MHz)	Limit (MHz)	Worst-Case Margin (MHz)
Low	5955	22.88	22.68	320	-297.12
Mid	6175	22.44	22.48	320	-297.52
High	6415	22.72	22.60	320	-297.28



LOW CHANNEL CHAIN 0

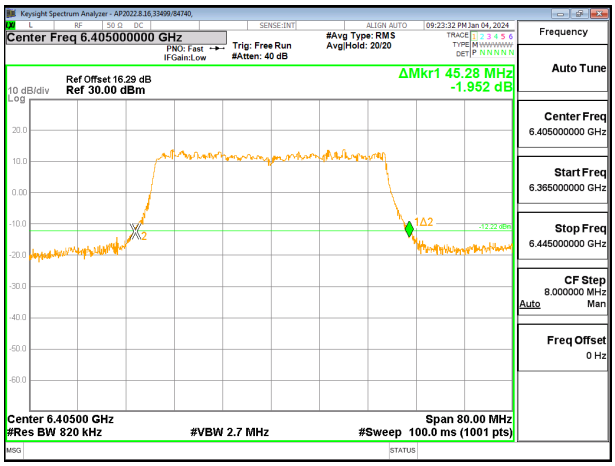


LOW CHANNEL CHAIN 1

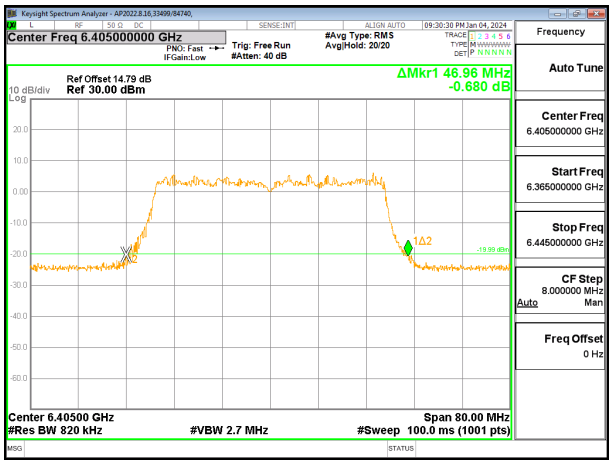
9.3.3. 802.11be EHT40 MODE 2TX IN THE UNII-5 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T

Channel	Frequency (MHz)	26 dB Bandwidth Chain 0 (MHz)	26 dB Bandwidth Chain 1 (MHz)	Limit (MHz)	Worst-Case Margin (MHz)
Low	5965	46.88	45.36	320	-273.12
Mid	6165	47.36	46.16	320	-272.64
High	6405	45.28	46.96	320	-273.04



HIGH CHANNEL CHAIN 0

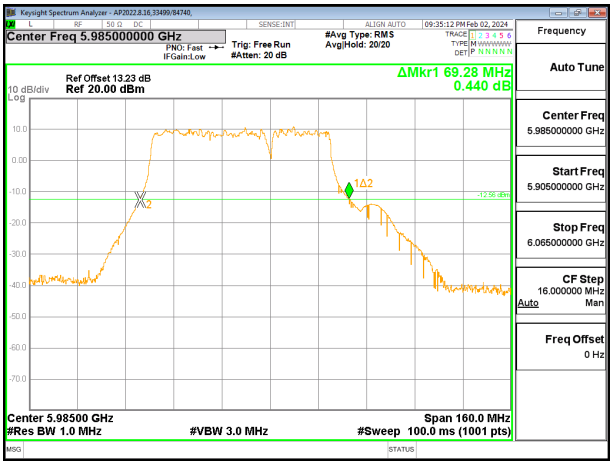


HIGH CHANNEL CHAIN 1

9.3.4. 802.11be EHT80 MODE 2TX IN THE UNII-5 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+242T (CONTIGUOUS)

Channel	Frequency (MHz)	26 dB Bandwidth Chain 0 (MHz)	26 dB Bandwidth Chain 1 (MHz)	Limit (MHz)	Worst-Case Margin (MHz)
Low	5985	69.28	81.76	320	-238.24
Mid	6145	81.60	81.12	320	-238.40
High	6385	79.84	80.64	320	-239.36

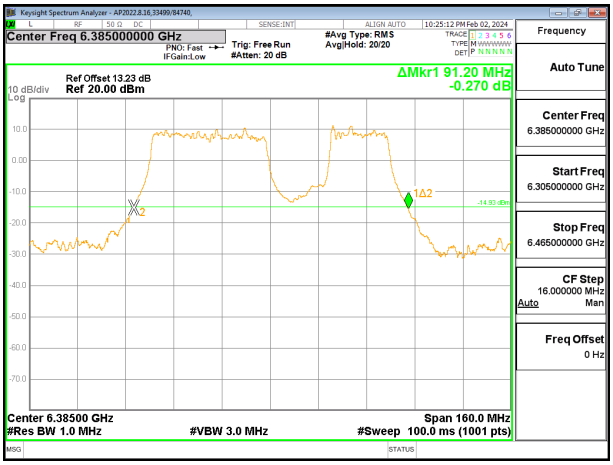


LOW CHANNEL CHAIN 0

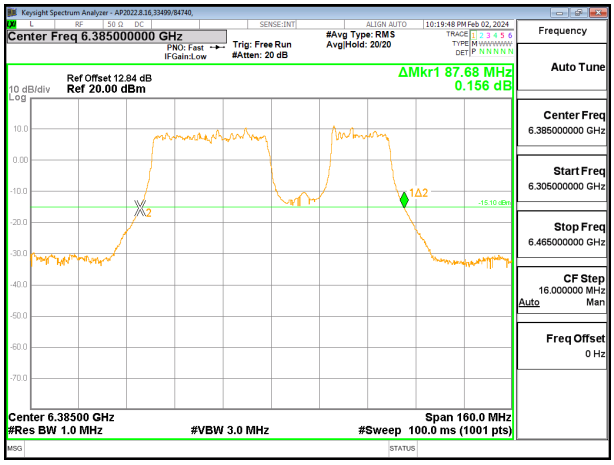
LOW CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 484T+242T (NON-CONTIGUOUS)

Channel	Frequency (MHz)	26 dB Bandwidth Chain 0 (MHz)	26 dB Bandwidth Chain 1 (MHz)	Limit (MHz)	Worst-Case Margin (MHz)
Low	5985	88.32	85.44	320	-231.68
Mid	6145	87.68	87.20	320	-232.32
High	6385	91.20	87.68	320	-228.80



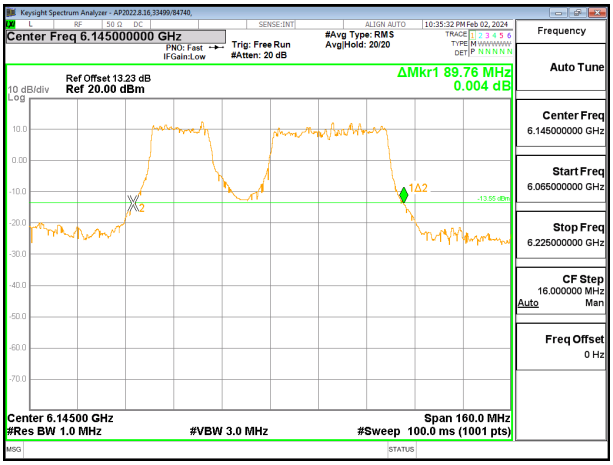
HIGH CHANNEL CHAIN 0



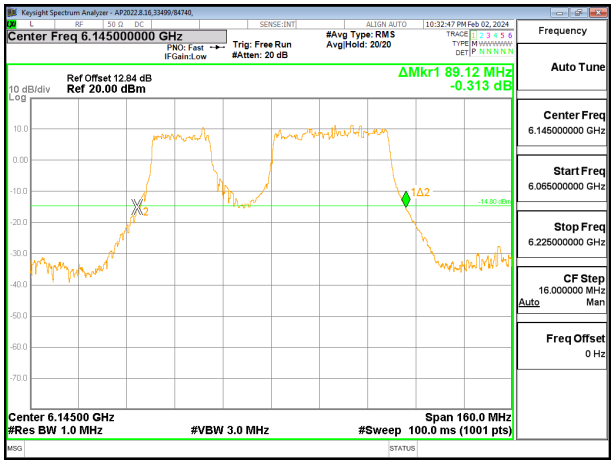
HIGH CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 242T+484T (NON-CONTIGUOUS)

Channel	Frequency (MHz)	26 dB Bandwidth Chain 0 (MHz)	26 dB Bandwidth Chain 1 (MHz)	Limit (MHz)	Worst-Case Margin (MHz)
Low	5985	88.48	85.92	320	-231.52
Mid	6145	89.76	89.12	320	-230.24
High	6385	88.48	88.16	320	-231.52



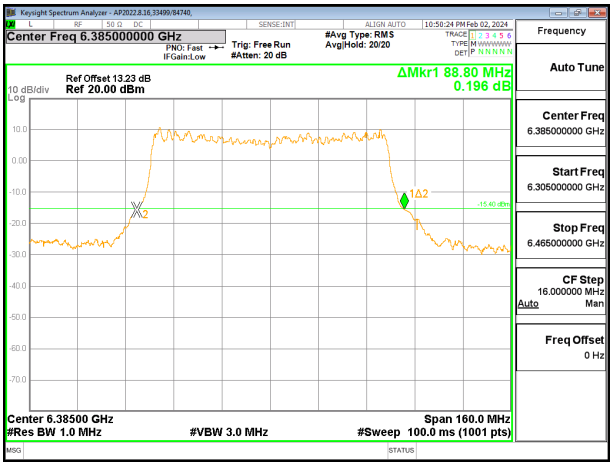
MID CHANNEL CHAIN 0



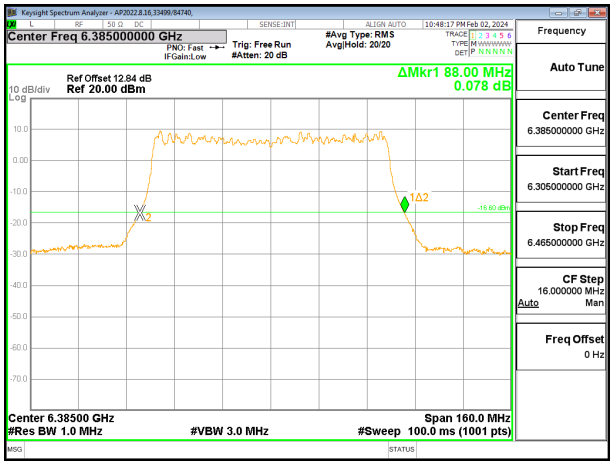
MID CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T

Channel	Frequency (MHz)	26 dB Bandwidth Chain 0 (MHz)	26 dB Bandwidth Chain 1 (MHz)	Limit (MHz)	Worst-Case Margin (MHz)
Low	5985	87.68	87.20	320	-232.32
Mid	6145	88.16	87.20	320	-231.84
High	6385	88.80	88.00	320	-231.20



HIGH CHANNEL CHAIN 0

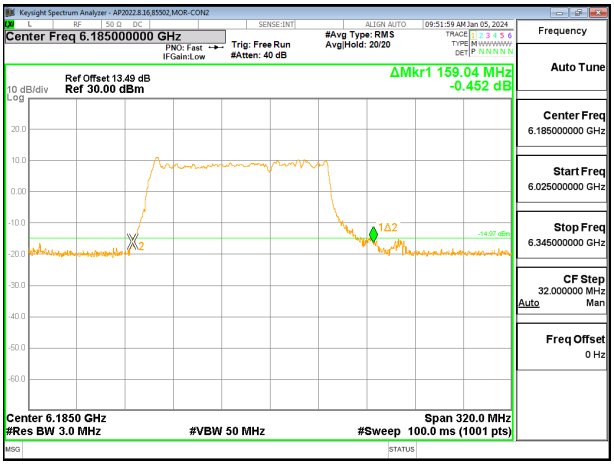


HIGH CHANNEL CHAIN 1

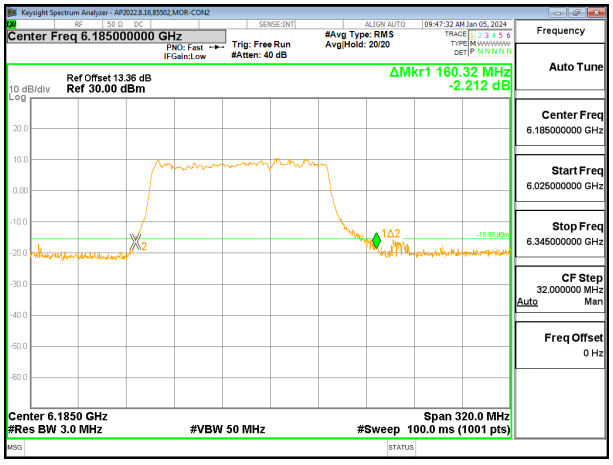
9.3.5. 802.11be EHT160 MODE 2TX IN THE UNII-5 BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE: 996T+484T (CONTIGUOUS)

Channel	Frequency (MHz)	26 dB Bandwidth Chain 0 (MHz)	26 dB Bandwidth Chain 1 (MHz)	Limit (MHz)	Worst-Case Margin (MHz)
Low	6025	171.84	159.36	320	-148.16
Mid	6185	159.04	160.32	320	-159.68
High	6345	145.60	143.68	320	-174.40



MID CHANNEL CHAIN 0



MID CHANNEL CHAIN 1