

2TX Chain 0 + Chain 1 CDD OFDMA MODE (IC) : 52T+26T

Test Engineer:	84740/44389, 33499/44389
Test Date:	2023/11/22

Bandwidth, Antenna Gain, and Limits

Channel	Frequency (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	Power Limit (dBm)	EIRP PSD Limit (dBm/1MHz)	PSD Limit (dBm/1MHz)
Low	5180	6.3955	5.44	8.26	18.06	12.62	10.00	1.74
Mid	5200	6.5938	5.44	8.26	18.19	12.75	10.00	1.74
High	5240	6.4274	5.44	8.26	18.08	12.64	10.00	1.74

Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5180	3.73	4.21	6.99	12.62	-5.63
Mid	5200	3.72	4.23	6.99	12.75	-5.76
High	5240	2.67	3.32	6.02	12.64	-6.62

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/1MHz)	Chain 1 Meas PSD (dBm/1MHz)	Total Corr'd D (dBm/1MHz)	PSD Limit (dBm/1MHz)	PSD Margin (dB)
Low	5180	-3.58	-3.53	-0.55	1.74	-2.29
Mid	5200	-3.94	-3.03	-0.45	1.74	-2.19
High	5240	-3.79	-3.37	-0.57	1.74	-2.31



2TX Chain 0 + Chain 1 CDD OFDMA MODE (FCC): 106T

Test Engineer:	85502/44389
Test Date:	2023/11/21

Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	Power Limit (dBm)	PSD Limit (dBm/1MHz)
Low	5180	5.44	8.26	24.00	8.74
Mid	5200	5.44	8.26	24.00	8.74
High	5240	5.44	8.26	24.00	8.74

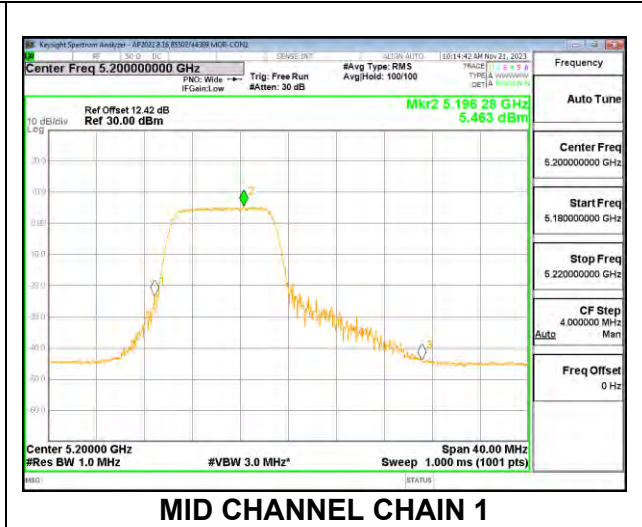
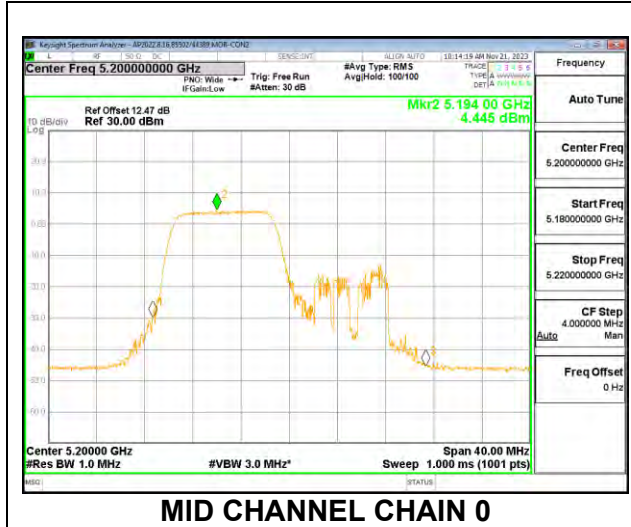
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5180	13.18	13.18	16.19	24.00	-7.81
Mid	5200	13.22	13.21	16.23	24.00	-7.77
High	5240	13.29	13.55	16.43	24.00	-7.57

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/1MHz)	Chain 1 Meas PSD (dBm/1MHz)	Total Corr'd PSD (dBm/1MHz)	PSD Limit (dBm/1MHz)	PSD Margin (dB)
Low	5180	4.48	5.14	7.83	8.74	-0.91
Mid	5200	4.45	5.46	7.99	8.74	-0.75
High	5240	5.48	4.40	7.98	8.74	-0.76



2TX Chain 0 + Chain 1 CDD OFDMA MODE (IC): 106T

Test Engineer:	84740/44389, 33499/44389
Test Date:	2023/11/22

Bandwidth, Antenna Gain, and Limits

Channel	Frequency (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	Power Limit (dBm)	EIRP PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5180	8.3320	5.44	8.26	19.21	13.77	10.00	1.74
Mid	5200	8.3346	5.44	8.26	19.21	13.77	10.00	1.74
High	5240	8.2193	5.44	8.26	19.15	13.71	10.00	1.74

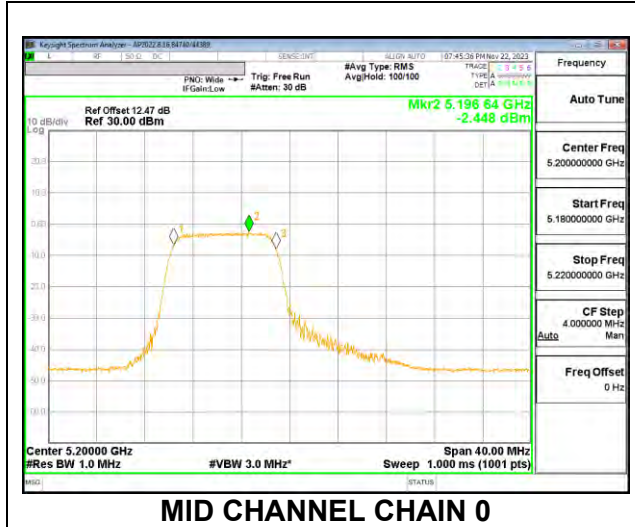
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5180	6.12	6.31	9.23	13.77	-4.54
Mid	5200	6.07	6.70	9.41	13.77	-4.36
High	5240	5.60	6.20	8.92	13.71	-4.79

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/1MHz)	Chain 1 Meas PSD (dBm/1MHz)	Total Corr'd D (dBm/1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Low	5180	-2.50	-2.16	0.68	1.74	-1.06
Mid	5200	-2.45	-1.78	0.91	1.74	-0.83
High	5240	-2.64	-2.00	0.70	1.74	-1.04



2TX Chain 0 + Chain 1 CDD OFDMA MODE (FCC): 106T + 26T

Test Engineer:	85502/44389
Test Date:	2023/11/21

Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	Power Limit (dBm)	PSD Limit (dBm/ 1MHz)
Low	5180	5.44	8.26	24.00	8.74
Mid	5200	5.44	8.26	24.00	8.74
High	5240	5.44	8.26	24.00	8.74

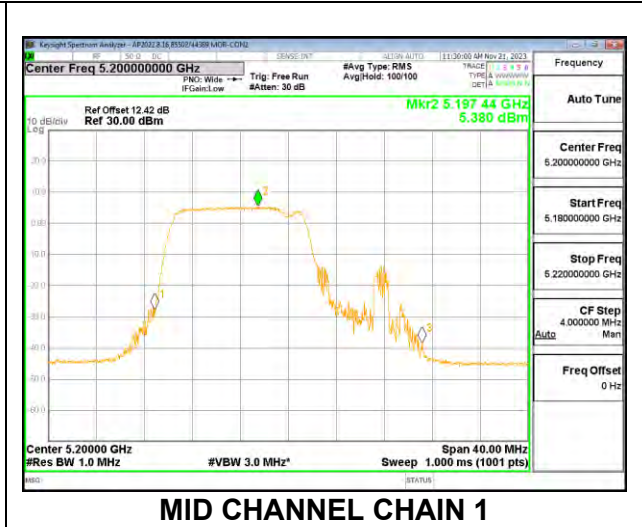
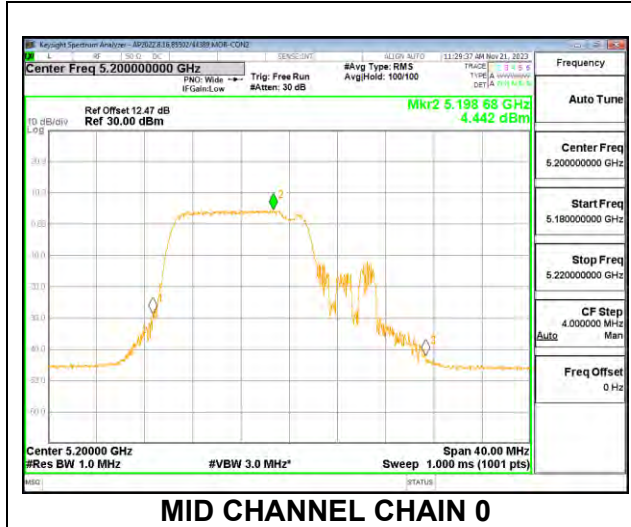
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5180	13.67	13.20	16.45	24.00	-7.55
Mid	5200	13.60	13.36	16.49	24.00	-7.51
High	5240	13.79	13.35	16.59	24.00	-7.41

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/1MHz)	Chain 1 Meas PSD (dBm/1MHz)	Total Corr'd PSD (dBm/1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Low	5180	4.26	5.35	7.85	8.74	-0.89
Mid	5200	4.44	5.38	7.95	8.74	-0.79
High	5240	5.20	3.76	7.55	8.74	-1.19



2TX Chain 0 + Chain 1 CDD MODE (IC): 106T + 26T

Test Engineer:	84740/44389, 33499/44389
Test Date:	2023/11/22

Bandwidth, Antenna Gain, and Limits

Channel	Frequency (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	Power Limit (dBm)	EIRP PSD Limit (dBm/1MHz)	PSD Limit (dBm/1MHz)
Low	5180	10.9180	5.44	8.26	20.38	14.94	10.00	1.74
Mid	5200	10.8040	5.44	8.26	20.34	14.90	10.00	1.74
High	5240	10.8380	5.44	8.26	20.35	14.91	10.00	1.74

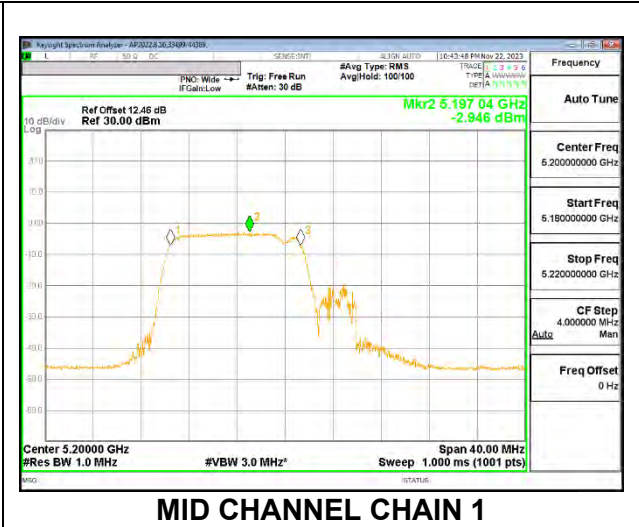
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5180	6.13	6.55	9.36	14.94	-5.59
Mid	5200	6.06	6.65	9.38	14.90	-5.52
High	5240	5.63	6.27	8.97	14.91	-5.94

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/1MHz)	Chain 1 Meas PSD (dBm/1MHz)	Total Corr'd D (dBm/1MHz)	PSD Limit (dBm/1MHz)	PSD Margin (dB)
Low	5180	-3.66	-3.07	-0.35	1.74	-2.09
Mid	5200	-3.49	-2.95	-0.20	1.74	-1.94
High	5240	-3.79	-2.88	-0.30	1.74	-2.04



2TX Chain 0 + Chain 1 CDD OFDMA MODE (FCC): 242T

Test Engineer:	85502/44389
Test Date:	2023/11/21

Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	Power Limit (dBm)	PSD Limit (dBm/ 1MHz)
Low	5180	5.44	8.26	24.00	8.74
Mid	5200	5.44	8.26	24.00	8.74
High	5240	5.44	8.26	24.00	8.74

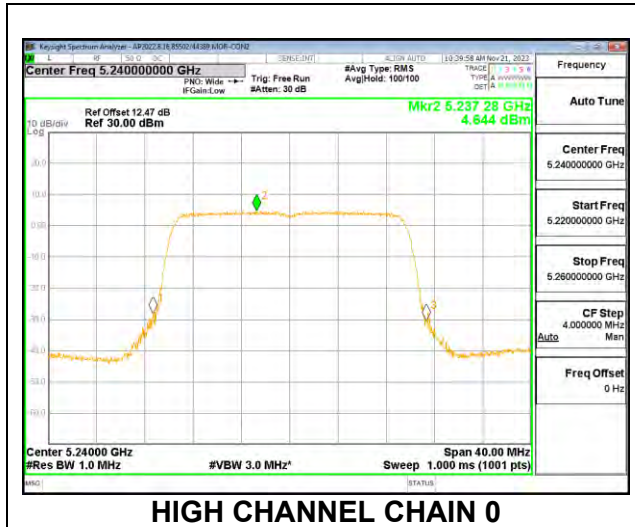
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5180	14.87	14.42	17.66	24.00	-6.34
Mid	5200	16.44	16.20	19.33	24.00	-4.67
High	5240	16.65	16.40	19.54	24.00	-4.46

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/1MHz)	Chain 1 Meas PSD (dBm/1MHz)	Total Corr'd PSD (dBm/1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Low	5180	4.21	5.42	7.86	8.74	-0.88
Mid	5200	4.41	5.44	7.96	8.74	-0.78
High	5240	4.64	5.27	7.98	8.74	-0.76



2TX Chain 0 + Chain 1 CDD OFDMA MODE (IC): 242T

Test Engineer:	84740/44389, 33499/44389
Test Date:	2023/11/22

Bandwidth, Antenna Gain, and Limits

Channel	Frequency (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	Power Limit (dBm)	EIRP PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5180	18.9260	5.44	8.26	22.77	17.33	10.00	1.74
Mid	5200	18.8210	5.44	8.26	22.75	17.31	10.00	1.74
High	5240	18.8530	5.44	8.26	22.75	17.31	10.00	1.74

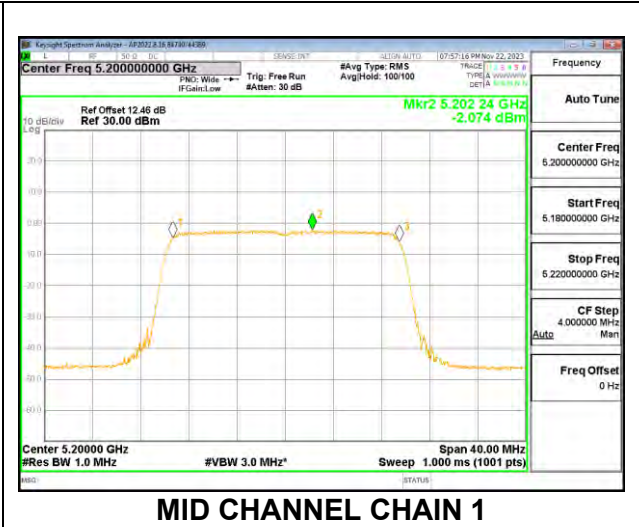
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5180	9.77	9.90	12.85	17.33	-4.48
Mid	5200	9.66	10.13	12.91	17.31	-4.39
High	5240	9.20	9.74	12.49	17.31	-4.83

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/1MHz)	Chain 1 Meas PSD (dBm/1MHz)	Total Corr'd D (dBm/1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Low	5180	-2.49	-2.31	0.61	1.74	-1.13
Mid	5200	-2.13	-2.07	0.91	1.74	-0.83
High	5240	-2.76	-2.11	0.59	1.74	-1.15



9.5.6. 802.11be EHT40 MODE IN THE 5.2 GHz BAND

2TX Chain 0 + Chain 1 CDD MODE (FCC): 484T

Test Engineer:	85502/44389
Test Date:	2023/11/21

Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	Power Limit (dBm)	PSD Limit (dBm/ 1MHz)
Low	5190	5.44	8.26	24.00	8.74
High	5230	5.44	8.26	24.00	8.74

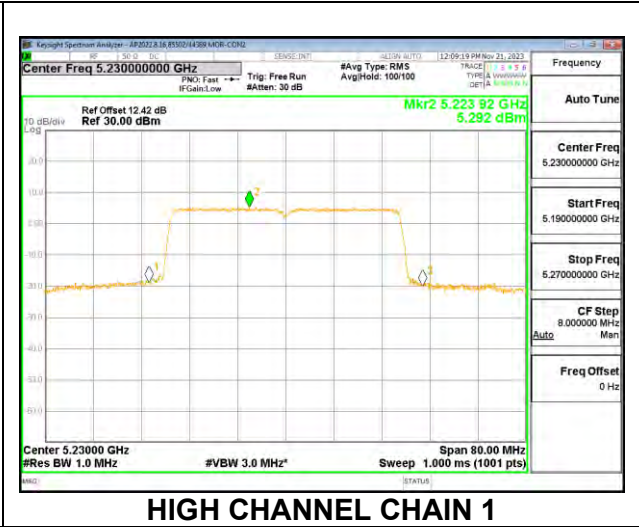
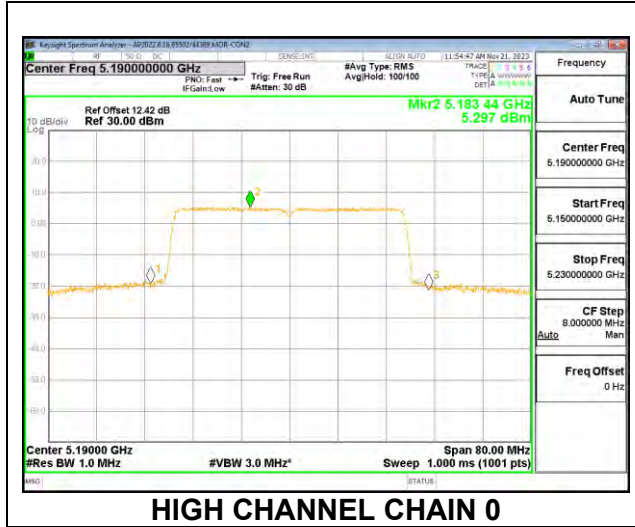
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5190	13.77	13.15	16.48	24.00	-7.52
High	5230	15.14	14.80	17.98	24.00	-6.02

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/ 1MHz)	Chain 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Low	5190	4.06	5.30	7.73	8.74	-1.01
High	5230	4.57	5.29	7.96	8.74	-0.78



2TX Chain 0 + Chain 1 CDD MODE (IC)

Test Engineer:	84740/44389, 33499/44389
Test Date:	2023/11/22

Bandwidth, Antenna Gain, and Limits

Channel	Frequency (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	Power Limit (dBm)	EIRP PSD Limit (dBm/1MHz)	PSD Limit (dBm/1MHz)
Low	5190	37.9340	5.44	8.26	23.00	17.56	10.00	1.74
High	5230	37.9710	5.44	8.26	23.00	17.56	10.00	1.74

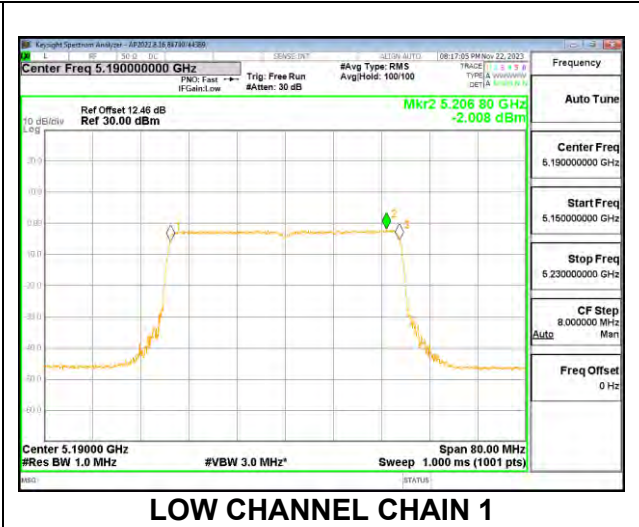
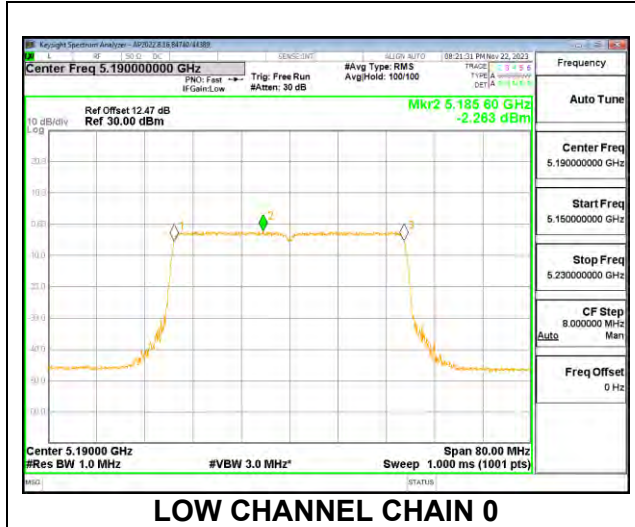
Duty Cycle CF (dB)		Included in Calculations of Corr'd Power & PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5190	10.31	10.12	13.23	17.56	-4.33
High	5230	10.77	10.84	13.82	17.56	-3.74

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/1MHz)	Chain 1 Meas PSD (dBm/1MHz)	Total Corr'd PSD (dBm/1MHz)	PSD Limit (dBm/1MHz)	PSD Margin (dB)
Low	5190	-2.26	-2.01	0.88	1.74	-0.86
High	5230	-2.56	-1.98	0.75	1.74	-0.99



9.5.7. 802.11be EHT80 MODE IN THE 5.2 GHz BAND

2TX Chain 0 + Chain 1 CDD MODE (FCC): 484T+242T (CONTIGUOUS)

Test Engineer:	85502/44389
Test Date:	2023/12/06

Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	Power Limit (dBm)	PSD Limit (dBm/1MHz)
Low	5210	5.44	8.26	24.00	8.74

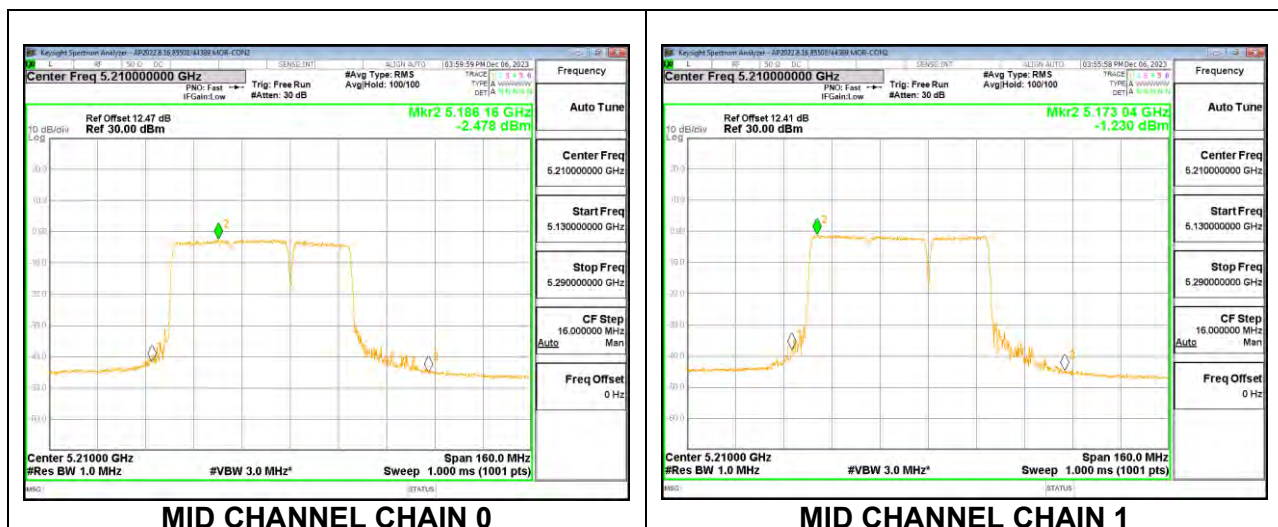
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5210	12.55	12.06	15.32	24.00	-8.68

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/1MHz)	Chain 1 Meas PSD (dBm/1MHz)	Total Corr'd PSD (dBm/1MHz)	PSD Limit (dBm/1MHz)	PSD Margin (dB)
Low	5210	-2.48	-1.23	1.20	8.74	-7.54



2TX Chain 0 + Chain 1 CDD MODE (IC): 484T+242T (CONTIGUOUS)

Test Engineer:	85502/44389
Test Date:	2023/12/06

Bandwidth, Antenna Gain, and Limits

Channel	Frequency (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	Power Limit (dBm)	EIRP PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Mid	5210	58.4110	5.44	8.26	23.00	17.56	10.00	1.74

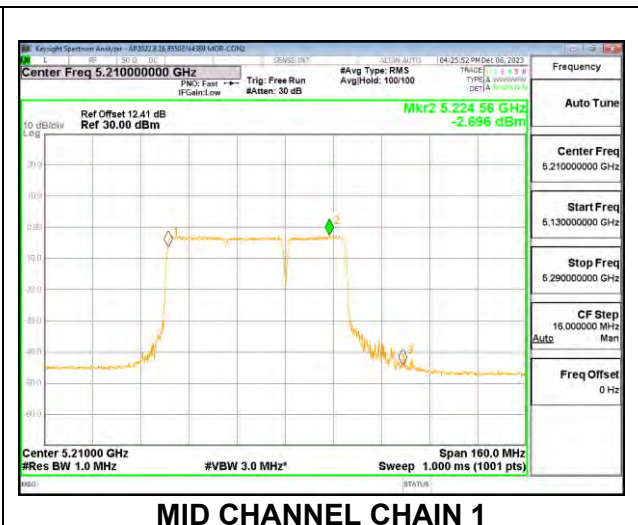
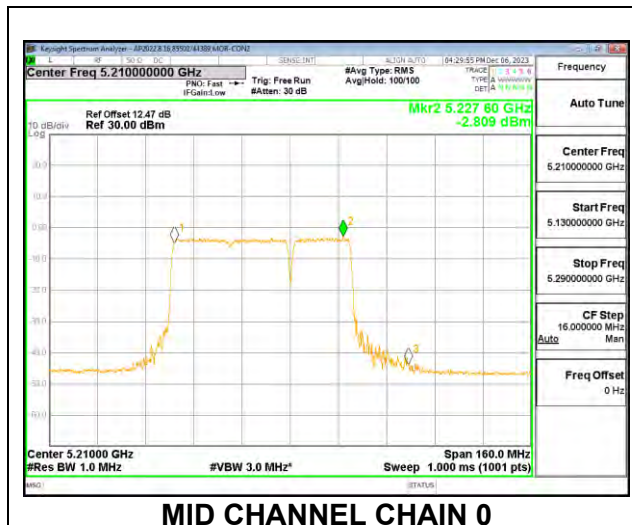
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Mid	5210	13.27	13.60	16.45	17.56	-1.11

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/ 1MHz)	Chain 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Mid	5210	-2.81	-2.70	0.26	1.74	-1.48



2TX Chain 0 + Chain 1 CDD MODE (FCC): 242T+484T (NON-CONTIGUOUS)

Test Engineer:	85502/44389
Test Date:	2023/12/06

Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	Power Limit (dBm)	PSD Limit (dBm/ 1MHz)
Low	5210	5.44	8.26	24.00	8.74

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5210	12.52	12.12	15.33	24.00	-8.67

2TX Chain 0 + Chain 1 CDD MODE (IC): 242T+484T (NON-CONTIGUOUS)

Test Engineer:	85502/44389
Test Date:	2024-01-24

Bandwidth, Antenna Gain, and Limits

Channel	Frequency (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	Power Limit (dBm)	EIRP PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Mid	5210	58.4110	5.44	8.26	23.00	17.56	10.00	1.74

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Mid	5210	13.24	12.90	16.08	17.56	-1.48

2TX Chain 0 + Chain 1 CDD MODE (FCC): 484T+242T (NON-CONTIGUOUS)

Test Engineer:	85502/44389
Test Date:	2023/12/06

Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	Power Limit (dBm)	PSD Limit (dBm/ 1MHz)
Low	5210	5.44	8.26	24.00	8.74

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5210	14.59	13.89	17.26	24.00	-6.74

2TX Chain 0 + Chain 1 CDD MODE (IC): 484T+242T (NON-CONTIGUOUS)

Test Engineer:	85502/44389
Test Date:	2024-01-24

Bandwidth, Antenna Gain, and Limits

Channel	Frequency (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	Power Limit (dBm)	EIRP PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Mid	5210	58.4110	5.44	8.26	23.00	17.56	10.00	1.74

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Mid	5210	13.27	12.88	16.09	17.56	-1.47

2TX Chain 0 + Chain 1 CDD MODE (FCC): 996T

Test Engineer:	85502/44389
Test Date:	2023/11/09

Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	Power Limit (dBm)	PSD Limit (dBm/ 1MHz)
Mid	5210	5.44	8.26	24.00	8.74

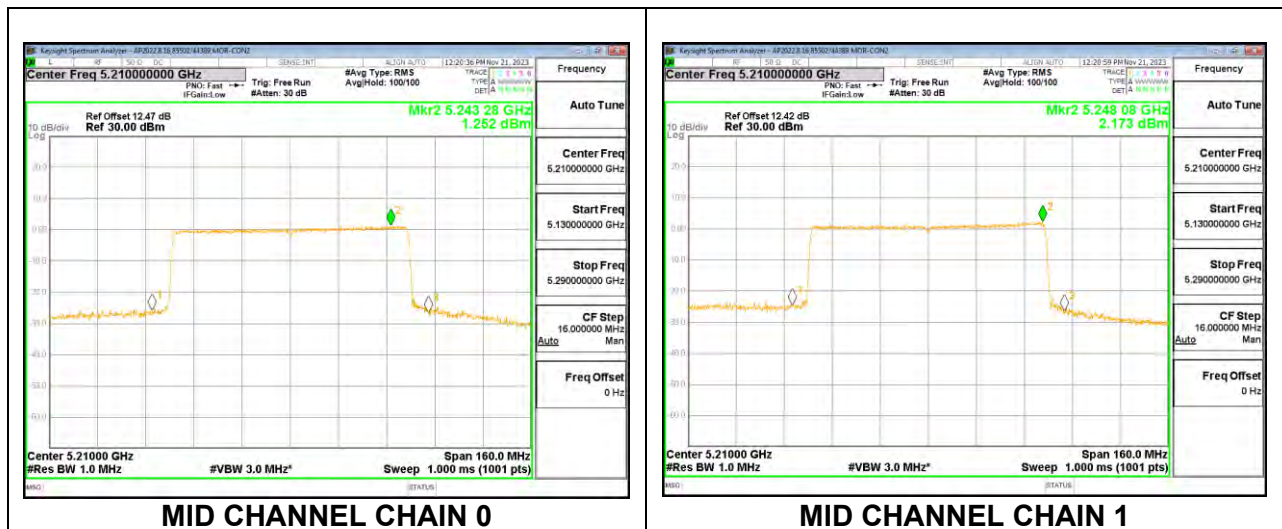
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Mid	5210	12.60	11.96	15.30	24.00	-8.70

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/ 1MHz)	Chain 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Mid	5210	1.25	2.17	4.75	8.74	-3.99



2TX Chain 0 + Chain 1 CDD MODE (IC): 996T

Test Engineer:	84740/44389, 33499/44389
Test Date:	2023/11/22

Bandwidth, Antenna Gain, and Limits

Channel	Frequency (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	Power Limit (dBm)	EIRP PSD Limit (dBm/1MHz)	PSD Limit (dBm/1MHz)
Mid	5210	77.6420	5.44	8.26	23.00	17.56	10.00	1.74

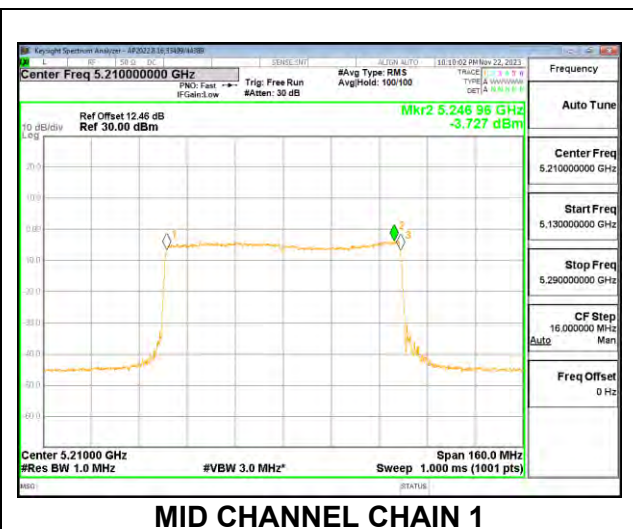
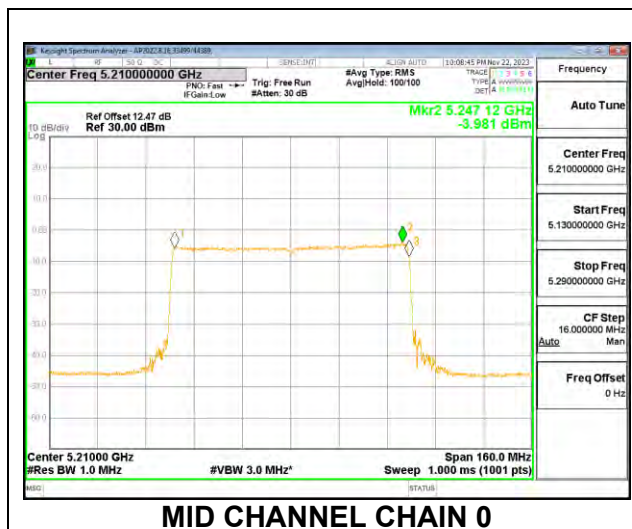
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Mid	5210	11.27	10.85	14.08	17.56	-3.48

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/1MHz)	Chain 1 Meas PSD (dBm/1MHz)	Total Corr'd PSD (dBm/1MHz)	PSD Limit (dBm/1MHz)	PSD Margin (dB)
Mid	5210	-3.98	-3.73	-0.84	1.74	-2.58



2TX Chain 0 + Chain 1 CDD MODE (FCC):SU

Test Engineer:	85502/44389
Test Date:	2023/11/09

Antenna Gain and Limits

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	Power Limit (dBm)	PSD Limit (dBm/ 1MHz)
Mid	5210	5.44	8.26	24.00	8.74

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Mid	5210	14.81	14.62	17.73	24.00	-6.27

2TX Chain 0 + Chain 1 CDD MODE (IC): SU

Test Engineer:	84740/44389, 33499/44389
Test Date:	2023/11/22

Bandwidth, Antenna Gain, and Limits

Channel	Frequency (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)	EIRP Power Limit (dBm)	Power Limit (dBm)	EIRP PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Mid	5210	77.6420	5.44	8.26	23.00	17.56	10.00	1.74

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Mid	5210	12.49	12.01	15.27	17.56	-2.29

9.5.8. 802.11a MODE IN THE 5.3 GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC)

Test Engineer:	84740/44389
Test Date:	2023/11/09

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5260	22.04	16.4910	5.44	8.26
Mid	5300	21.68	16.4780	5.44	8.26
High	5320	21.92	16.4290	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/1MHz)	ISED PSD Limit (dBm/1MHz)	PSD Limit (dBm/1MHz)
Low	5260	24.00	23.17	29.17	23.17	8.74	11.00	8.74
Mid	5300	24.00	23.17	29.17	23.17	8.74	11.00	8.74
High	5320	24.00	23.16	29.16	23.16	8.74	11.00	8.74

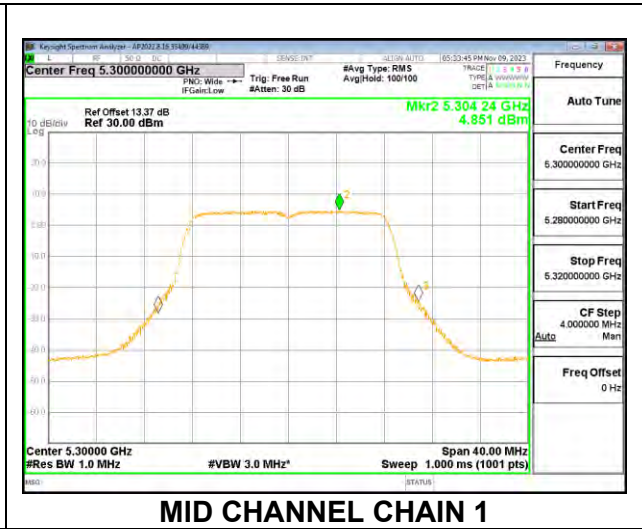
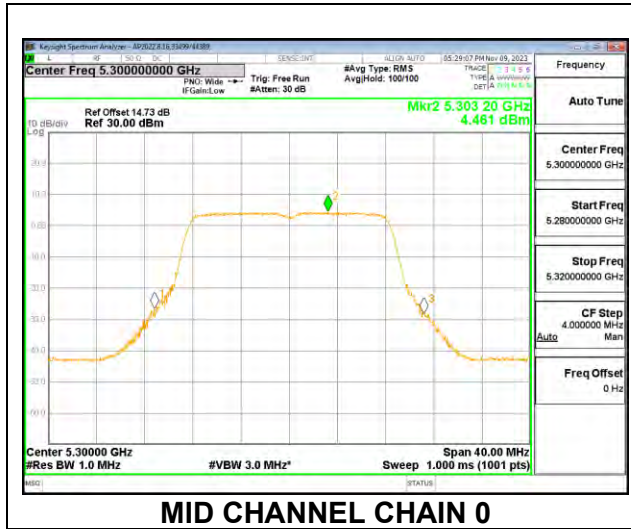
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5260	15.83	15.58	18.72	23.17	-4.46
Mid	5300	16.29	16.68	19.50	23.17	-3.67
High	5320	16.33	16.44	19.40	23.16	-3.76

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/1MHz)	Chain 1 Meas PSD (dBm/1MHz)	Total Corr'd PSD (dBm/1MHz)	PSD Limit (dBm/1MHz)	PSD Margin (dB)
Low	5260	4.67	4.17	7.44	8.74	-1.30
Mid	5300	4.47	4.85	7.67	8.74	-1.07
High	5320	4.47	4.20	7.35	8.74	-1.39



9.5.9. 802.11n HT20 MODE IN THE 5.3 GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC)

Test Engineer:	33499/44389
Test Date:	2023/11/09

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5260	22.72	17.6730	5.44	8.26
Mid	5300	22.84	17.7820	5.44	8.26
High	5320	23.08	17.6460	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5260	24.00	23.47	29.47	23.47	8.74	11.00	8.74
Mid	5300	24.00	23.50	29.50	23.50	8.74	11.00	8.74
High	5320	24.00	23.47	29.47	23.47	8.74	11.00	8.74

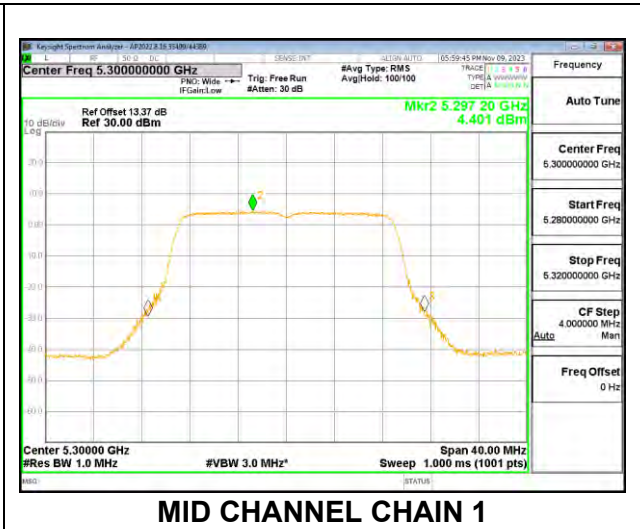
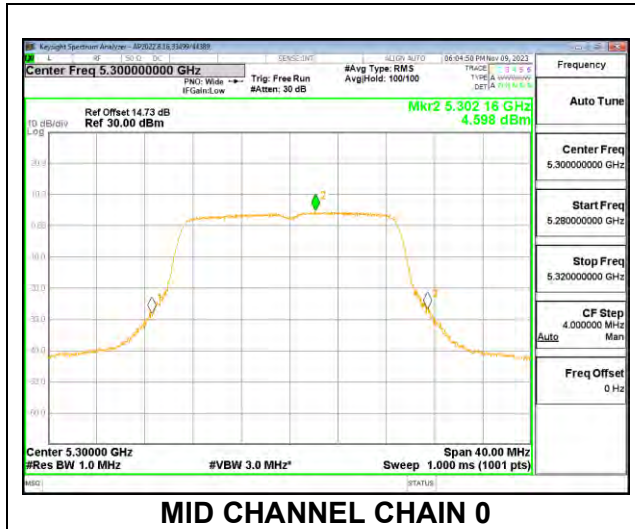
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5260	16.24	16.23	19.25	23.47	-4.23
Mid	5300	16.32	16.38	19.36	23.50	-4.14
High	5320	16.41	16.58	19.51	23.47	-3.96

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/ 1MHz)	Chain 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Low	5260	4.44	4.36	7.41	8.74	-1.33
Mid	5300	4.60	4.40	7.51	8.74	-1.23
High	5320	4.31	4.51	7.42	8.74	-1.32



9.5.10. 802.11n HT40 MODE IN THE 5.3 GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC)

Test Engineer:	33499/44389
Test Date:	2023/11/09

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5270	46.40	36.356	5.44	8.26
High	5310	45.68	36.387	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5270	24.00	24.00	30.00	24.00	8.74	11.00	8.74
High	5310	24.00	24.00	30.00	24.00	8.74	11.00	8.74

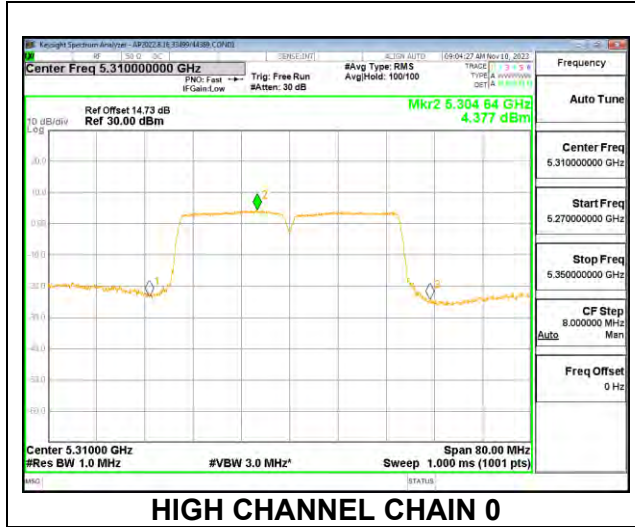
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margi (dB)
Low	5270	13.74	14.03	16.90	24.00	-7.10
High	5310	13.75	14.08	16.93	24.00	-7.07

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/ 1MHz)	Chain 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margi n (dB)
Low	5270	3.86	4.88	7.41	8.74	-1.33
High	5310	4.34	4.58	7.47	8.74	-1.27



9.5.11. 802.11ac VHT80 MODE IN THE 5.3 GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC)

Test Engineer:	85502/44389
Test Date:	2023/11/10

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Mid	5290	93.92	76.08	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Mid	5290	24.00	24.00	30.00	24.00	8.74	11.00	8.74

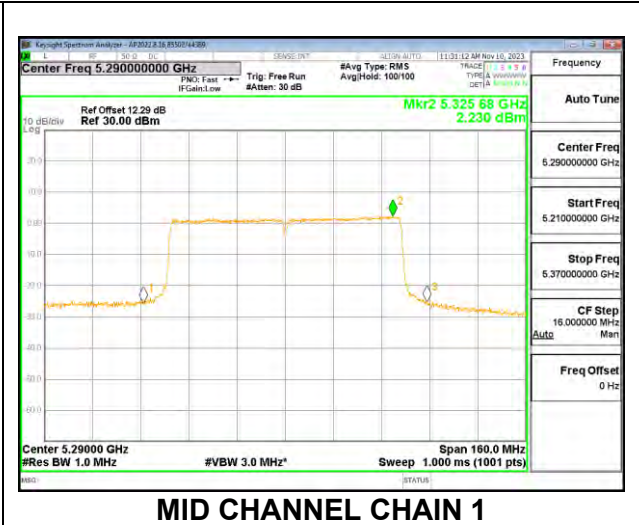
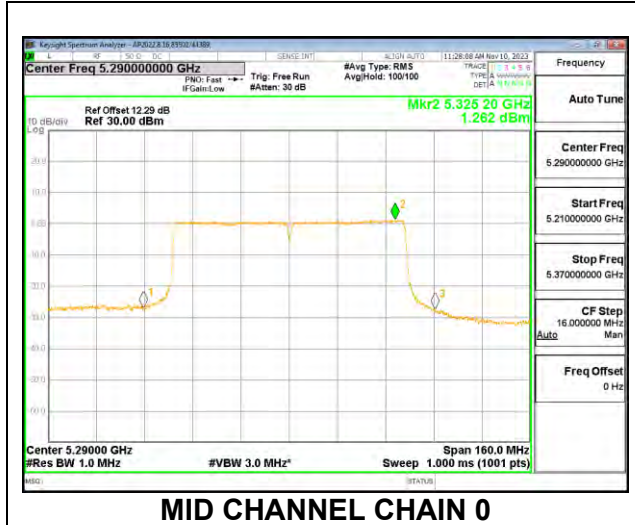
Duty Cycle CF (dB)	0.11	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margi (dB)
Mid	5290	12.47	12.82	15.66	24.00	-8.34

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/ 1MHz)	Chain 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margi n (dB)
Mid	5290	1.26	2.23	4.89	8.74	-3.85



9.5.12. 802.11ac VHT160 MODE 2TX IN THE 5.2/5.3GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC)

Test Engineer:	85502/44389
Test Date:	2023/11/10

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Mid	5250	155.48	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED EIRP Limit (dBm)	Max ISED Power (dBm)	Power Limit (dBm)
Mid	5250	24.00	23.00	17.56	17.56

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Mid	5250	12.54	12.75	15.66	17.56	-1.90

9.5.13. 802.11be EHT20 MODE 2TX IN THE 5.3GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE (FCC+IC): 26T

Test Engineer:	84740/44389
Test Date:	2023/11/09

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5260	20.28	2.2148	5.44	8.26
Mid	5300	20.28	2.2771	5.44	8.26
High	5320	20.28	2.3561	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5260	24.00	14.45	20.45	14.45	8.74	11.00	8.74
Mid	5300	24.00	14.57	20.57	14.57	8.74	11.00	8.74
High	5320	24.00	14.72	20.72	14.72	8.74	11.00	8.74

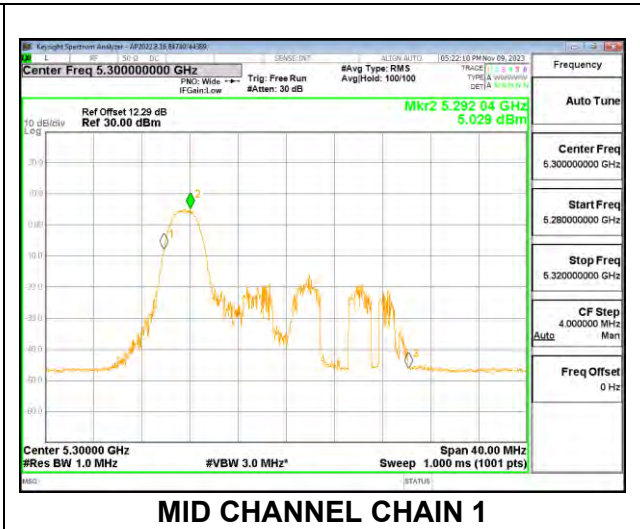
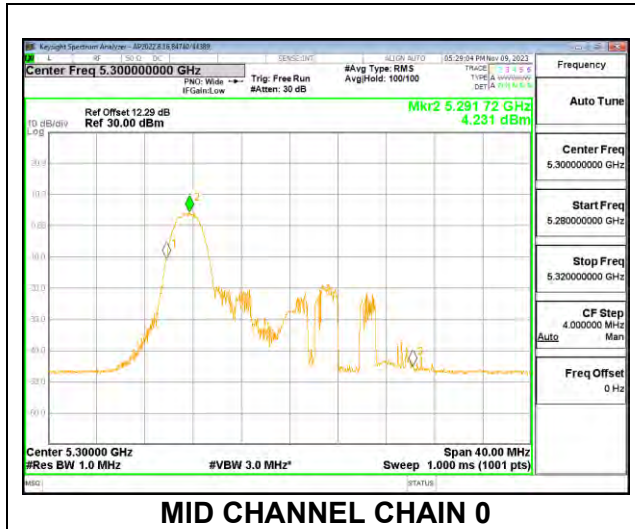
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5260	7.22	7.79	10.52	14.45	-3.93
Mid	5300	7.63	7.86	10.76	14.57	-3.82
High	5320	7.46	7.66	10.57	14.72	-4.15

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/ 1MHz)	Chain 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Low	5260	4.09	4.87	7.51	8.74	-1.23
Mid	5300	4.23	5.03	7.66	8.74	-1.08
High	5320	4.19	5.03	7.64	8.74	-1.10



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE (FCC+IC): 52T

Test Engineer:	84740/44389
Test Date:	2023/11/09

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5260	20.36	4.1899	5.44	8.26
Mid	5300	20.48	4.1365	5.44	8.26
High	5320	20.36	4.1405	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5260	24.00	17.22	23.22	17.22	8.74	11.00	8.74
Mid	5300	24.00	17.17	23.17	17.17	8.74	11.00	8.74
High	5320	24.00	17.17	23.17	17.17	8.74	11.00	8.74

Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5260	10.43	10.75	13.60	17.22	-3.62
Mid	5300	10.25	10.33	13.30	17.17	-3.87
High	5320	10.17	10.65	13.43	17.17	-3.74

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/ 1MHz)	Chain 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Low	5260	4.13	5.12	7.66	8.74	-1.08
Mid	5300	4.38	4.66	7.54	8.74	-1.20
High	5320	3.90	5.28	7.66	8.74	-1.08



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE (FCC+IC): 52T+26T

Test Engineer:	84740/44389
Test Date:	2023/11/09

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5260	19.12	6.9027	5.44	8.26
Mid	5300	19.24	6.5375	5.44	8.26
High	5320	19.36	6.6031	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5260	23.81	19.39	25.39	19.39	8.74	11.00	8.74
Mid	5300	23.84	19.15	25.15	19.15	8.74	11.00	8.74
High	5320	23.87	19.20	25.20	19.20	8.74	11.00	8.74

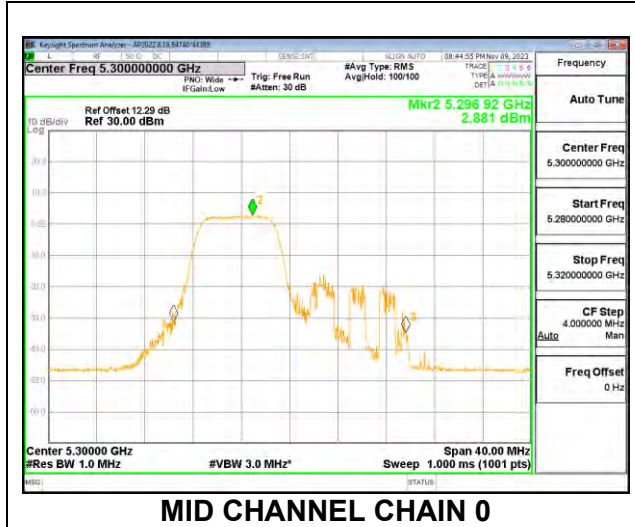
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5260	10.56	10.74	13.66	19.39	-5.73
Mid	5300	10.50	10.85	13.69	19.15	-5.47
High	5320	10.59	10.72	13.67	19.20	-5.53

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/ 1MHz)	Chain 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Low	5260	2.91	3.87	6.42	8.74	-2.32
Mid	5300	2.88	4.29	6.65	8.74	-2.09
High	5320	2.85	4.10	6.53	8.74	-2.21



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE (FCC+IC): 106T

Test Engineer:	84740/44389
Test Date:	2023/11/09

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5260	20.72	8.2524	5.44	8.26
Mid	5300	20.92	8.2360	5.44	8.26
High	5320	21.04	8.3296	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/1MHz)	ISED PSD Limit (dBm/1MHz)	PSD Limit (dBm/1MHz)
Low	5260	24.00	20.17	26.17	20.17	8.74	11.00	8.74
Mid	5300	24.00	20.16	26.16	20.16	8.74	11.00	8.74
High	5320	24.00	20.21	26.21	20.21	8.74	11.00	8.74

Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5260	13.25	13.17	16.22	20.17	-3.95
Mid	5300	13.22	13.35	16.30	20.16	-3.86
High	5320	13.39	13.52	16.47	20.21	-3.74

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/1MHz)	Chain 1 Meas PSD (dBm/1MHz)	Total Corr'd PSD (dBm/1MHz)	PSD Limit (dBm/1MHz)	PSD Margin (dB)
Low	5260	4.20	5.04	7.65	8.74	-1.09
Mid	5300	4.36	4.90	7.65	8.74	-1.09
High	5320	4.81	4.47	7.65	8.74	-1.09



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE (FCC+IC): 106T+26T

Test Engineer:	84740/44389
Test Date:	2023/11/09

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5260	21.00	10.9390	5.44	8.26
Mid	5300	21.16	10.9400	5.44	8.26
High	5320	21.28	10.9610	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5260	24.00	21.39	27.39	21.39	8.74	11.00	8.74
Mid	5300	24.00	21.39	27.39	21.39	8.74	11.00	8.74
High	5320	24.00	21.40	27.40	21.40	8.74	11.00	8.74

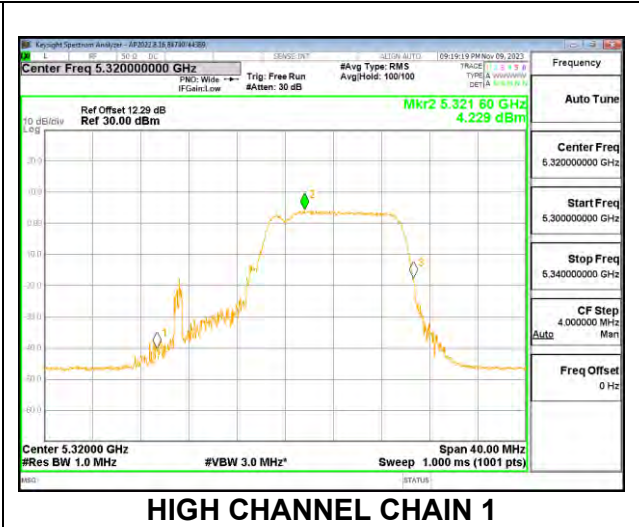
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5260	13.49	13.35	16.43	21.39	-4.96
Mid	5300	13.43	13.54	16.50	21.39	-4.89
High	5320	13.30	13.69	16.51	21.40	-4.89

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/ 1MHz)	Chain 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Low	5260	3.56	4.20	6.90	8.74	-1.84
Mid	5300	3.35	4.06	6.73	8.74	-2.01
High	5320	3.72	4.23	6.99	8.74	-1.75



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE (FCC+IC): 242T

Test Engineer:	84740/44389
Test Date:	2023/11/09

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5260	22.48	18.9750	5.44	8.26
Mid	5300	22.44	18.9510	5.44	8.26
High	5320	22.56	18.9760	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5260	24.00	23.78	29.78	23.78	8.74	11.00	8.74
Mid	5300	24.00	23.78	29.78	23.78	8.74	11.00	8.74
High	5320	24.00	23.78	29.78	23.78	8.74	11.00	8.74

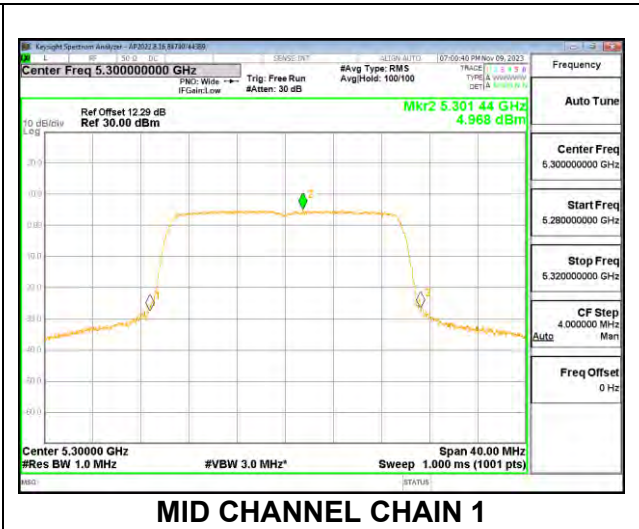
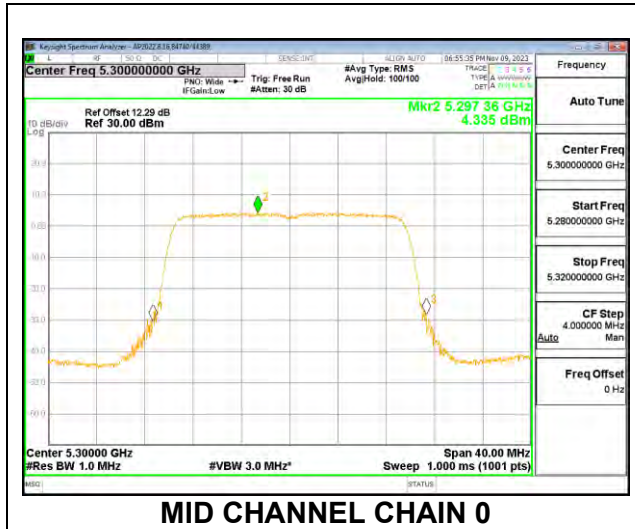
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5260	16.17	16.02	19.11	23.78	-4.68
Mid	5300	16.67	16.89	19.79	23.78	-3.98
High	5320	16.25	16.13	19.20	23.78	-4.58

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/ 1MHz)	Chain 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Low	5260	4.56	4.45	7.52	8.74	-1.22
Mid	5300	4.34	4.97	7.67	8.74	-1.07
High	5320	4.13	4.88	7.53	8.74	-1.21



9.5.14. 802.11be EHT40 MODE 2TX IN THE 5.3GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE (FCC+IC): 484T

Test Engineer:	84740/44389
Test Date:	2023/11/09

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5270	44.16	37.9630	5.44	8.26
High	5310	44.48	37.9490	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5270	24.00	24.00	30.00	24.00	8.74	11.00	8.74
High	5310	24.00	24.00	30.00	24.00	8.74	11.00	8.74

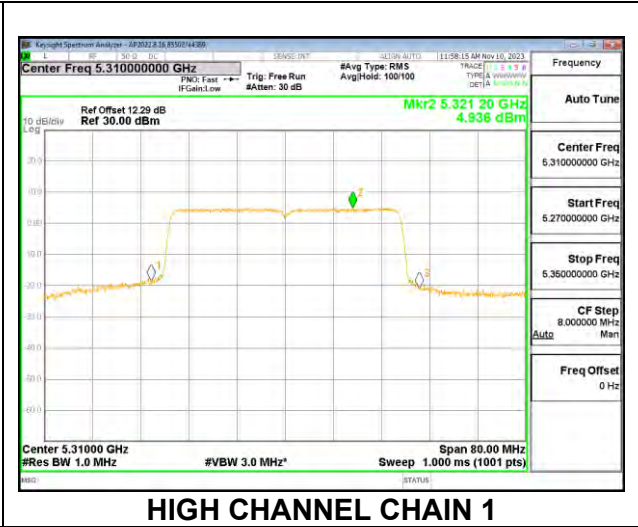
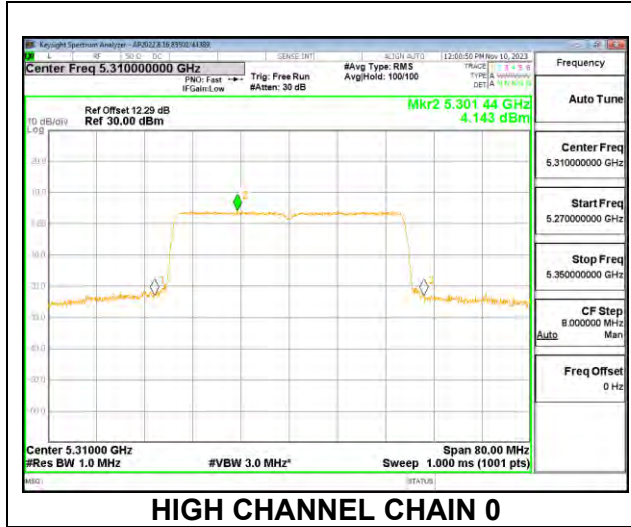
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margi (dB)
Low	5270	12.30	12.43	15.38	24.00	-8.62
High	5310	12.39	12.54	15.48	24.00	-8.52

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/ 1MHz)	Chain 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margi n (dB)
Low	5270	4.16	4.77	7.49	8.74	-1.25
High	5310	4.14	4.94	7.57	8.74	-1.17



9.5.15. 802.11be EHT80 MODE 2TX IN THE 5.3GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE (FCC+IC): 484T+242T (CONTIGUOUS)

Test Engineer:	33499/44389
Test Date:	2023/12/6

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Mid	5290	82.56	58.7410	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Mid	5290	24.00	24.00	30.00	24.00	8.74	11.00	8.74

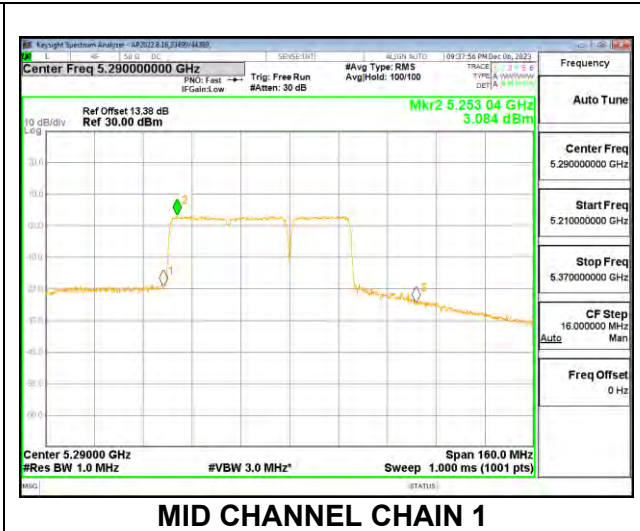
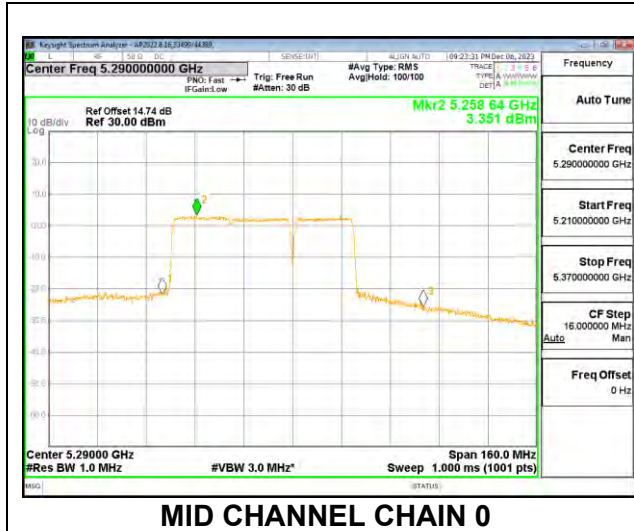
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margi (dB)
Mid	5290	12.89	12.96	15.94	24.00	-8.06

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/ 1MHz)	Chain 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margi n (dB)
Mid	5290	3.35	3.08	6.23	8.74	-2.51



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE (FCC+IC): 242T+484T (NON-CONTIGUOUS)

Test Engineer:	33499/44389
Test Date:	2023/12/6

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Mid	5290	82.56	58.7410	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Mid	5290	24.00	24.00	30.00	24.00	8.74	11.00	8.74

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margi (dB)
Mid	5290	12.80	12.97	15.90	24.00	-8.10

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE (FCC+IC): 484T+242T (NON-CONTIGUOUS)

Test Engineer:	33499/44389
Test Date:	2023/12/6

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Mid	5290	82.56	58.7410	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Mid	5290	24.00	24.00	30.00	24.00	8.74	11.00	8.74

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margi (dB)
Mid	5290	13.10	13.47	16.30	24.00	-7.70

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE (FCC+IC): 996T

Test Engineer:	85502/44389
Test Date:	2023/11/10

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Mid	5290	89.92	77.9420	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Mid	5290	24.00	24.00	30.00	24.00	8.74	11.00	8.74

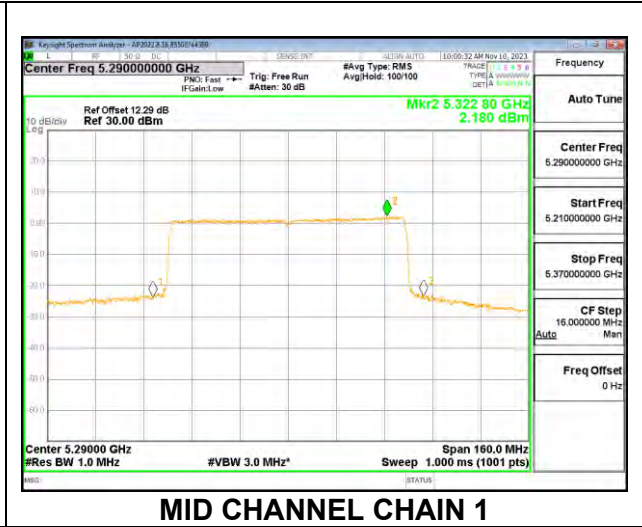
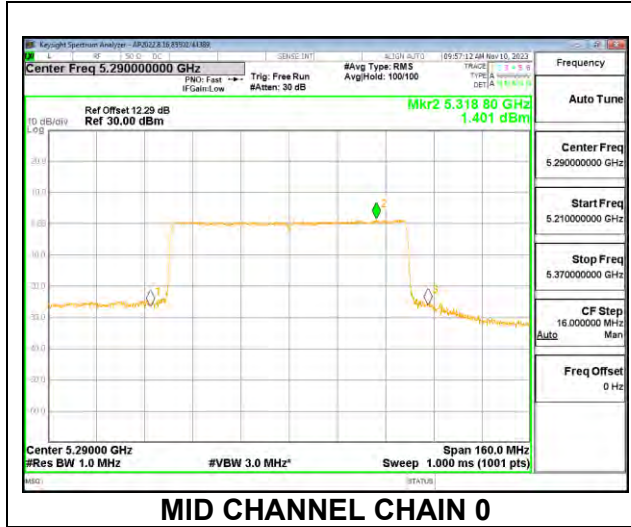
Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margi (dB)
Mid	5290	11.90	12.50	15.22	24.00	-8.78

PSD Results

Channel	Frequency (MHz)	Chain 0 Meas PSD (dBm/ 1MHz)	Chain 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margi n (dB)
Mid	5290	1.40	2.18	4.82	8.74	-3.92



2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE (FCC+IC): SU

Test Engineer:	85502/44389
Test Date:	2023/11/10

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Mid	5290	89.92	77.9420	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Mid	5290	24.00	24.00	30.00	24.00	8.74	11.00	8.74

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margi (dB)
Mid	5290	12.47	12.89	15.70	24.00	-8.30

9.5.16. 802.11be EHT160 MODE 2TX IN THE 5.2/5.3GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE (FCC+IC): 996T+484T (CONTIGUOUS)

Test Engineer:	28076/44389
Test Date:	10/18/2023 and 10/19/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Mid	5250	117.51	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED EIRP Limit (dBm)	Max ISED Power (dBm)	Power Limit (dBm)
Mid	5250	24.00	23.00	17.56	17.56

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Mid	5250	13.72	13.69	16.72	17.56	-0.84

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE (FCC+IC): 484T+996T (NON-CONTIGUOUS)

Test Engineer:	28076/44389
Test Date:	10/18/2023 and 10/19/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Mid	5250	117.51	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED EIRP Limit (dBm)	Max ISED Power (dBm)	Power Limit (dBm)
Mid	5250	24.00	23.00	17.56	17.56

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Mid	5250	12.66	13.05	15.87	17.56	-1.69

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE (FCC+IC): 996T+484T (NON-CONTIGUOUS)

Test Engineer:	28076/44389
Test Date:	10/18/2023 and 10/19/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Mid	5250	117.51	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED EIRP Limit (dBm)	Max ISED Power (dBm)	Power Limit (dBm)
Mid	5250	24.00	23.00	17.56	17.56

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Mid	5250	12.66	12.77	15.73	17.56	-1.83

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE (FCC+IC): 2x996T

Test Engineer:	85502/44389
Test Date:	2023/11/10

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Mid	5250	155.46	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED EIRP Limit (dBm)	Max ISED Power (dBm)	Power Limit (dBm)
Mid	5250	24.00	23.00	17.56	17.56

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Mid	5250	11.89	12.21	15.06	17.56	-2.50

2TX CHAIN 0 + CHAIN 1 CDD OFDMA MODE (FCC+IC): SU

Test Engineer:	85502/44389
Test Date:	2023/11/10

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Mid	5250	155.46	5.44	8.26

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED EIRP Limit (dBm)	Max ISED Power (dBm)	Power Limit (dBm)
Mid	5250	24.00	23.00	17.56	17.56

Output Power Results

Channel	Frequency (MHz)	Chain 0 Meas Power (dBm)	Chain 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Mid	5250	12.54	12.87	15.72	17.56	-1.84

9.5.17. 802.11a MODE IN THE 5.6 GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC)

Test Engineer:	85502/44389, 33499/44389
Test Date:	2023/11/22 – 12/04/2023

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5500	21.4800	16.6200	3.52	6.40
Mid	5580	21.7200	16.6400	3.52	6.40
High	5700	21.6800	16.5930	3.52	6.40
144	5720	15.9200	16.5821	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/1MHz)	ISED PSD Limit (dBm/1MHz)	PSD Limit (dBm/1MHz)
Low	5500	24.00	23.21	29.21	23.21	10.60	11.00	10.60
Mid	5580	24.00	23.21	29.21	23.21	10.60	11.00	10.60
High	5700	24.00	23.20	29.20	23.20	10.60	11.00	10.60
144	5720	23.02	23.20	29.20	23.02	10.60	11.00	10.60

Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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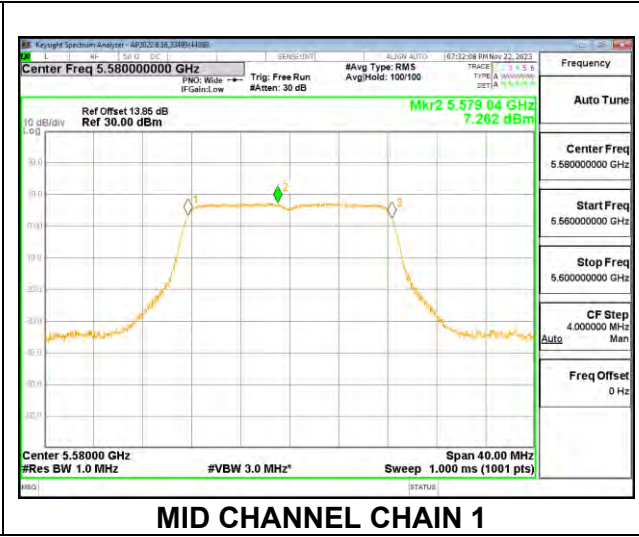
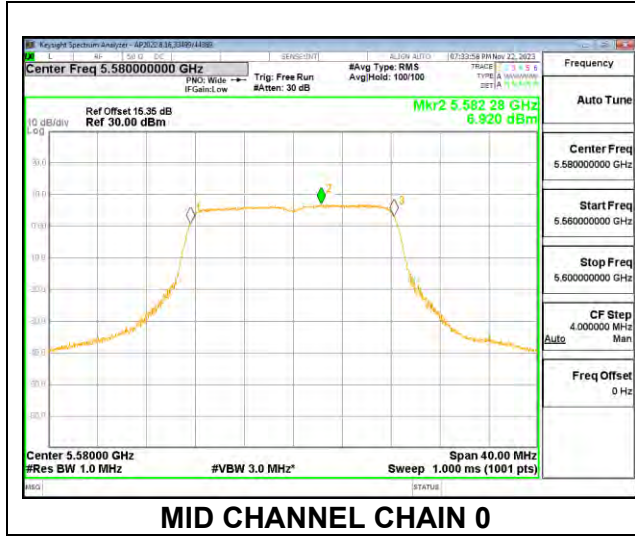
Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5500	16.21	16.79	19.52	23.21	-3.69
Mid	5580	17.65	18.30	21.00	23.21	-2.21
High	5700	17.51	17.33	20.43	23.20	-2.77
144	5720	18.40	18.78	21.60	23.02	-1.41

PSD Results

Channel	Frequency (MHz)	CHAIN 0 Meas PSD (dBm/1MHz)	CHAIN 1 Meas PSD (dBm/1MHz)	Total Corr'd PSD (dBm/1MHz)	PSD Limit (dBm/1MHz)	PSD Margin (dB)
Low	5500	6.533	7.375	9.985	10.60	-0.62
Mid	5580	6.920	7.262	10.105	10.60	-0.50
High	5700	7.852	5.963	10.020	10.60	-0.58
144	5720	6.478	6.780	9.642	10.60	-0.96

MID CHANNEL



9.5.18. 802.11n HT20 MODE IN THE 5.6 GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC)

Test Engineer:	85502/44389, 33499/44389
Test Date:	2023/11/22 – 12/04/2023

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5500	22.8800	17.7930	3.52	6.40
Mid	5580	22.9200	17.7150	3.52	6.40
High	5700	22.7200	17.7440	3.52	6.40
144	5720	16.4400	17.7740	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5500	24.00	23.50	29.50	23.50	10.60	11.00	10.60
Mid	5580	24.00	23.48	29.48	23.48	10.60	11.00	10.60
High	5700	24.00	23.49	29.49	23.49	10.60	11.00	10.60
144	5720	23.16	23.50	29.50	23.16	10.60	11.00	10.60

Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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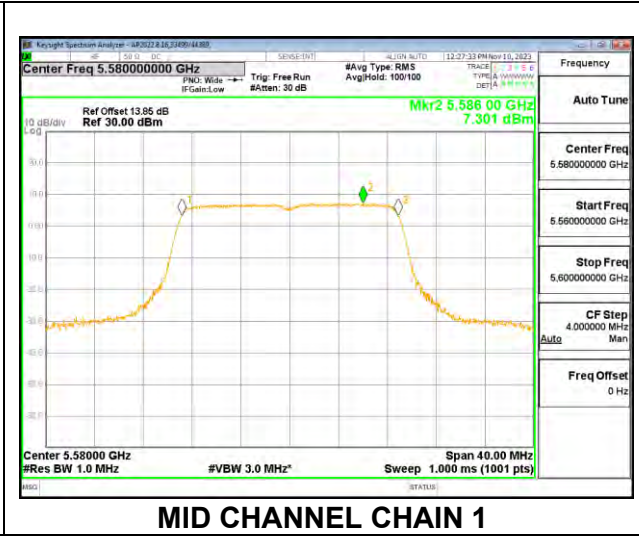
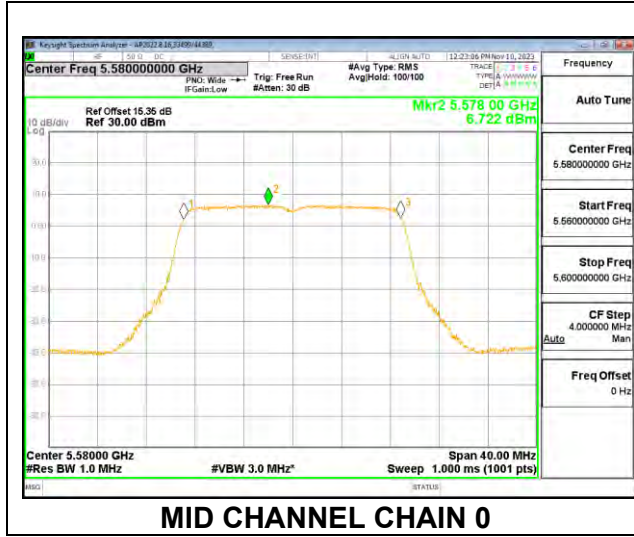
Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5500	15.97	16.41	19.21	23.50	-4.30
Mid	5580	17.22	17.85	20.56	23.48	-2.93
High	5700	17.56	17.39	20.49	23.49	-3.00
144	5720	18.31	18.66	21.50	23.16	-1.66

PSD Results

Channel	Frequency (MHz)	CHAIN 0 Meas PSD (dBm/ 1MHz)	CHAIN 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Low	5500	6.407	7.005	9.727	10.60	-0.87
Mid	5580	6.722	7.301	10.031	10.60	-0.57
High	5700	6.895	6.914	9.915	10.60	-0.69
144	5720	5.843	5.996	8.930	10.60	-1.67

MID CHANNEL



9.5.19. 802.11n HT40 MODE IN THE 5.6 GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC)

Test Engineer:	85502/44389, 33499/44389
Test Date:	2023/11/22 – 12/04/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5510	46.0800	36.3460	3.52	6.40
Mid	5550	45.9200	36.4040	3.52	6.40
High	5670	45.7600	36.2660	3.52	6.40
142	5710	38.2000	36.4540	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/1MHz)	ISED PSD Limit (dBm/1MHz)	PSD Limit (dBm/1MHz)
Low	5510	24.00	24.00	30.00	24.00	10.60	11.00	10.60
Mid	5550	24.00	24.00	30.00	24.00	10.60	11.00	10.60
High	5670	24.00	24.00	30.00	24.00	10.60	11.00	10.60
142	5710	24.00	24.00	30.00	24.00	10.60	11.00	10.60

Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5510	14.97	15.92	18.48	24.00	-5.52
Mid	5550	18.29	19.77	22.10	24.00	-1.90
High	5670	17.51	17.64	20.59	24.00	-3.41
142	5710	18.71	18.66	21.70	24.00	-2.30

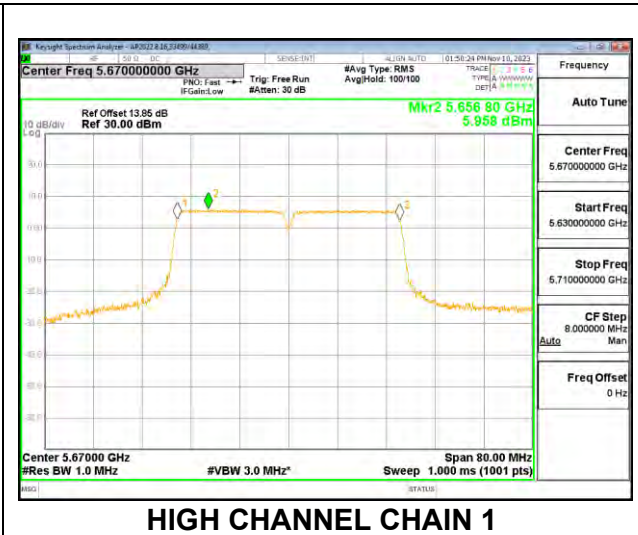
PSD Results

Channel	Frequency (MHz)	CHAIN 0 Meas PSD (dBm/1MHz)	CHAIN 1 Meas PSD (dBm/1MHz)	Total Corr'd PSD (dBm/1MHz)	PSD Limit (dBm/1MHz)	PSD Margin (dB)
Low	5510	4.16	3.95	7.06	10.60	-3.54
Mid	5550	4.54	5.36	7.98	10.60	-2.62
High	5670	5.56	5.96	8.77	10.60	-1.83
142	5710	3.66	3.27	6.48	10.60	-4.12

HIGH CHANNEL



HIGH CHANNEL CHAIN 0



HIGH CHANNEL CHAIN 1

9.5.20. 802.11ac VHT80 MODE IN THE 5.6 GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC)

Test Engineer:	85502/44389, 33499/44389
Test Date:	2023/11/22 – 12/04/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5530	93.1200	76.1940	3.52	6.40
High	5610	93.7600	76.0700	3.52	6.40
138	5690	18.8800	76.2150	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5530	24.00	24.00	30.00	24.00	10.60	11.00	10.60
High	5610	24.00	24.00	30.00	24.00	10.60	11.00	10.60
138	5690	23.76	24.00	30.00	23.76	10.60	11.00	10.60

Duty Cycle CF (dB)	0.11	Included in Calculations of Corr'd Power & PSD
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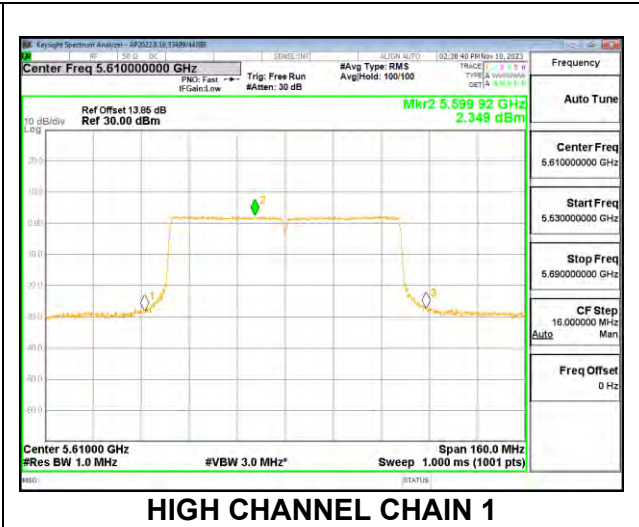
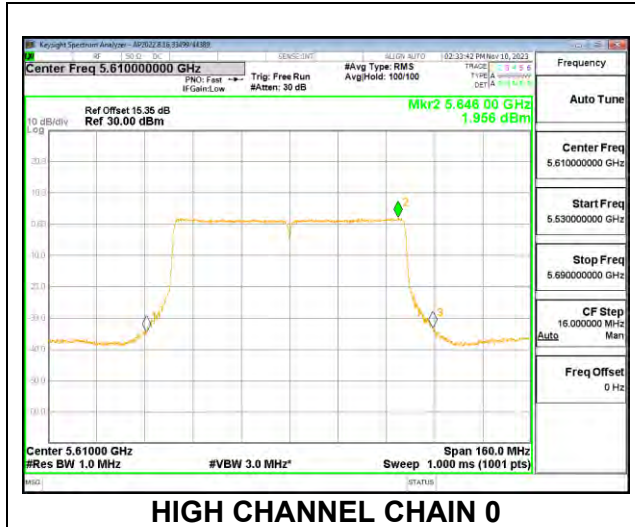
Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5530	15.52	15.95	18.75	24.00	-5.25
High	5610	18.13	18.59	21.38	24.00	-2.62
138	5690	18.45	18.41	21.44	23.76	-2.32

PSD Results

Channel	Frequency (MHz)	CHAIN 0 Meas PSD (dBm/ 1MHz)	CHAIN 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Low	5530	1.21	2.21	4.86	10.60	-5.74
High	5610	1.96	2.35	5.28	10.60	-5.32
138	5690	0.53	2.49	4.74	10.60	-5.86

HIGH CHANNEL



9.5.21. 802.11ac VHT160 MODE IN THE 5.6 GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC)

Test Engineer:	85502/44389, 33499/44389
Test Date:	2023/11/22 – 12/04/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
MID	5570	178.88	155.35	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)
MID	5570	24.00	24.00	30.00	24.00

Duty Cycle CF (dB)	0.43	Included in Calculations of Corr'd Power & PSD
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Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margi n (dB)
MID	5570	14.34	14.85	17.61	24.00	-6.39

9.5.22. 802.11be EHT20 MODE IN THE 5.6 GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC) – 26T

Test Engineer:	84740/44389, 33499/44389, 85502,/44389
Test Date:	2023/12/1 – 12/04/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5500	20.24	2.29	3.52	6.40
Mid	5580	20.24	2.28	3.52	6.40
High	5700	20.40	2.25	3.52	6.40
144	5720	20.24	2.21	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/1MHz)	ISED PSD Limit (dBm/1MHz)	PSD Limit (dBm/1MHz)
Low	5500	24.00	14.60	20.60	14.60	10.60	11.00	10.60
Mid	5580	24.00	14.57	20.57	14.57	10.60	11.00	10.60
High	5700	24.00	14.53	20.53	14.53	10.60	11.00	10.60
144	5720	24.00	14.45	20.45	14.45	10.60	11.00	10.60

Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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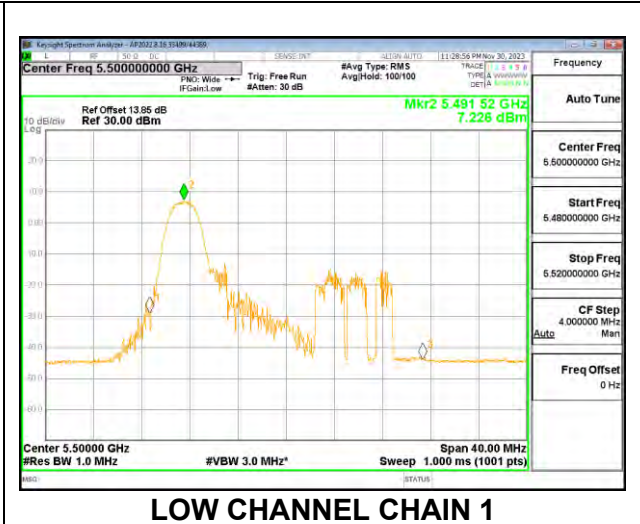
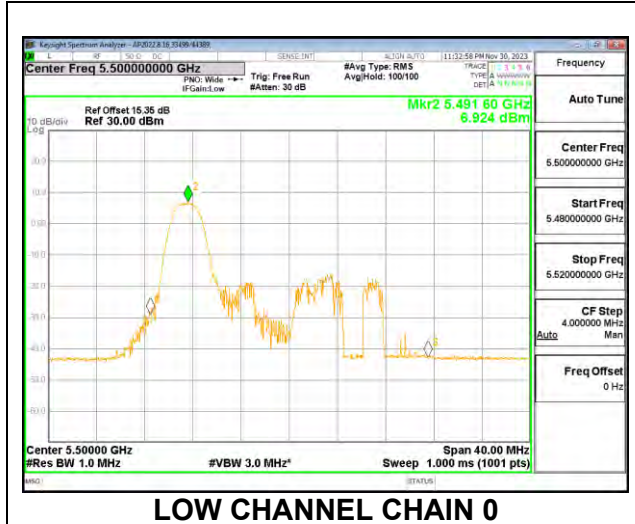
Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5500	9.36	9.81	12.60	14.60	-2.00
Mid	5580	8.96	9.33	12.16	14.57	-2.42
High	5700	8.39	8.17	11.29	14.53	-3.24
144	5720	9.76	9.35	12.57	14.45	-1.88

PSD Results

Channel	Frequency (MHz)	CHAIN 0 Meas PSD (dBm/1MHz)	CHAIN 1 Meas PSD (dBm/1MHz)	Total Corr'd PSD (dBm/1MHz)	PSD Limit (dBm/1MHz)	PSD Margin (dB)
Low	5500	6.924	7.226	10.088	10.60	-0.51
Mid	5580	6.143	7.373	9.812	10.60	-0.79
High	5700	6.657	6.445	9.563	10.60	-1.04
144	5720	5.793	7.578	9.787	10.60	-0.81

LOW CHANNEL



2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC) – 52T

Test Engineer:	84740/44389, 33499/44389, 85502,/44389
Test Date:	2023/12/1 – 12/04/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5500	20.52	4.251	3.52	6.40
Mid	5580	20.56	4.098	3.52	6.40
High	5700	20.48	4.055	3.52	6.40
144	5720	16.24	4.067	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/1MHz)	ISED PSD Limit (dBm/1MHz)	PSD Limit (dBm/1MHz)
Low	5500	24.00	17.28	23.28	17.28	10.60	11.00	10.60
Mid	5580	24.00	17.13	23.13	17.13	10.60	11.00	10.60
High	5700	24.00	17.08	23.08	17.08	10.60	11.00	10.60
144	5720	23.11	17.09	23.09	17.09	10.60	11.00	10.60

Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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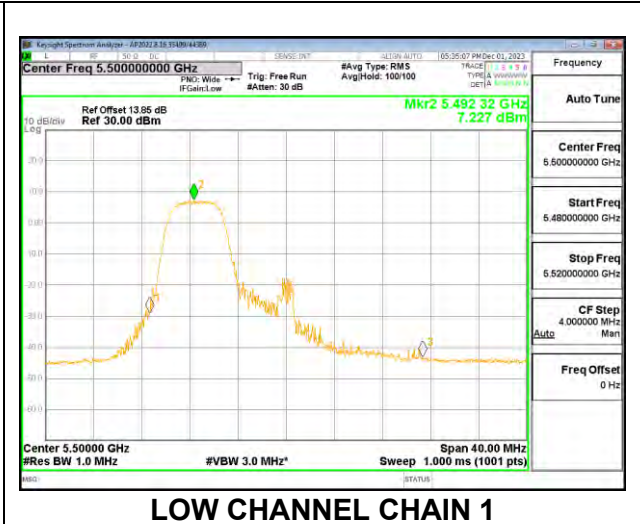
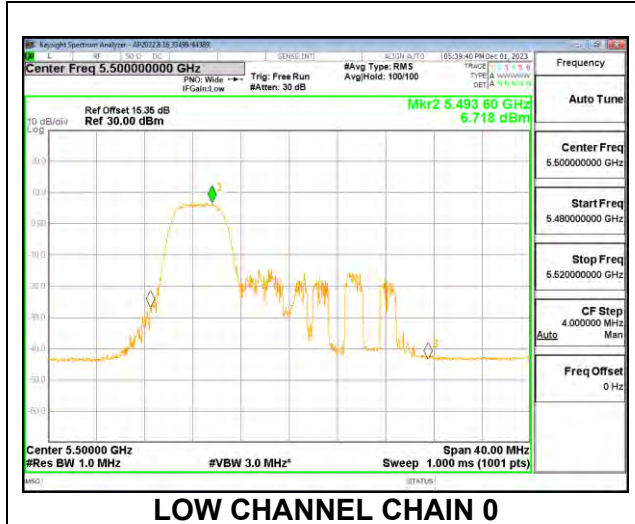
Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5500	12.04	12.68	15.38	17.28	-1.90
Mid	5580	12.26	12.94	15.62	17.13	-1.50
High	5700	11.82	11.98	14.91	17.08	-2.17
144	5720	12.53	12.35	15.45	17.09	-1.64

PSD Results

Channel	Frequency (MHz)	CHAIN 0 Meas PSD (dBm/1MHz)	CHAIN 1 Meas PSD (dBm/1MHz)	Total Corr'd PSD (dBm/1MHz)	PSD Limit (dBm/1MHz)	PSD Margin (dB)
Low	5500	6.718	7.227	9.990	10.60	-0.61
Mid	5580	6.694	6.925	9.821	10.60	-0.78
High	5700	6.757	6.958	9.869	10.60	-0.73
144	5720	5.917	7.377	9.718	10.60	-0.88

LOW CHANNEL



2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC) – 52T + 26T

Test Engineer:	84740/44389, 33499/44389, 85502./44389
Test Date:	2023/12/1 – 12/04/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5500	19.2000	6.1623	3.52	6.40
Mid	5580	19.2000	6.3068	3.52	6.40
High	5700	19.4400	6.2130	3.52	6.40
144	5720	14.6400	6.1741	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5500	23.83	18.90	24.90	18.90	10.60	11.00	10.60
Mid	5580	23.83	19.00	25.00	19.00	10.60	11.00	10.60
High	5700	23.89	18.93	24.93	18.93	10.60	11.00	10.60
144	5720	22.66	18.91	24.91	18.91	10.60	11.00	10.60

Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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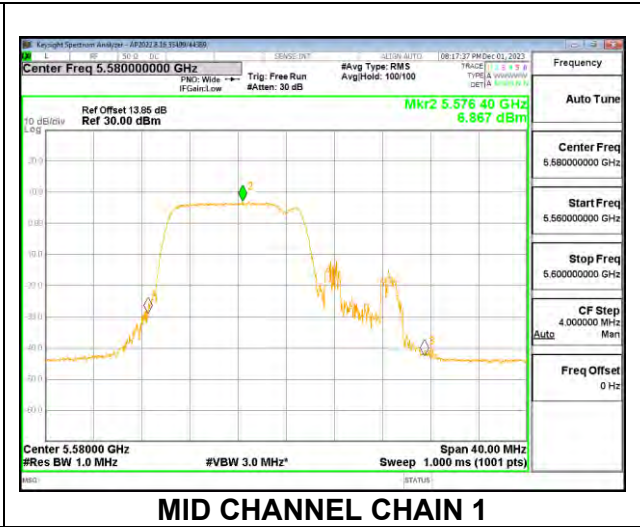
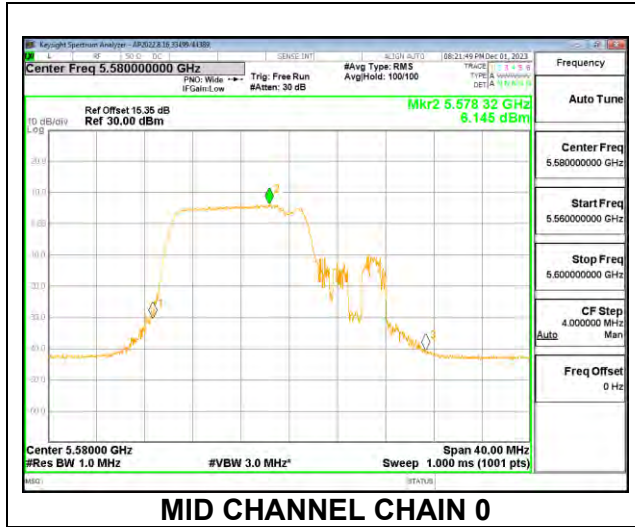
Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5500	12.31	12.73	15.54	18.90	-3.36
Mid	5580	12.71	12.56	15.65	19.00	-3.35
High	5700	12.06	11.95	15.02	18.93	-3.92
144	5720	12.31	12.19	15.26	18.91	-3.65

PSD Results

Channel	Frequency (MHz)	CHAIN 0 Meas PSD (dBm/ 1MHz)	CHAIN 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Low	5500	5.046	5.898	8.503	10.60	-2.10
Mid	5580	6.145	6.867	9.531	10.60	-1.07
High	5700	4.973	5.105	8.050	10.60	-2.55
144	5720	4.919	5.988	8.497	10.60	-2.10

MID CHANNEL



2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC) – 106T

Test Engineer:	84740/44389, 33499/44389, 85502,/44389
Test Date:	2023/12/1 – 12/04/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5500	20.8000	8.3928	3.52	6.40
Mid	5580	20.7200	8.3450	3.52	6.40
High	5700	21.0000	8.3094	3.52	6.40
144	5720	16.2800	8.1587	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5500	24.00	20.24	26.24	20.24	10.60	11.00	10.60
Mid	5580	24.00	20.21	26.21	20.21	10.60	11.00	10.60
High	5700	24.00	20.20	26.20	20.20	10.60	11.00	10.60
144	5720	23.12	20.12	26.12	20.12	10.60	11.00	10.60

Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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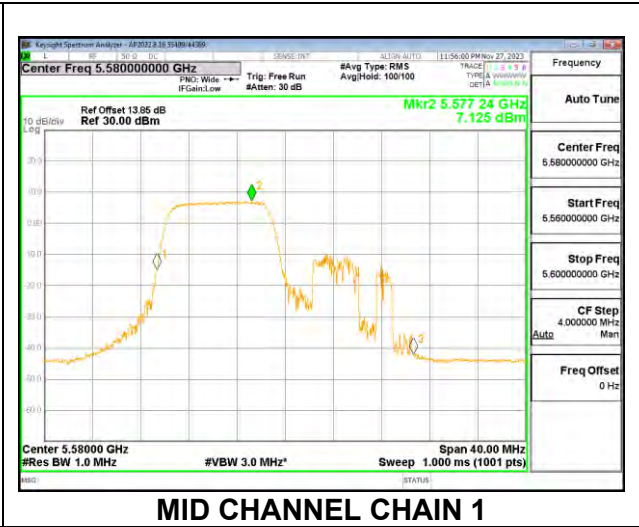
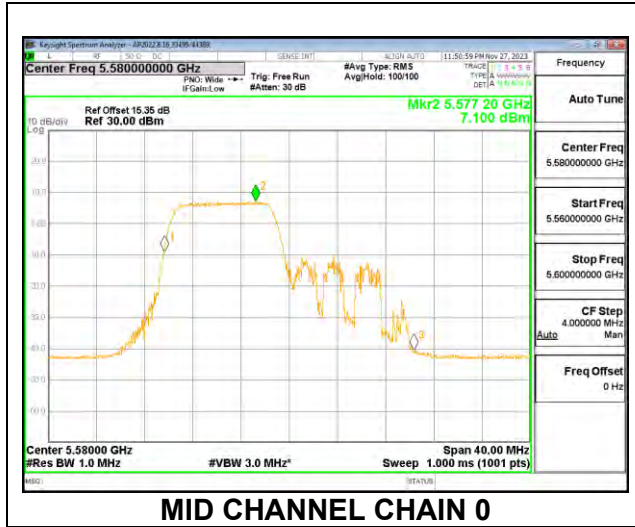
Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5500	15.16	15.68	18.44	20.24	-1.80
Mid	5580	15.26	15.93	18.62	20.21	-1.60
High	5700	15.75	15.85	18.81	20.20	-1.39
144	5720	15.27	14.99	18.14	20.12	-1.97

PSD Results

Channel	Frequency (MHz)	CHAIN 0 Meas PSD (dBm/ 1MHz)	CHAIN 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Low	5500	6.732	7.180	9.972	10.60	-0.63
Mid	5580	7.100	7.125	10.123	10.60	-0.48
High	5700	7.327	6.115	9.773	10.60	-0.83
144	5720	6.193	7.791	10.075	10.60	-0.52

MID CHANNEL



2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC) – 106T + 26T

Test Engineer:	84740/44389, 33499/44389, 85502,/44389
Test Date:	2023/12/1 – 12/04/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5500	21.1600	10.7190	3.52	6.40
Mid	5580	21.0800	10.7530	3.52	6.40
High	5700	21.4000	10.7530	3.52	6.40
144	5720	16.2800	10.8960	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/1MHz)	ISED PSD Limit (dBm/1MHz)	PSD Limit (dBm/1MHz)
Low	5500	24.00	21.30	27.30	21.30	10.60	11.00	10.60
Mid	5580	24.00	21.32	27.32	21.32	10.60	11.00	10.60
High	5700	24.00	21.32	27.32	21.32	10.60	11.00	10.60
144	5720	23.12	21.37	27.37	21.37	10.60	11.00	10.60

Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5500	15.26	15.94	18.62	21.30	-2.68
Mid	5580	15.43	15.90	18.68	21.32	-2.63
High	5700	16.01	15.89	18.96	21.32	-2.35
144	5720	15.30	15.11	18.22	21.37	-3.16

PSD Results

Channel	Frequency (MHz)	CHAIN 0 Meas PSD (dBm/1MHz)	CHAIN 1 Meas PSD (dBm/1MHz)	Total Corr'd PSD (dBm/1MHz)	PSD Limit (dBm/1MHz)	PSD Margin (dB)
Low	5500	5.950	6.553	9.272	10.60	-1.33
Mid	5580	4.418	6.515	8.602	10.60	-2.00
High	5700	5.452	6.568	9.056	10.60	-1.54
144	5720	5.437	6.752	9.154	10.60	-1.45

LOW CHANNEL



LOW CHANNEL CHAIN 0



LOW CHANNEL CHAIN 1

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC) – 242T

Test Engineer:	84740/44389, 33499/44389, 85502,/44389
Test Date:	2023/12/1 – 12/04/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5500	22.6400	18.8950	3.52	6.40
Mid	5580	22.6400	18.7316	3.52	6.40
High	5700	22.5600	18.8739	3.52	6.40
144	5720	16.4800	19.0020	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5500	24.00	23.76	29.76	23.76	10.60	11.00	10.60
Mid	5580	24.00	23.73	29.73	23.73	10.60	11.00	10.60
High	5700	24.00	23.76	29.76	23.76	10.60	11.00	10.60
144	5720	23.17	23.79	29.79	23.17	10.60	11.00	10.60

Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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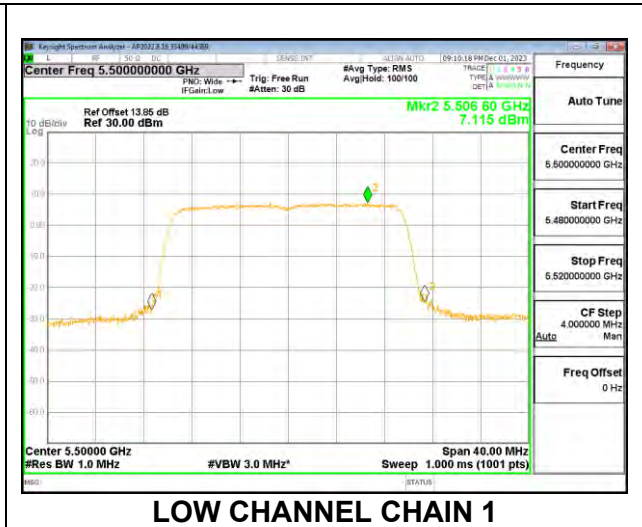
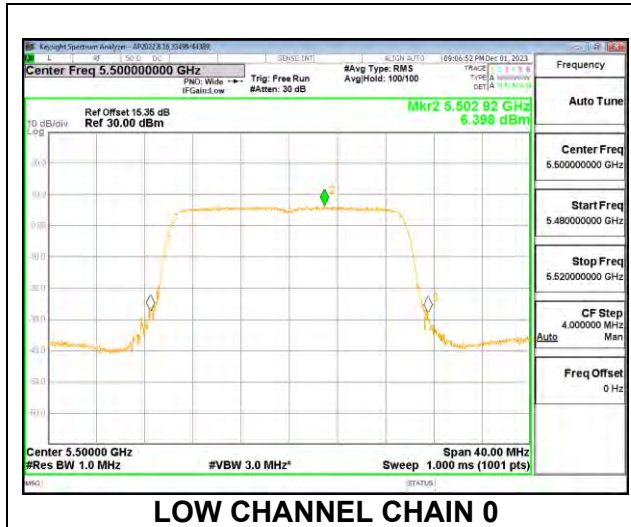
Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5500	16.11	16.61	19.38	23.76	-4.39
Mid	5580	17.73	18.42	21.10	23.73	-2.63
High	5700	17.12	17.28	20.21	23.76	-3.55
144	5720	18.40	18.71	21.57	23.17	-1.60

PSD Results

Channel	Frequency (MHz)	CHAIN 0 Meas PSD (dBm/ 1MHz)	CHAIN 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Low	5500	6.398	7.115	9.782	10.60	-0.82
Mid	5580	6.420	6.724	9.585	10.60	-1.02
High	5700	6.410	6.231	9.332	10.60	-1.27
144	5720	6.054	7.286	9.724	10.60	-0.88

LOW CHANNEL



9.5.23. 802.11be EHT40 MODE IN THE 5.6 GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC) – 484T

Test Engineer:	84740/44389, 33499/44389, 85502,/44389
Test Date:	2023/12/1 – 12/04/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5510	45.3600	37.6792	3.52	6.40
Mid	5550	46.0800	37.7952	3.52	6.40
High	5670	45.2000	37.6761	3.52	6.40
142	5710	37.4000	37.8110	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5510	24.00	24.00	30.00	24.00	10.60	11.00	10.60
Mid	5550	24.00	24.00	30.00	24.00	10.60	11.00	10.60
High	5670	24.00	24.00	30.00	24.00	10.60	11.00	10.60
142	5710	24.00	24.00	30.00	24.00	10.60	11.00	10.60

Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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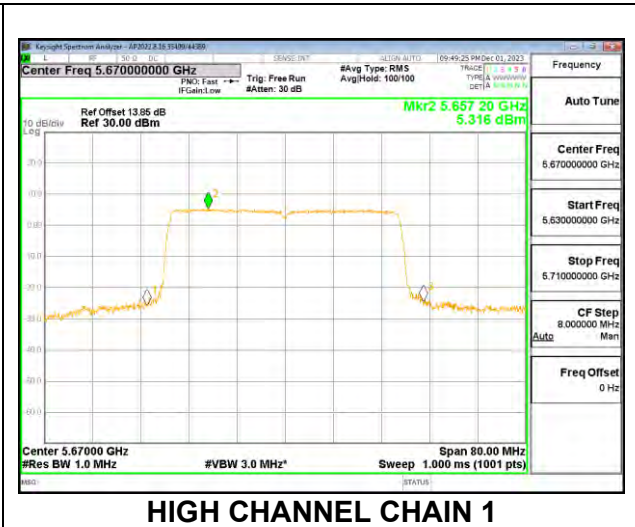
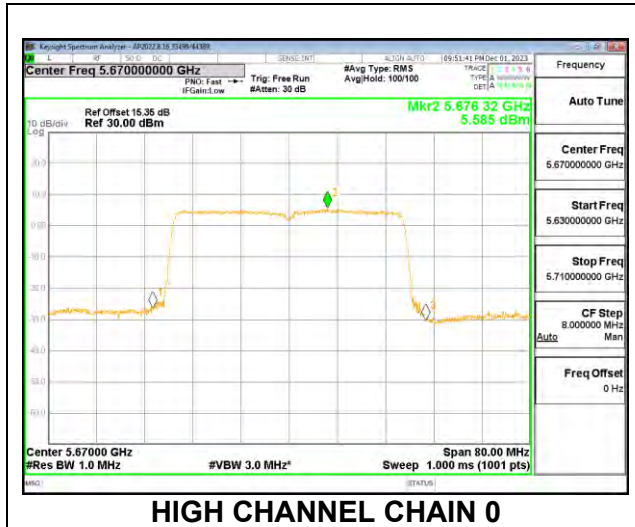
Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5510	13.22	14.05	16.67	24.00	-7.33
Mid	5550	17.43	18.05	20.76	24.00	-3.24
High	5670	18.71	18.80	21.77	24.00	-2.23
142	5710	19.10	18.66	21.90	24.00	-2.10

PSD Results

Channel	Frequency (MHz)	CHAIN 0 Meas PSD (dBm/ 1MHz)	CHAIN 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Low	5510	4.43	5.09	7.78	10.60	-2.82
Mid	5550	4.34	5.67	8.06	10.60	-2.54
High	5670	5.59	5.32	8.46	10.60	-2.14
142	5710	3.89	5.06	7.53	10.60	-3.07

HIGH CHANNEL



9.5.24. 802.11be EHT80 MODE IN THE 5.6 GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC) – 484T + 242T (CONTIGUOUS)

Test Engineer:	85502/44389
Test Date:	2023/12/05

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5530	85.2800	57.9860	3.52	6.40
High	5610	84.9600	58.0860	3.52	6.40
138	5690	81.4000	58.5210	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5530	24.00	24.00	30.00	24.00	10.60	11.00	10.60
High	5610	24.00	24.00	30.00	24.00	10.60	11.00	10.60
138	5690	24.00	24.00	30.00	24.00	10.60	11.00	10.60

Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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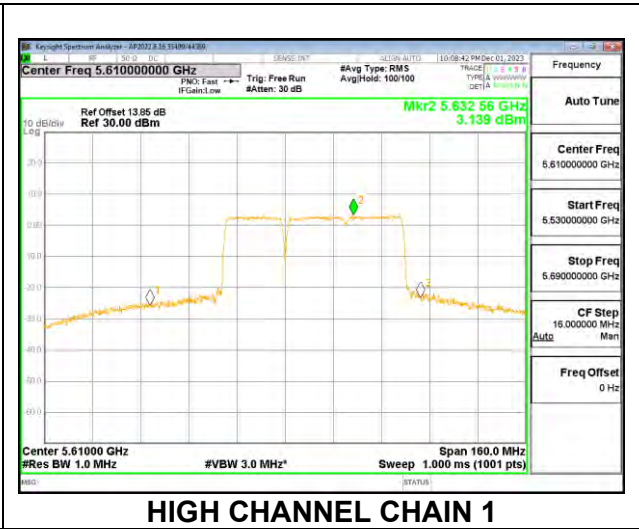
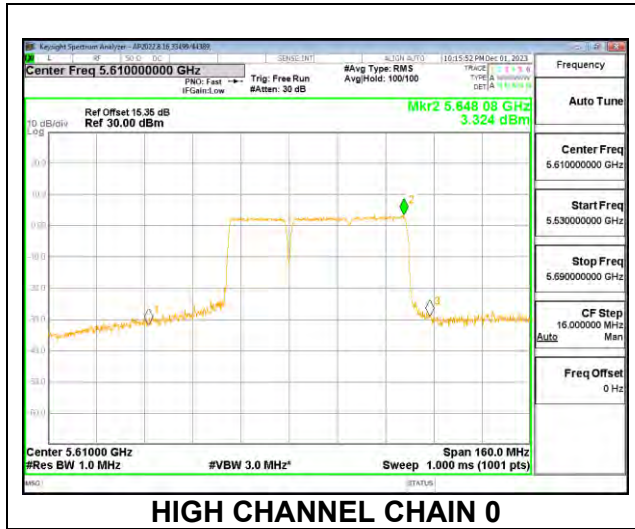
Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5530	12.16	12.97	15.59	24.00	-8.41
High	5610	18.37	18.38	21.39	24.00	-2.61
138	5690	18.98	19.20	22.10	24.00	-1.90

PSD Results

Channel	Frequency (MHz)	CHAIN 0 Meas PSD (dBm/ 1MHz)	CHAIN 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margin (dB)
Low	5530	1.94	3.32	5.70	10.60	-4.90
High	5610	3.22	3.14	6.19	10.60	-4.41
138	5690	1.49	3.71	5.75	10.60	-4.85

HIGH CHANNEL



2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC) – 242T + 484T (NON-CONTIGUOUS)

Test Engineer:	85502/44389
Test Date:	2023/12/05

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5530	85.2800	57.9860	3.52	6.40
High	5610	84.9600	58.0860	3.52	6.40
138	5690	81.4000	58.5210	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5530	24.00	24.00	30.00	24.00	10.60	11.00	10.60
High	5610	24.00	24.00	30.00	24.00	10.60	11.00	10.60
138	5690	24.00	24.00	30.00	24.00	10.60	11.00	10.60

Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5530	11.97	12.81	15.42	24.00	-8.58
High	5610	18.16	18.42	21.30	24.00	-2.70
138	5690	18.94	18.70	21.83	24.00	-2.17

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC) – 484T + 242T (NON-CONTIGUOUS)

Test Engineer:	85502/44389
Test Date:	2023/12/05

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5530	85.2800	57.9860	3.52	6.40
High	5610	84.9600	58.0860	3.52	6.40
138	5690	81.4000	58.5210	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5530	24.00	24.00	30.00	24.00	10.60	11.00	10.60
High	5610	24.00	24.00	30.00	24.00	10.60	11.00	10.60
138	5690	24.00	24.00	30.00	24.00	10.60	11.00	10.60

Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5530	11.96	12.77	15.39	24.00	-8.61
High	5610	18.14	18.36	21.26	24.00	-2.74
138	5690	18.85	18.85	21.86	24.00	-2.14

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC) – 996T

Test Engineer:	84740/44389, 33499/44389, 85502./44389
Test Date:	2023/12/1 – 12/04/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5530	89.6000	77.2883	3.52	6.40
High	5610	89.9200	77.4480	3.52	6.40
138	5690	80.1200	77.2340	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5530	24.00	24.00	30.00	24.00	10.60	11.00	10.60
High	5610	24.00	24.00	30.00	24.00	10.60	11.00	10.60
138	5690	24.00	24.00	30.00	24.00	10.60	11.00	10.60

Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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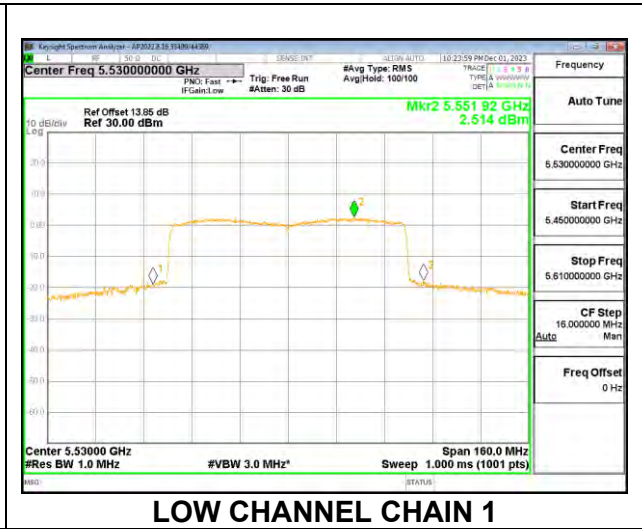
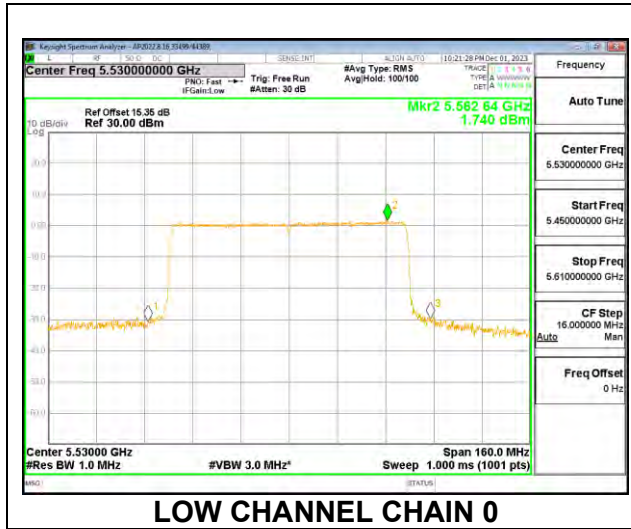
Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margi n (dB)
Low	5530	14.93	15.76	18.38	24.00	-5.62
High	5610	19.00	19.30	22.16	24.00	-1.84
138	5690	19.68	19.77	22.74	24.00	-1.26

PSD Results

Channel	Frequency (MHz)	CHAIN 0 Meas PSD (dBm/ 1MHz)	CHAIN 1 Meas PSD (dBm/ 1MHz)	Total Corr'd PSD (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)	PSD Margi n (dB)
Low	5530	1.740	2.514	5.155	10.60	-5.45
High	5610	1.480	1.881	4.695	10.60	-5.90
138	5690	-0.193	1.400	3.686	10.60	-6.91

LOW CHANNEL



2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC) – SU

Test Engineer:	84740/44389, 33499/44389, 85502,/44389
Test Date:	2023/12/1 – 12/04/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
Low	5530	89.6000	77.2883	3.52	6.40
High	5610	89.9200	77.4480	3.52	6.40
138	5690	80.1200	77.2340	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)	FCC PSD Limit (dBm/ 1MHz)	ISED PSD Limit (dBm/ 1MHz)	PSD Limit (dBm/ 1MHz)
Low	5530	24.00	24.00	30.00	24.00	10.60	11.00	10.60
High	5610	24.00	24.00	30.00	24.00	10.60	11.00	10.60
138	5690	24.00	24.00	30.00	24.00	10.60	11.00	10.60

Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margi n (dB)
Low	5530	15.46	16.42	18.98	24.00	-5.02
High	5610	18.01	18.47	21.26	24.00	-2.74
138	5690	18.23	18.48	21.37	24.00	-2.63

9.5.25. 802.11be EHT160 MODE IN THE 5.6 GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC) – 996T+484T (CONTIGUOUS)

Test Engineer:	84740/44389, 33499/44389, 85502,/44389
Test Date:	2023/12/1 – 12/21/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
MID	5570	147.84	117.50	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)
MID	5570	24.00	24.00	30.00	24.00

Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
MID	5570	13.09	13.44	16.28	24.00	-7.72

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC) – 484T + 996T (NON-CONTIGUOUS)

Test Engineer:	84740/44389, 33499/44389, 85502,/44389
Test Date:	2023/12/1 – 12/21/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
MID	5570	147.84	117.50	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)
MID	5570	24.00	24.00	30.00	24.00

Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
MID	5570	12.14	12.33	15.25	24.00	-8.75

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC) – 996T+484T (NON-CONTIGUOUS)

Test Engineer:	84740/44389, 33499/44389, 85502,/44389
Test Date:	2023/12/1 – 12/21/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
MID	5570	147.84	117.50	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)
MID	5570	24.00	24.00	30.00	24.00

Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
MID	5570	12.11	12.38	15.26	24.00	-8.74

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC) – 2*996T

Test Engineer:	84740/44389, 33499/44389, 85502,/44389
Test Date:	2023/12/1 – 12/04/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
MID	5570	176.3200	153.7563	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)
MID	5570	24.00	24.00	30.00	24.00

Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd PSD
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Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margi n (dB)
MID	5570	11.03	11.67	14.37	24.00	-9.63

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC) – SU

Test Engineer:	84740/44389, 33499/44389, 85502,/44389
Test Date:	2023/12/1 – 12/04/2023

Bandwidth and Antenna Gain

Channel	Frequency (MHz)	Min 26 dB BW (MHz)	Min 99% BW (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBi)
MID	5570	176.3200	153.7563	3.52	6.40

Limits

Channel	Frequency (MHz)	FCC Power Limit (dBm)	ISED Power Limit (dBm)	ISED EIRP Limit (dBm)	Power Limit (dBm)
MID	5570	24.00	24.00	30.00	24.00

Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margi n (dB)
MID	5570	15.22	15.81	18.54	24.00	-5.46

9.5.26. 802.11a MODE IN THE 5.8 GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC)

Test Engineer:	33499/44389
Test Date:	11-13-2023

Antenna Gain and Limit

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBm)	FCC/ISED Power Limit (dBm)	FCC/ISED PSD Limit (dBm/500KHz)
Low	5745	4.60	7.41	30.00	28.59
Mid	5785	4.60	7.41	30.00	28.59
High	5825	4.60	7.41	30.00	28.59
144	5720	4.60	7.41	30.00	28.59

Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5745	19.69	20.80	23.29	30.00	-6.71
Mid	5785	20.60	20.39	23.51	30.00	-6.49
High	5825	20.97	20.60	23.80	30.00	-6.20
144	5720	18.40	18.78	21.60	30.00	-8.40

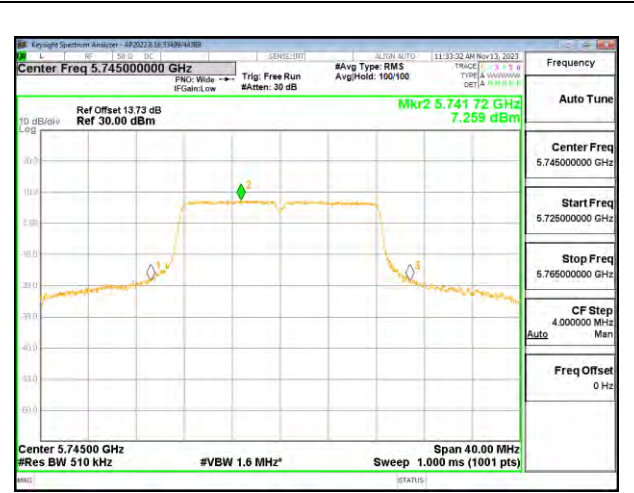
PSD Results

Channel	Frequency (MHz)	CHAIN 0 Meas PSD (dBm/500KHz)	CHAIN 1 Meas PSD (dBm/500KHz)	Total Corr'd PSD (dBm/500KHz)	PSD Limit (dBm/500KHz)	PSD Margin (dB)
Low	5745	8.254	7.259	10.795	28.59	-17.79
Mid	5785	7.922	6.911	10.456	28.59	-18.13
High	5825	7.917	7.564	10.754	28.59	-17.84
144	5720	3.674	3.553	6.624	28.59	-21.97

LOW CHANNEL



LOW CHANNEL CHAIN 0



LOW CHANNEL CHAIN 1

9.5.27. 802.11n HT20 MODE IN THE 5.8 GHz BAND

2TX CHAIN 0 + CHAIN 1 CDD MODE (FCC+IC)

Test Engineer:	33499/44389
Test Date:	2023/12/5

Antenna Gain and Limit

Channel	Frequency (MHz)	Directional Gain for Power (dBi)	Directional Gain for PSD (dBm)	FCC/ISED Power Limit (dBm)	FCC/ISED PSD Limit (dBm/500KHz)
Low	5745	4.60	7.41	30.00	28.59
Mid	5785	4.60	7.41	30.00	28.59
High	5825	4.60	7.41	30.00	28.59
144	5720	4.60	7.41	30.00	28.59

Duty Cycle CF (dB)	0.00	Included in Calculations of Corr'd Power & PSD
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Output Power Results

Channel	Frequency (MHz)	CHAIN 0 Meas Power (dBm)	CHAIN 1 Meas Power (dBm)	Total Corr'd Power (dBm)	Power Limit (dBm)	Power Margin (dB)
Low	5745	20.62	20.32	23.48	30.00	-6.52
Mid	5785	20.47	20.11	23.30	30.00	-6.70
High	5825	20.45	20.06	23.27	30.00	-6.73
144	5720	18.31	18.66	21.50	30.00	-8.50

PSD Results

Channel	Frequency (MHz)	CHAIN 0 Meas PSD (dBm/500KHz)	CHAIN 1 Meas PSD (dBm/500KHz)	Total Corr'd PSD (dBm/500KHz)	PSD Limit (dBm/500KHz)	PSD Margin (dB)
Low	5745	7.750	7.151	10.471	28.59	-18.12
Mid	5785	7.573	6.701	10.169	28.59	-18.42
High	5825	7.417	6.828	10.143	28.59	-18.45
144	5720	2.926	3.222	6.087	28.59	-22.50