

1. Voltage and current at RF part

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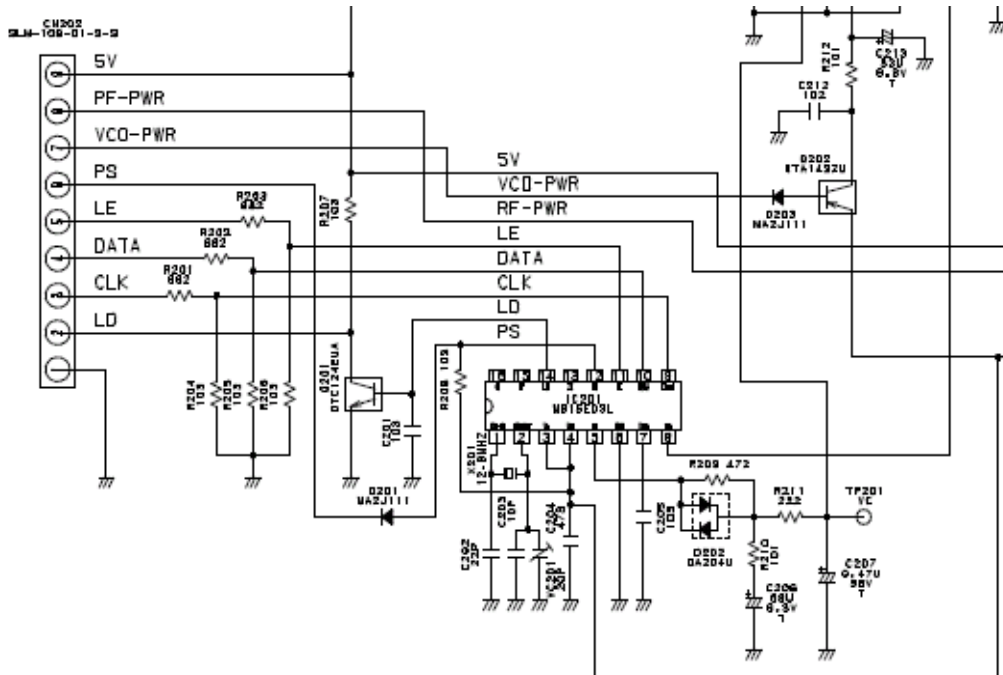
	Low (CH30-01)	Mid(CH32-01)	High(CH33-47)
Vce	3.8 V	3.9 V	3.8 V
Ic (mA)	12 mA	11 mA	12 mA

2. Description of main feature circuit

(1) Stabilization of frequency

Oscillating Method : PLL synthesized

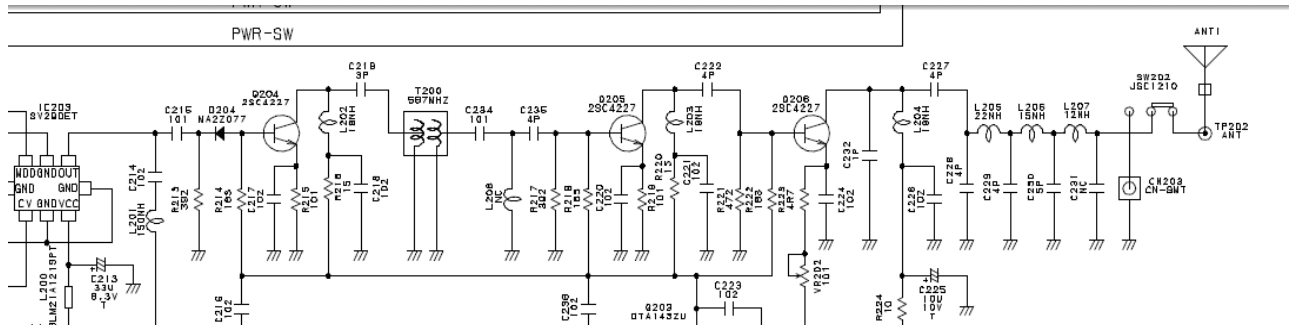
The stability of this oscillating method relies on the performance of crystal oscillator. We adopted in the 35BT Tokyo Denpa made crystal which has the frequency deviation ± 10 PPM or less under the environmental temperature $-10^{\circ}\text{C} \sim +60^{\circ}\text{C}$.



(2) Reducing Spurious radiation

In order for us to suppress the radiation, "LC" constructed dielectric filter is positioned behind the current amplification part.

This π type, tri-level filter works as a low passed filter.



(3) Limiting Audio modulation

Modulation limiting circuit made of diode is positioned on the side of the output part of AF low passed filter circuit in order to restrict the audio modulation.

