1. Voltage and current at RF part

TrQ10[2SC4227]

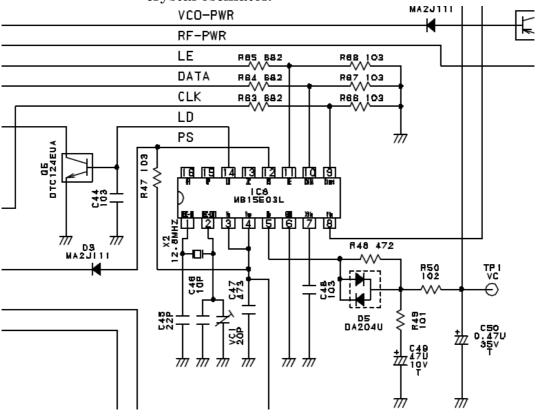
	Low (CH42-01)	Mid(CH43-47)	High(CH45-47)
Vce	4.2 V	4.2 V	4.1 V
Ic (mA)	9 m A	9 m A	11 m A

2. Description of main feature of circuit

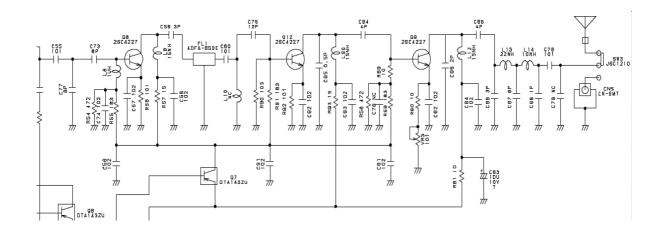
(1) Stabilization of frequency

Oscillator Method: PLL Synthesized

The stabilization of this oscillating method rely on crystal oscillator performance. Its accepting range is frequency deviation \pm 10PPM or less under the environment temperature minus 10degree to plus 60degree under using Tokyo Denpa made crystal oscillator.



(2) How to reduce the spurious radiation In order for us to suppress the radiation, dielectric filter is positioned between VCO output and currency amplification. It is like low passed filter which is formed with third layered π type filter.



(3) Restriction of Audio modulation The modulation is limited by limiter circuit design after fixing diodes on the output part of AF low cut filter.

