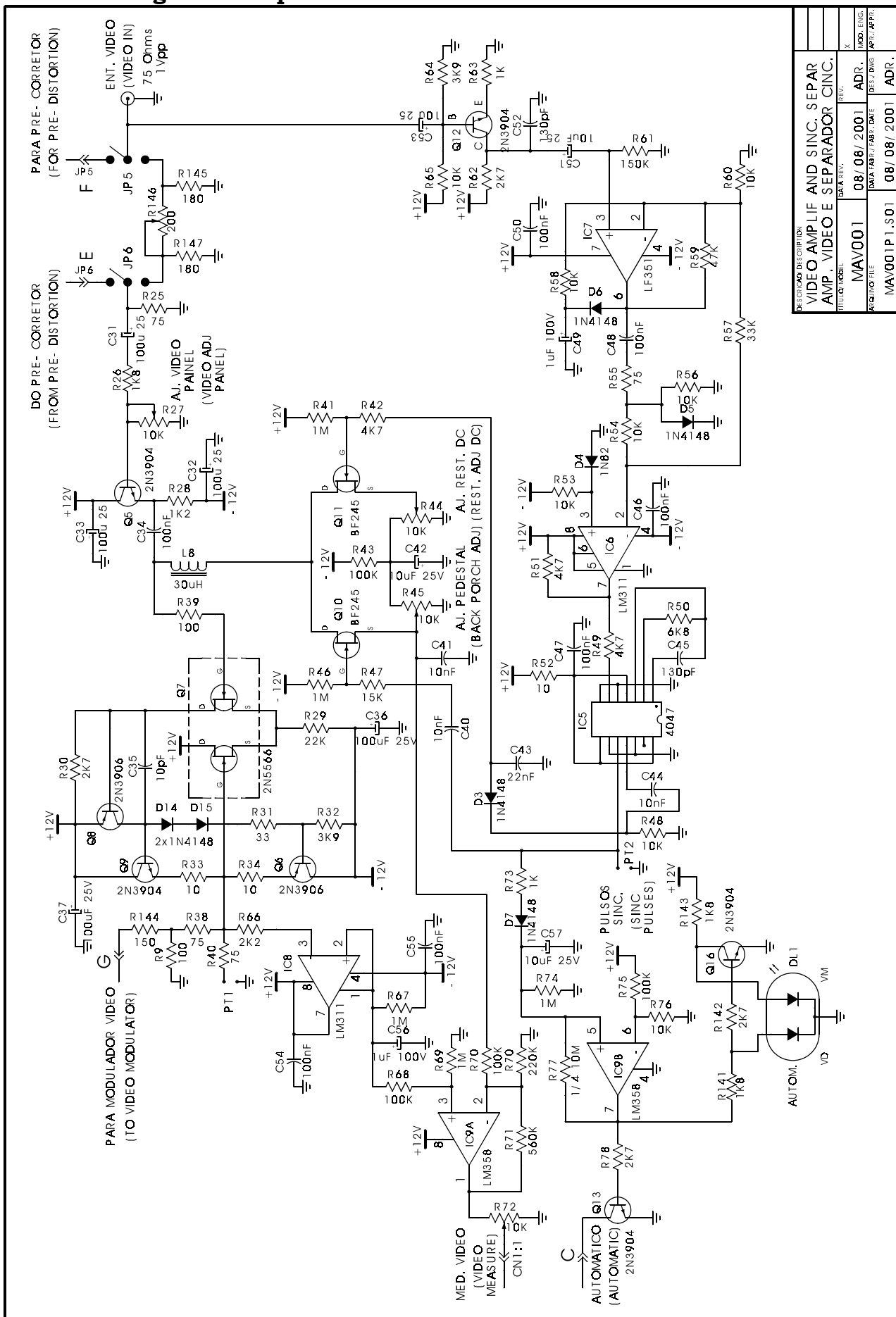
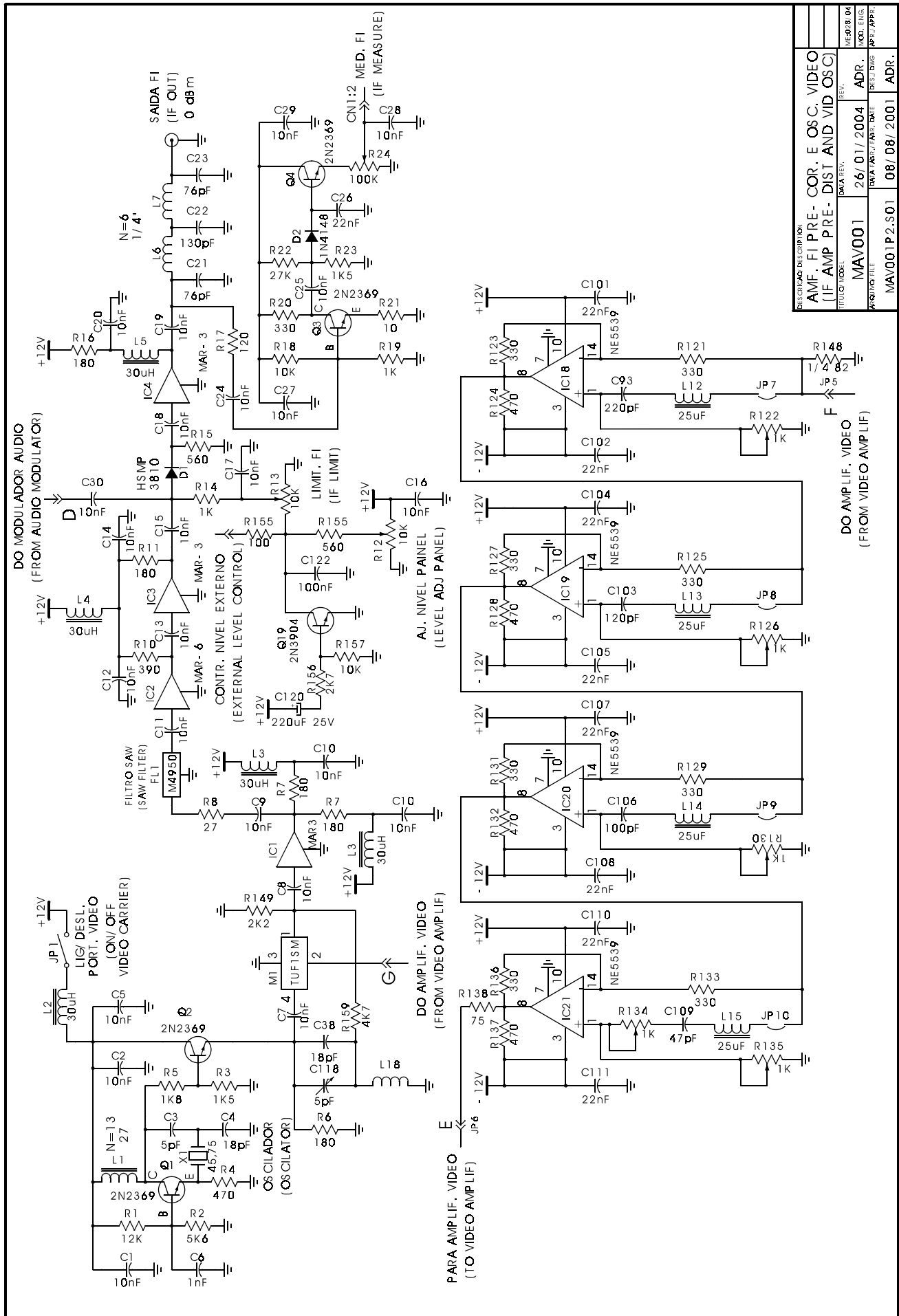


5. Circuit Diagram MAV001:

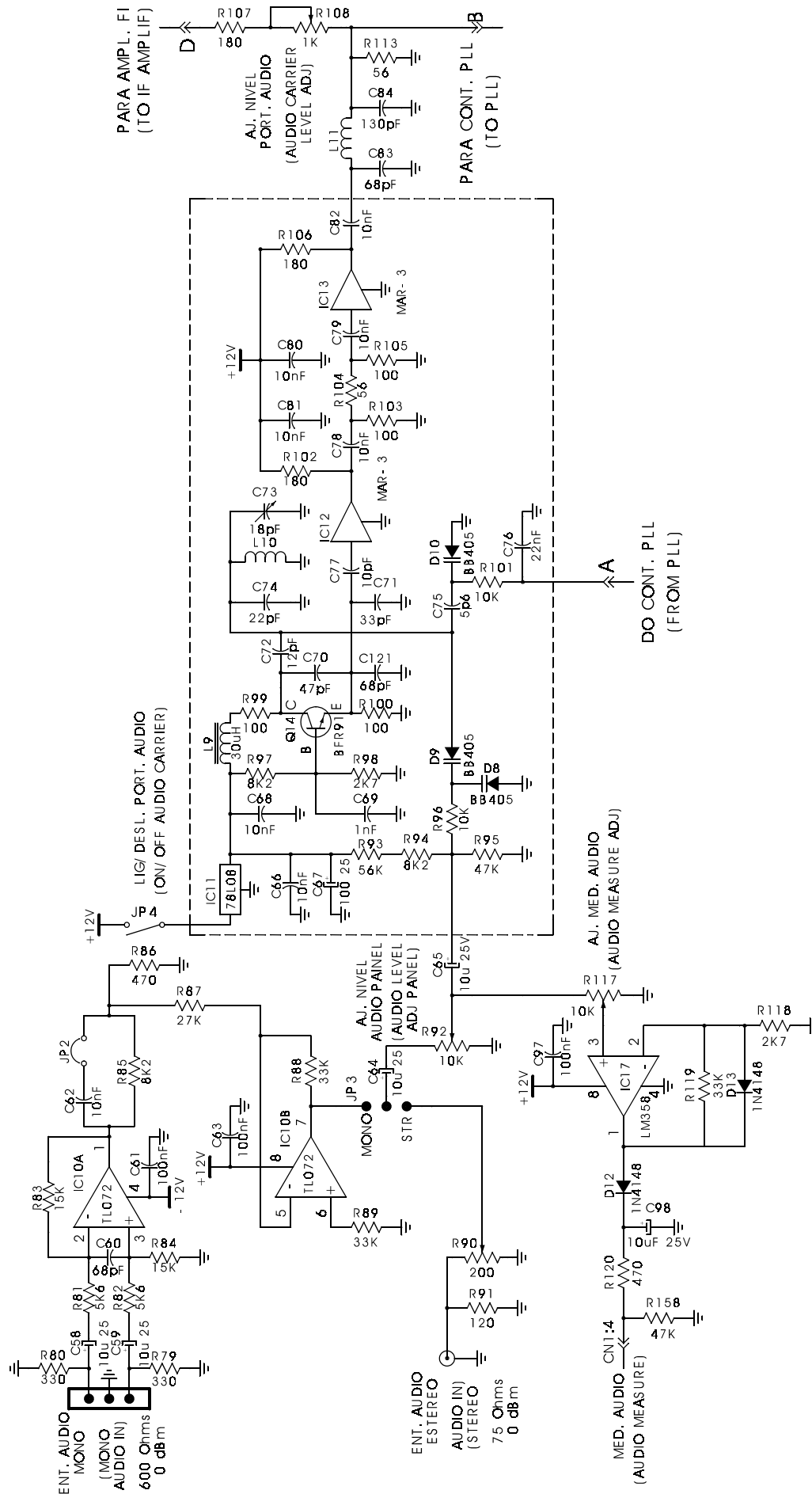
5.1. Circuit Diagram first part:



5.2 Circuit Diagram second part:

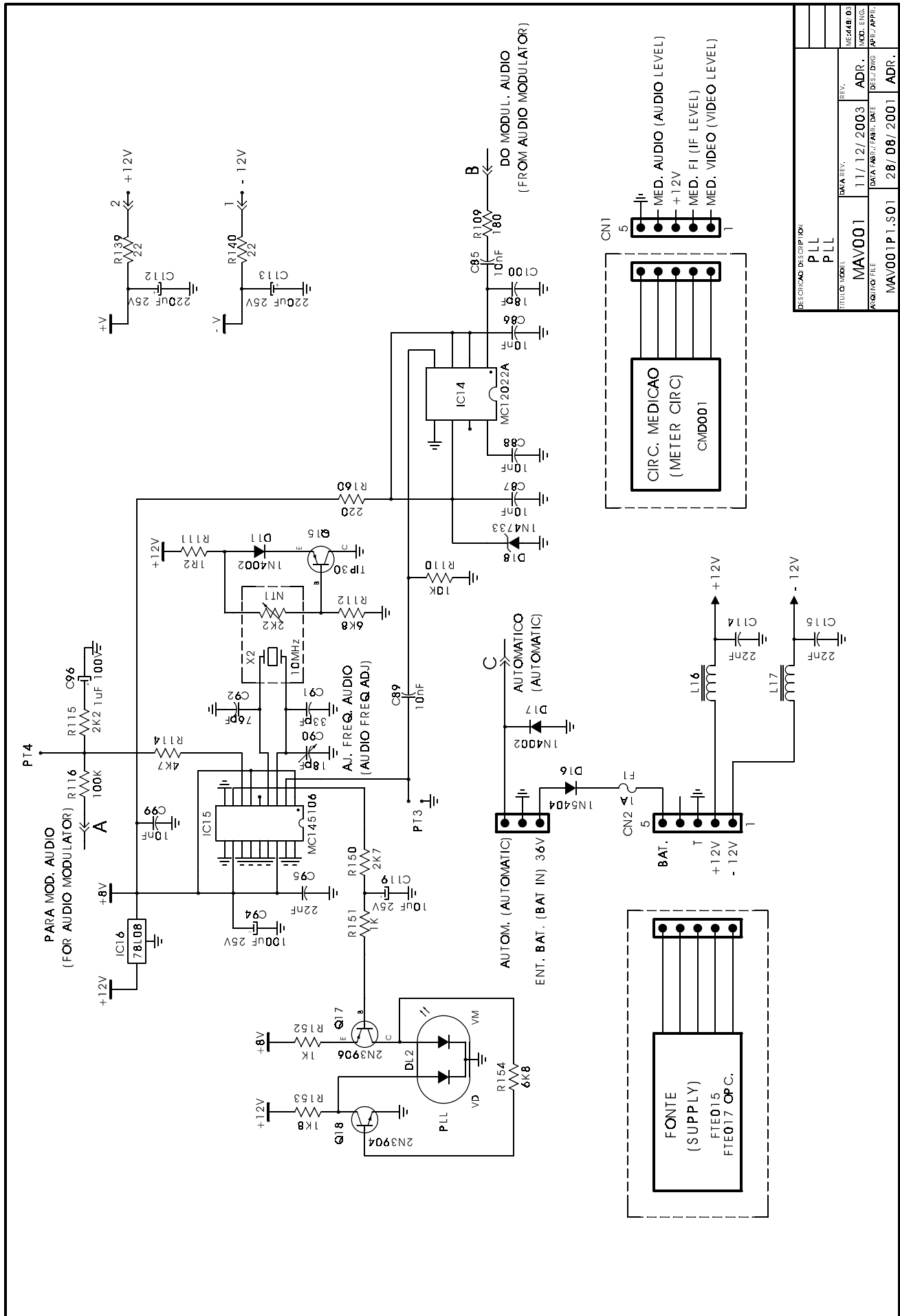


5.3. Circuit Diagram third part:

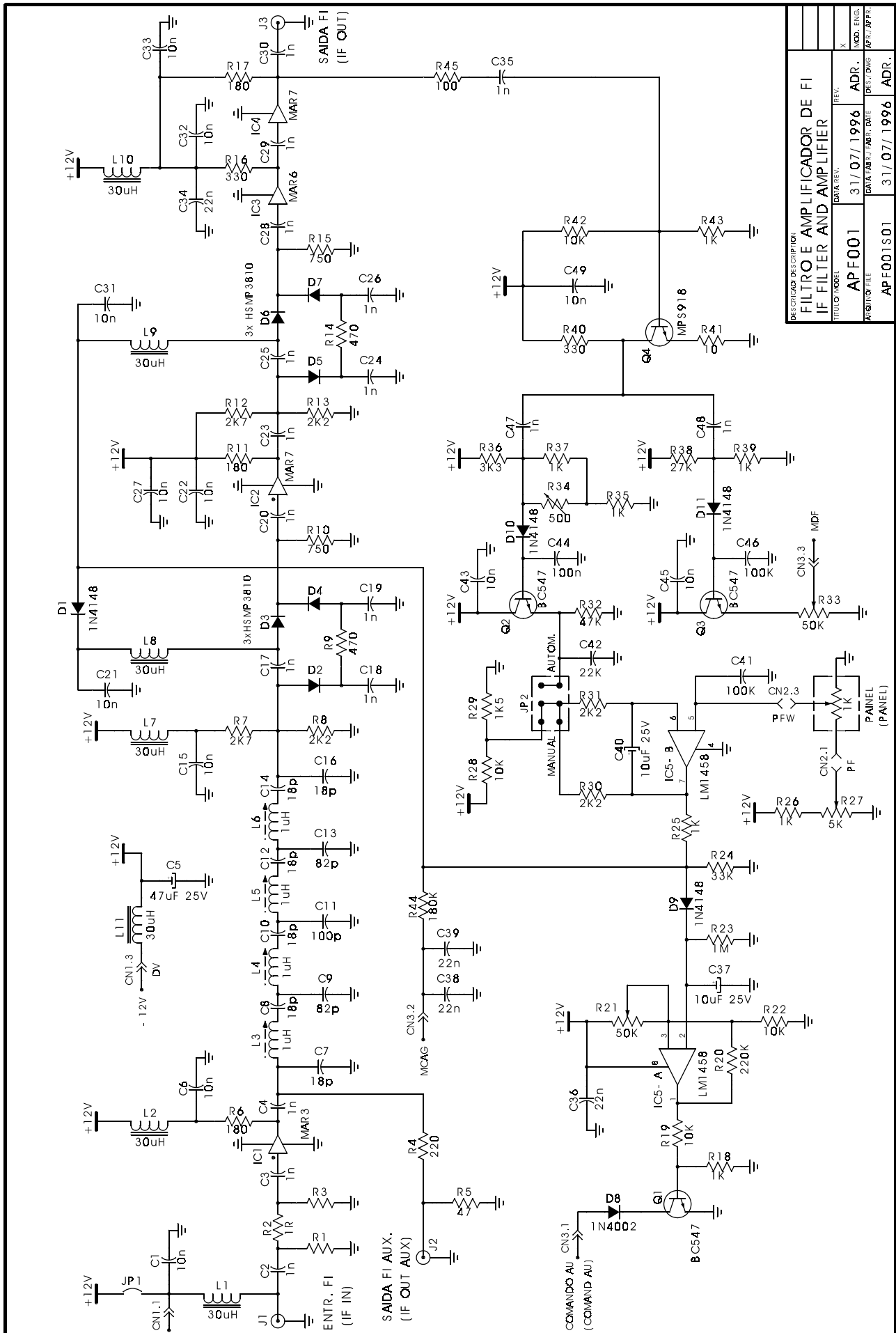


DESCRIPTION		DATE	TIME	BY	REMARKS
MODULATOR AUDIO		26/01/2004	ADR.	MOD. ENG.	MD-39/04
AUDIO MODULATOR		DATA PATH / ABR. DATE	DESIGN	APP. APPR.	
		08/08/2001	ADR.		

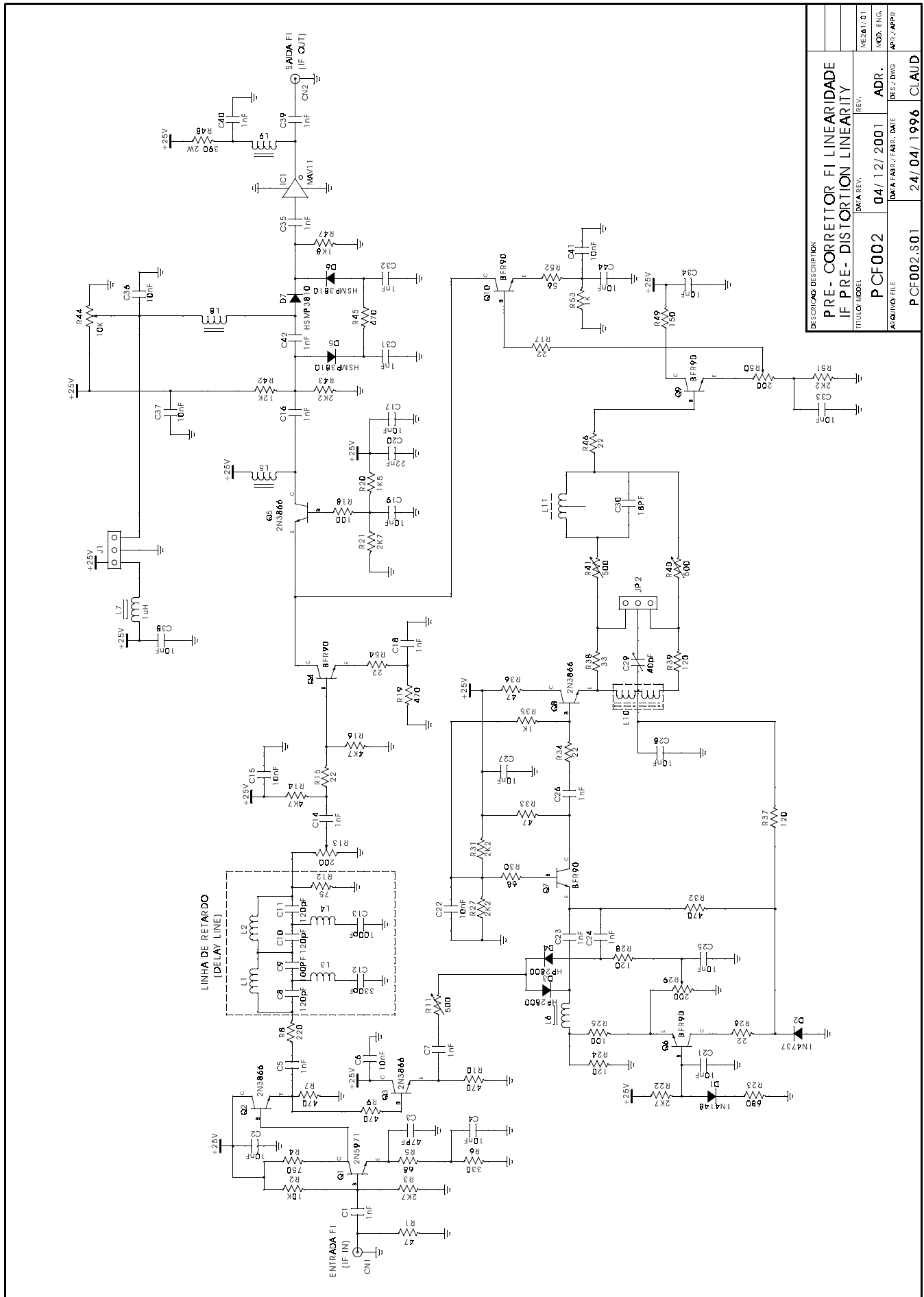
5.4. Circuit Diagram fourth part:



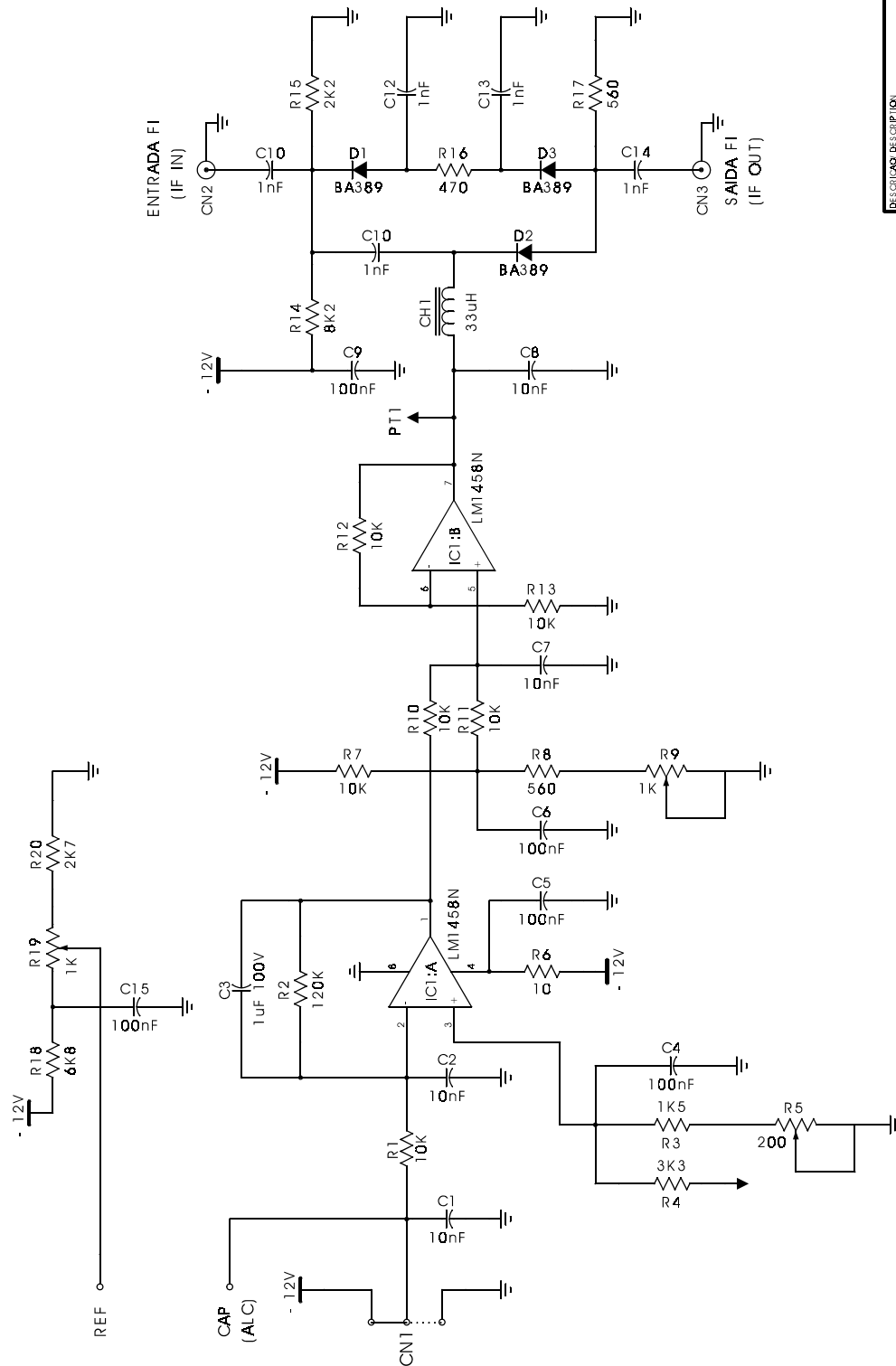
3.5. Circuit diagram APF001:



4.5. Circuit diagram PCF002:

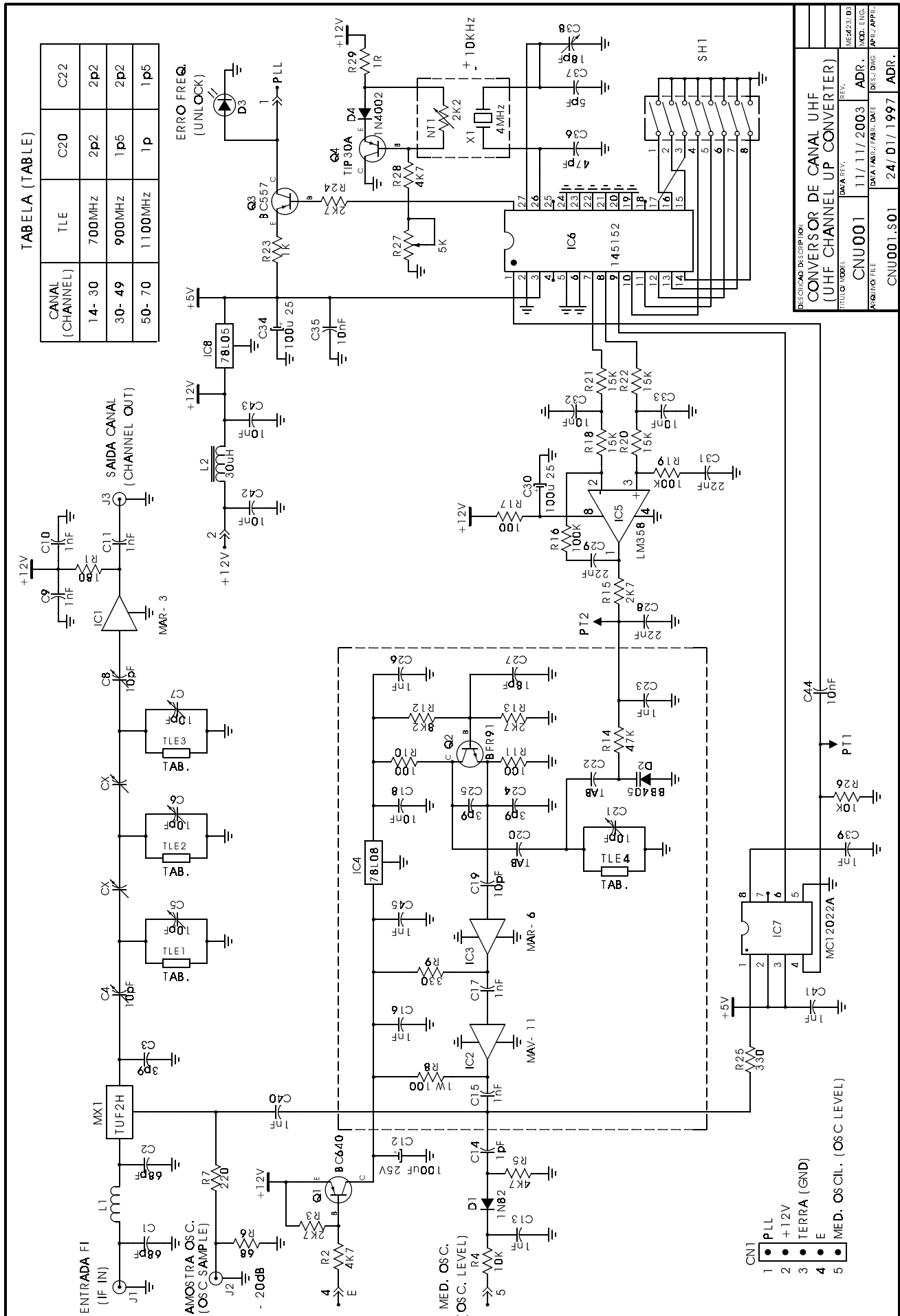


5.4. Circuit Diagram CAP001:

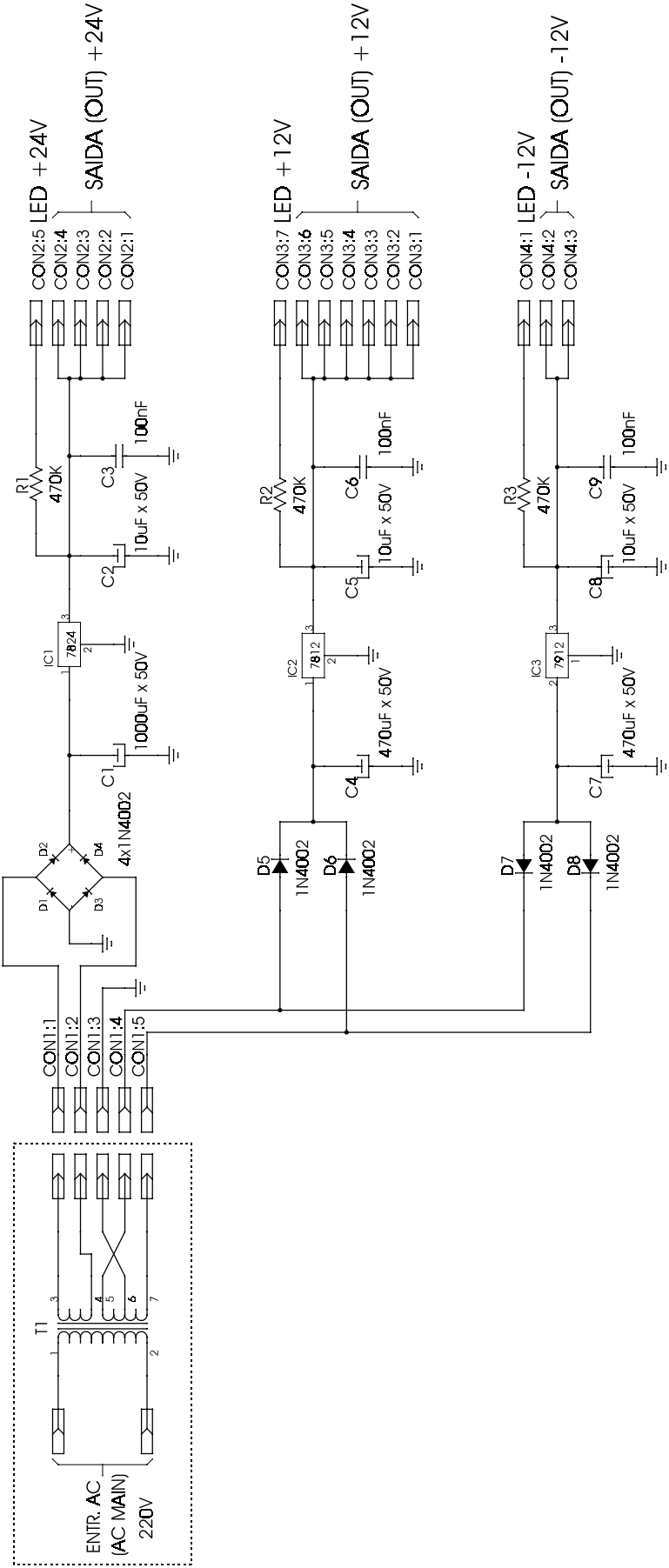


DESCRIPCION			
CONTROLE AUTOM. DE POTENCIA (AUTOMATIC LEVEL CONTROL)			
TITULO	MODEL	DATA REV.	REV.
CAP001		15/05/1996	ADR.
ANEXO FILE	DATA FAB./FABR. DATE	DES./DWS	APR./APPR.
CAP001.S01	15/05/1996		ADR.

6.6. Circuit diagram CNU001:



8.3. Circuit diagram FTE023:



DESCRICAO/DESCRIPTION			
FONTE CONVERSOR (UP CONVERTER SUPPLY)			
TITULO/MODEL	DATA REV/REV DATE	REV.	
FTE023	08/08/2002	ADR.	
ARQUIVO/FILE	DATA FABR/FABR DATE	DES/DWG	MOD ENG.
TW-FTE023.SCH	08/04/2002	RAF.	APR/APPR.