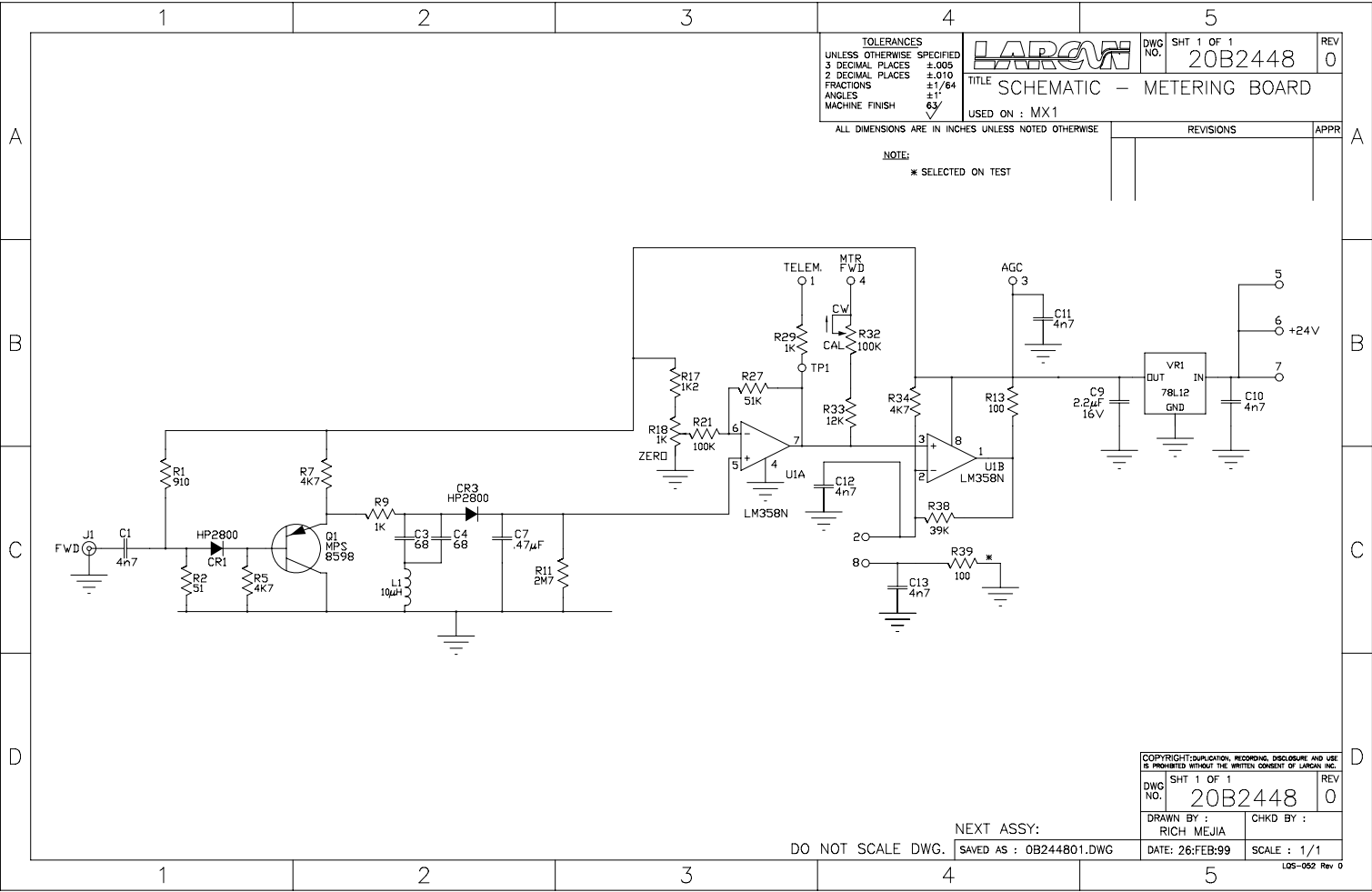
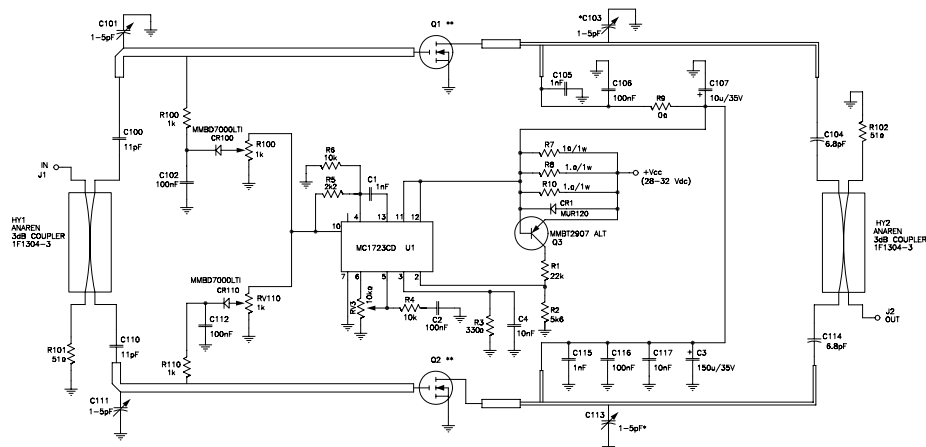
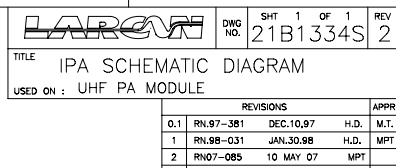






*NOTE: C103 AND C113 MAY BECOME 2.5-10pF
(2320-2 BLUE)
(DETERMINED DURING TESTING PHASE)

** Q1 AND Q2 WERE ORIGINALLY MRF181S LDMOS FETS (ARE NOW OBSOLETE) AT REV 2 OF THIS DRAWING, Q1 AND Q2 WERE REPLACED BY MRF282SR1 LDMOS FETS WHICH HAVE 3db HIGHER GAIN OVERALL GAIN OF THIS ASSEMBLY CHANGED FROM 15db WITH THE MRF181 TO 18db WITH THE MRF282.

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DWG NO.	SHT 1 21B1334S	OF 1	REV 2
DRAWN BY : H.DRAPPEL		CHKD BY : S.HAIDAU	
DATE: 12:MAY:97		SCALE : N/A	

DO NOT SCALE DWG. NEXT ASSY: 21B1324
SAVED AS : 1B1334SR2.dwg

ASSEMBLY DWG G9 , 10A1453 SHEET 9

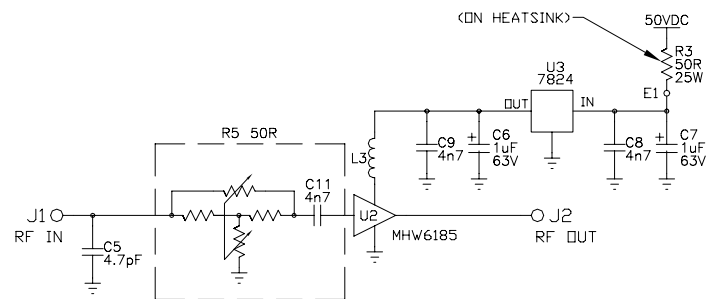


DWG NO.	SHT 17	REV
10A1453		0

TITLE SCHEMATIC- PREAMP

USED ON : DTT1M

REVISIONS		APPR
0	RN08-057 3 MAR 08 DS	



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DWG NO.	SHT 17	REV
10A1453		0

DRAWN BY :	CHKD BY :
DANIELA S.	

SAVED AS: 0A145316R0.DWG

DATE: 8 OCT 02	SCALE : N/A
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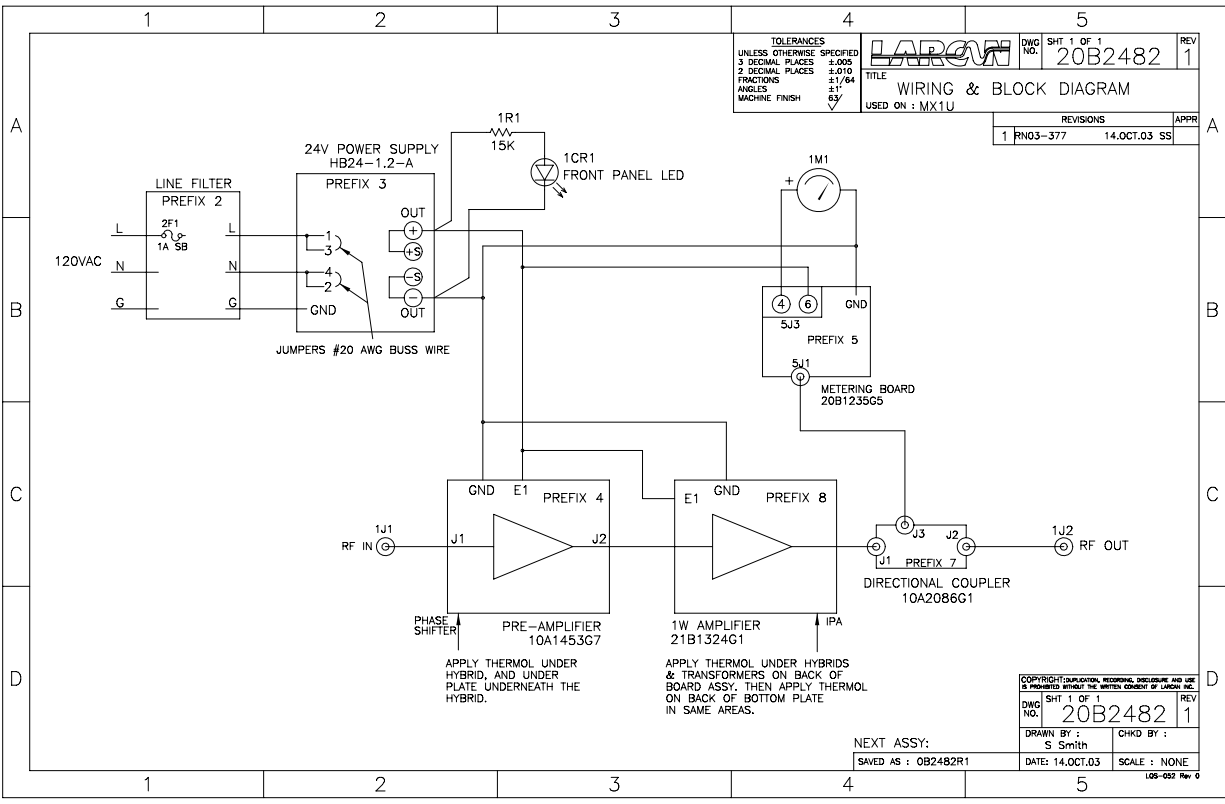
L05-052 Rev 0

TOLERANCES	SPECIFIED
UNLESS OTHERWISE SPECIFIED	
3 DECIMAL PLACES	±.003
2 DECIMAL PLACES	±.010
FRACTIONS	±1/64
ANGLES	±1'
MACHINE FINISH	±.001



DWG NO.	20B2482	REV	1
TITLE		WIRING & BLOCK DIAGRAM	
USED ON :		MX1U	

REVISIONS	APPR
1 RN03-377 14.OCT.03 SS	



NEXT ASSY:
 SAVED AS : 0B2482R1

DWG NO.	20B2482	REV	1
DRAWN BY :		CHKD BY :	
S Smith			
DATE: 14.OCT.03		SCALE : NONE	

105-052 Rev 0