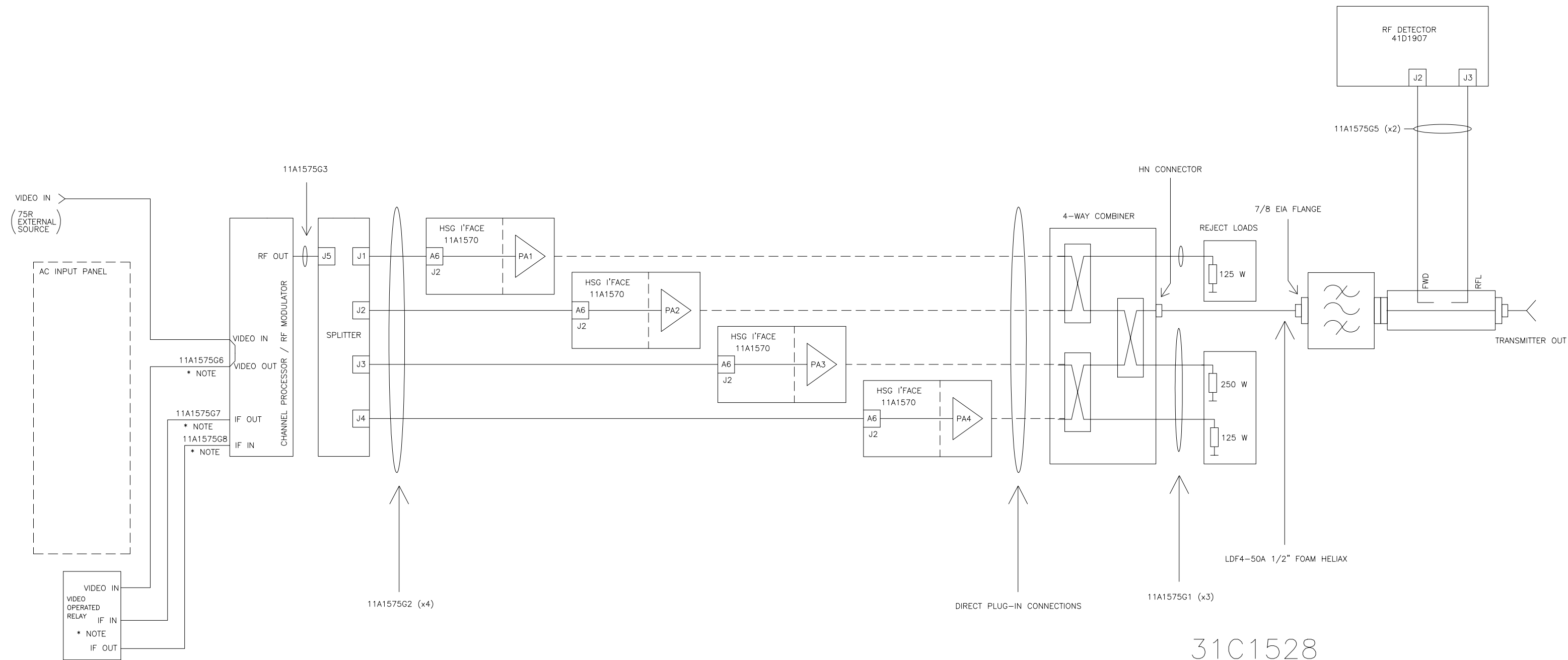
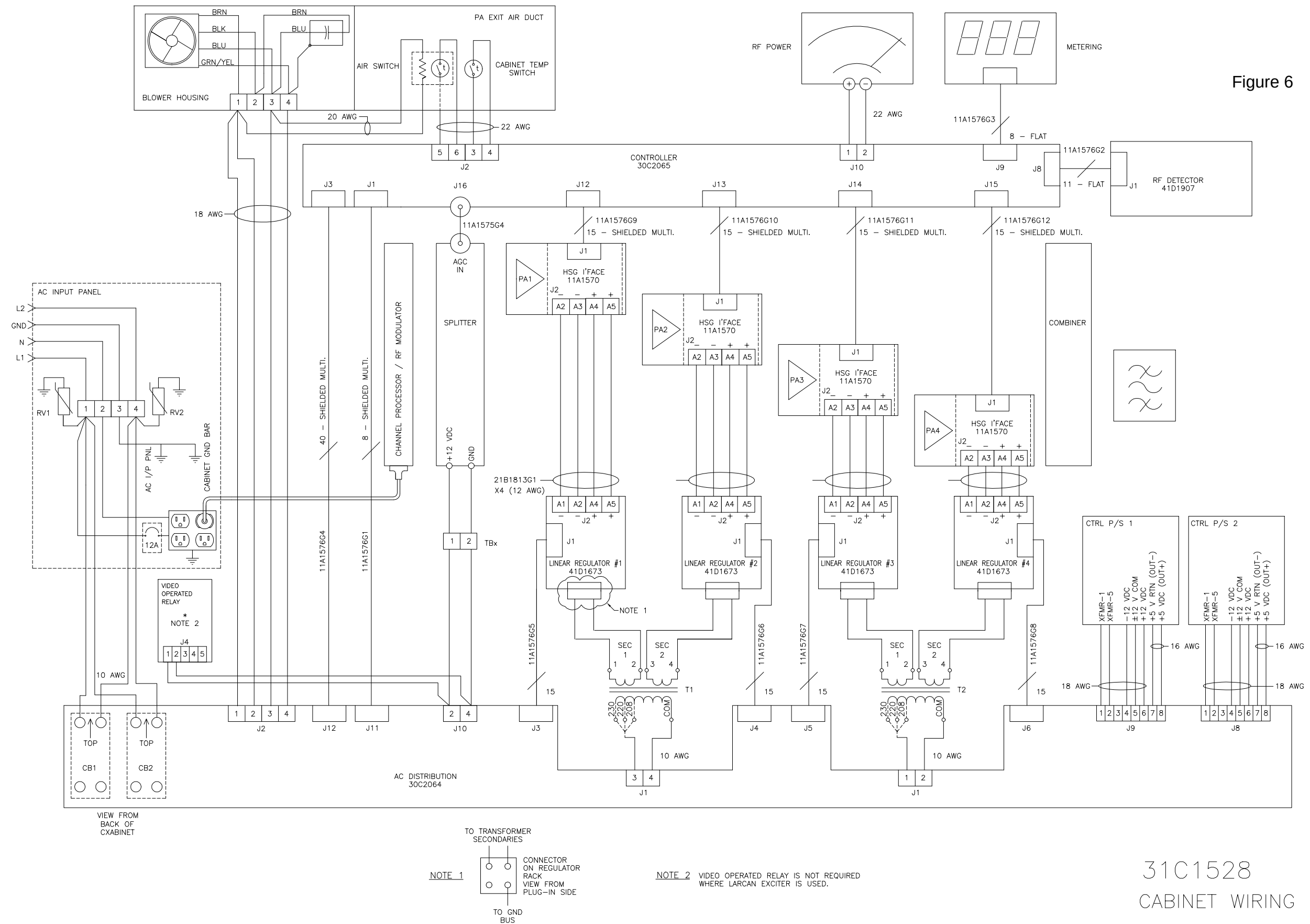


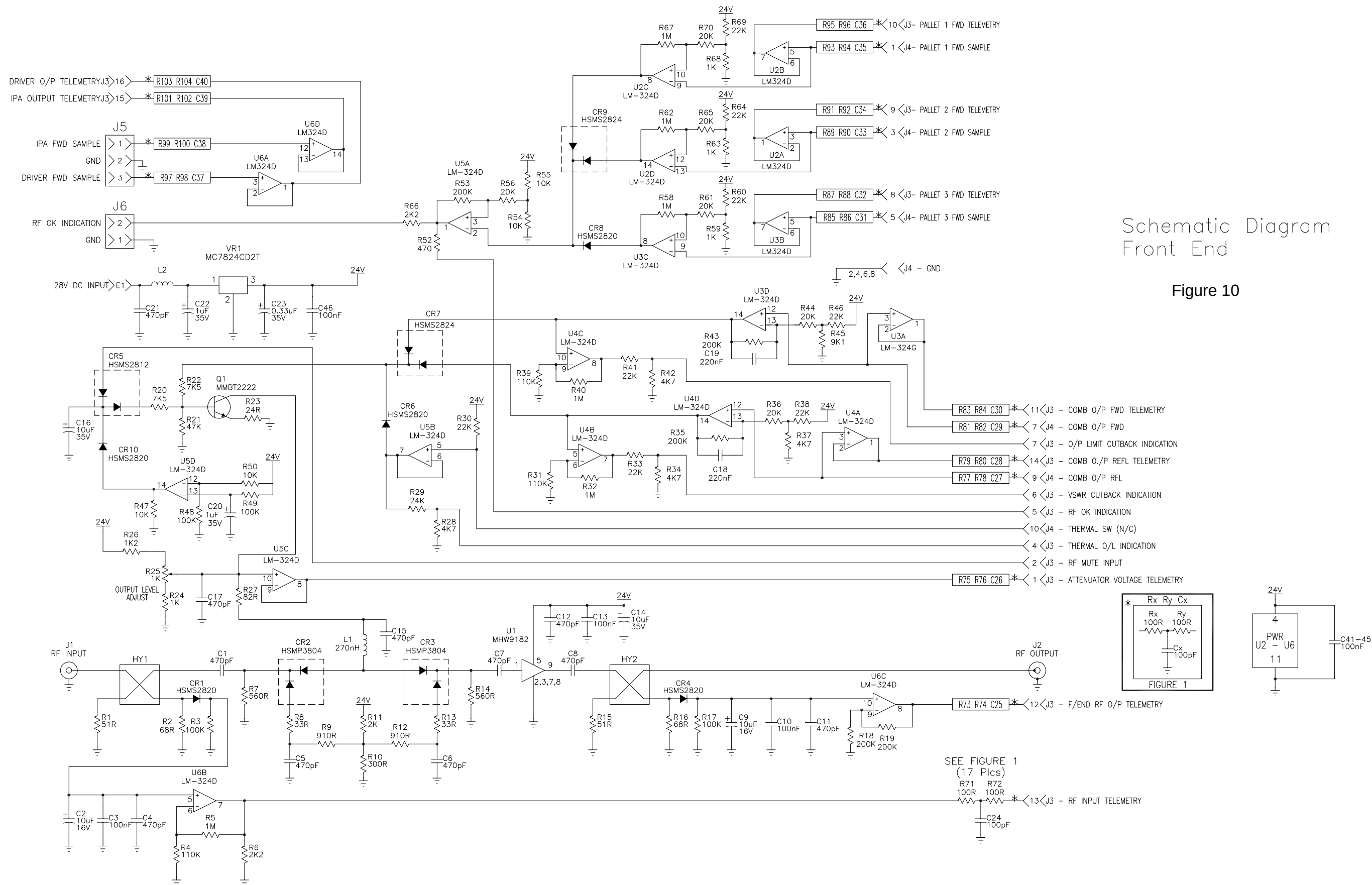
Figure 5



NOTE: VIDEO OPERATED RELAY IS NOT REQUIRED WHERE LARCAN EXCITER IS USED.

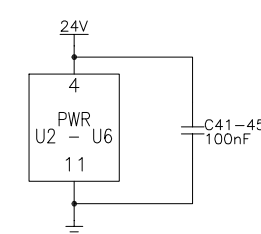
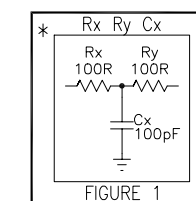
31C1528
CABINET WIRING — RF





Schematic Diagram
Front End

Figure 10



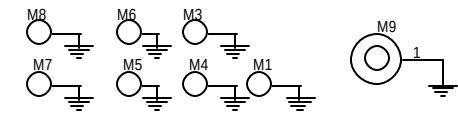
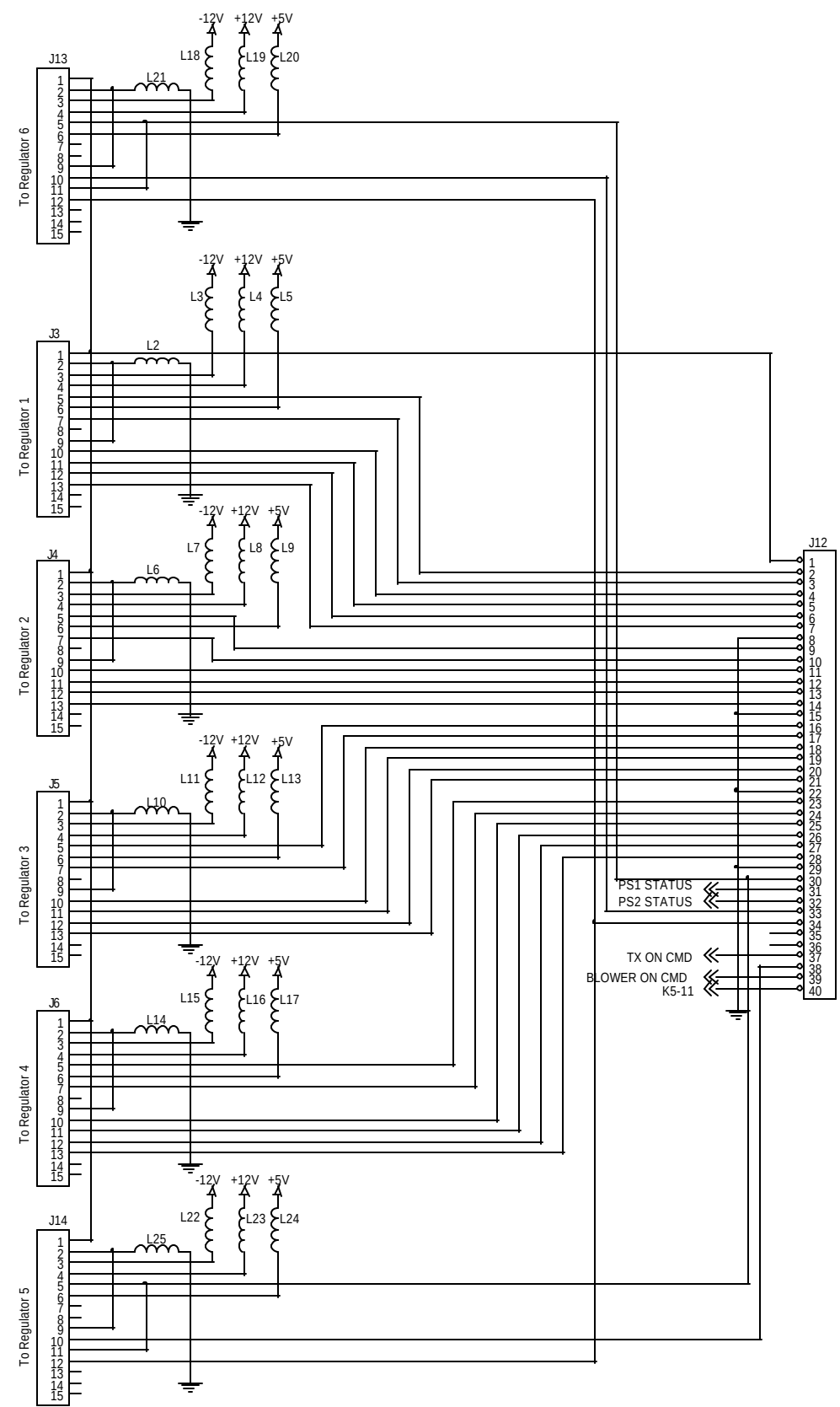
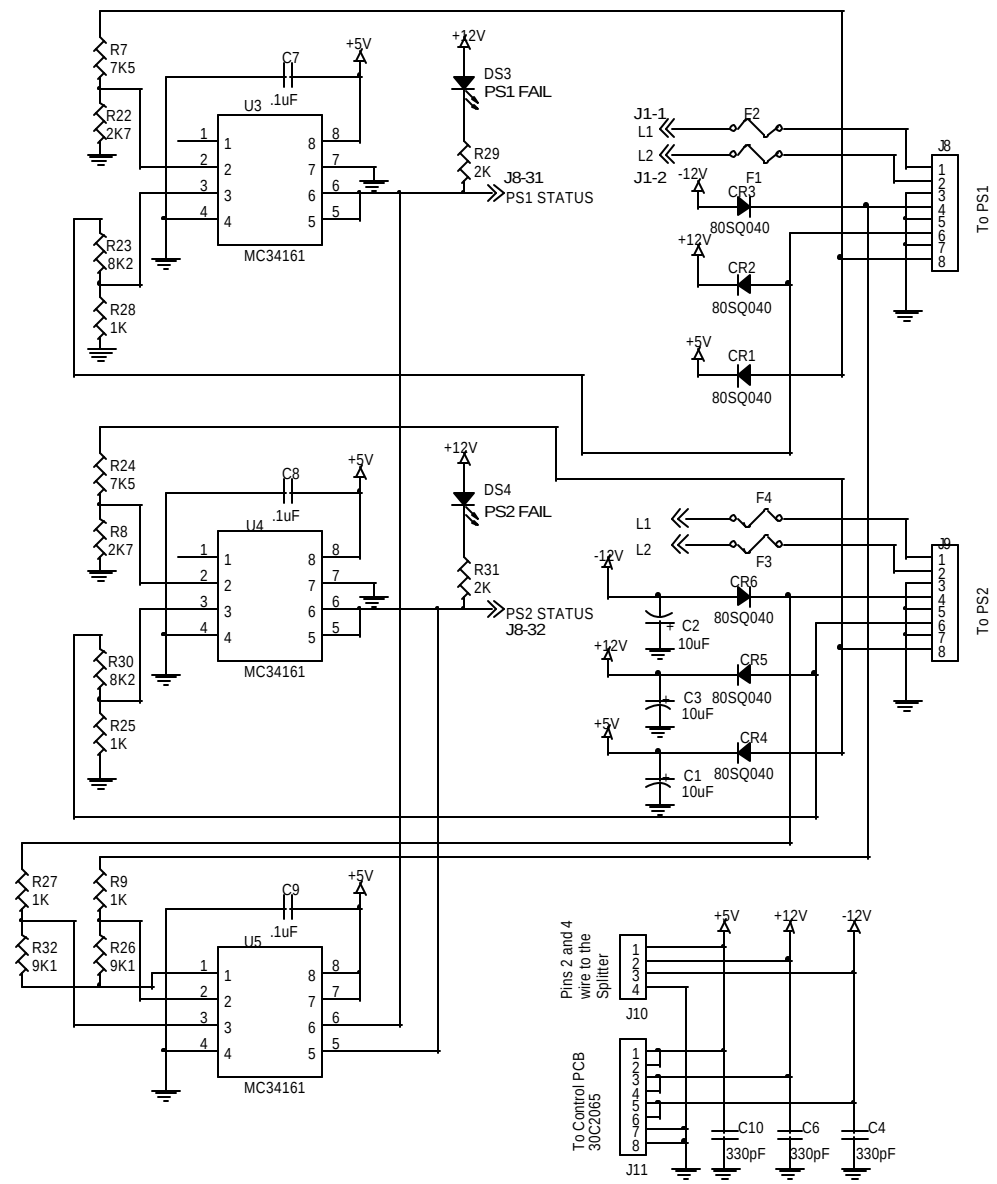
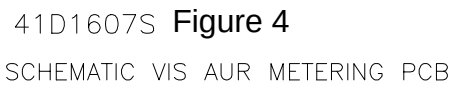
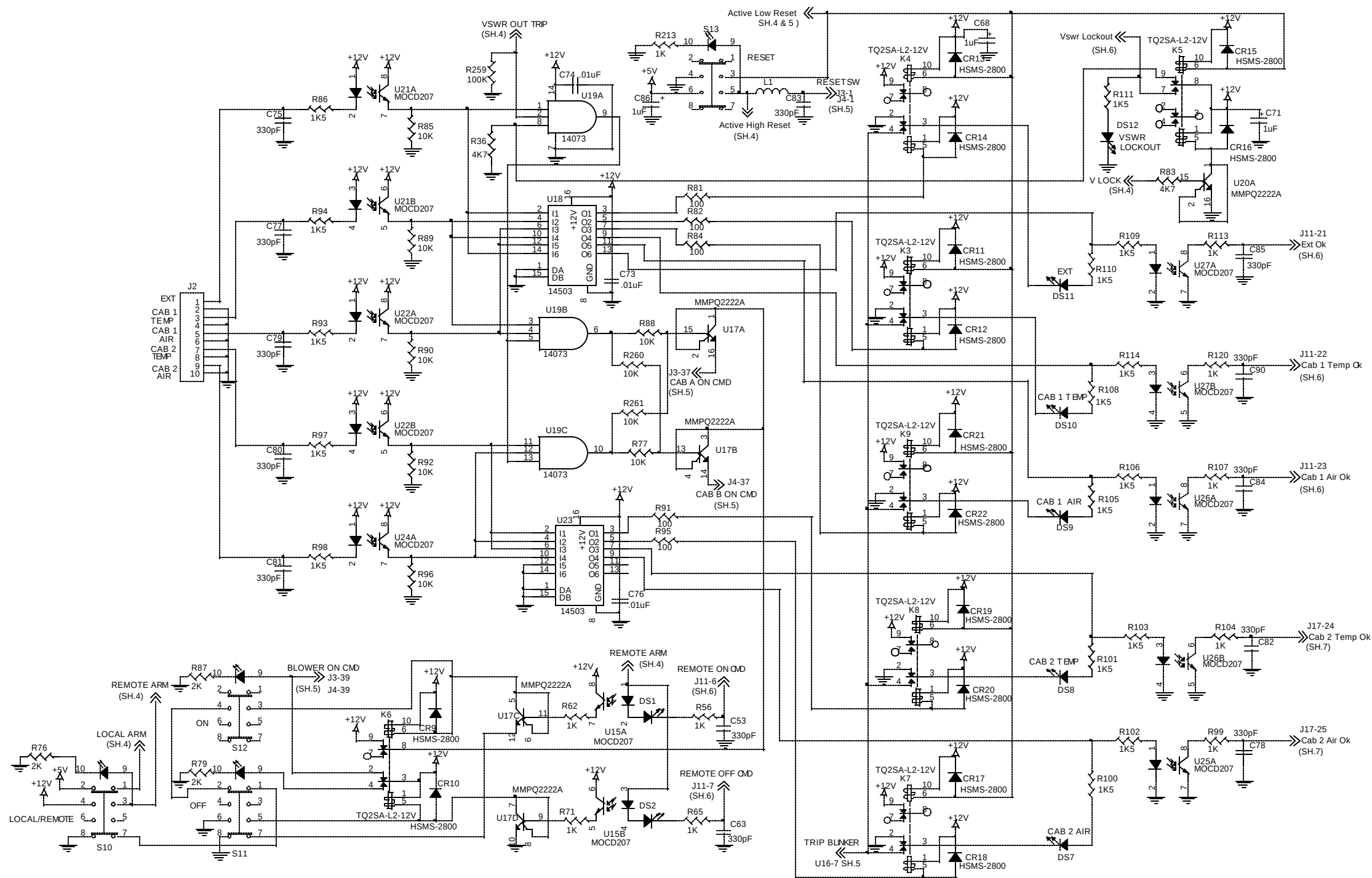


Figure 5

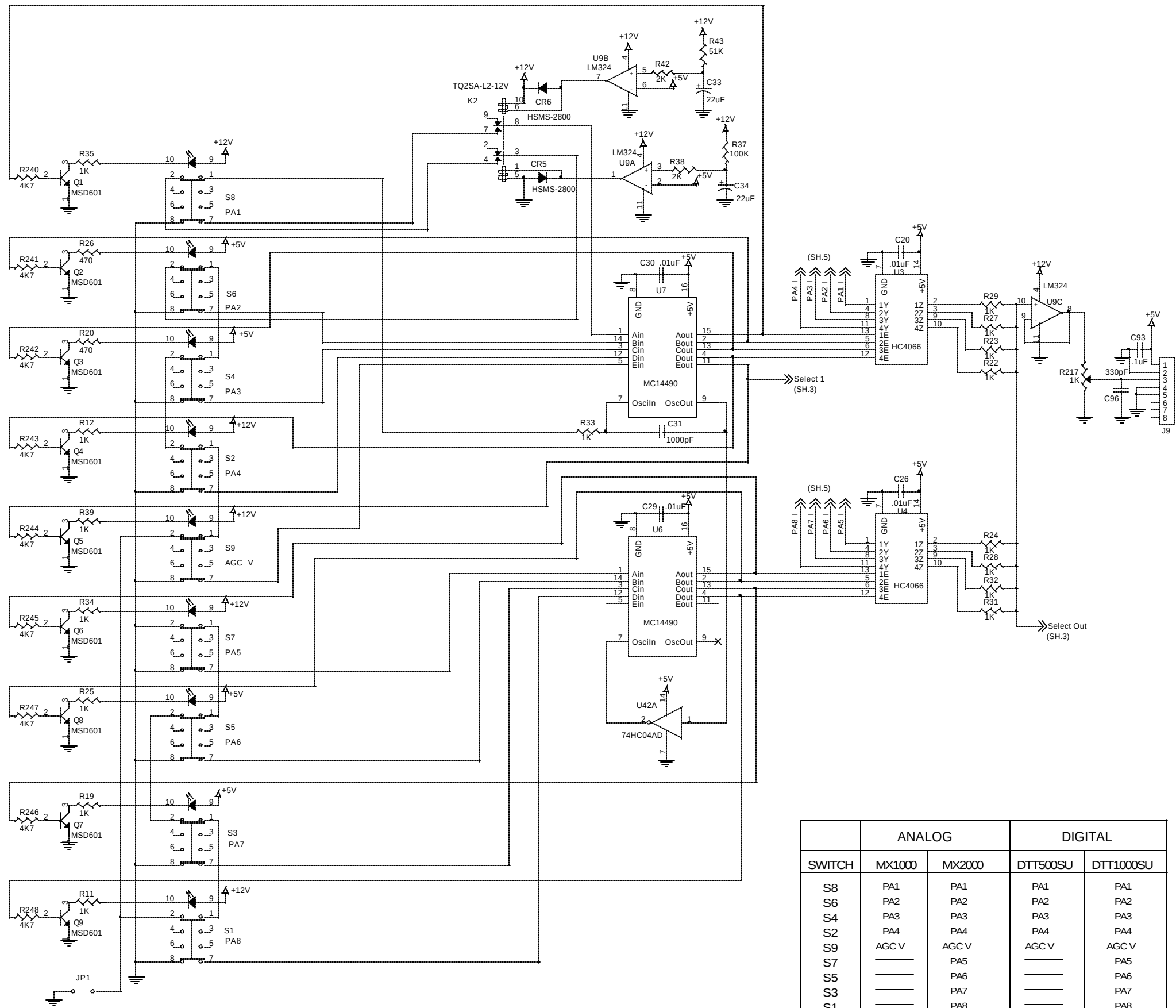




NOTE:
LAST REF.DESIGN.

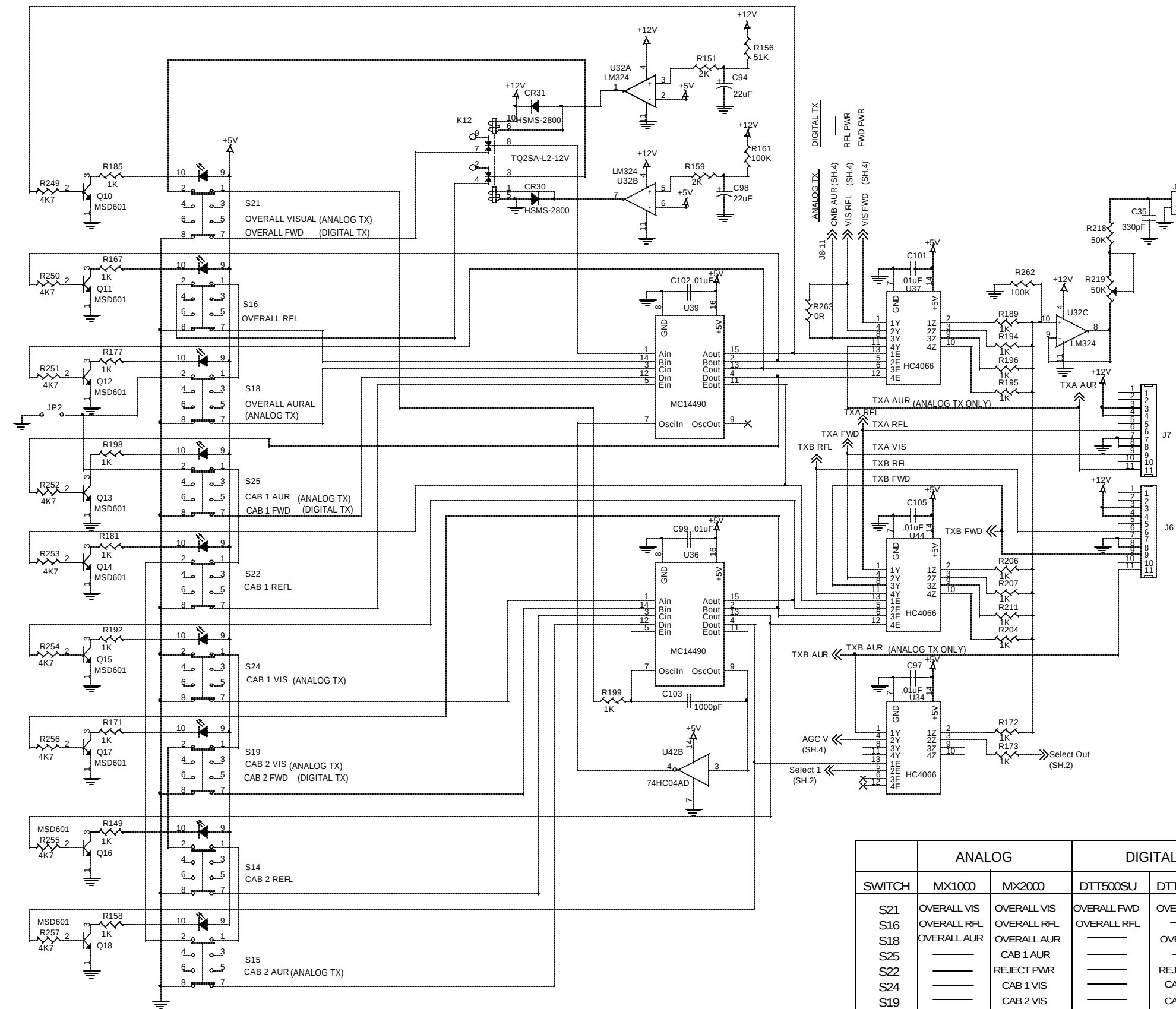
CR34	R287
C144	S25
DS16	U53
J17	VR2
K12	
L7	
Q18	

Figure 4



	ANALOG		DIGITAL	
SWITCH	MX1000	MX2000	DTT500SU	DTT1000SU
S8	PA1	PA1	PA1	PA1
S6	PA2	PA2	PA2	PA2
S4	PA3	PA3	PA3	PA3
S2	PA4	PA4	PA4	PA4
S9	AGC V	AGC V	AGC V	AGC V
S7	_____	PA5	_____	PA5
S5	_____	PA6	_____	PA6
S3	_____	PA7	_____	PA7
S1	_____	PA8	_____	PA8

Figure 5



	ANALOG		DIGITAL	
SWITCH	MX1000	MX2000	DTT500SU	DTT1000SU
S21	OVERALL VIS	OVERALL VIS	OVERALL FWD	OVERALL FWD
S16	OVERALL RFL	OVERALL RFL	OVERALL RFL	OVERALL RFL
S18	OVERALL AUR	OVERALL AUR	OVERALL AUR	OVERALL AUR
S25	OVERALL AUR	CAB 1 AUR	OVERALL AUR	OVERALL AUR
S22	OVERALL AUR	REJECT PWR	OVERALL AUR	REJECT PWR
S24	OVERALL AUR	CAB 1 VIS	OVERALL AUR	CAB 1 FWD
S19	OVERALL AUR	CAB 2 VIS	OVERALL AUR	CAB 2 FWD
S14	OVERALL AUR	OVERALL AUR	OVERALL AUR	OVERALL AUR
S15	OVERALL AUR	CAB 2 AUR	OVERALL AUR	OVERALL AUR

Figure 6

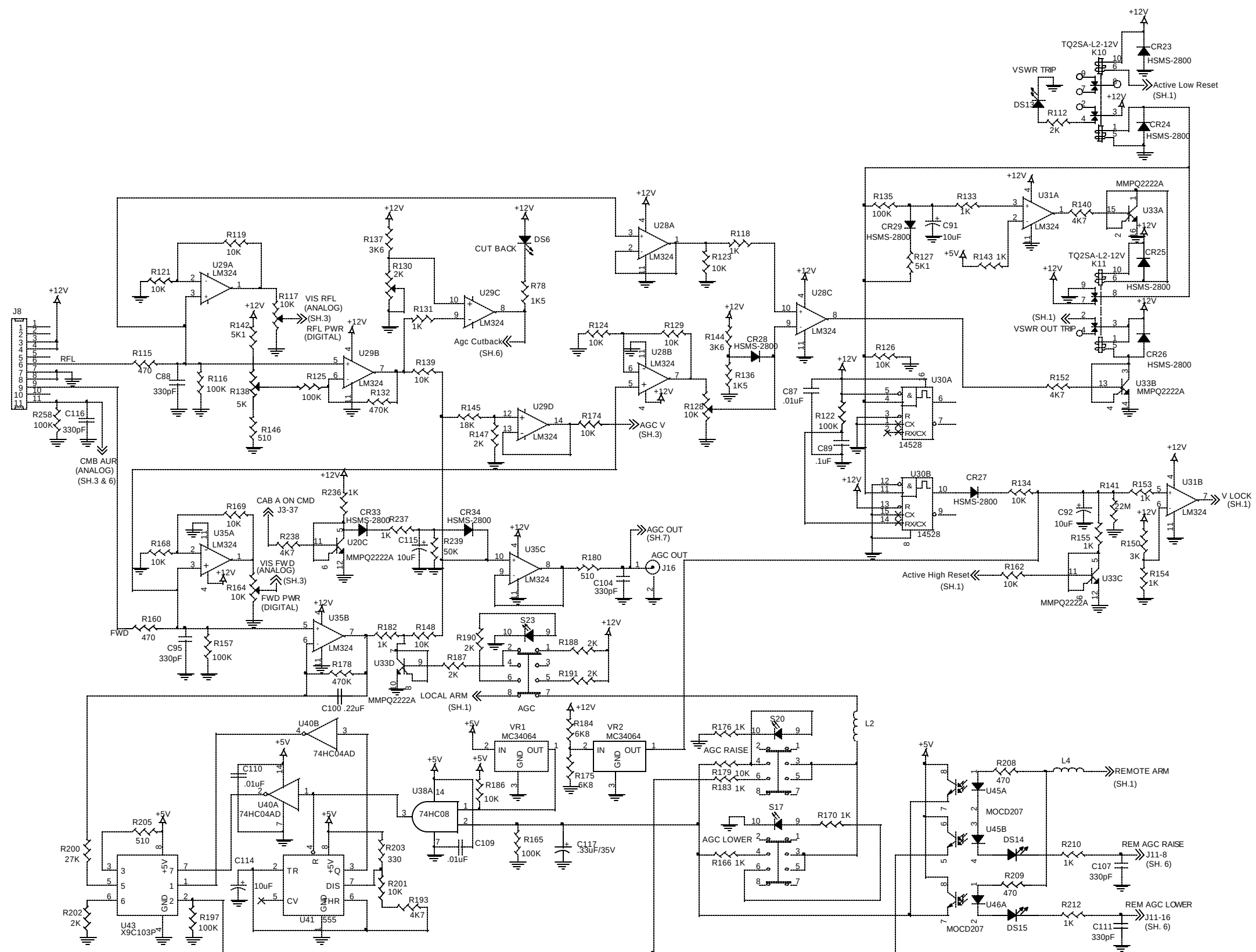


Figure 7

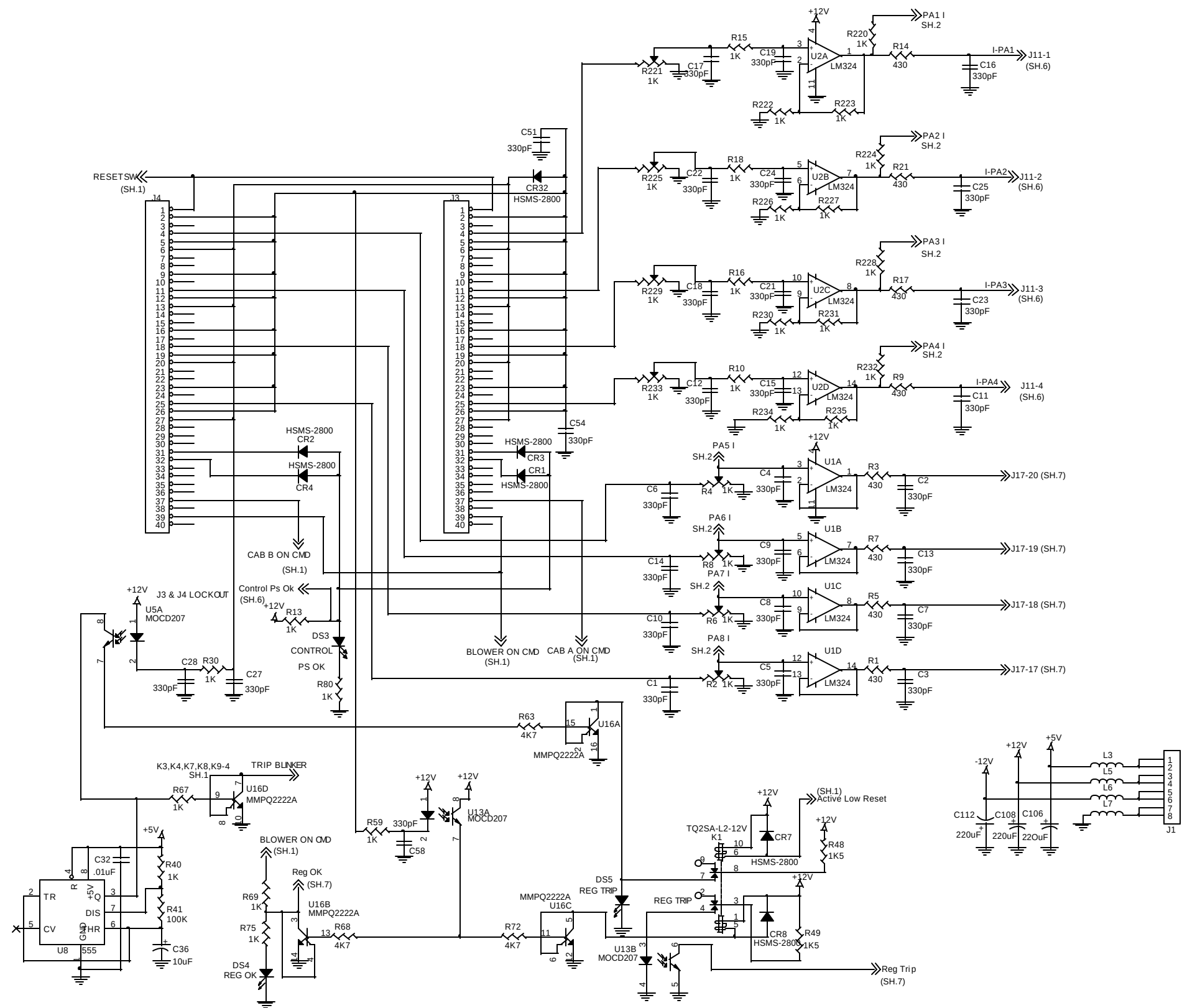


Figure 8

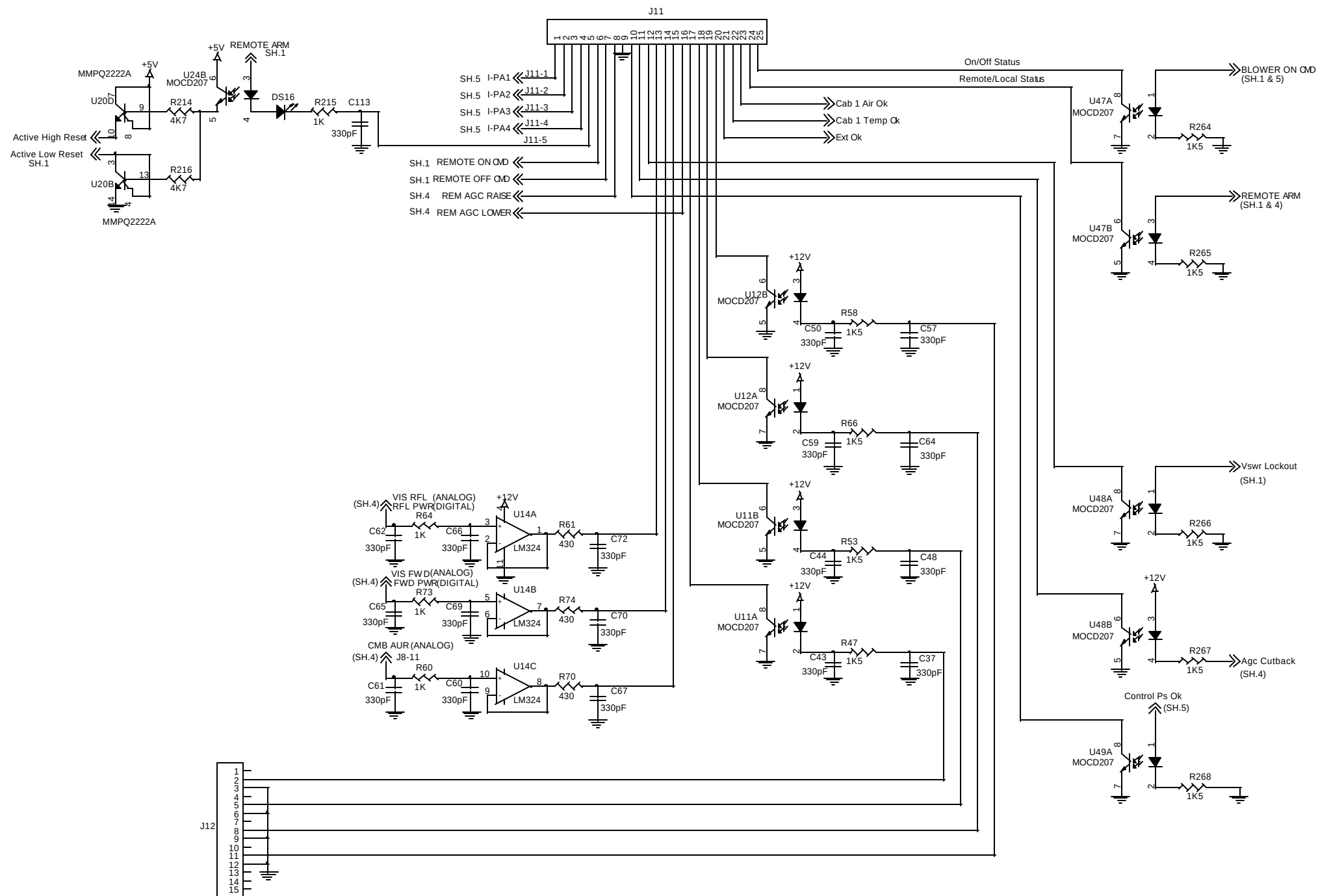
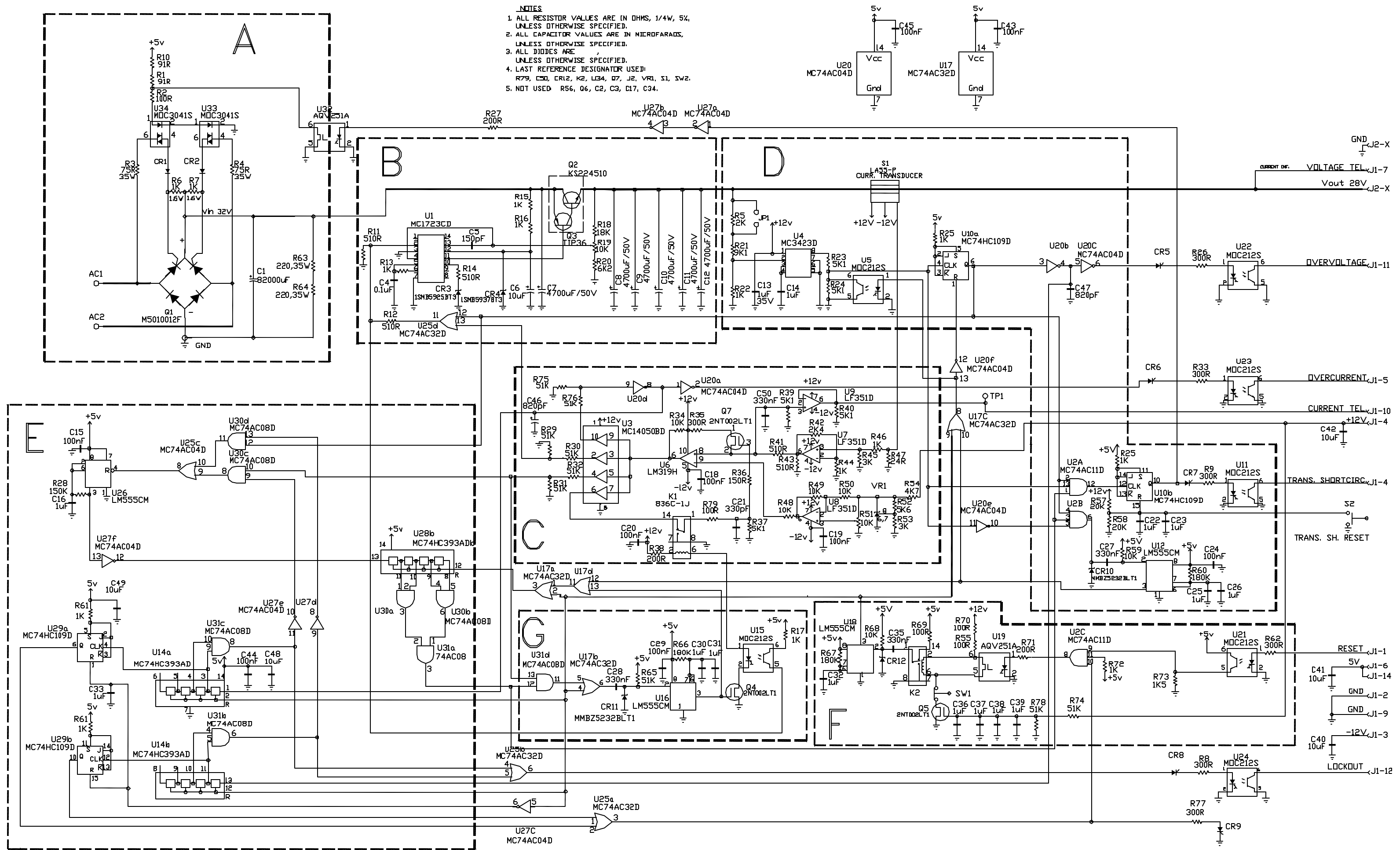


Figure 9



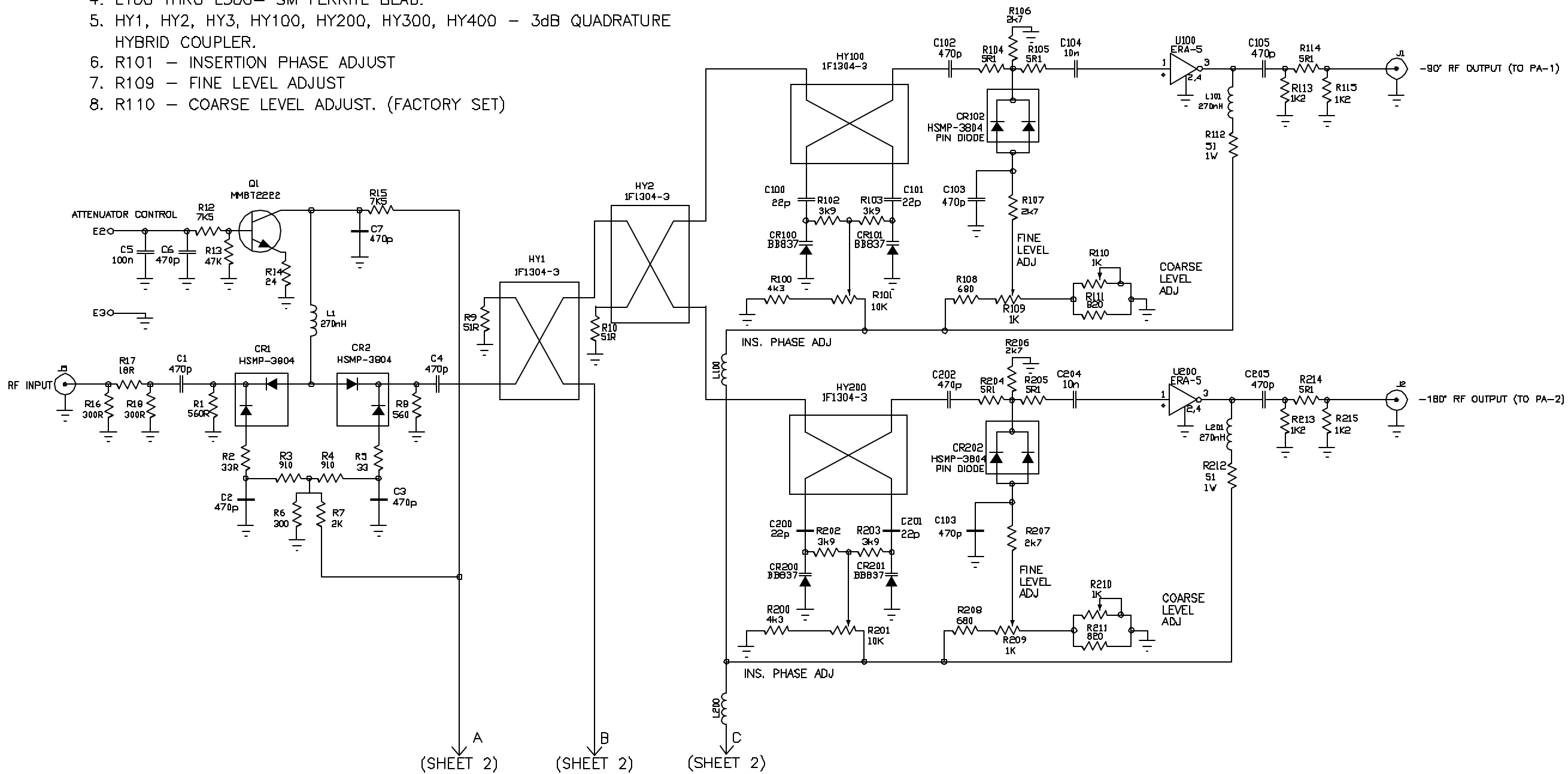


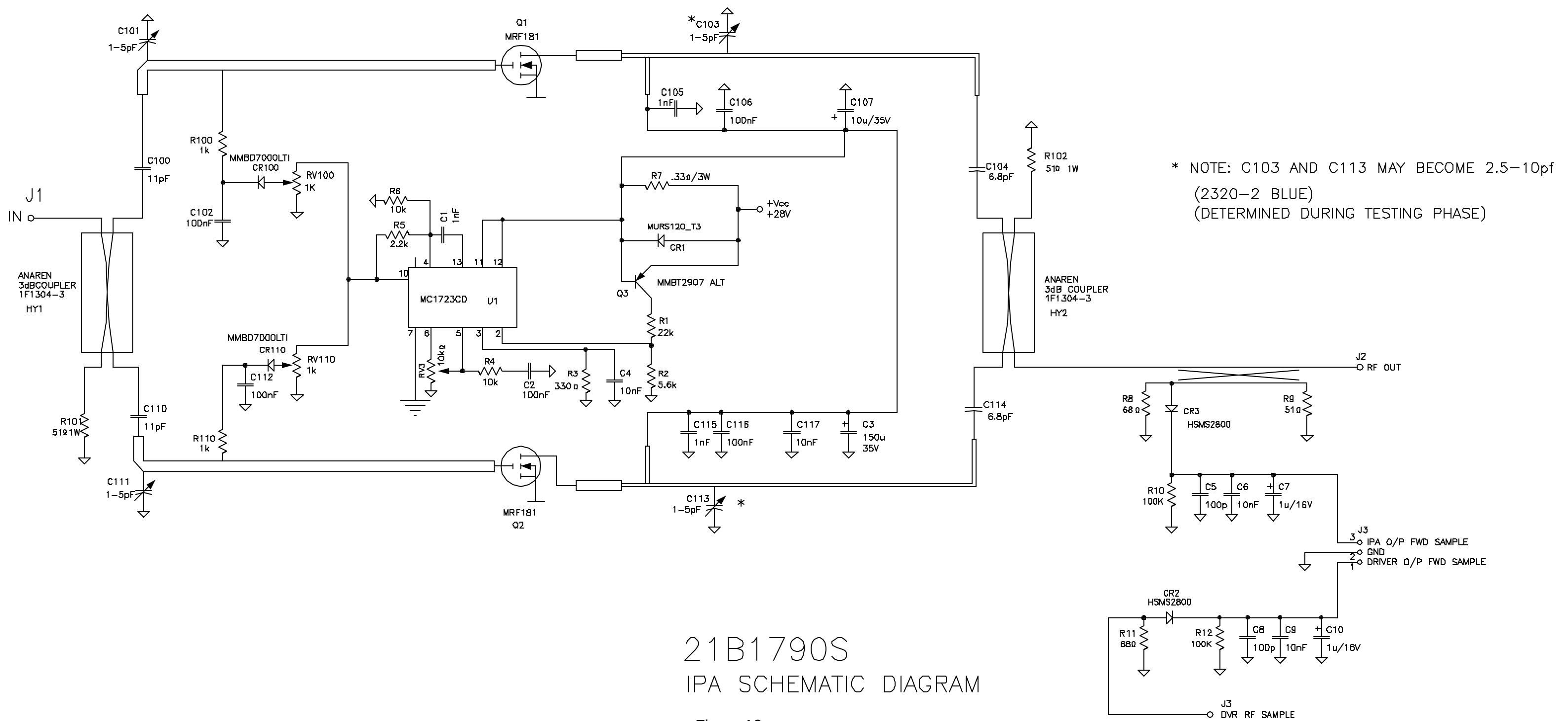
41D1680S Figure 4
SCH. - LIN. REGULATOR

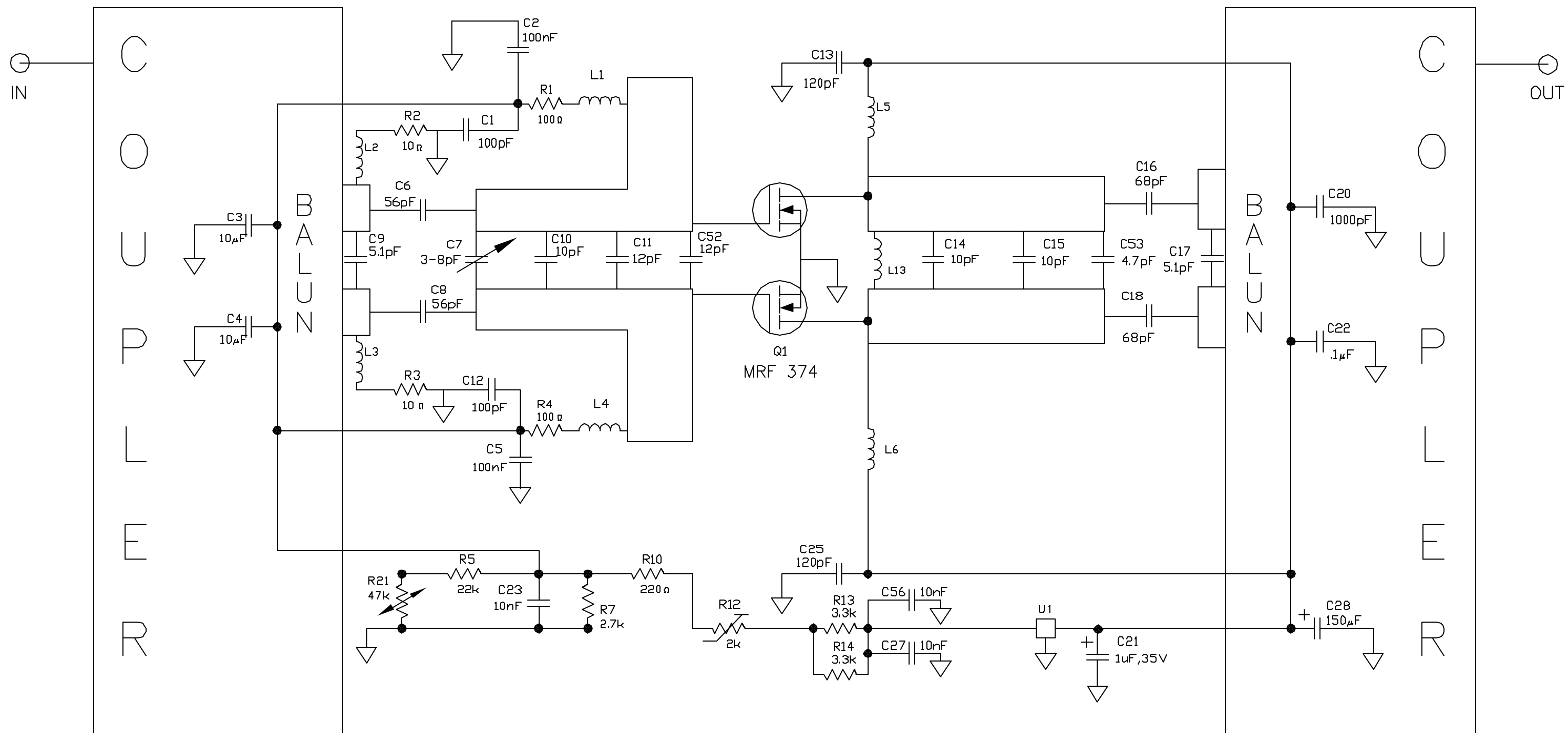
Figure 3a

NOTES

1. ALL RESISTOR VALUES ARE IN OHMS, 1/4W, 5%,
UNLESS OTHERWISE SPECIFIED.
2. LAST REFERENCE DESIGNATOR USED:
R415, C405, CR402, HY400, J5, L401, VR1, U400.
4. L100 THRU L300- SM FERRITE BEAD.
5. HY1, HY2, HY3, HY100, HY200, HY300, HY400 - 3dB QUADRATURE
HYBRID COUPLER.
6. R101 - INSERTION PHASE ADJUST
7. R109 - FINE LEVEL ADJUST
8. R110 - COARSE LEVEL ADJUST. (FACTORY SET)







21B1751S
SCHEM-HALF PALLET

Figure 14

