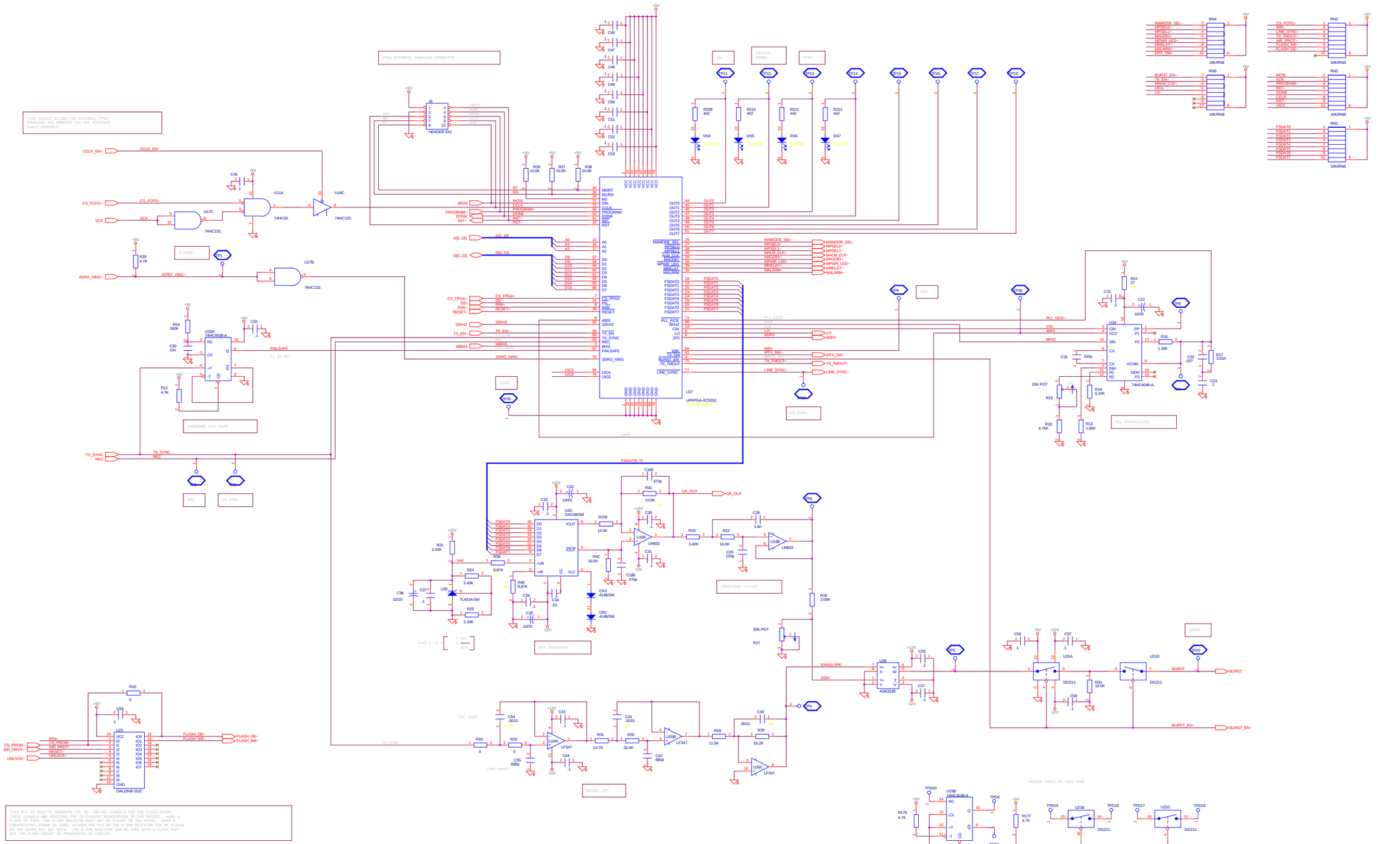
FPGA EXTERNAL DOWNLOAD CONNECTOR
THIS ENABLE ALLOWS FOR EXTERNAL FPGA DOWNLOAD AND PROBING VIA THE XCHECKER CABLE ASSEMBLY.
HARDWARE FAIL SAFE
D/A CONVERTER
SMOOTHING FILTER
BESSEL LPF
PLL SYNTHESIZER
UNUSED PARTS ON THIS PAGE
THIS PLD IS USED TO GENERATE THE OE- AND WE- SIGNALS FOR THE FLASH EPROM. THESE SIGNALS ARE REQUIRED FOR IN-CIRCUIT PROGRAMMING OF THE DEVICE. WHEN A FLASH IS USED, THE 0 OHM RESISTOR MUST NOT BE PLACED ON THE BOARD. WHEN A CONVENTIONAL EPROM IS USED, EITHER THE PLD OR THE 0 OHM RESISTOR CAN BE PLACED ON THE BOARD BUT NOT BOTH. THE 0 OHM RESISTOR CAN BE USED WITH A FLASH PART BUT THE FLASH CANNOT BE PROGRAMMED IN-CIRCUIT.
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