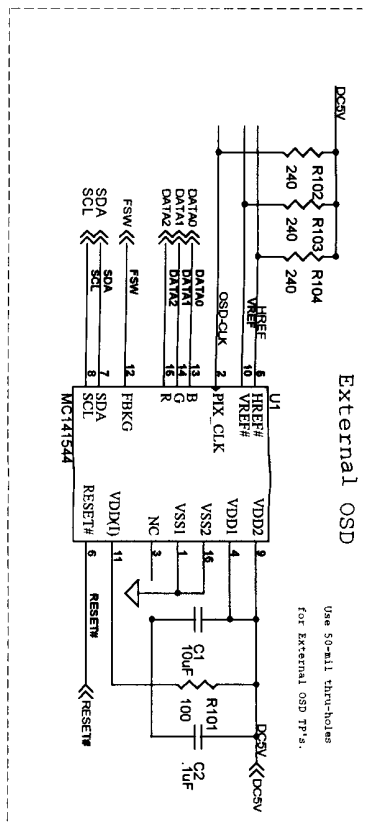
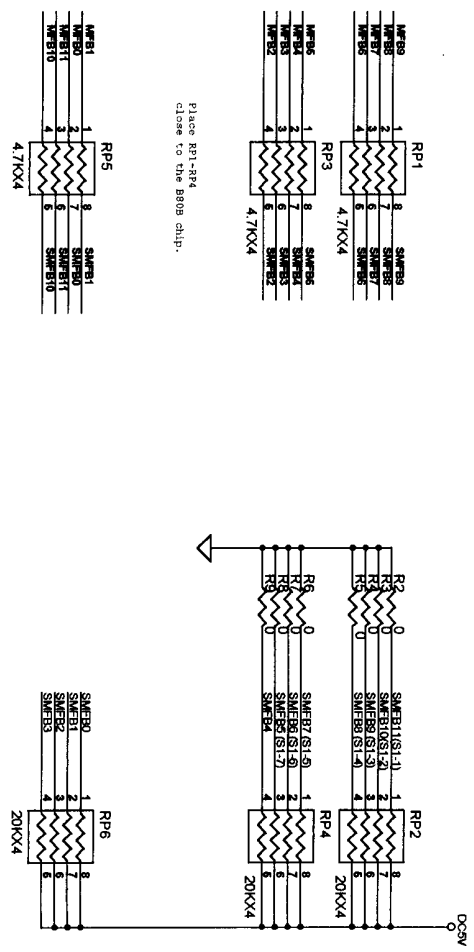


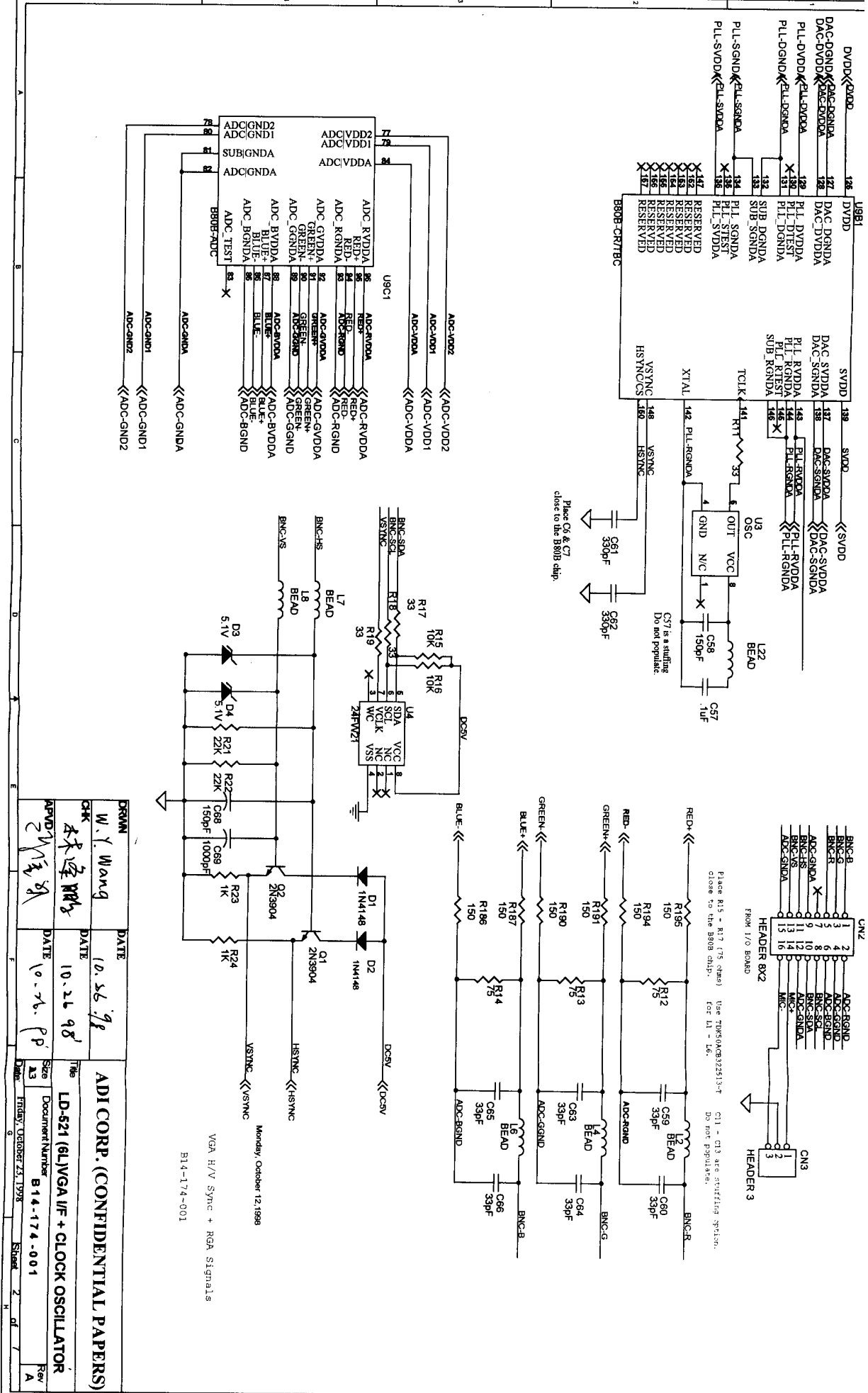
Notice:
s11:2:3:4)

- 0110 LG 15" XGA
- 1000 NEC 15" XGA
- 1010 Samsung 14"/15" XGA
- 1011 Sharp 14"/15" XGA
- 1011 Toshiba 15" XGA



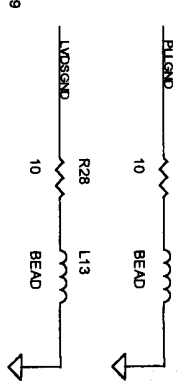
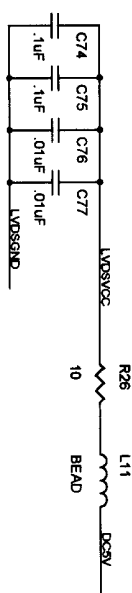
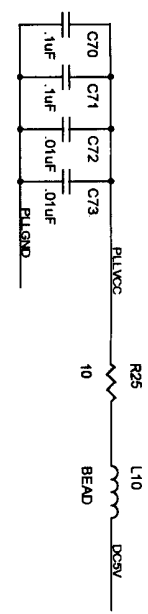
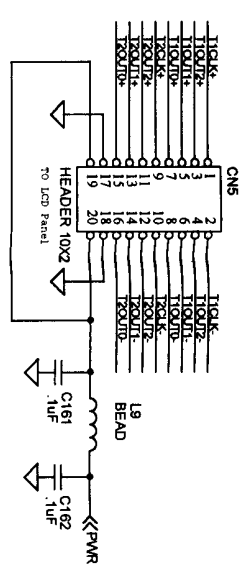
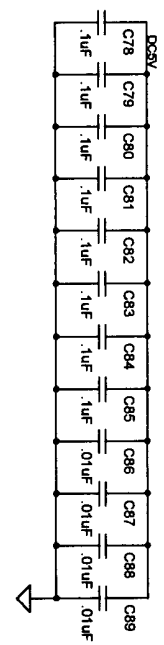
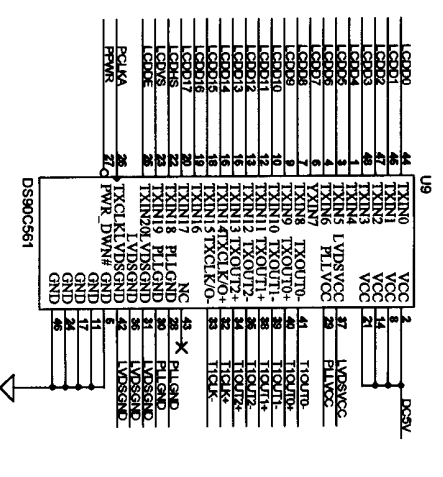
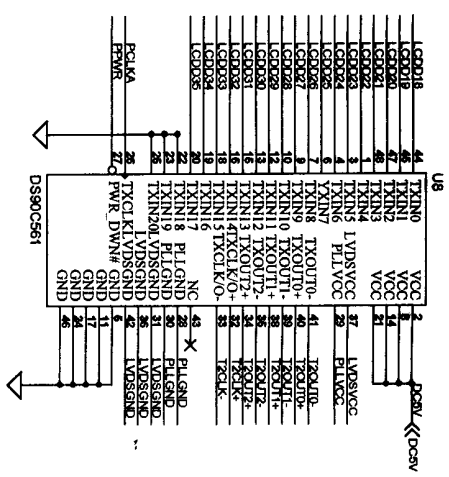
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W. Y. Wang	10.26.98	10.26.98	10.26.98
CHK	DATE	DATE	DATE
李永達	10.26.98	10.26.98	10.26.98
APVD	DATE	DATE	DATE
李永達	10.26.98	10.26.98	10.26.98
Title		Size	
ADI CORP. (CONFIDENTIAL PAPERS)		A3	
Document Number		Date	
LD-521 (6L) LCD MONITOR OSD Interface		TRINARY OCTOBER 21 1998	
B 14-174-001		Sheet 1 of 1	

FCC ID: BR8LD-521



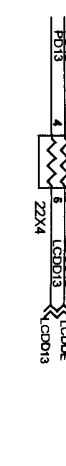
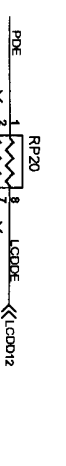
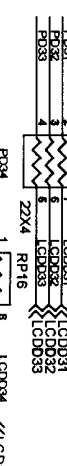
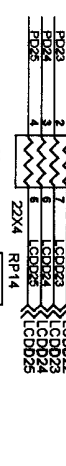
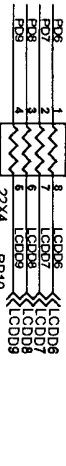
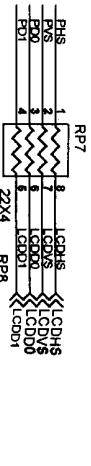
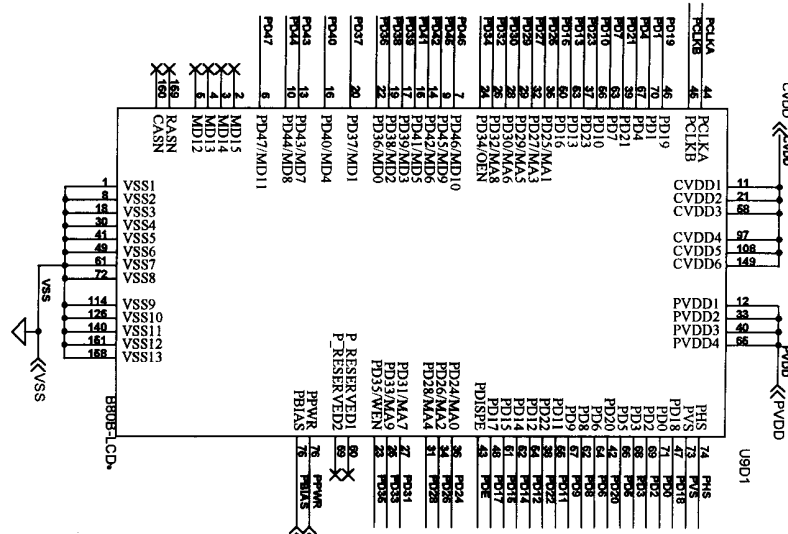
DESIGN	DATE	DATE	DATE
W. J. Wang	10.26.98	10.26.98	10.26.98
CHK	DATE	DATE	DATE
林凌	10.26.98	10.26.98	10.26.98
APVD	DATE	DATE	DATE
2/1/98	10.26.98	10.26.98	10.26.98
ADI CORP. (CONFIDENTIAL PAPERS)			
Title: LD-521 (6LVGA I/F + CLOCK OSCILLATOR)			
Size	Document Number	Rev	Rev
A3	B 14-174-001	A	A
Date	Friday, October 23, 1998	Sheet	2 of 7

VGA H/V Sync + RGA Signals
 B14-174-001
 Monday, October 12, 1998

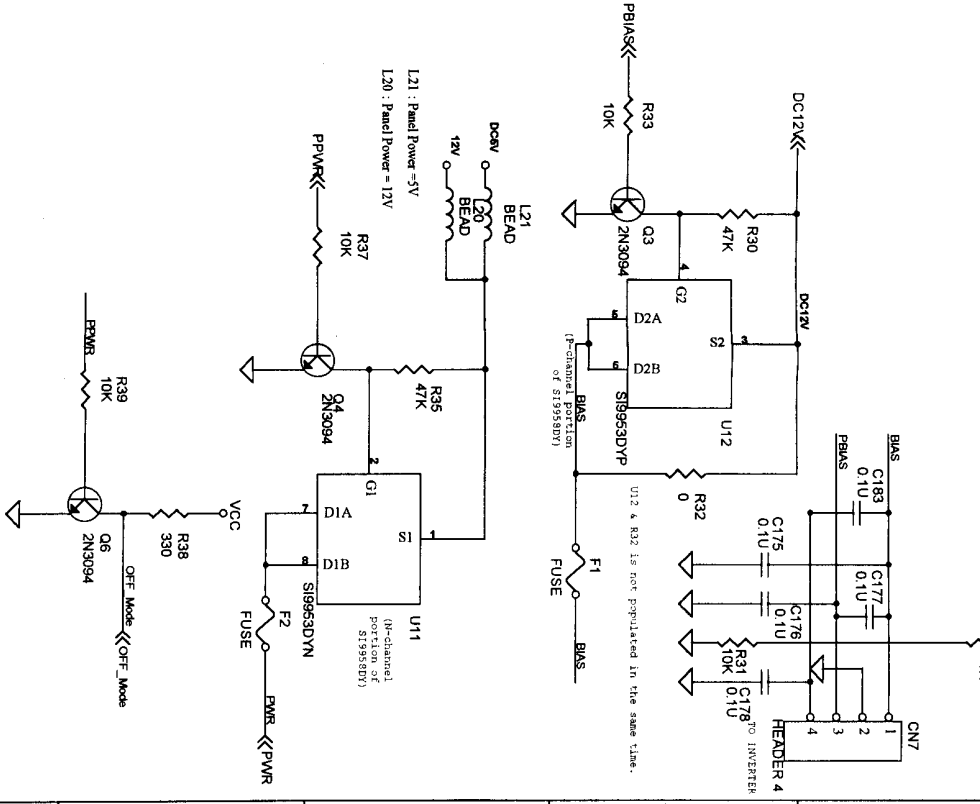


DESIGN	DATE	DESIGNER	DATE
W. Y. Wang	10.26.98	W. Y. Wang	10.26.98
CHK	DATE	CHK	DATE
林逢明		林逢明	
APVD	DATE	APVD	DATE
林逢明	10.26.98	林逢明	10.26.98
Title		Size	
ADI CORP. (CONFIDENTIAL PAPERS)		A3	
Document Number		Date	
LD-621 (6L) LCD Panel Interface		Friday, October 23, 1998	
814-174-001		Sheet 3 of 7	
Rev	A		

B80B Panel Interface



FAILURE POWER CIRCUIT

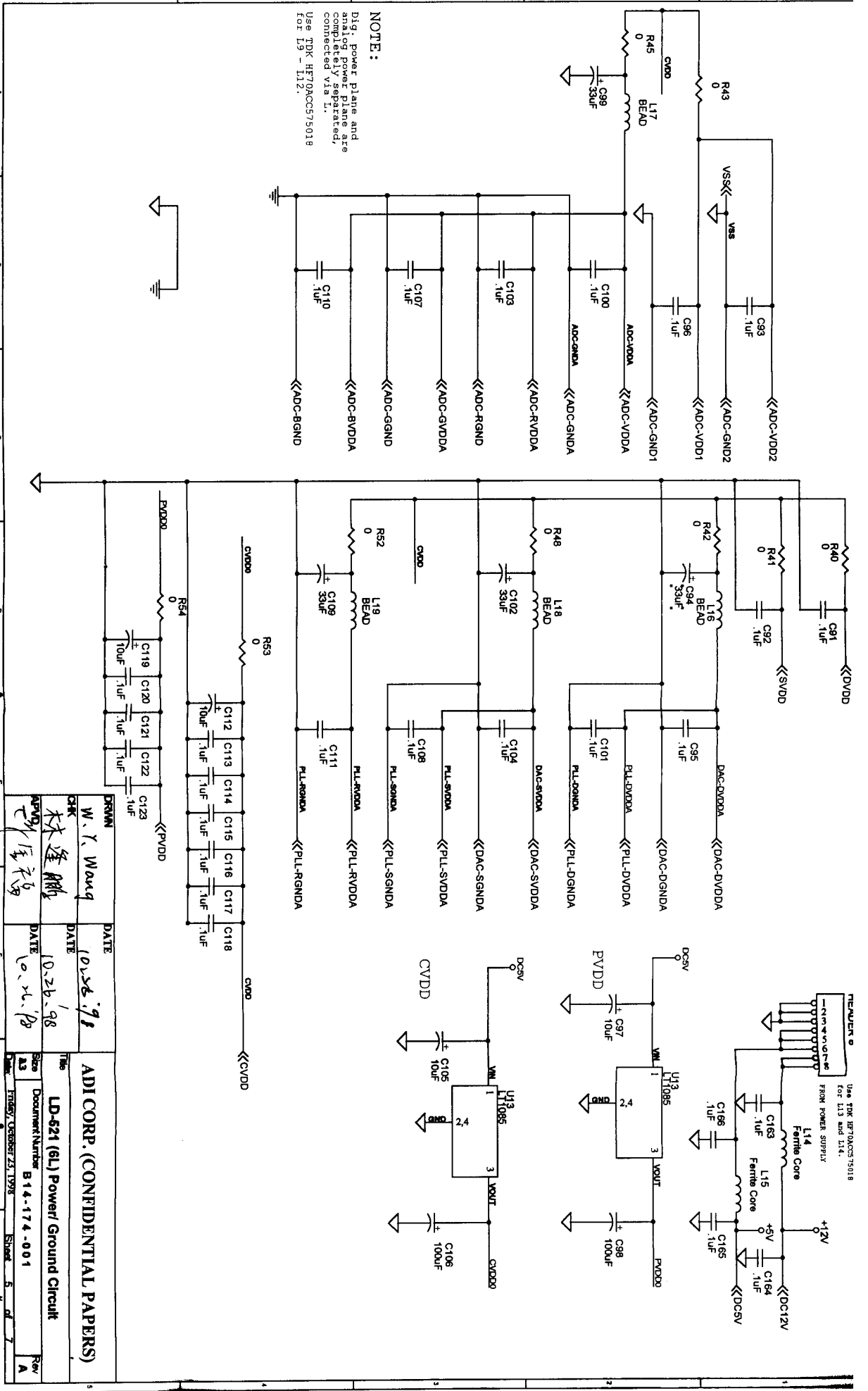


FRMAN W. Y. Wang 10.26.98 DATE	DATE 10.26.98
CK 林達聰 10.26.98 DATE	DATE 10.26.98
Title LD-521 (6L) ASIC Panel I/F + Panel Power Circuit	
Size A3 Document Number B14-174-001	Date Friday, October 23, 1998 Sheet 4 of 7
Rev A	

ADI CORP. (CONFIDENTIAL PAPERS)

NOTE:
Dig. power plane and analog power plane are completely separated, connected via L.

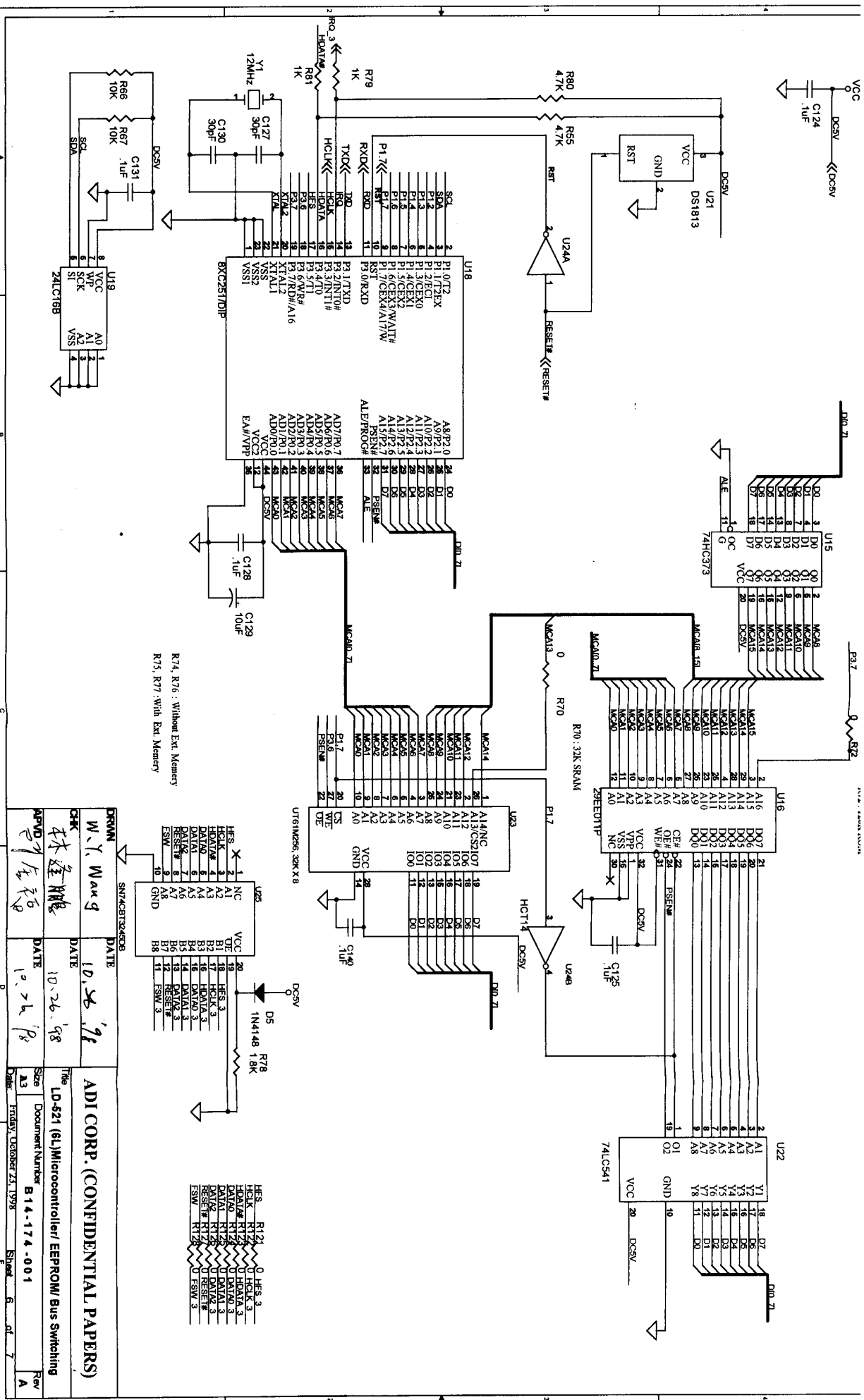
Use TDK HF70ACC575018
for L9 - L12.



MEASURE 9
Use TDK HF70ACC575018
for L13 and L14.

DATE	DATE	DATE	DATE	DATE
10.26.98	10.26.98	10.26.98	10.26.98	10.26.98
W. Y. Wang	W. Y. Wang	W. Y. Wang	W. Y. Wang	W. Y. Wang
CHK	CHK	CHK	CHK	CHK
APR 09/10	APR 09/10	APR 09/10	APR 09/10	APR 09/10

ADI CORP. (CONFIDENTIAL PAPERS)
 Title: LD-521 (6L) Power/Ground Circuit
 Doc No: LD-521 (6L)
 Document Number: B 14-174-001
 Date: THURSDAY, DECEMBER 23, 1998
 Sheet 5 of 7
 Rev A



R74, R76: Without Ext. Memory
R75, R77: With Ext. Memory

DRUM	DATE	ADDITIONAL INFORMATION
W. Y. Wang	10.26.98	Initial Design
林建龍	10.26.98	Initial Design
APVD	10.26.98	Approved

ADI CORP. (CONFIDENTIAL PAPERS)

The LD-621 (6L) Microcontroller/ EEPROM/ Bus Switching

Document Number: B14-174-001

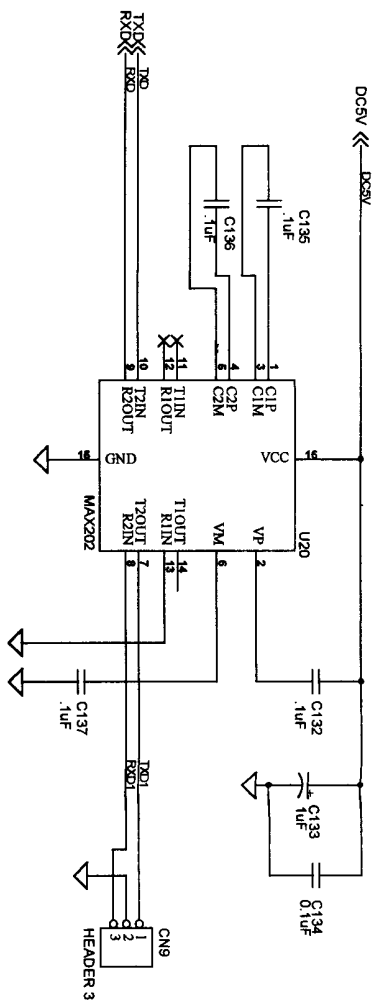
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Date: Friday, October 21, 1998

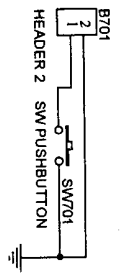
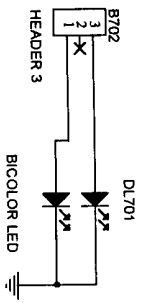
Sheet: 6 of 7

Rev: A

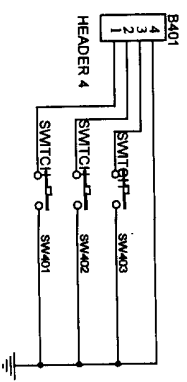
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3	HES	0 HES
4	HES	0 HES
5	HES	0 HES
6	HES	0 HES
7	HES	0 HES
8	HES	0 HES
9	HES	0 HES
10	HES	0 HES
11	HES	0 HES



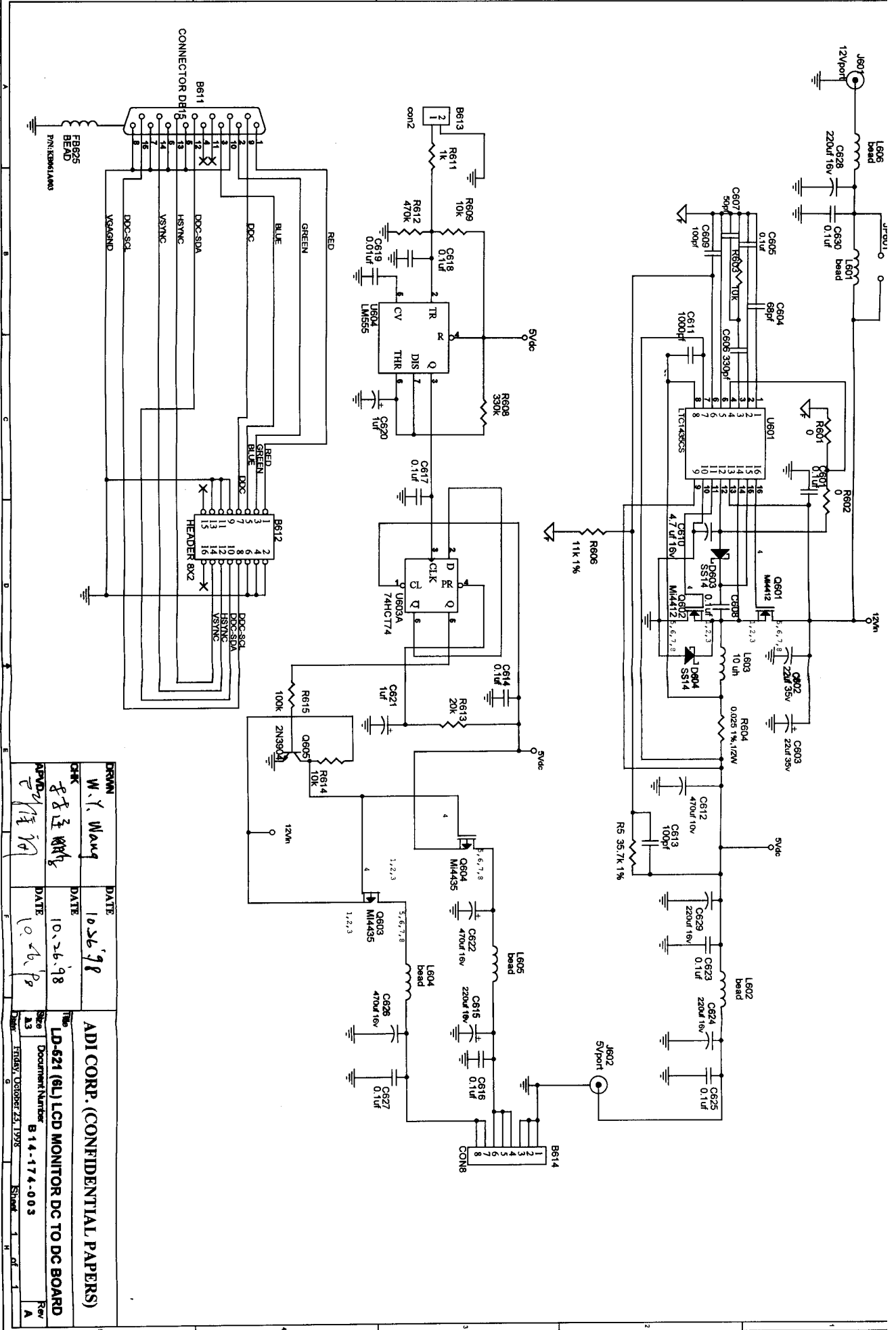
DRAWN	DATE	Title	ADICORP. (CONFIDENTIAL PAPERS)
W. Y. Wang	10.26.98		
CHK	DATE	Doc Number	LD-621 (6L) RS-232 Series Port
林達聰	10.26.98	Doc Number	B14-174-001
APUD	DATE	TRD	1998 OCTOBER 25 1998
李宇王	10.26.98	Sheet	7 of 7
		Rev	A



LED board



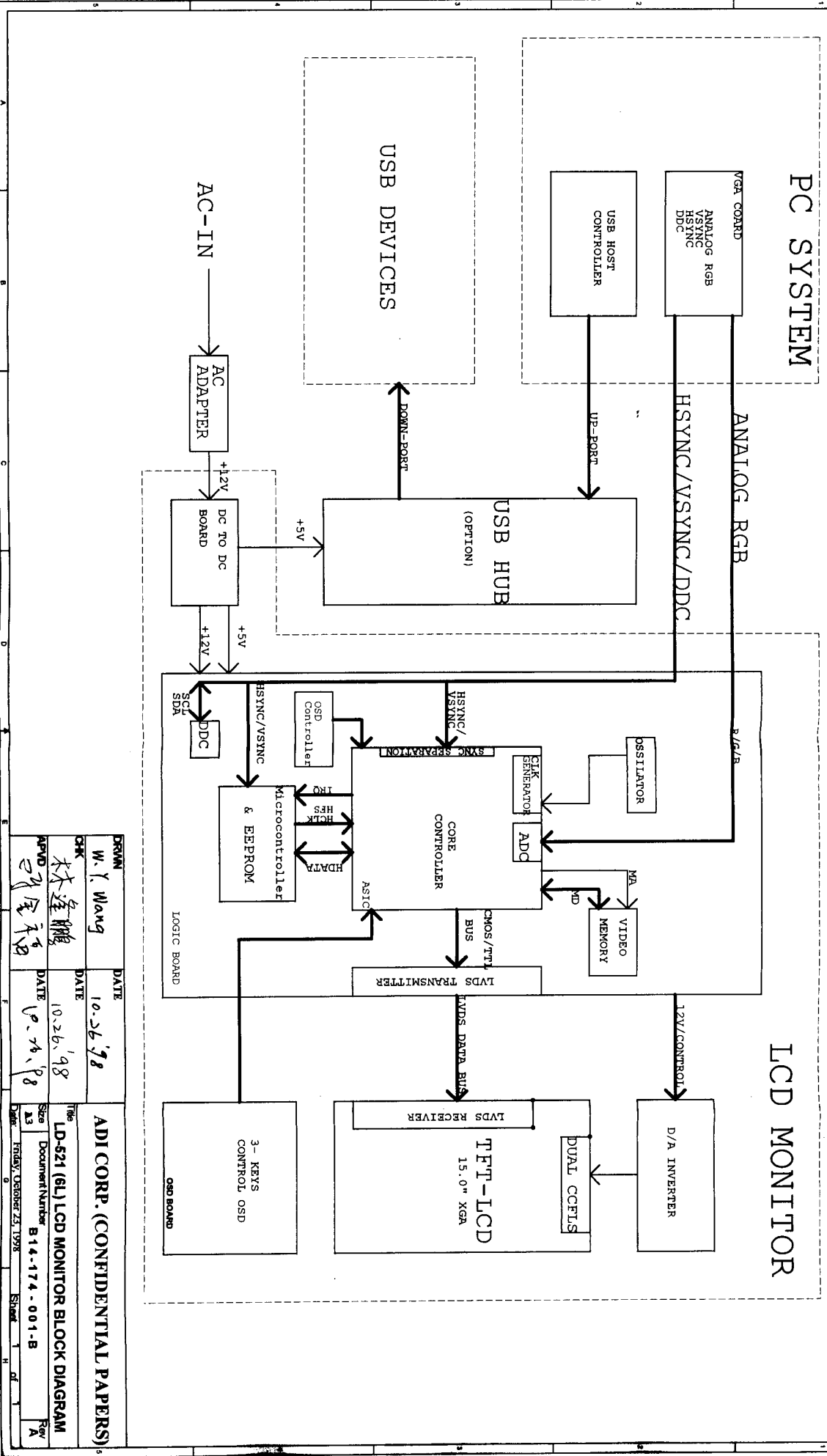
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W. Y. Wang	10.26.98	
CHK	DATE	
林達強	10.26.98	
APPRD	DATE	
李國柱	10.26.98	
Title		LD-521 (GL) LCD MONITOR LED+SWITCH BOARD
Size		A3
Document Number		B 14-174-002
Date		INDEX, October 23, 1998
Sheet		1 of 1
Rev		A



DRWN	DATE	REV
W. Y. Wang	10-26-98	A
CHK	10-26-98	
APVD	10-26-98	

ADI CORP. (CONFIDENTIAL PAPERS)	
Doc #	Rev
LD-621 (BL) LCD MONITOR DC TO DC BOARD	A
Document Number: 814-174-003	
Size: 2.3	
TRNG: 08/08/23.1793	
Sheet 1 of 1	

LCD MONITOR LD-521 BLOCK DIAGRAM



DRWN	DATE	DATE	DATE
W. Y. Wang	10-26-98	10-26-98	10-26-98
CHK	DATE	DATE	DATE
林逢龍	10-26-98	10-26-98	10-26-98
APVD	DATE	DATE	DATE
巴平	10-26-98	10-26-98	10-26-98
ADI CORP. (CONFIDENTIAL PAPERS) The LD-521 (6L) LCD MONITOR BLOCK DIAGRAM Size Document Number B14-174-001-B Date: TRIDAY, OCTOBER 23, 1998 Sheet 1 of 1			