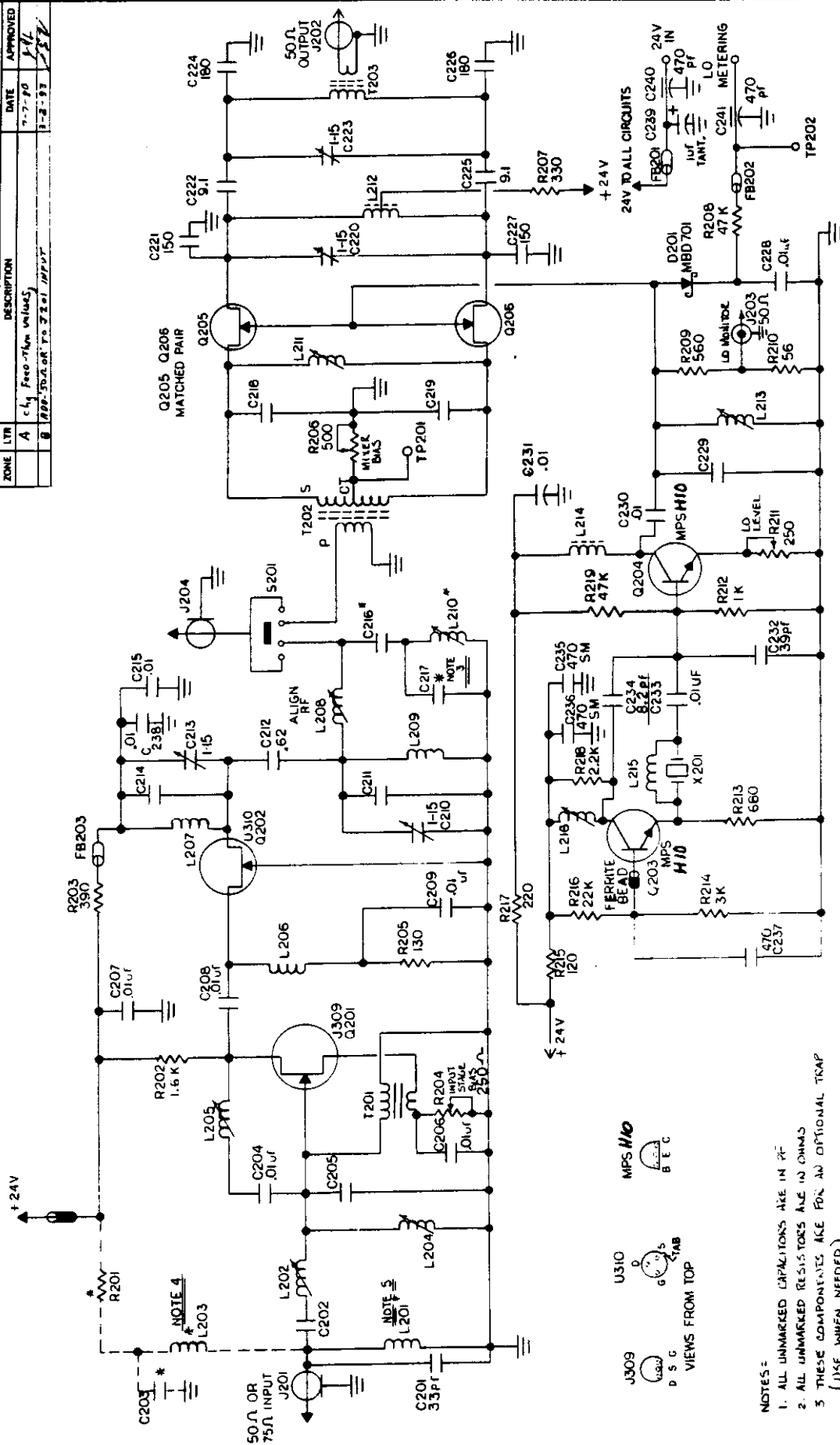




	QTY REQD	CODE IDENT	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION
PARTS LIST				
TELEVISION TECHNOLOGY CORP. ARVADA COLO., 80003				
INPUT CONVERTER XLIFM , XLIOFM SCHEMATIC				
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES $\pm .XXX \pm \quad \pm$			CONTRACT NO.	
MATERIAL			DRAWN MAJOR	DATE
			CHECKED JAL	11-6-79
				3-24-80
FINISH				
DO NOT SCALE DRAWING				
APPLICATION	NEXT ASSY	USED ON		
X	200			
S	201			
V	206			
J	203			
D	201			
G	241			

NOTES:

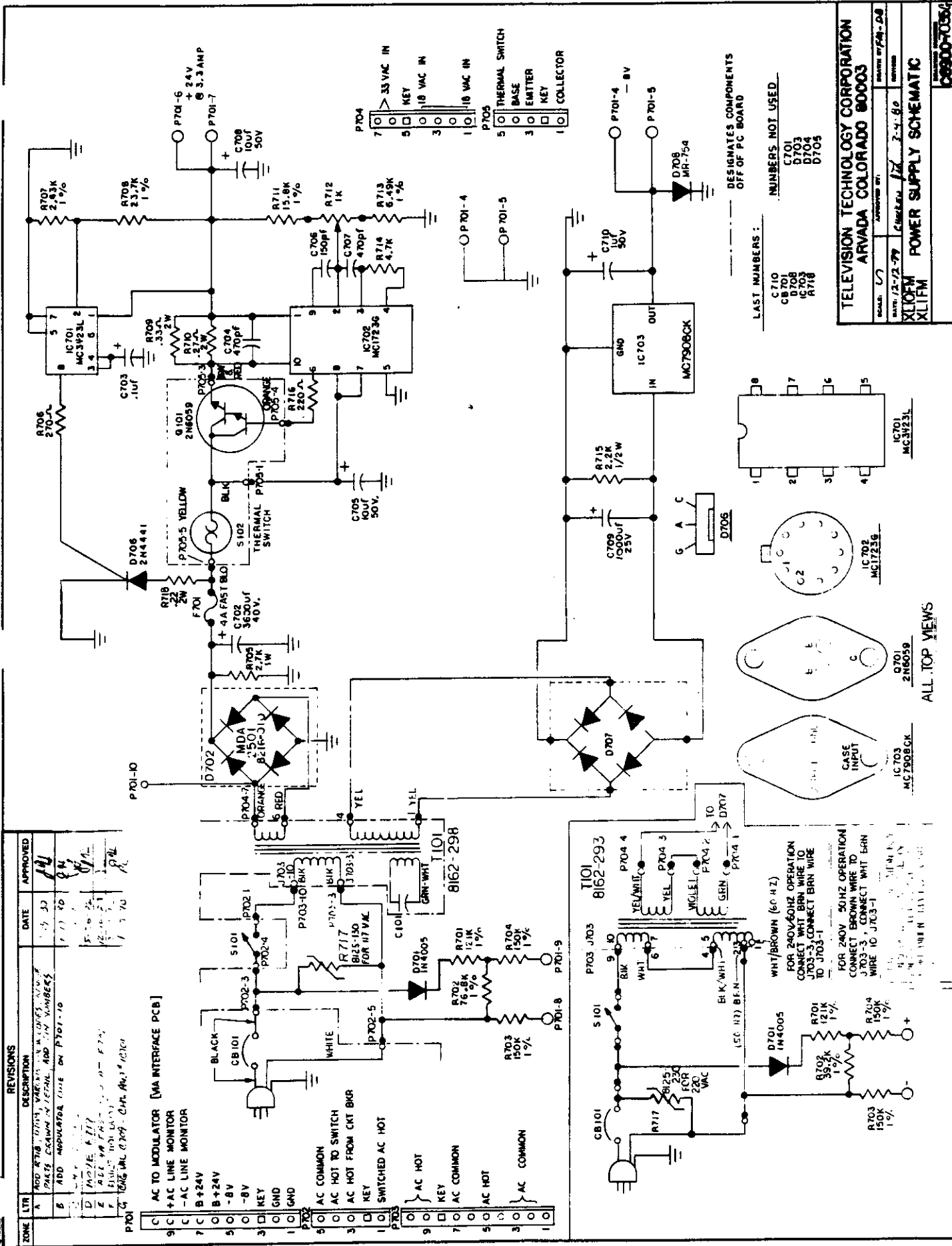
4. THESE COMPONENTS TO BE USED FOR THE DI POWER ON THE ARVADA LINE (EXCEPTED FROM)
5. NOT USED WHEN DIVERGING DC BOOLE ON THE ANTENNA LINE.
6. CAPACITORS WITH MAXIMUM VOLTAGE RATING WITH FREQUENCY

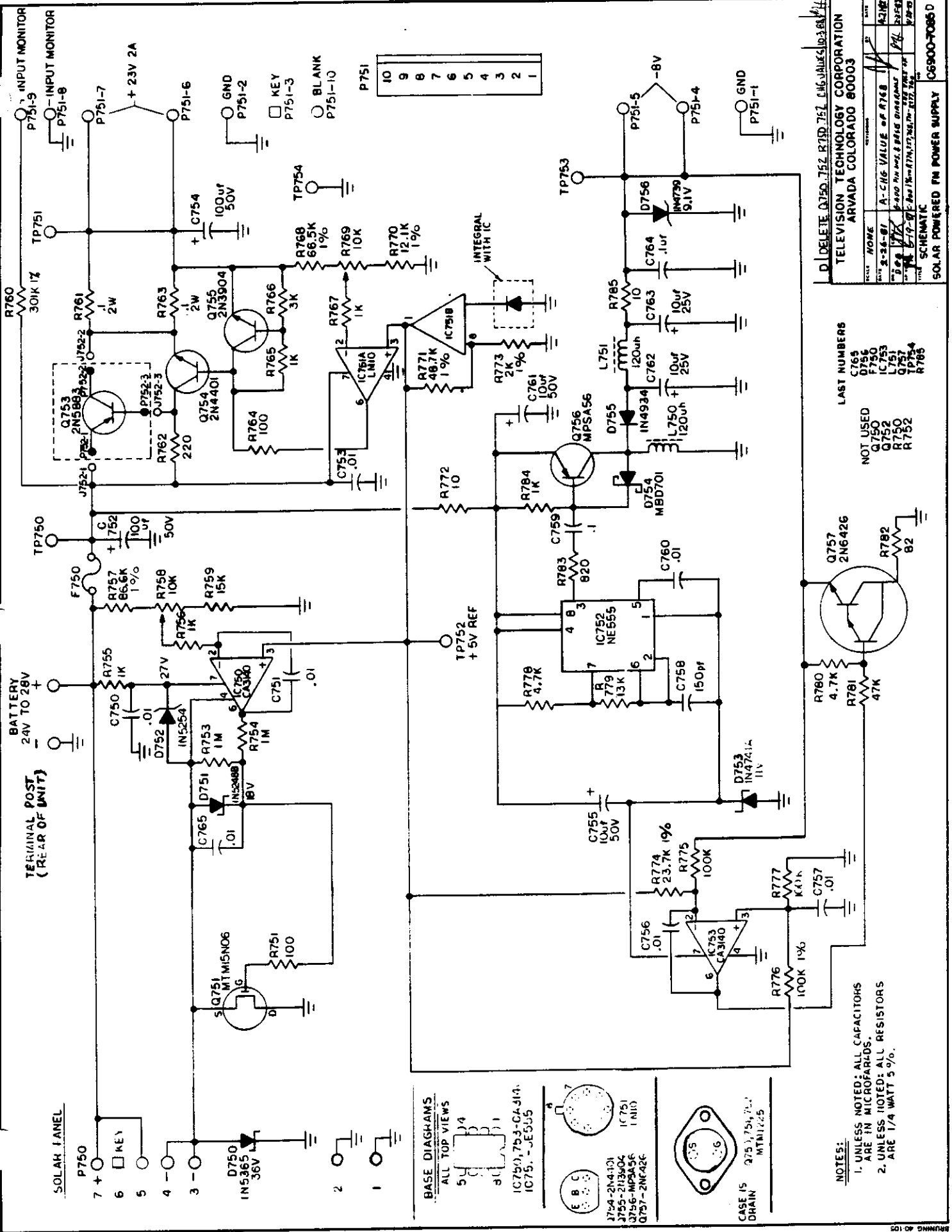
SIZE CODE IDENT NO. DRAWING NO.
C **O6900-2035 B**

SCALE (GRI)
 SHEET 1 OF 1

San Francisco, CA 94102

ZONE	LTR	DESCRIPTION	REVISIONS
A		ADD RTG LTR, VARIATION	
B		PHYS. DRAWN IN FIELD. ADD	
C		ADD MODULATOR LINE ON P	
D		MOD. & LTR	
E		ADD 4A FMS -	
F		ELIM. TON LAY	
G		TRANS. VAL. 0.209 - CHG. 0.010	

[illegible]



- NOTES:
1. UNLESS NOTED: ALL CAPACITORS ARE IN MICROFARADS.
 2. UNLESS NOTED: ALL RESISTORS ARE 1/4 WATT 5%.

LAST NUMBERS

NOT USED

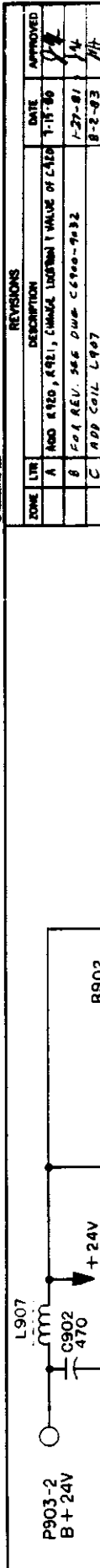
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TELEVISION TECHNOLOGY CORPORATION
ARVADA COLORADO 80003

D. DELETE Q750, 751, 752, 753, 754, 755, 756, 757, 758, 759, 760, 761, 762, 763, 764, 765, 766, 767, 768, 769, 770, 771, 772, 773, 774, 775, 776, 777, 778, 779, 780, 781, 782, 783, 784, 785, 786, 787, 788, 789, 790, 791, 792, 793, 794, 795, 796, 797, 798, 799, 800, 801, 802, 803, 804, 805, 806, 807, 808, 809, 810, 811, 812, 813, 814, 815, 816, 817, 818, 819, 820, 821, 822, 823, 824, 825, 826, 827, 828, 829, 830, 831, 832, 833, 834, 835, 836, 837, 838, 839, 840, 841, 842, 843, 844, 845, 846, 847, 848, 849, 850, 851, 852, 853, 854, 855, 856, 857, 858, 859, 860, 861, 862, 863, 864, 865, 866, 867, 868, 869, 870, 871, 872, 873, 874, 875, 876, 877, 878, 879, 880, 881, 882, 883, 884, 885, 886, 887, 888, 889, 890, 891, 892, 893, 894, 895, 896, 897, 898, 899, 900, 901, 902, 903, 904, 905, 906, 907, 908, 909, 910, 911, 912, 913, 914, 915, 916, 917, 918, 919, 920, 921, 922, 923, 924, 925, 926, 927, 928, 929, 930, 931, 932, 933, 934, 935, 936, 937, 938, 939, 940, 941, 942, 943, 944, 945, 946, 947, 948, 949, 950, 951, 952, 953, 954, 955, 956, 957, 958, 959, 960, 961, 962, 963, 964, 965, 966, 967, 968, 969, 970, 971, 972, 973, 974, 975, 976, 977, 978, 979, 980, 981, 982, 983, 984, 985, 986, 987, 988, 989, 990, 991, 992, 993, 994, 995, 996, 997, 998, 999, 1000

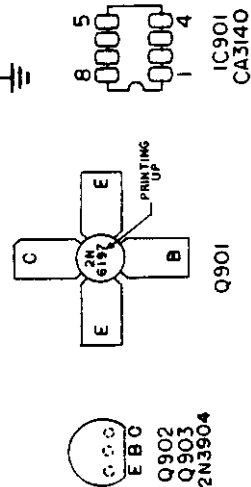
SOLAR POWERED FM POWER SUPPLY C6900-7086 D

REVISIONS			
ZONE	LTR	DESCRIPTION	DATE
A	1	ADD R902, R901, CHANGE LOGIC IN VALUE OF C902	1-15-60
B	2	ADD REV. 345 DWA C6900-9432	1-27-61
C	3	ADD COIL L907	8-2-63



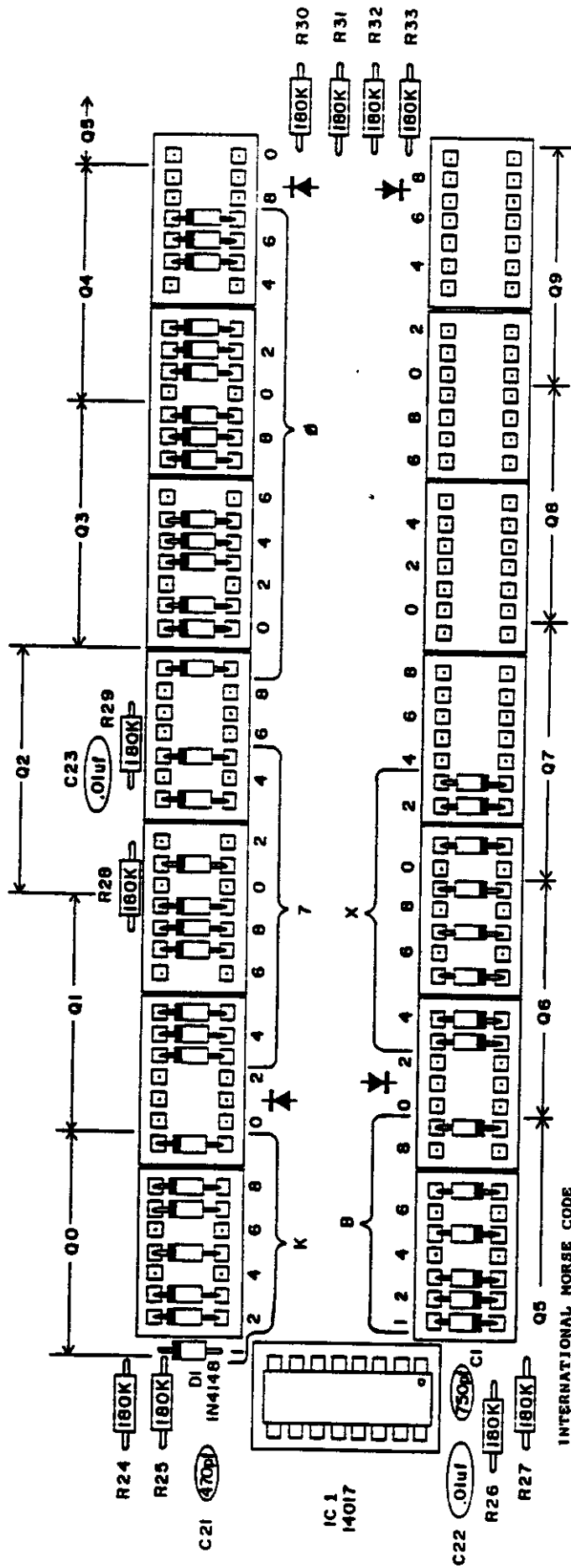
LAST NUMBERS	
C924	D904
IC901	J902
L907	P903
Q901	Q902
Q903	Q904
R901	R902
R903	R904
R905	R906
R907	R908
R909	R910
R911	R912
R913	R914
R915	R916
R917	R918
R919	R920
R921	R922
R923	R924
R925	R926
R927	R928
R929	R930
R931	R932
R933	R934
R935	R936
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R945	R946
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R961	R962
R963	R964
R965	R966
R967	R968
R969	R970
R971	R972
R973	R974
R975	R976
R977	R978
R979	R980
R981	R982
R983	R984
R985	R986
R987	R988
R989	R990
R991	R992
R993	R994
R995	R996
R997	R998
R999	R1000

- NOTES
1. UNLESS NOTED, ALL CAPACITORS ARE IN MICROFARADS.
 2. UNLESS NOTED, ALL RESISTORS ARE 1/4W 5%.
 3. CAPACITORS MARKED M ARE MONOLITHIC TYPE.



TOP VIEWS

TELEVISION TECHNOLOGY CORPORATION	
ARVADA COLORADO 80003	
DATE	1-15-60
DESIGNED BY	1-27-61
REVIEWED BY	8-2-63
SCHEMATIC 1.1 WATT XLFM AMPLIFIER	
C6900-90360	



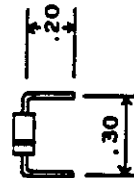
ALPHABET

ALPHABET	NUMERALS
A	1
B	2
C	3
D	4
E	5
F	6
G	7
H	8
I	9
J	0
K	1
L	2
M	3
N	4
O	5
P	6
Q	7
R	8
S	9
T	0
U	1
V	2
W	3
X	4
Y	5
Z	6

• - DOT = 1 DIODE.

— - DASH = 3 DIODES IN LINE.

IN4148 DIODE



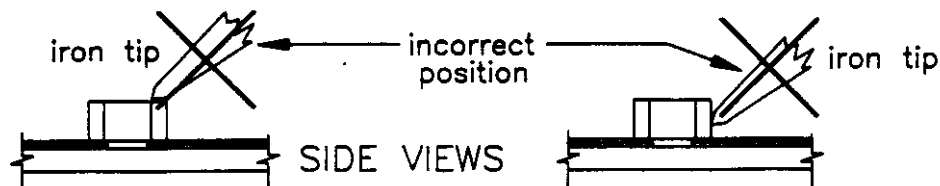
BEND AND TRIM PROGRAMMING DIODE LEADS AS SHOWN

DIODE ARRANGEMENT SHOWN IS FOR CODE SETTING - K78BX

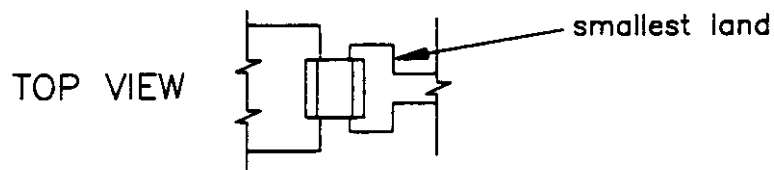
TELEVISION TECHNOLOGY CORPORATION ARVADA, COLORADO 80003	
SCALE: 2:1	APPROVED BY: <i>[Signature]</i>
DATE: 2-15-79	REVISION
CODE KEYER PROGRAMMING DIAGRAM	
DRAWING NUMBER 91370-8012	

THE FOLLOWING PROCEDURE IS BASED ON INFORMATION SUPPLIED BY
AMERICAN TECHNICAL CERAMICS CORPORATION (BULLETIN No. 201)

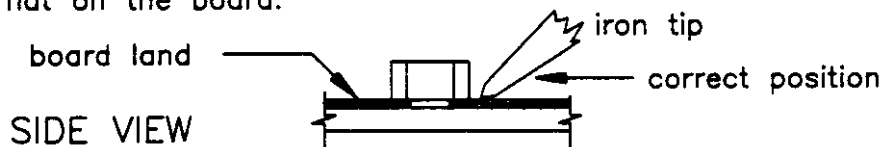
1. The recommended soldering iron is a temperature controlled, low voltage, grounded tip unit such as the Weller model WTCNP with a 600°F chisel tip.
2. Never, never use a non-temperature controlled iron larger than 25 watts to solder chip caps. The caps have an internal glass seal that melts at 320° F. An iron larger than this will cause internal damage to the chip that may not show up as a failure until weeks later.



3. Never touch the soldering iron tip directly to the chip cap end termination or the ceramic body. See the illustrations above.
4. Chips that have gold terminations must never be attached with solder that does not have a 1.4% to 2% silver content (SN62 recommended). Chips with solder tinned terminations may be soldered with common 60-40 or 63-37 tin-lead solder.
5. Pre-tin the circuit board where the chip is to be soldered. Use the smallest quantity of solder possible so the chip will lay flat on the board.



6. Determine which board land is smaller and work on this end first.
7. Place the chip on the board with a pair of tweezers and make sure it is resting flat on the board.



8. Place the iron tip on the board about 1/16 of an inch (1.6mm) away from the chip termination. Apply solder to the land-soldering iron tip interface and when the solder begins to flow, slowly move the iron tip towards the chip, then quickly remove the iron.
9. Examine the chip to make sure it is still flat on the board. Repeat the above step on the other end of the chip.
10. The solder fillets should be evenly flowed and free of solder peaks and valleys.
11. Clean the flux from the board with a suitable cleaner.

TELEVISION TECHNOLOGY CORP.

APVD BY:	DRAWN BY: JH	DATE: 2-21-1992
CHIP CAPACITOR SOLDERING PROCEDURE		DRAWING NUMBER 7800-0003A

FACTORY TEST DATA
FOR MODEL XL__FM__

JOB # _____	SERIAL # _____	OPTIONS:
DATE COMPLETE _____		MODULATOR ☐
INPUT CHANNEL _____	FREQUENCY _____ MHz	TVK ☐
OUTPUT CHANNEL _____	FREQUENCY _____ MHz	DECODER ☐

CONDITION OF TEST:

INPUT SIGNAL

OUTPUT POWER ☐ 1W ☐ 10W (No. 1) 91% 100 % Modulation (L + R)

OUTPUT POWER ☐ 1W ☐ 10W (No. 2) 9% 19KHz Pilot

FRONT METER READINGS:

MULTIMETER	FULL SCALE	
	XL1	XL10
LINE VOLTS _____	200V	
+24 _____	30V	
- 8 _____	10V	
INPUT SIGNAL _____	20dB	
VCO VOLTAGE _____	20V	
Ipa 1 _____	200mA	2A
Ipa 2 _____	200mA	2A
DEVIATION _____	100 %	

SERIAL NO. PA-1 _____

SERIAL NO. PA-2 _____

		FULL SCALE	
POWER METER		XL1	XL10
FWD	PA-1 _____	2W	20W
REV	PA-1 _____	.2W	2W
FWD	PA-2 _____	2W	20W
REV	PA-2 _____	.2W	2W

MODULATOR P.C. BOARD (6900-5025)

TONE DECODER FREQUENCY _____

DEVIATION LIMIT SET _____ kHz

INPUT CONVERTER (6900-2000)

SERIAL NUMBER _____

LO FREQUENCY _____ MHz

GAIN _____ dB

LO VOLTAGE (TP202) _____ Volts

UPCONVERTER P.C. BOARD (6900-4025)

LO FREQUENCY _____ MHz

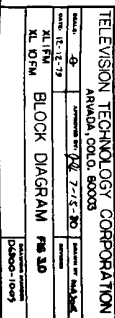
OUTPUT POWER _____ dBm

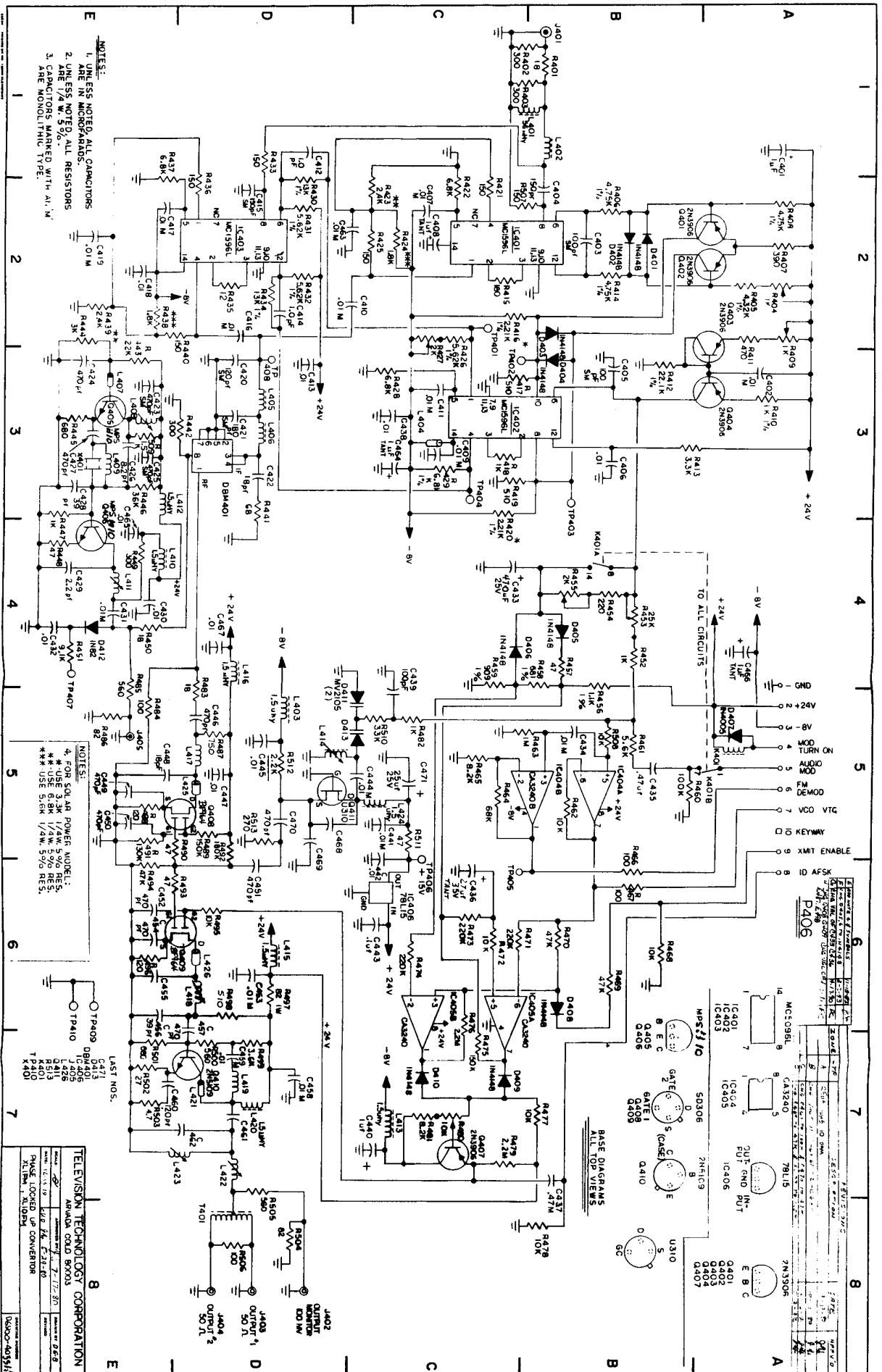
LO VOLTAGE (TP407) _____ Volts

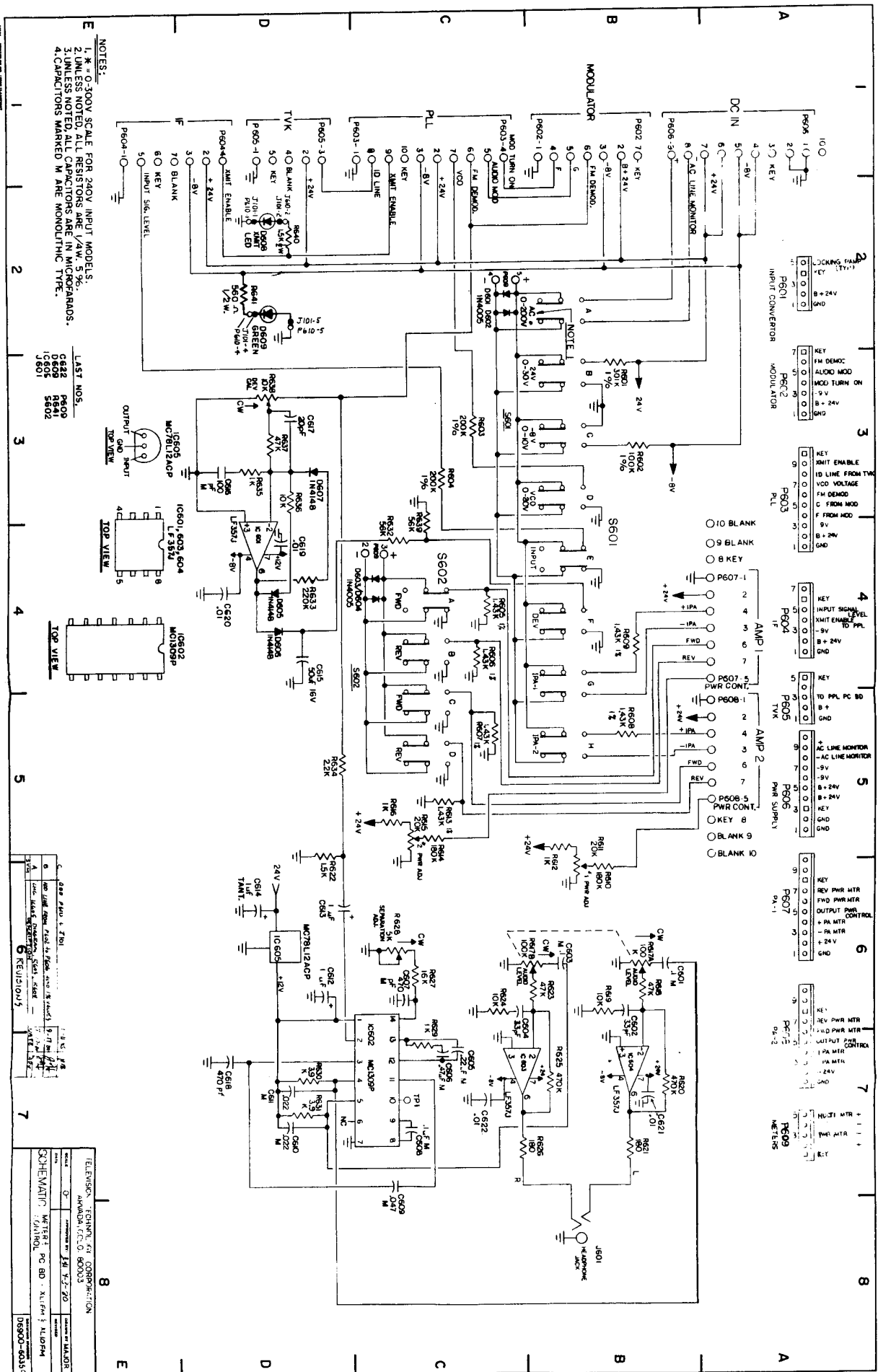
OUTPUT SPURIOUS PRODUCTS

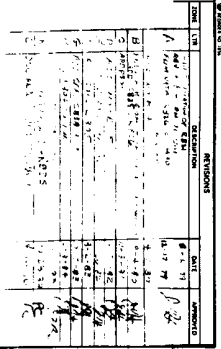
SPECIFICATION LIMIT

	PA-1	PA-2	
2nd HARMONIC _____ dB	_____ dB		(REFERENCED TO RATED POWER) Min. -80 dB
3rd HARMONIC _____ dB	_____ dB		(REFERENCED TO RATED POWER) Min. -70 dB
OUTPUT 10.7 MHz _____ dB	_____ dB		(REFERENCED TO RATED POWER) Min. -70 dB
ALL OTHER SPURIOUS _____ dB	_____ dB		(REFERENCED TO RATED POWER) Min. -100 dB

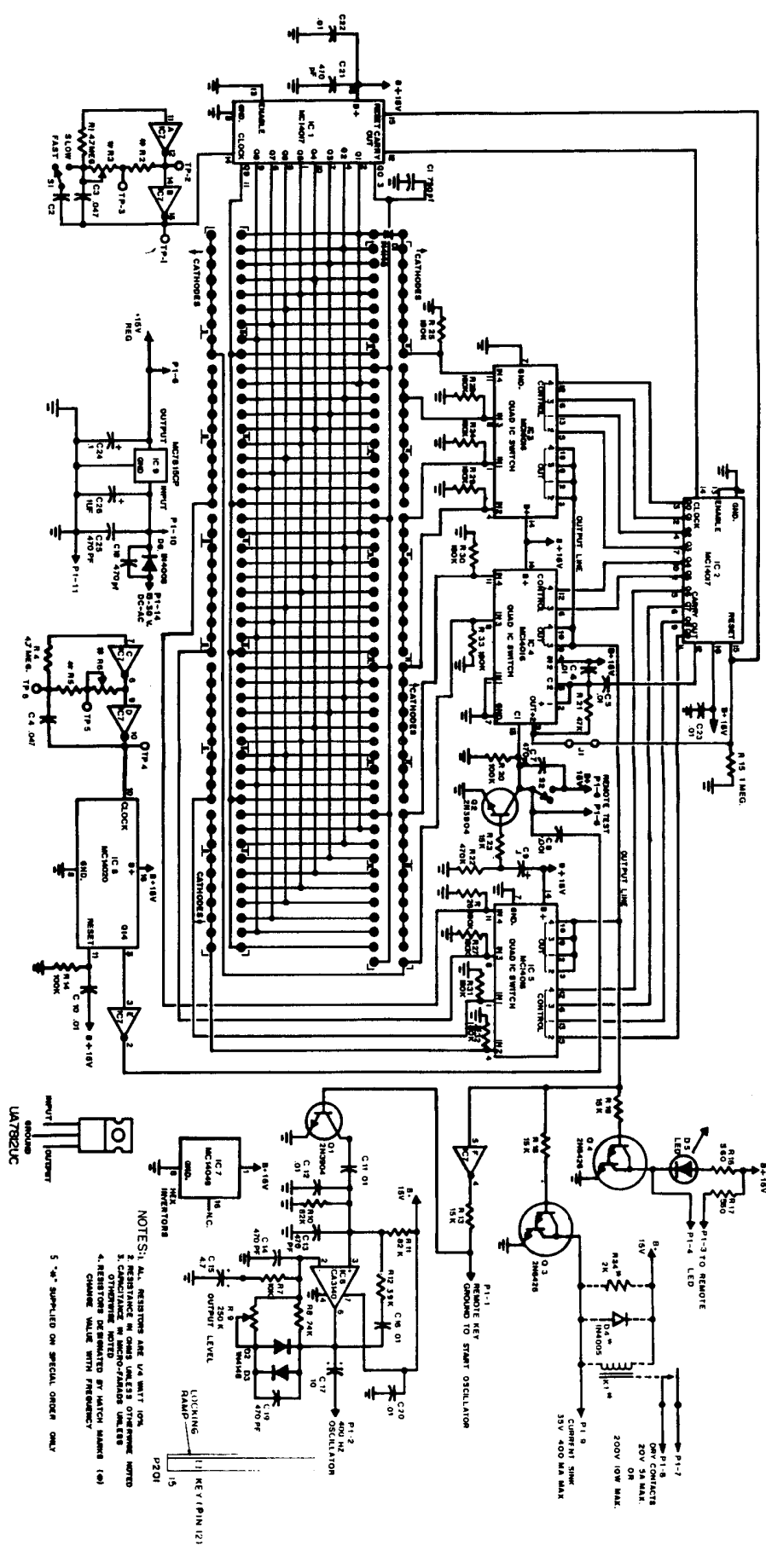








REVISIONS		DATE	BY
1	REVISED	6-6-77	JMC
2	REVISED	6-6-77	JMC



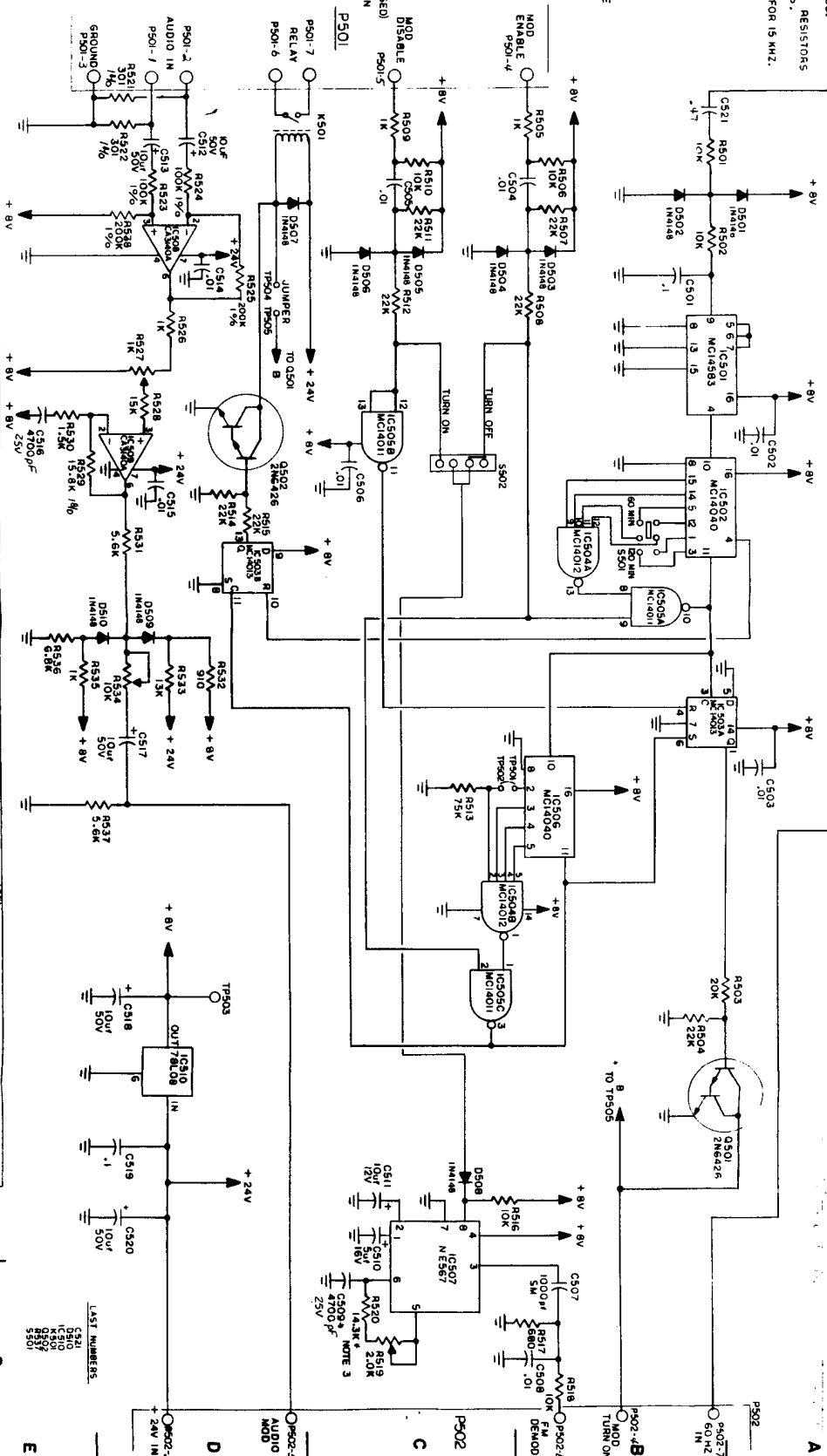
NOTES: 1. ALL RESISTORS ARE 1/4 WATT 5% TOLERANCE UNLESS NOTED OTHERWISE.
 2. CAPACITORS ARE MICRO-FARAD UNLESS NOTED OTHERWISE.
 3. CRYSTAL IS 32,768 KHZ.
 4. REMOTE KEY IS REMANENT BY SWITCH MARKING (K)
 5. "A" SUPPLIED ON SPECIAL ORDER ONLY

TELEVISION TECHNOLOGY CORPORATION	
ARVADA, COLORADO 80003	
DATE	6-6-77
DESIGNED BY	JMC
CHECKED BY	
APPROVED BY	
TVK1 CODE KEYS	1100-0000

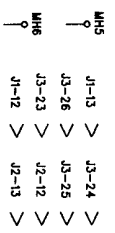
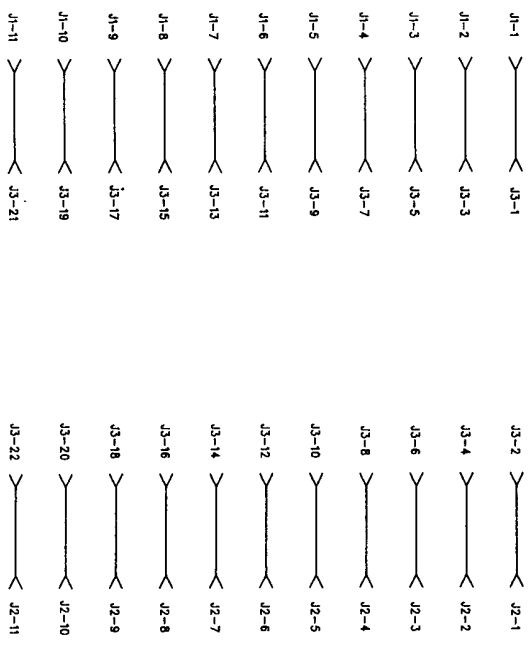
- NOTES:
1. UNLESS NOTED, ALL CAPACITORS ARE IN MICROFARADS.
 2. UNLESS NOTED, ALL RESISTORS ARE 1/4 WATT 5%.
 3. VALUES SHOWN ARE FOR 15 KHZ.

- P501
- 1 AUDIO IN
 - 2 AUDIO IN
 - 3 MOD ENABLE
 - 4 MOD DISABLE
 - 5 RELAY
 - 6 KEYWAY
 - 7 BLANK
 - 8 BLANK
 - 9
 - 10

- P502
- 1 GROUND
 - 2 +24V
 - 3 -8V (NOT USED)
 - 4 MOD TURN ON
 - 5 AUDIO MOD
 - 6 FM DEMOD
 - 7 80 KHZ
 - 8 KEYWAY

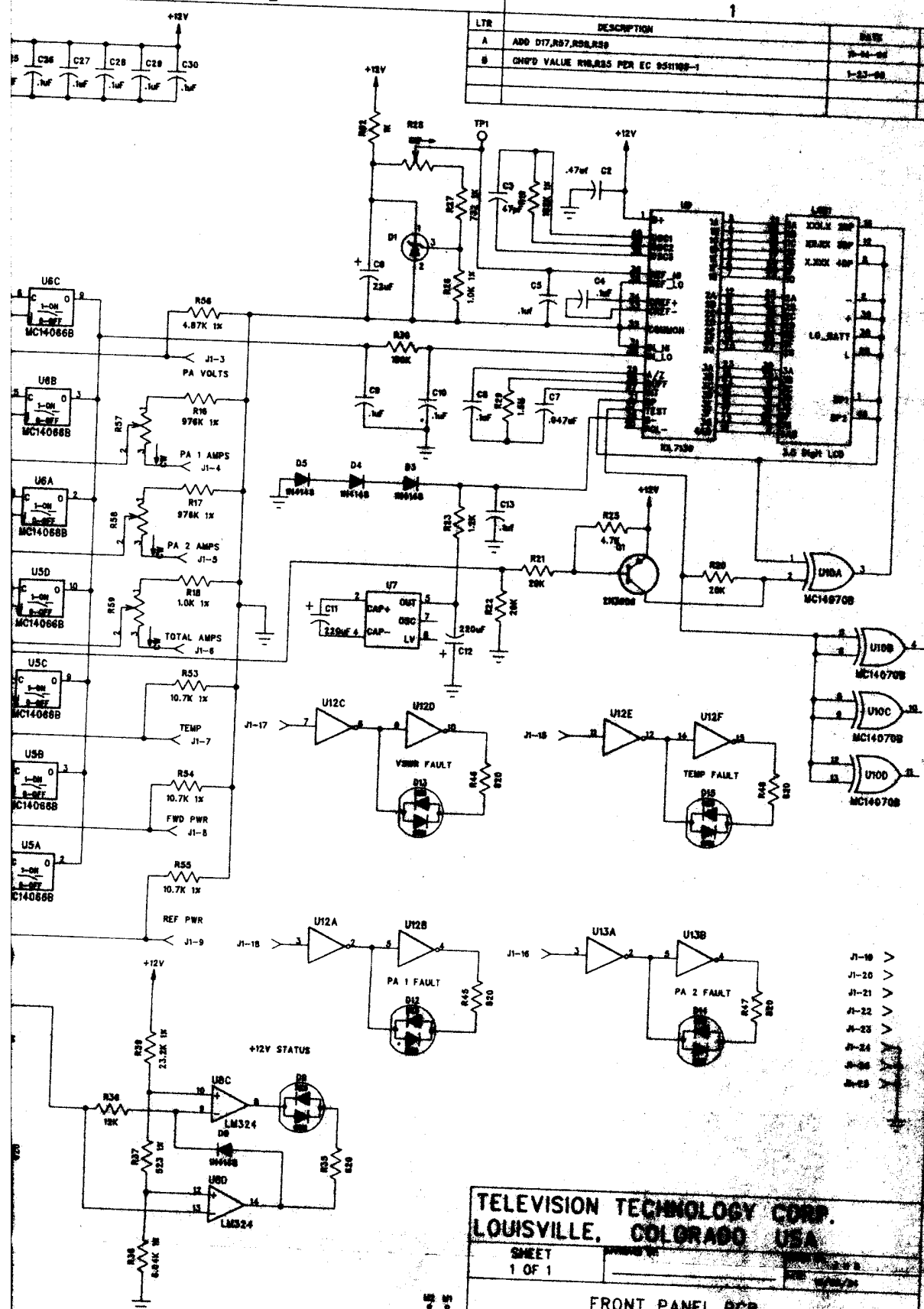


LTR	DESCRIPTION	DATE	APVD



TELEVISION TECHNOLOGY CORP. LOUISVILLE, COLORADO USA	
SHEET 1 OF 1	APPROVED BY: _____ DATE: 10/13/94
REAR TERMINAL INTERFACE	
FMS 500 CHASSIS	DRAWING NUMBER: B4501-1235

LTR	DESCRIPTION	DATE	APPROV
A	ADD D17,R57,R58,R59	7-24-68	STB
B	CHNGD VALUE R58,R55 PER EC 951109-1	1-23-68	AKB



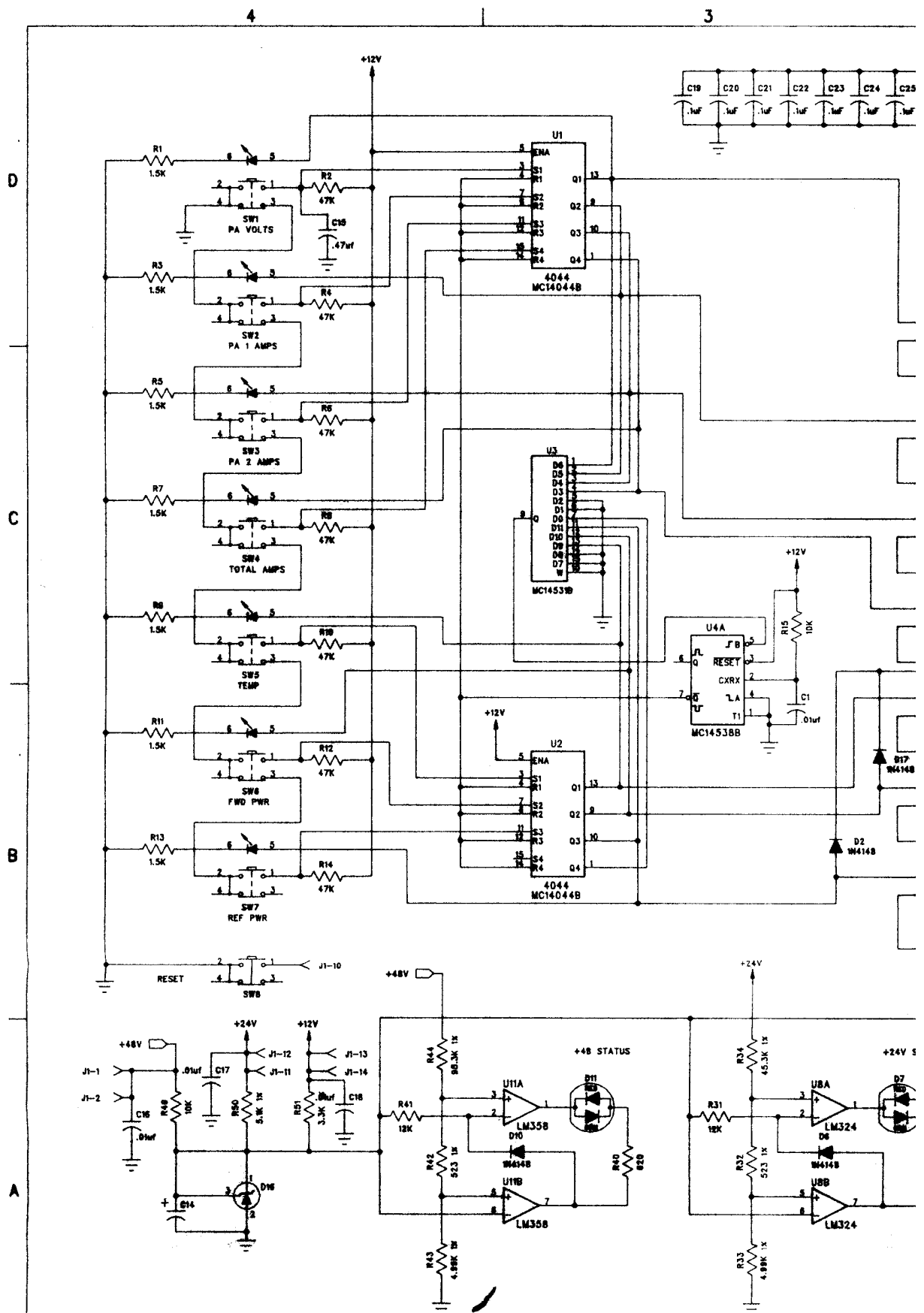
TELEVISION TECHNOLOGY CORP.
LOUISVILLE, COLORADO USA

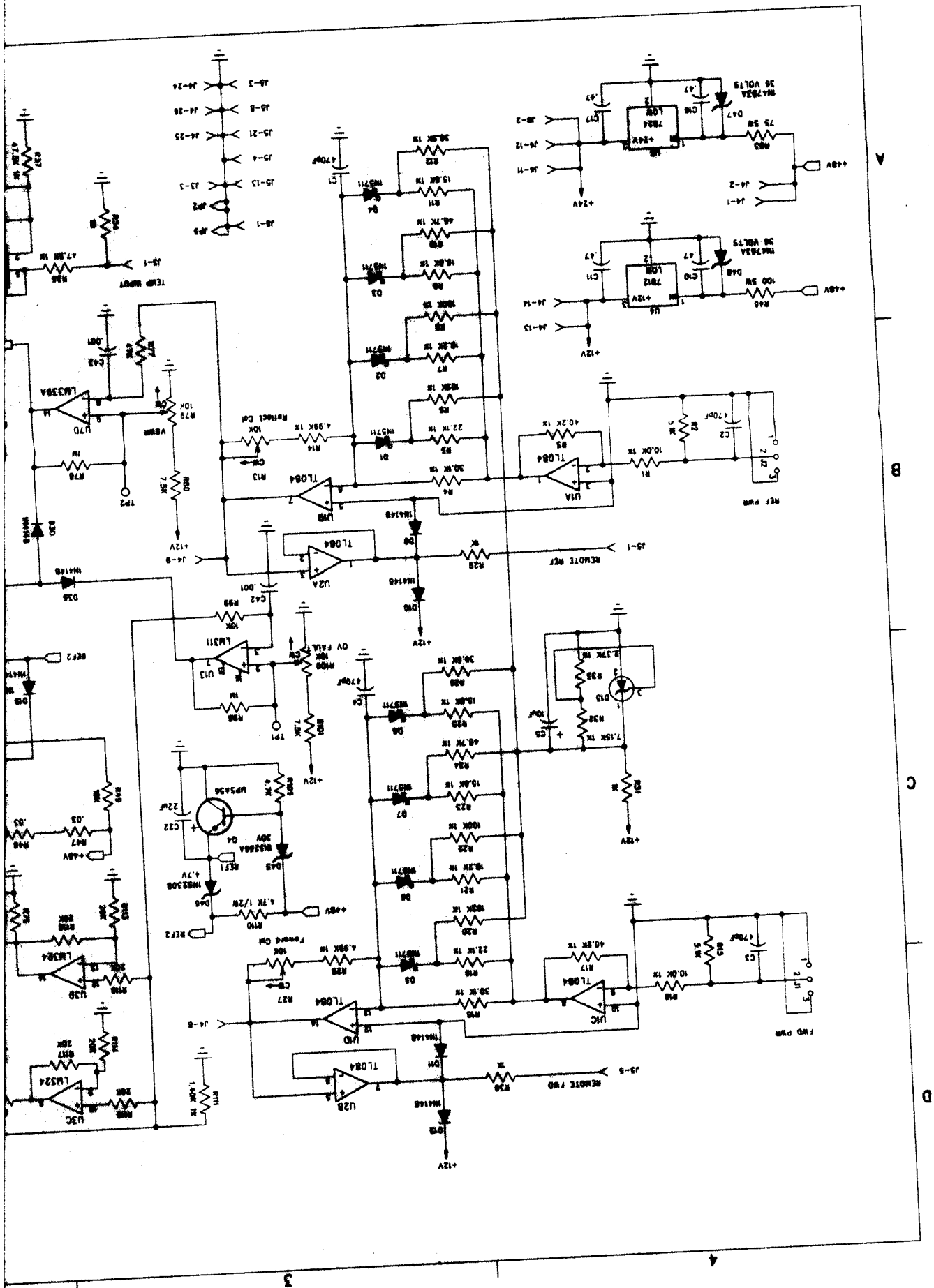
SHEET 1 OF 1

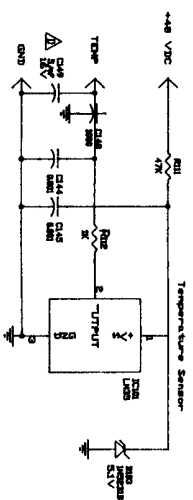
FRONT PANEL PCB

FMS 500

C4501-1135B

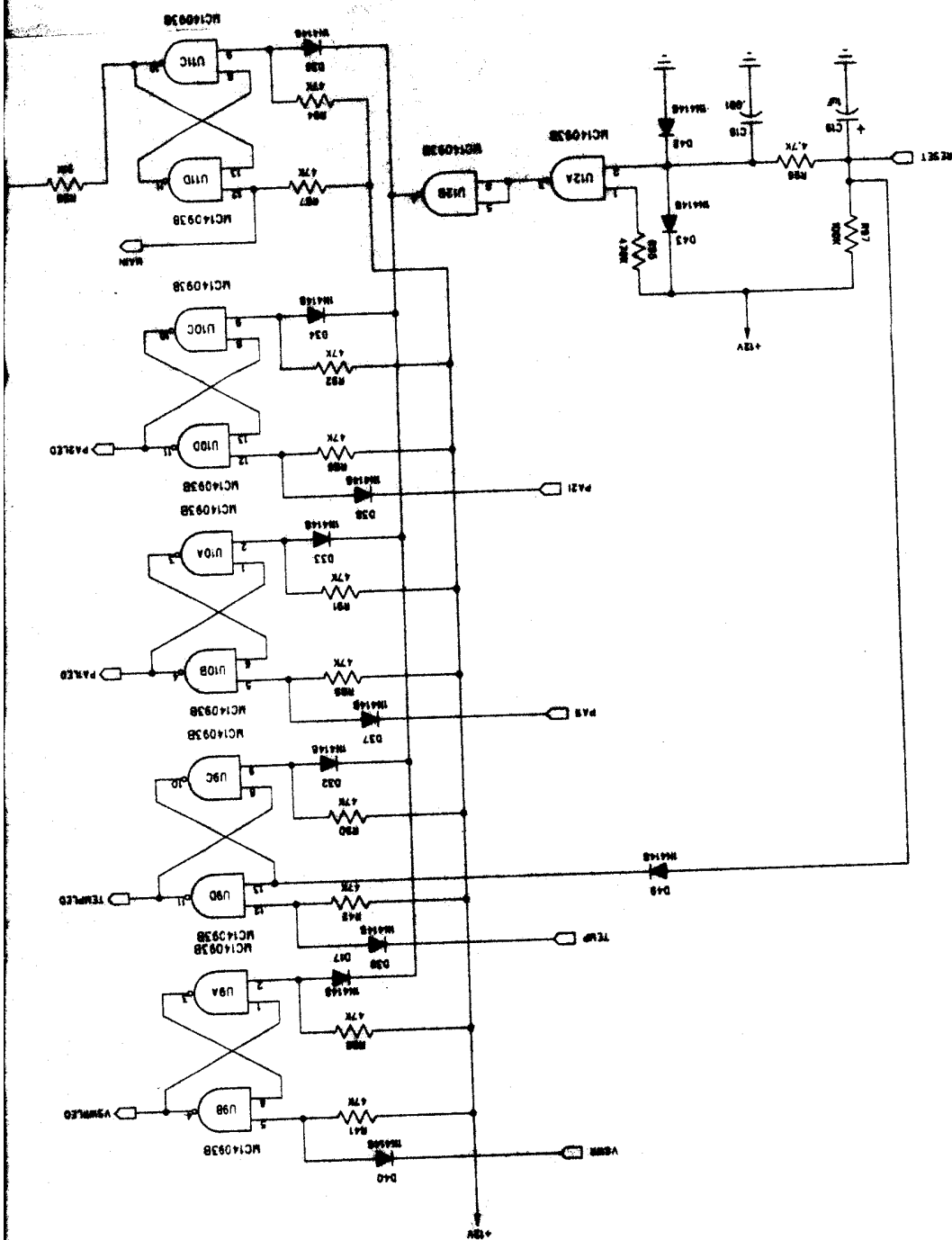




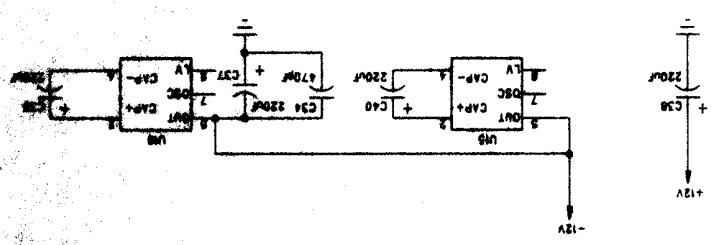
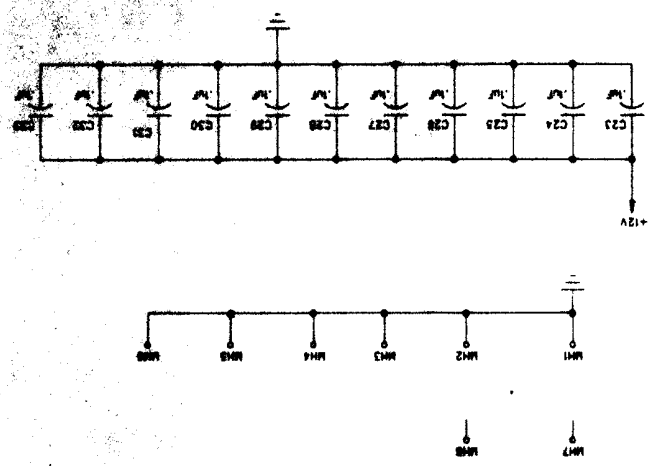
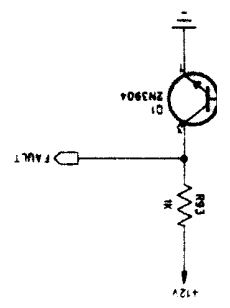


LTR	DESCRIPTION	DATE	APVD
A	FIX **48V INPUT	12-15-94	
B	EC#941205-05, RILI VAS RLS	04-04-95	BR
C	EC#950818-01, VALUE CHANGES	09-22-95	BR
D	960223-04, ADDED CAP149	3-6-96	EJS

LARCAN-TTC, INC.	
LOUISVILLE, COLORADO USA	
DO NOT SCALE DRAWING	APPROVED BY: <i>U. Cooper</i>
SCALE 1=1.37	DRAWN BY: BR
DATE: 10-28-94	
POWER AMPLIFIER SCHEMATIC	
FMS 500	DRAWING NUMBER
POWER AMPLIFIER	B4501-1035D



FMS 500	
C4501-13356	
MAIN CONTROL PCB	
SHEET 2 OF 2	
LOUISVILLE, COLORADO USA	
LARGAN-TIC INC	



DESCRIPTION	QTY	REF
SEE SHEET ONE FOR ALL DIMENSIONS AND ALL SPECIFICATIONS		