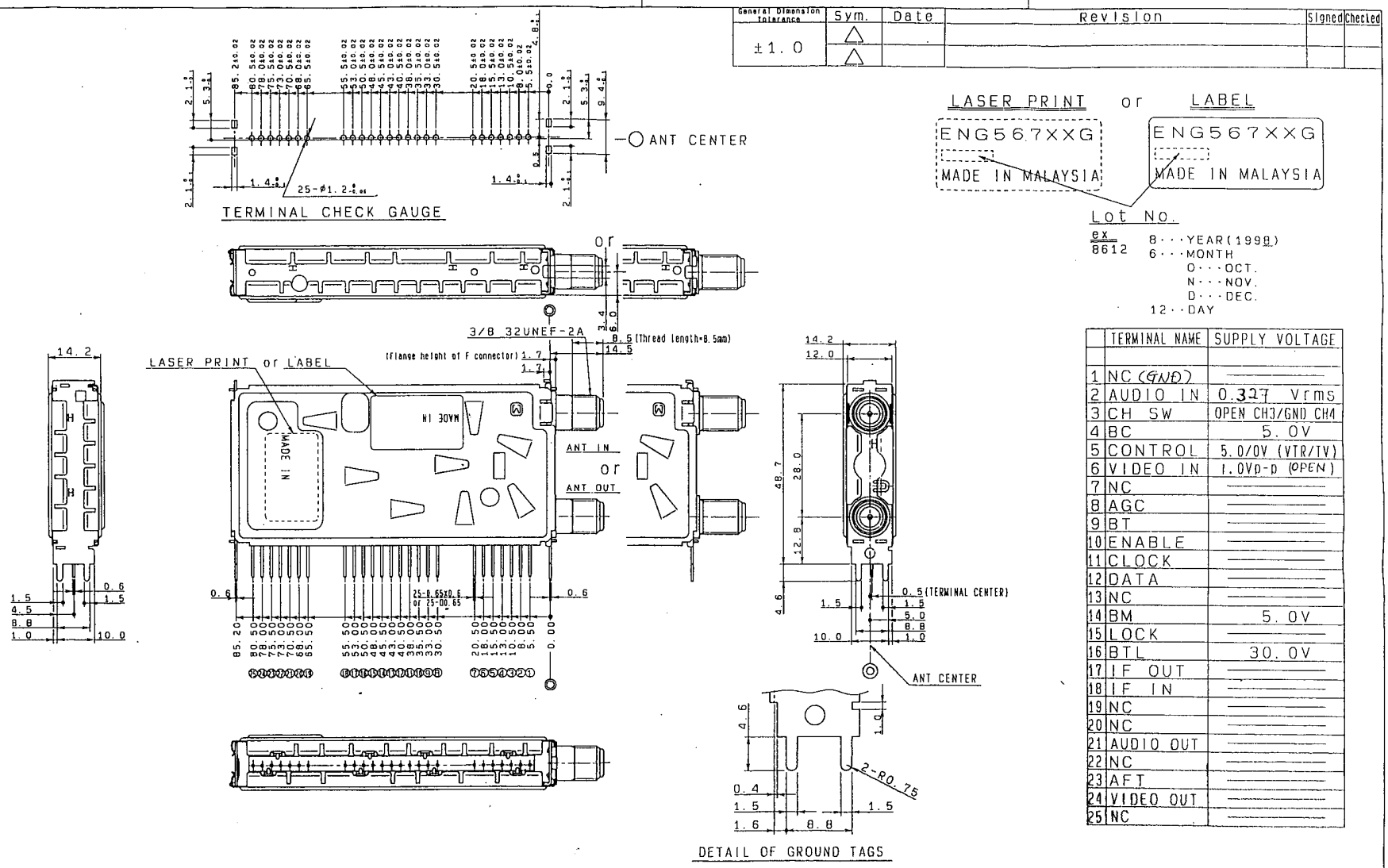
General Dimension Tolerance	Sym.	Date	Revision	Signed	Checked
±1.0	△				
	△				



	TERMINAL NAME	SUPPLY VOLTAGE
1	NC (GND)	
2	AUDIO IN	0.327 Vrms
3	CH SW	OPEN CH3/GND CH4
4	BC	5.0V
5	CONTROL	5.0/0V (VTR/TV)
6	VIDEO IN	1.0Vp-p (OPEN)
7	NC	
8	AGC	
9	BT	
10	ENABLE	
11	CLOCK	
12	DATA	
13	NC	
14	BM	5.0V
15	LOCK	
16	BTL	30.0V
17	IF OUT	
18	IF IN	
19	NC	
20	NC	
21	AUDIO OUT	
22	NC	
23	AFT	
24	VIDEO OUT	
25	NC	

Sym.	Item or Code No.	Material & Size	Qt.	Process	Remark
	ENG567~~XX~~06G				

(TENTATIVE)

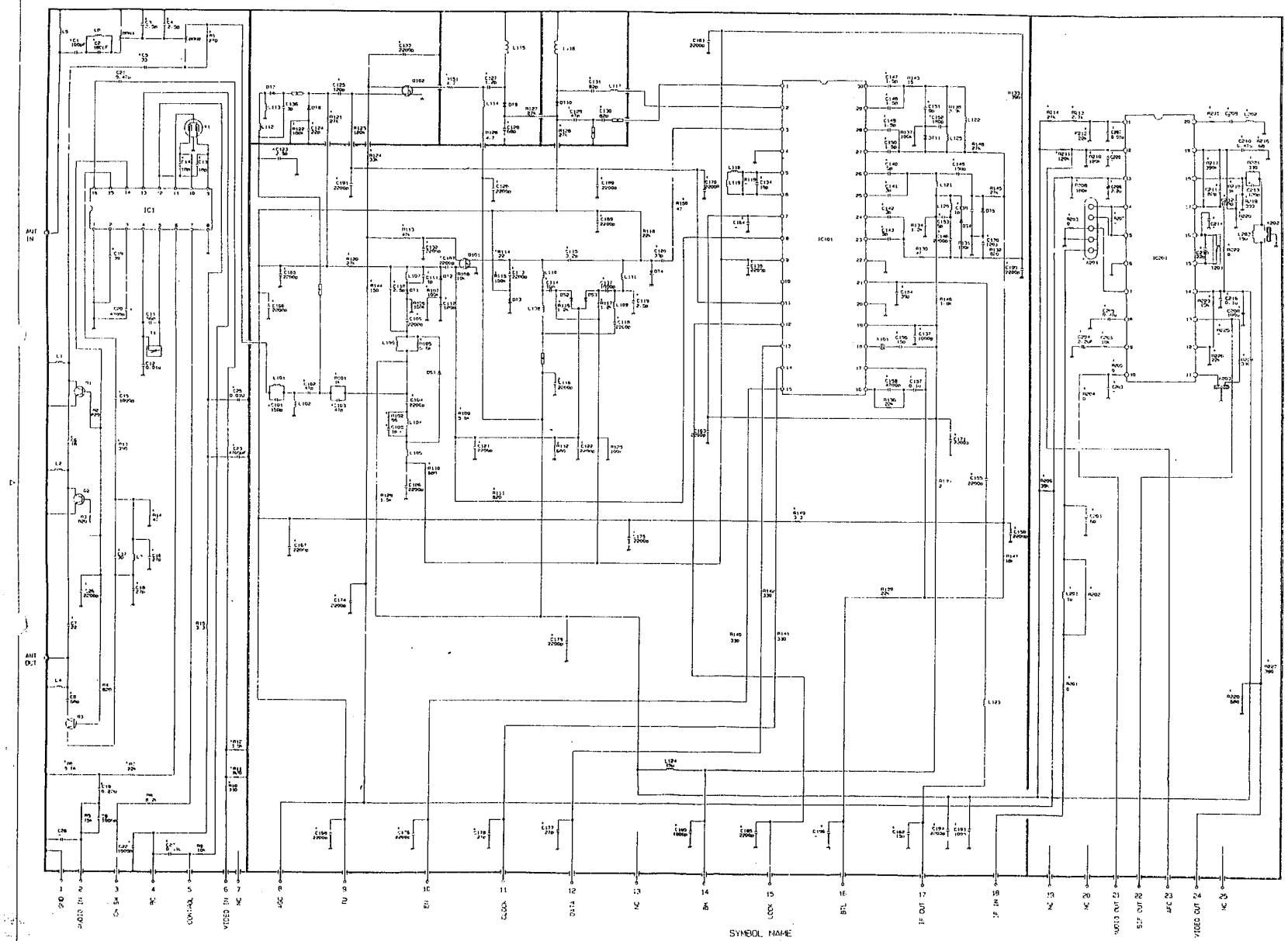
Scale	Designed	Drawn	Traced	Checked	Approved
1/1	S.H.LIM 31 JUL 98	S.H.LIM 31 JUL 98	24/8/98	1-8-98	

Model: 3IN1 (NTSC)

Name: PRODUCT SPECIFICATION

No. DM17-****-**

A3 Matsushita Electronic Components (M) Sdn. Bhd. | High Frequency Products Division 3rd Angle System Unit:mm



TRANSISTOR PIN CONNECTION

SYMBOL NAME

INPUT JACK | FEED THRU | TEST POINT | PATTERN INDICATOR

*This value is not fixed

TERMINAL SUPPLY VOLTAGE

Terminal No.	Terminal Name	Supply Voltage (V)
1	GND	0
2	AUDIO IN	0.5Vrms
3	CS	0.5V (3.3V)
4	NC	0
5	CONTROL	5 (3V)
6	VCC33 IN	3 (Top of panel)
7	NC	
8	NC	
9	DU	
10	EU	
11	CLOCK	
12	DATA	
13	NC	
14	BU	
15	LOCK	3.3V
16	BTL	30.5V
17	IF OUT	
18	IF IN	
19	NC	
20	NC	
21	AUDIO OUT	
22	SIF OUT	
23	AFC OUT	
24	VIDEO OUT	
25	NC	

SYMBOL

Symbol	Symbol No.	Semiconductor
	D101-D104	MA25TT
	D105-D109	MA25TX (NC,NB,15,20)
	D107-D111	MA25TX (NC,NB,15,20)
	IC101	MB113
	IC201	MB3756
	X101	142 5PH (3.5MM)
	X201	SM 45/4/1P1H
	X202	LPS4PH(1.4MM)
	X203	EC4PH(3.5/3.5MM)(NC)

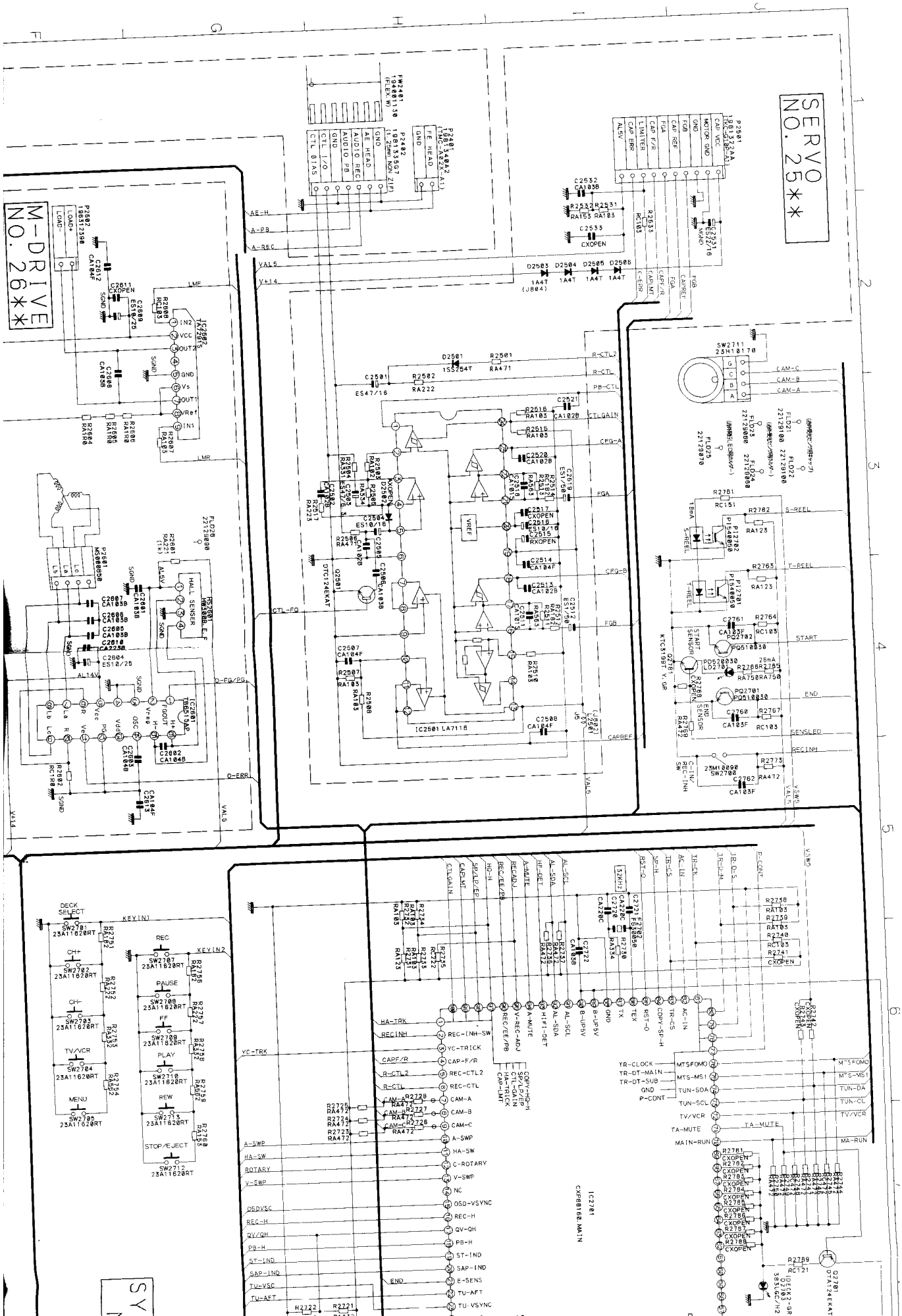
(CHG'96/4/6) 06

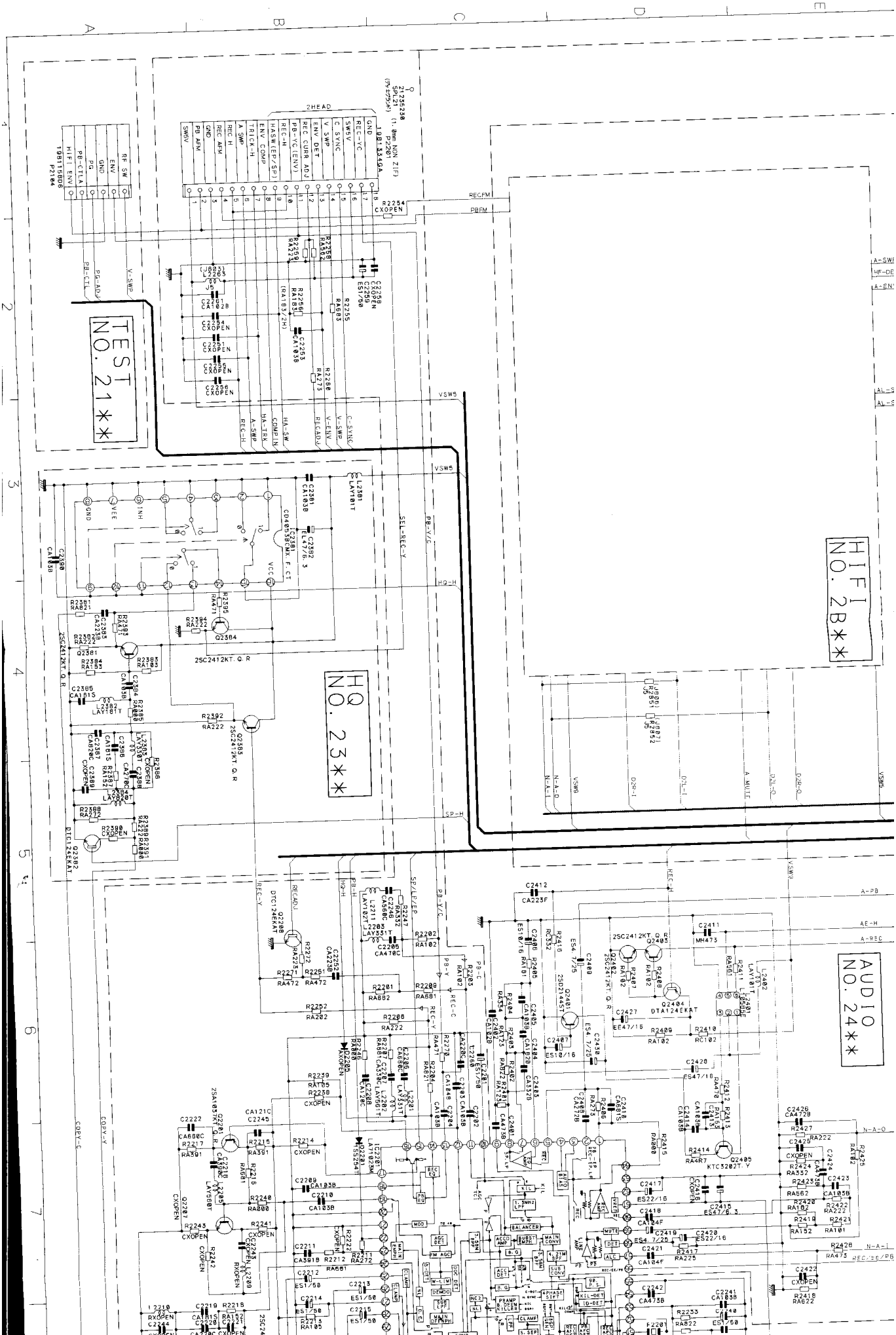
DIGITAL TUNER

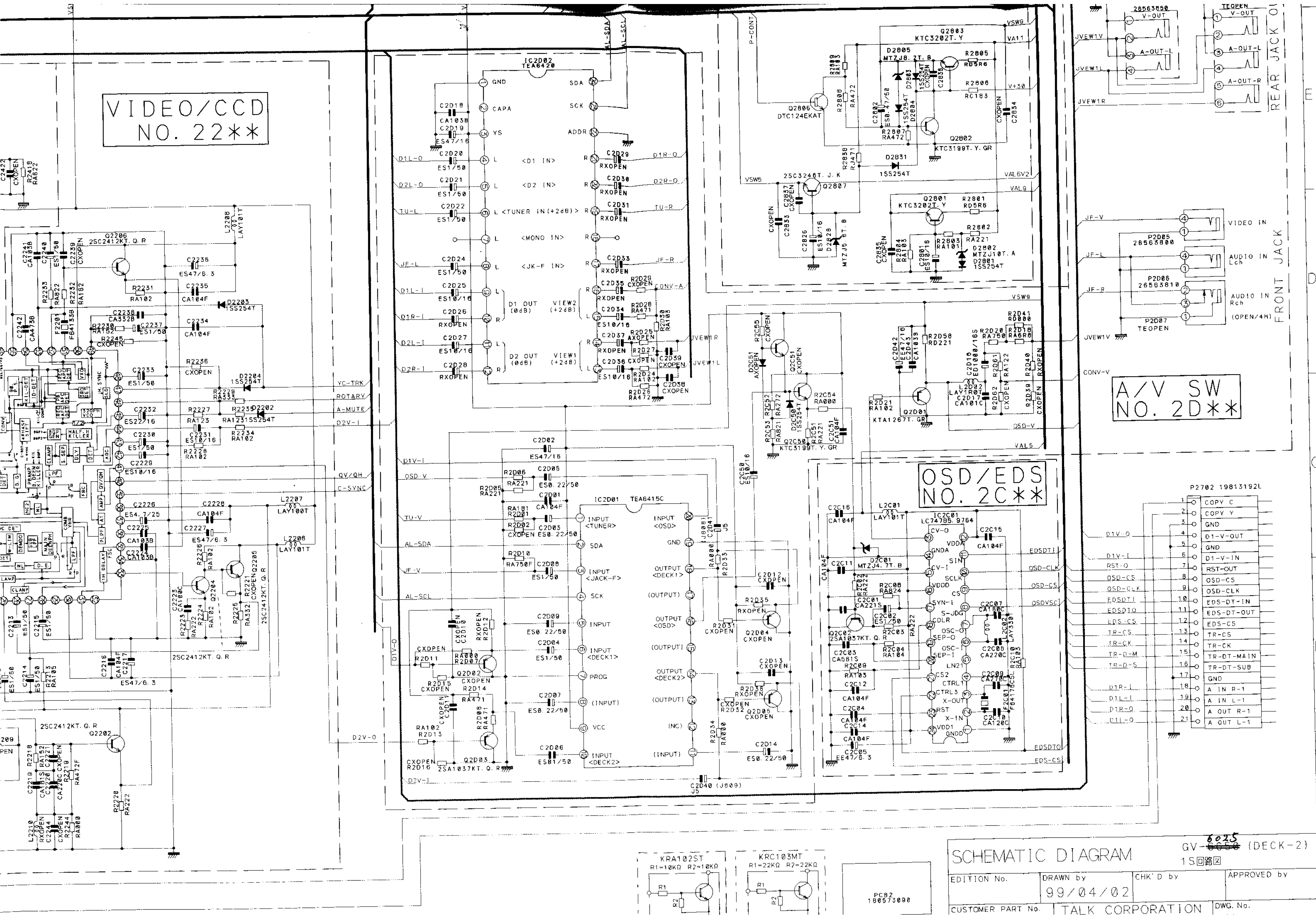
CONNECTION DIAGRAM

DM18-XXXX-00

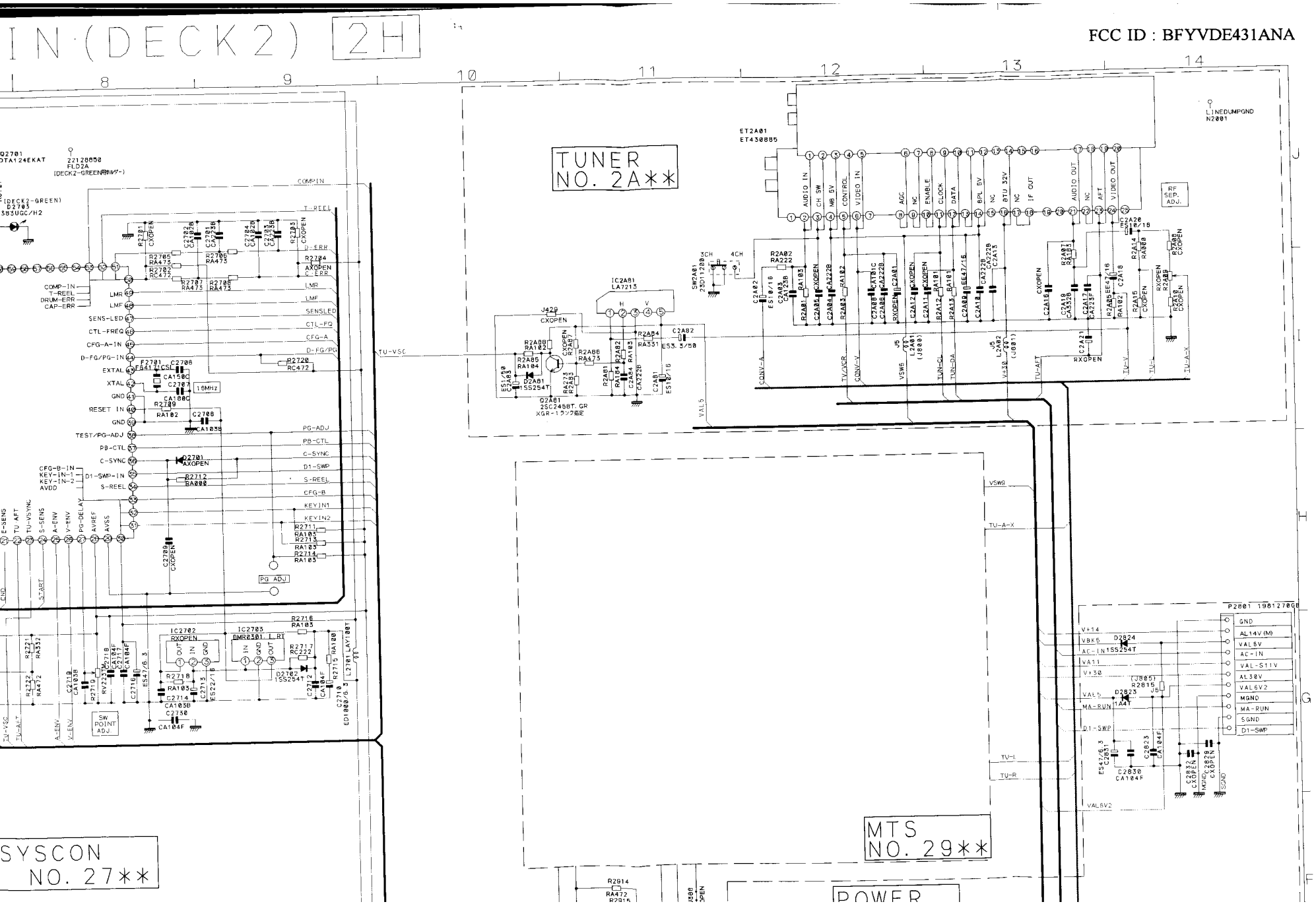
A3

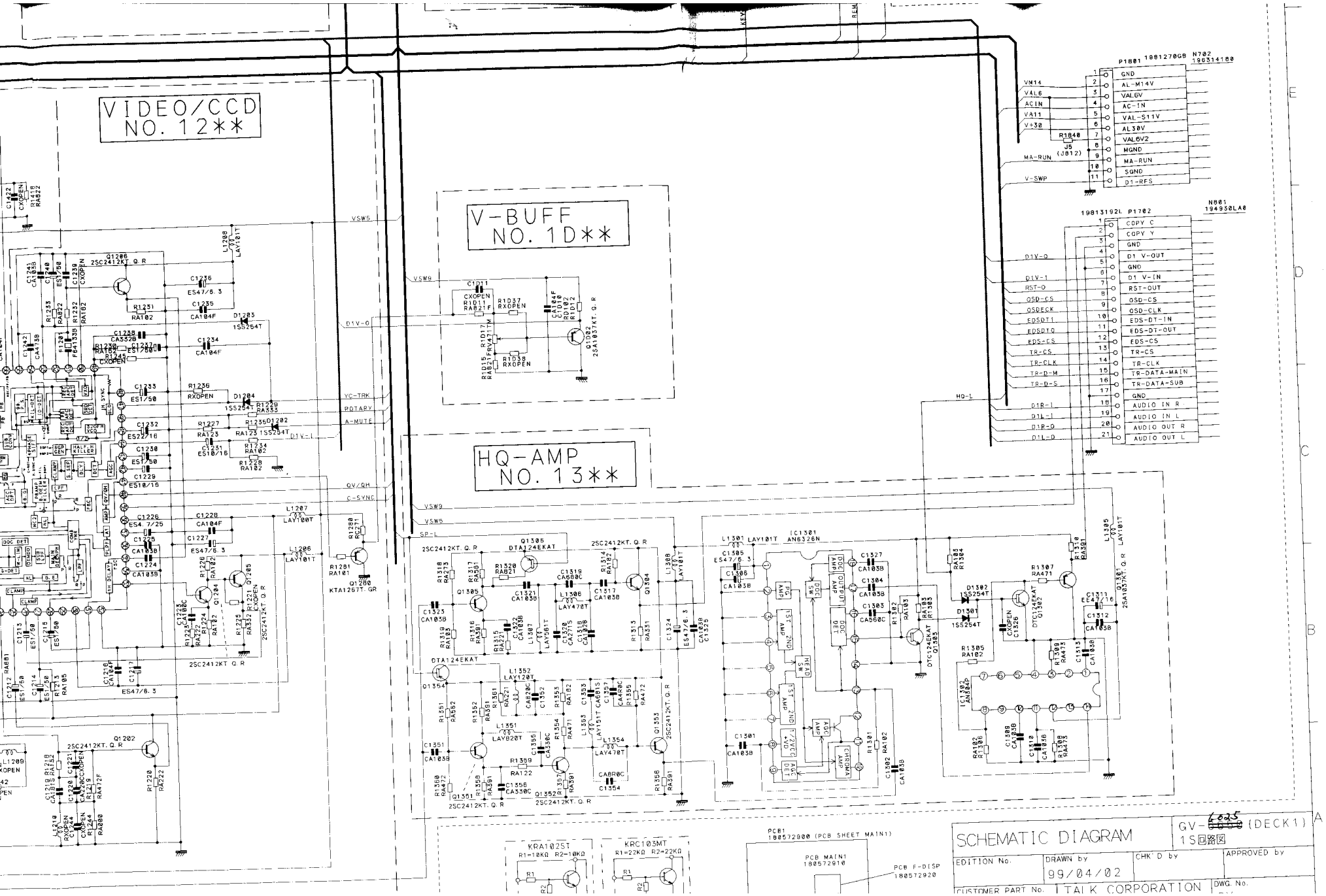

SERVO NO. 25**
M-DRIVE NO. 26**
SYSTEM CONTROL NO. 27**
IC2501 LA7118
IC2601 TB6515AP
IC2701 CXP88168.MAIN
P2481 1981340A2 (TRC-2A1)
P2482 1981335G7 (1.25mm NON ZIF)
P2591 1981372AA
P2587 1981739B
P2601 MS900050
P2701 1981372AA
AE-H
A-PB
A-REC
VALS
V+14
R-CTL2
R-CTL
PB-CTL
CTLGAIN
CFG-A
FGA
CFG-B
FGB
CTL-FG
CAPREF
D-FG/PG
D-ERR
LMF
LMR
CAM-C
CAM-B
CAM-A
S-REEL
T-REEL
START
END
SENSLED
RECINH
DECK SELECT
KEYIN1
KEYIN2
REC
PAUSE
FF
PLAY
REW
STOP/EJECT
CH+
CH-
TV/VCR
MENU
HA-TRK
YC-TRK
CAPF/R
R-CTL2
R-CTL
A-SWP
HA-SW
ROTARY
V-SWP
OSDVSC
REC-H
QV/QH
PB-H
ST-IND
SAP-IND
TU-VSC
TU-AFT
TR-CLOCK
TR-DT-MAIN
TR-DT-SUB
P-CONT
TUN-SCL
TV/VCR
TA-MUTE
MAIN-RUN
MA-RUN
TUN-DA
TUN-CL


TEST NO. 21**
HIFI NO. 2B**
HQ NO. 23**
AUDIO NO. 24**
2HEAD
RF SW
ENV
GND
PG
PB-CTL
HIFI ENV
PB ATM
REC ATM
REC H
A SWP
TRICK-H
ENV COMP
HASW(EP/SP)
REC-H
PB-YC(ENV)
REC CURR ADJ
ENV DET
V SWP
C SYNC
SWSV
REC-YC
GND
RECFM
PBFM
VSW5
SEL-REC-Y
PB-Y/C
HQ-H
SP-H
REC-Y
PB-Y
PB-C
REC-C
COPY-Y
COPY-C
A-PB
AE-H
A-REC
N-A-Q
N-A-I
REC/PB
2SC2412KT.Q.R
DTC124EKAT
2SA1037KT.Q.R
2SD2145ST
DTA124EKAT
KTC3202T-Y
BALANCER
MAIN AMP
SUB CONV
KILL-DET
ID-DET
PRAMP
BIAS
MUTE
ALC
DET
DEMOD
FM AGC
ACCD AMP
BURST AMP
CLAMP
KILL
REC EQ
PB EQ
REC AMP
PB AMP
SP EQ
SP LP
SP AMP
SP PHASE SHIFT
SP DET
SP PB
SP REC
SP REC/PB
SP LP
SP BPF
SP HPF
SP AMP

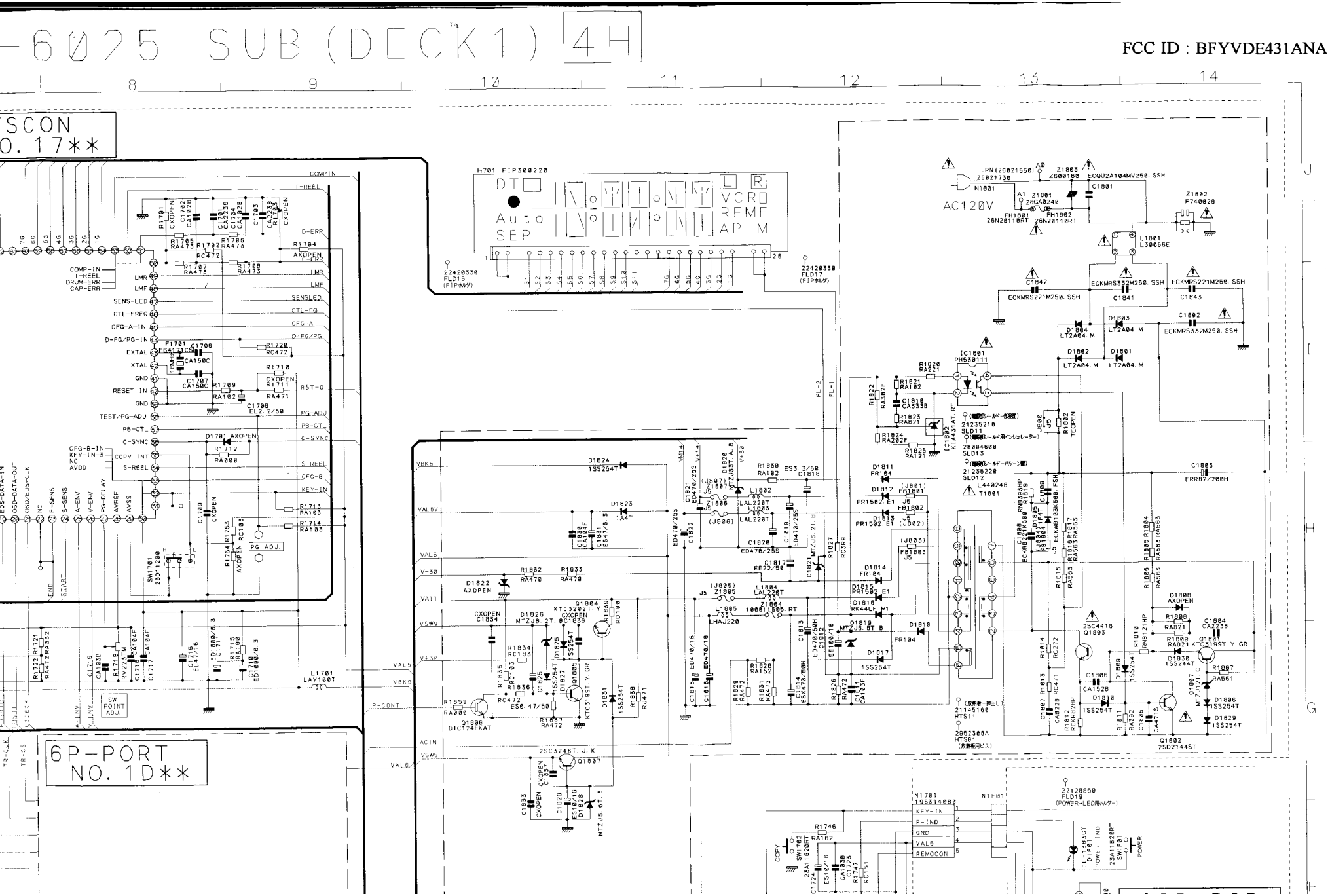

VIDEO/CCD NO. 22**
A/V SW NO. 2D**
OSD/EDS NO. 2C**
REAR JACK
FRONT JACK
VIDEO IN
AUDIO IN Lch
AUDIO IN Rch
(OPEN/4H)
IC2D02 TEA6420
IC2D01 TEA6415C
IC2C01 LC74785.9764
P2702 19813192L
COPY C
COPY Y
GND
D1-V-OUT
GND
D1-V-IN
RST-OUT
OSD-CS
OSD-CLK
EDS-DT-IN
EDS-DT-OUT
EDS-CS
TR-CS
TR-CK
TR-DT-MAIN
TR-DT-SUB
GND
A IN R-1
A IN L-1
A OUT R-1
A OUT L-1
KRA102ST
R1=10KΩ R2=10KΩ
KRC103MT
R1=22KΩ R2=22KΩ
PCB2 180573090
SCHEMATIC DIAGRAM
GV-6025 (DECK-2)
EDITION No.
DRAWN by
99/04/02
CHK'D by
APPROVED by
CUSTOMER PART No.
TALK CORPORATION
DWG. No.

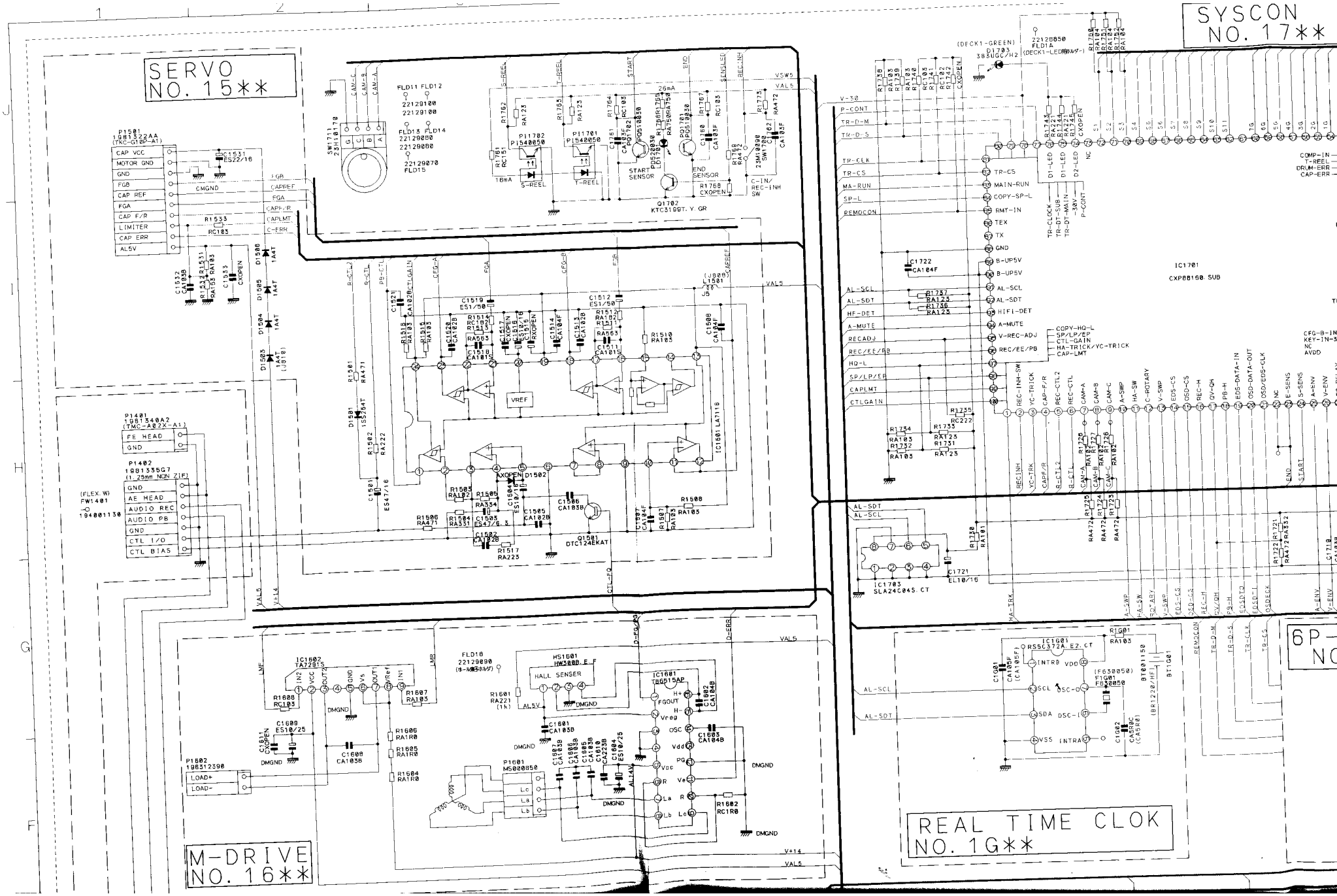
FCC ID : BFYVDE431ANA

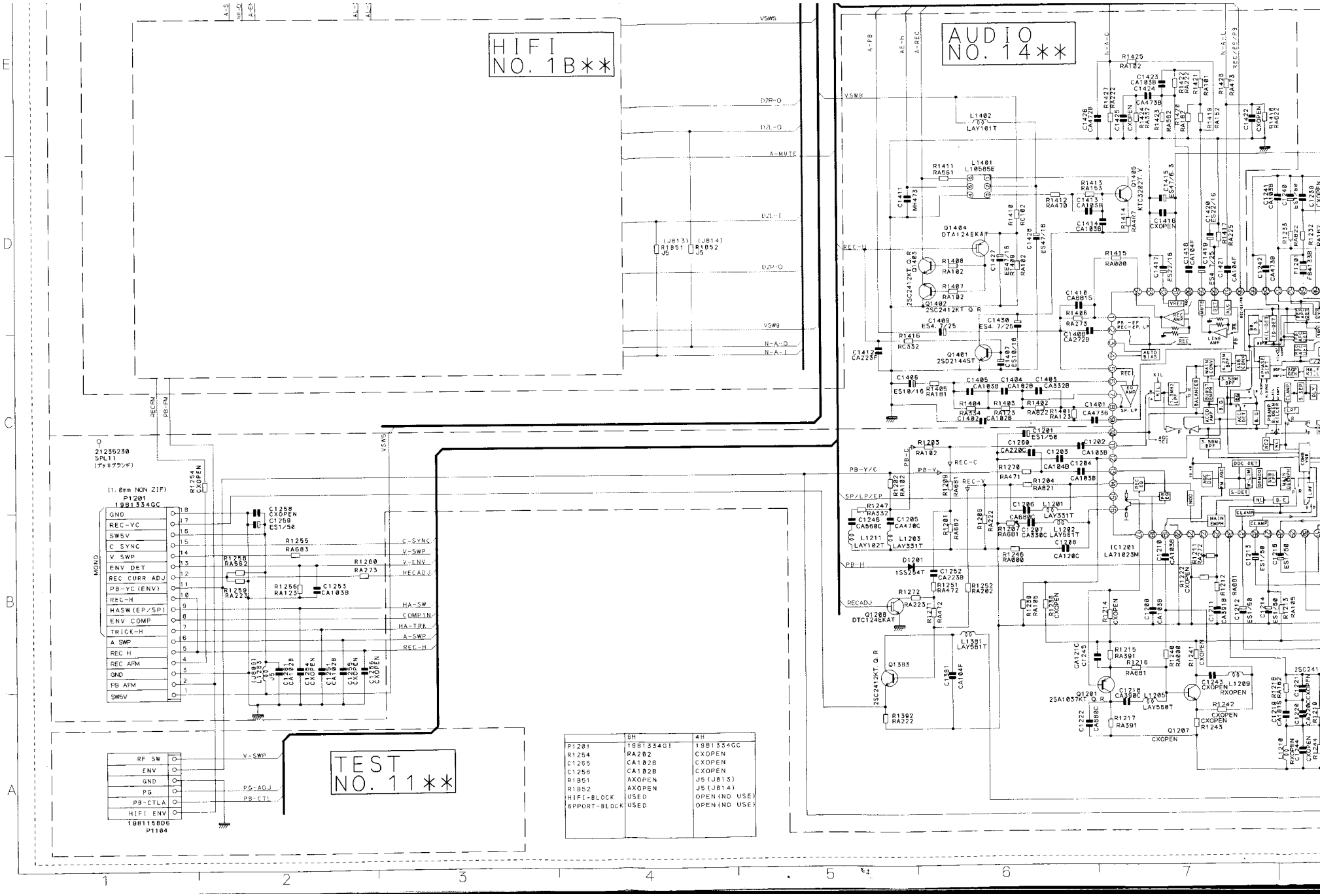



VIDEO/CCD
NO. 12**
V-BUFF
NO. 1D**
HQ-AMP
NO. 13**
P1801 1981270GB N702 190314180
GND
AL-M14V
VAL6V
AC-IN
VAL-S11V
AL30V
VAL6V2
MGND
MA-RUN
SGND
D1-RFS
19813192L P1702 N801 194950LA0
COPY C
COPY Y
GND
D1 V-OUT
GND
D1 V-IN
RST-OUT
OSD-CS
OSD-CLK
EDS-DT-IN
EDS-DT-OUT
EDS-CS
TR-CS
TR-CLK
TR-DATA-MAIN
TR-DATA-SUB
GND
AUDIO IN R
AUDIO IN L
AUDIO OUT R
AUDIO OUT L
VM14
VAL6
ACIN
VA11
V+30
MA-RUN
V-SWP
D1V-O
D1V-I
RST-O
OSD-CS
OSDECK
EDSDT1
EDSDTO
EDS-CS
TR-CS
TR-CLK
TR-D-M
TR-D-S
D1R-I
D1L-I
D1R-O
D1L-O
HQ-L
VSW5
VSW9
YC-TRK
ROTARY
A-MUTE
QV/QH
C-SYNC
SP-L
KRA102ST
R1=10KΩ R2=10KΩ
KRC103MT
R1=22KΩ R2=22KΩ
PCB1
180572900 (PCB SHEET MAIN1)
PCB MAIN1
180572910
PCB F-DISP
180572920
SCHEMATIC DIAGRAM
GV-6025 (DECK1)
1S回路図
EDITION No.
DRAWN by
99/04/02
CHK'D by
APPROVED by
CUSTOMER PART No.
TALK CORPORATION
DWG. No.

FCC ID : BFYVDE431ANA




SERVO
NO. 15**
SYSCON
NO. 17**
M-DRIVE
NO. 16**
REAL TIME CLOK
NO. 1G**
IC1701
CXP88160.SUB
IC1601
LA7116
IC1602
TA7291S
IC1603
SLA24C04S.CT
IC1601
RS5372A.E2.CT
IC1601
TB6515AP
HS1601
HM3000.E.F
P1601
MS000850
P1501
1981322AA
(TMC-G1B0-A1)
P1401
1981340A2
(TMC-A02X-A1)
P1402
1981335G7
(1.25mm NON ZIF)
P1502
198512390
Q1501
DTC124EKAT
Q1702
KTC3198T.Y.GR
START SENSOR
END SENSOR
VREF
VSW5
VAL5
V+14
DMGND
AL5V
AL14V
6P-
NO


HIFI
NO. 1B**
AUDIO
NO. 14**
TEST
NO. 11**
P1201
1981334GC
MONO
GND
REC-YC
SW5V
C SYNC
V SWP
ENV DET
REC CURR ADJ
PB-YC (ENV)
REC-H
HASW (EP/SP)
ENV COMP
TRICK-H
A SWP
REC H
REC AFM
GND
PB AFM
SW5V
RF SW
ENV
GND
PG
PB-CTLA
HIFI ENV
1981158D6
P1104
C-SYNC
V-SWP
V-ENV
RECADJ
HA-SW
COMPIN
HA-TRK
A-SWP
REC-H
PG-ADJ
PB-CTL
IC1201
LA71023M
Q1201
2SA1037KT Q R
Q1207
CXOPEN
Q1404
DTA124EKAT
Q1405
KTC3202T Y
Q1402
2SC2412KT Q R
Q1401
2SD2144ST
Q1408
DTC124EKAT
Q1585
2SC2412KT Q R
L1401
L10585E
L1402
LAY101T
L1381
LAY561T
L1201
LAY331T
L1202
LAY561T
L1203
LAY331T
L1211
LAY102T
L1205
LAY560T
D1201
1SS284T
VSW5
D2R-O
D2L-O
A-MUTE
D2L-I
D2P-O
N-A-O
N-A-I
RECPM
PB-PM
21235230
SPL11
(ヴッキ*グランド)
(1.0mm NON ZIF)
R1254
CXOPEN
R1255
RA683
R1256
RA123
R1258
RA552
R1259
RA223
R1260
RA273
C1253
CA103B
C1258
CXOPEN
C1259
ES1/50
PB-Y/C
PB-Y
REC-C
REC-Y
SP/LP/EP
PB-H
RECADJ
PB-C
REC-H
VSW6
N-A-O
N-A-I
REC/EE/PB
A-PB
AE-H
A-REC
VREF
MUTE
DET
ALC
REC
AUTO BIAS
KILL
BALANCED
BUSS
ACO
AMP
MAIN
EMPH
CLAMP
DOC DET
DEMOD
S-DET
NL
D.L
NOD
BPF
BLANK
KILLER
CLAMP
MAIN
EMPH
LINE
AMP
SP.LP
PB-EP
REC-EP, LP
REC
AMP
AGC
TC1
AGC
DET
AGC
AMP
REC
DET
REC
EQ
REC
AMP
REC
DET
REC
EQ
REC
AMP
REC
DET
REC
EQ
RE