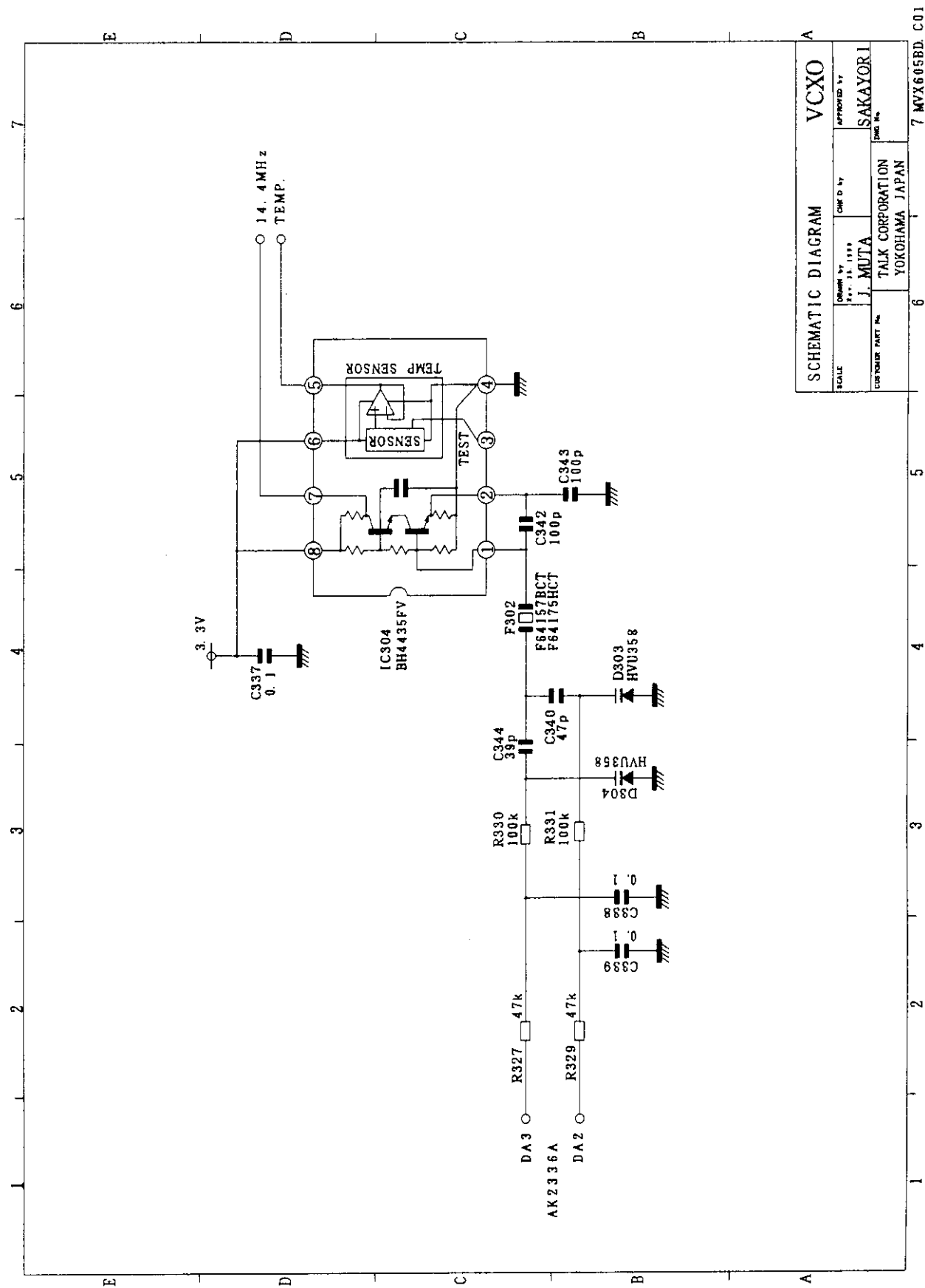
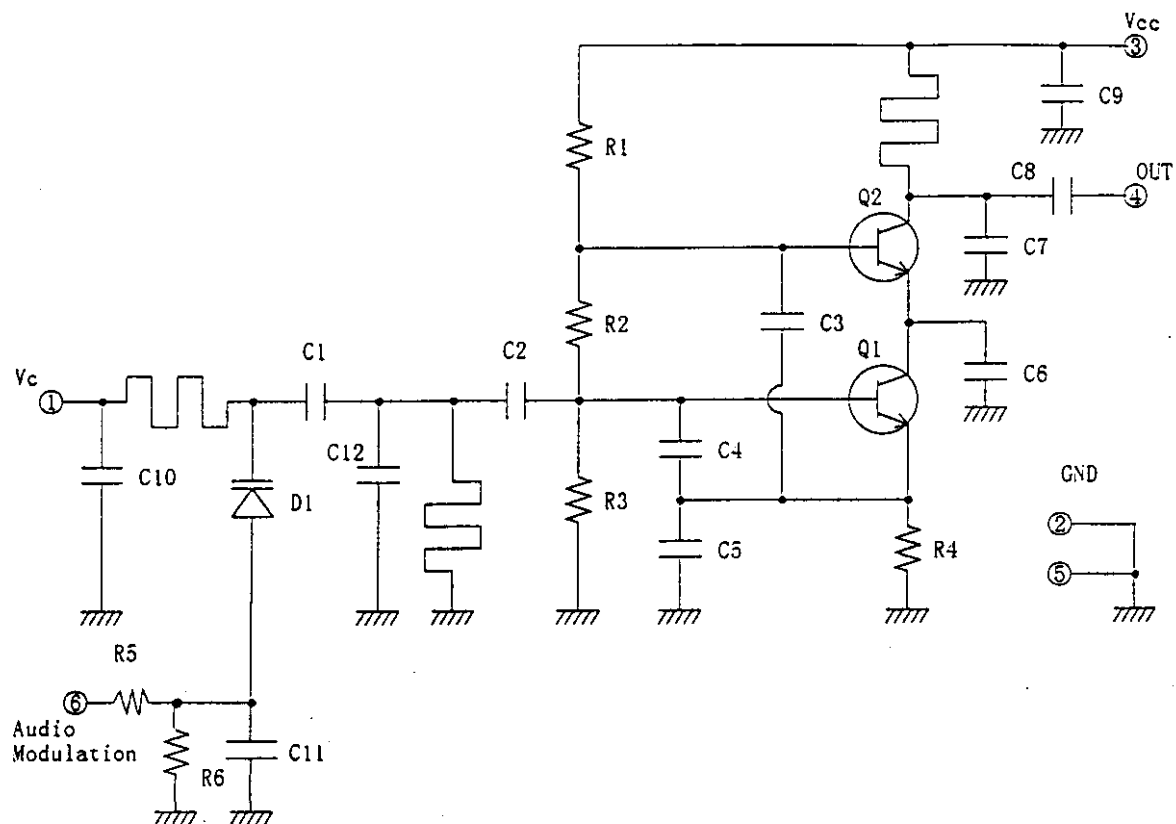
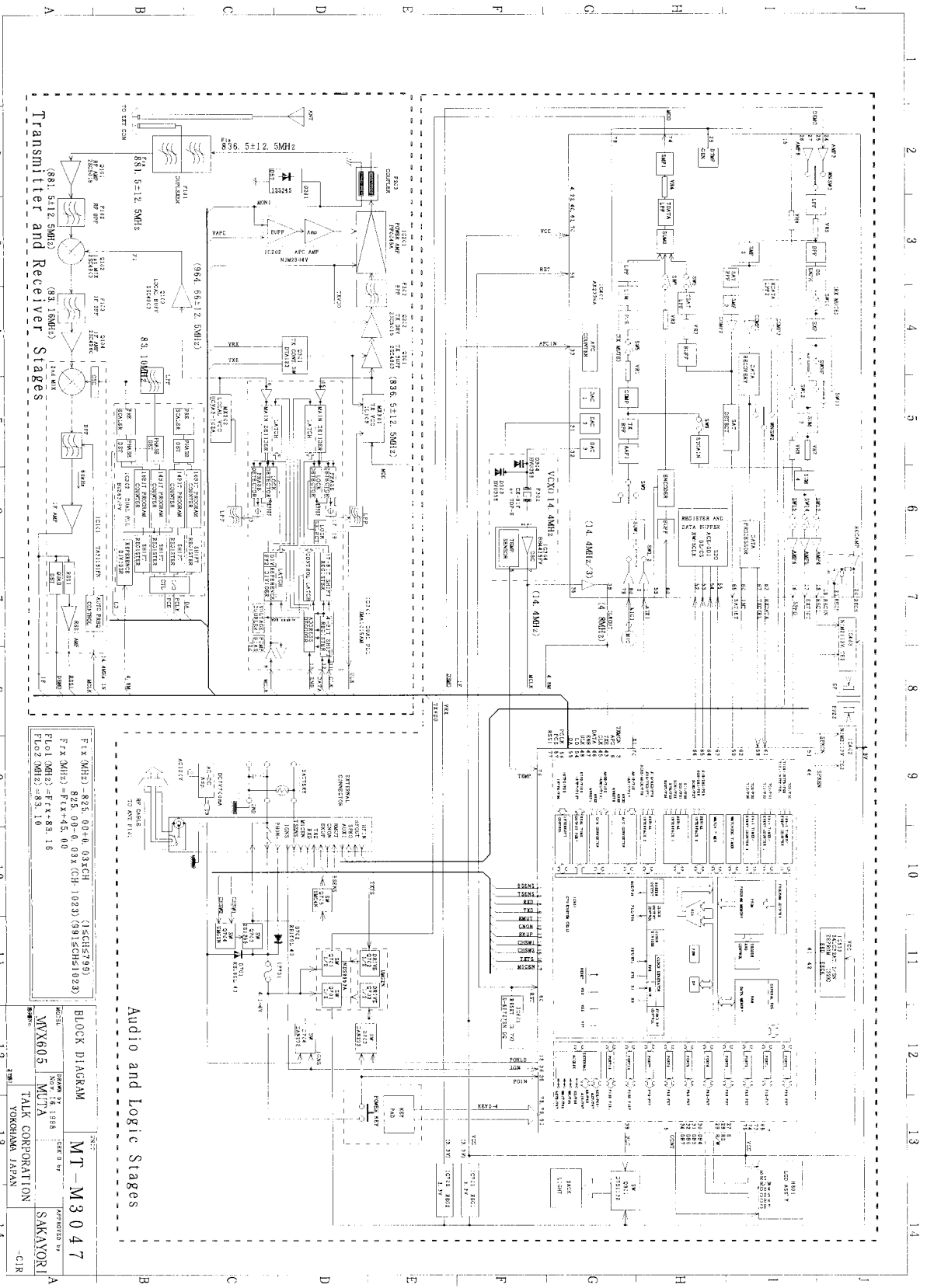




## 18. Circuit



SCHEMATIC DIAGRAM OF TX VCO IL108



Transmitter and Receiver Stages

Audio and Logic Stages

F1x (MHz) - 85.00 ± 0.03 CH (1SCH2795)  
 F1x (MHz) - F1x - 45.00  
 F101 (MHz) - F1x - 83.16  
 F102 (MHz) - 83.10

|                  |               |             |             |
|------------------|---------------|-------------|-------------|
| BLOCK DIAGRAM    |               | MT-M3047    |             |
| MODEL            | DATE          | DESIGNED BY | APPROVED BY |
| MYX605           | NOV. 18, 1988 | Y. KAWA     | S. KAWA     |
| TALK CORPORATION |               | SAKAYORI    |             |
| YOKOHAMA, JAPAN  |               | -CIR        |             |

Applicant: Shinton Co., Ltd.

Transmitter Type: 8FYM3047

