The chip U7/U8, MP1482 supplies the board main voltage +3.3V and +1.2V. +3.3V generate +2.5V by an audion Q1, +1.8V by a LDO U11. These three voltage driver the main chip BCM6333. A 64MHz crystal oscillator provides the clock to the main chip. The main chip provides a 120MHz clock with a SDRAM and provides a 33MHz clock with the flash chip U9.

Wireless part:

The BCM4312 wireless chip required 2 voltages. One is the board main voltage +3.3V, the other is a +1.5V alone generated by L22. A 20MHz crystal oscillator provides the clock to the wireless chip. The wireless chip provides an 802.11b/g wireless solution. The maximum support rate is 54Mbps. The wireless chip and the main chip connection via SDIO bus, it is a 25MHz clock bus.

DSL part:

The AFE chip BCM6301 is driven by +5V voltages. The maximum support rate is 24Mbps/1Mbps.

100M eth port:

BCM5325 support 4 10M/100M Ethernet port. A 25M crystal oscillator provides the clock to the 100M eth chip. The chip and the main chip connection via PHY to PHY bus, it is a 125MHz clock bus.