Voltage and Current into Final Devices AMR80

806-824 MHz PA

 $\overline{\text{2 final MMIC devices in parallel}}$, V = +5 VDC, I = 800 mA for each device

851-869 MHz PA

 $\overline{2 \text{ final MMIC devices in parallel}}$, V = +5 VDC, I = 800 mA for each device