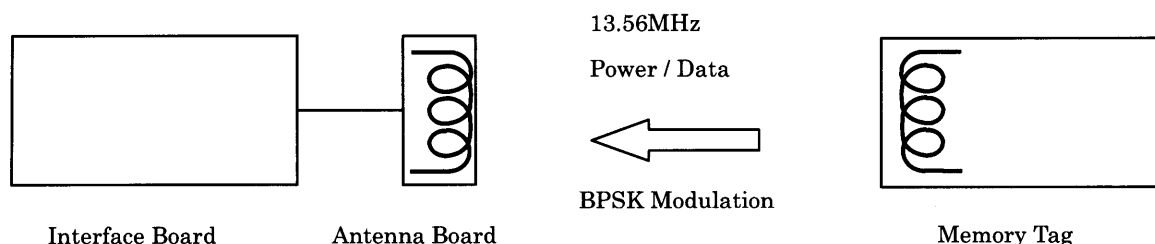
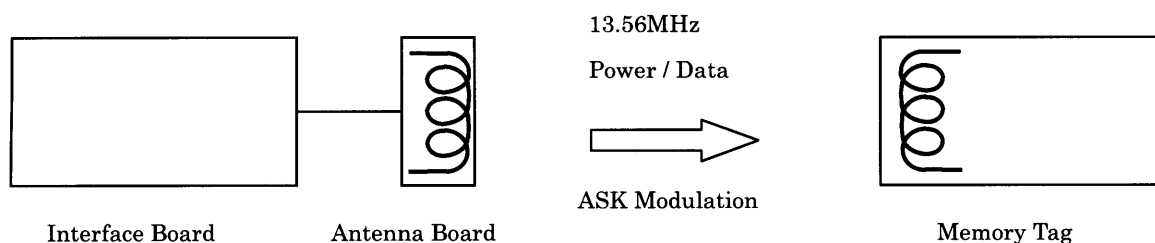


Instruction handbook for Memory Tag System *"Memory TAG1"*

This Memory Tag System consists of Interface Board, Antenna Board and Memory Tag. The system is a data transmit/receive unit between CPU connected to I/B and FeRAM (Ferroelectric RAM) that is stored in Memory Tag.

Interface Board supplies the electric power and transmits data to Memory Tag by the use of electromagnetic energy via Antenna Board with ASK modulation and also receives data from Memory Tag with BPSK modulation.



FCC (Federal Communication Commission)

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Note: This equipment has been tested and found to comply with the limits for Class A digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

Do not make any changes or modifications to the equipment unless otherwise specified in the manual. If such change or modifications should be made, you could be required to stop operation of the equipment.