Test report No. Page

: 29EE0246-HO-01-E

Page Issued date FCC ID : 1 of 23 : May 22, 2009

: AZD161

EMI TEST REPORT

Test Report No.: 29EE0246-HO-01-E

Applicant

Canon Inc.

Type of Equipment

WLAN Module

Model No.

: CH9-1161

FCC ID

.

:

:

AZD161

Test regulation

FCC Part 15 Subpart E: 2009 Section 15.407(DFS test only)

Test Result

Complied

- 1. This test report shall not be reproduced in full or partial, without the written approval of UL Japan, Inc.
- 2. The results in this report apply only to the sample tested.
- 3. This sample tested is in compliance with above regulation.
- 4. The test results in this report are traceable to the national or international standards.
- 5. This test report must not be used by the customer to claim product certification, approval, or endorsement by NVLAP, NIST, or any agency of the Federal Government.

Date of test

March 27,2009

Tested by

Takahiro Hatakeda EMC Services

Approved by:

Tetsuo Maeno

Site Manager of EMC Services



This laboratory is accredited by the NVLAP LAB CODE 200572-0, U.S.A. The tests reported herein have been performed in accordance with its terms of accreditation.

*As for the range of Accreditation in NVLAP, you may refer to the WEB address, http://uljapan.co.jp/emc/nvlap.html

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SECTION 1: Customer information

Company Name : Canon Inc.

Address : 30-2, Shimomaruko 3-chome, Ohta-ku, Tokyo, 146-8501, Japan

Telephone Number : +81-3757-6798
Facsimile Number : +81-3757-8431
Contact Person : Kiyoshi Sahoyama

SECTION 2: Equipment under test (E.U.T.)

2.1 Identification of E.U.T.

Type of Equipment : WLAN Module Model No. : CH9-1161
Serial No. : ES7006
Rating : DC3.3V
Receipt Date of Sample : March 18, 2009

Country of Mass-production : Japan

Condition of EUT : Production prototype

(Not for Sale: This sample is equivalent to mass-produced items.)

Modification of EUT : No Modification by the test lab

2.2 Product Description

Model No: CH9-1161 (referred to as the EUT in this report) is the WLAN Module.

Equipment Type : Transceiver Clock frequency : 38.4MHz Method of Frequency Generation : Crystal

Operating voltage (inner) : DC1.8V, DC2.85V Operating temperature range : -10 to +60 deg. C.

	IEEE802.11b	IEEE802.11g	IEEE802.11a
Frequency of operation	2412-2462MHz	2412-2462MHz	5180-5240MHz (W52)
			5260-5320MHz (W53)
			5500-5700MHz (W56) *1)
			5745-5825MHz (W58)
Type of modulation	DSSS	OFDM	OFDM
Channel spacing	5MHz	5MHz	20MHz
ITU Code	G1D	D1D	D1D
Antenna type	Planar Inverted F antenna	Planar Inverted F antenna	Planar Inverted F antenna
Antenna Gain	-2.18dBi	-2.18dBi	1.85dBi (W52/W53)
			2.62dBi (W56)
			1.50dBi (W58)

^{*1)} Frequency range of 5580-5660MHz is not used in Canada.

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SECTION 3: Scope of Report

This report only covers DFS requirement subject to 5250-5350MHz band and 5470-5725MHz band, as specified by the following referenced procedures.

SECTION 4: Test specification, procedures & results

4.1 Test Specificationv

Test Specification : FCC Part15 Subpart E: 2009, final revised on February 27, 2009

Title : FCC 47CFR Part15 Radio Frequency Device

Subpart E Unlicensed National Information Infrastructure Devices

Section 15.407 General technical requirements

Test Specification : FCC 06-96 APPENDIX

Title : COMPLIANCE MEASUREMENT PROCEDURES FOR

UNLICENSED-

NATIONAL INFORMATION INFRASTRUCTURE DEVICES OPERATING IN THE 5250-5350 MHz AND 5470-5725MHz BANDS

INCORPORATING DYNAMIC FREQUENCY SELECTION

FCC 15.31 (e)

The RF Module has own regulator.

The RF Module is constantly provided voltage (DC1.8 / 2.85V) through own regulator regardless of input voltage. Therefore, this EUT complies with the requirement.

FCC Part 15.203/212 Antenna requirement

The EUT has a unique coupling/antenna connector (UFL). Therefore the equipment complies with the requirement of 15.203/212.

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4.2 Procedures and results

Table 1: Applicability of DFS Requirements

Requirement	Operating Mode Client without Radar Detection	Test Procedures & Limits	Deviation	Results
U-NII Detection Bandwidth	Not required	FCC 06-96 Appendix 7.8.1	N/A	N/A
Initial Channel	Not required	FCC15.407 (h)	N/A	N/A
Availability Check Time		FCC 06-96 Appendix 7.8.2.1		
		RSS-210 A9.4		
Radar Burst at the	Not required	FCC15.407 (h)	N/A	N/A
Beginning of the Channel Availability Check Time		FCC 06-96 Appendix 7.8.2.2		
Check Time		RSS-210 A9.4		
Radar Burst at the	Not required	FCC15.407 (h)	N/A	N/A
End of the Channel Availability Check		FCC 06-96 Appendix 7.8.2.3		
Time		RSS-210 A9.4		
In-Service Monitoring	Yes	FCC15.407 (h)	N/A	Complied
for Channel Move Time, Channel Closing Transmission		FCC 06-96 Appendix 7.8.3		
Time	** di	RSS-210 A9.4		
In-Service Monitoring	Yes *	FCC15.407 (h)	N/A	Complied
for Non-Occupancy period		FCC 06-96 Appendix 7.8.3 RSS-210 A9.4		
Statistical Performance Check	Not required	FCC15.407 (h) FCC 06-96 Appendix 7.8.4	N/A	N/A

^{*}Although this test was not required in FCC 06-96, it was performed as additional test.

Table 2: DFS Detection Thresholds for Master Devices and Client Devices With Radar

Maximum Transmit Power	Value (See Notes 1 and 2)
≥ 200 milliwatt	-64 dBm
< 200 milliwatt	-62 dBm

Note 1: This is the level at the input of the receiver assuming a 0 dBi receive antenna.

Note 2: Throughout these test procedures an additional 1 dB has been added to the amplitude of the test transmission waveforms to account for variations in measurement equipment. This will ensure that the test signal is at or above the detection threshold level to trigger a DFS response.

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Table 3 DFS Response Requirement Values

Parameter	Value
Non-occupancy period	Minimum 30 minutes
Channel Availability Check Time	60 seconds
Channel Move Time	10 seconds
	See Note 1
Channel Closing Transmission Time	200 milliseconds + an aggregate of 60
	milliseconds over remaining 10 second period.
	See Notes 1 and 2
U-NII Detection Bandwidth	Minimum 80% of the U-NII 99% transmission
	power bandwidth
	See Note 3

Note 1: The instant that the Channel Move Time and the Channel Closing Transmission Time begins is as follows:

- For the Short Pulse Radar Test Signals this instant is the end of the *Burst*.
- For the Frequency Hopping radar Test Signal, this instant is the end of the last radar Burst generated
- For the Long Pulse Radar Test Signal this instant is the end of the 12 second period defining the *Radar Waveform*.

Note 2: The Channel Closing Transmission Time is comprised of 200 milliseconds starting at the beginning of the Channel Move Time plus any additional intermittent control signals required to facilitate a Channel move (an aggregate of 60 milliseconds) during the remainder of the 10 second period. The aggregate duration of control signal will not count quiet periods in between transmissions.

Note 3: During the U-NII Detection Bandwidth detection test, radar type 1 is used and for each frequency step the minimum percentage of detection is 90 percent. Measurements are performed with no data traffic.

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Table 4 Short Pulse Radar Test Waveform

Radar Type	Pulse Width (μsec)	PRI (μsec)	Number of Pulses	Minimum Percentage of Successful Detection	Minimum Number of Traials
1	1	1428	18	60%	30
2	1-5	150-230	23-29	60%	30
3	6-10	200-500	16-18	60%	30
4	11-20	200-500	12-16	60%	30
Aggregate (Rader T	Types 1-4)	80%	120		

Table 5 Long Pulse Radar Test Waveform

Radar Type	Pulse Width (µsec)	Chip Width (MHz)	PRI (µsec)	Number of Pulses per Burst	Number of Burst	Minimum Percentage of Successful Detection	Minimum Number of Trials
5	50-100	5-20	1000-2000	1-3	8-20	80%	30

Table 6 Frequency Hopping Radar Test Waveform

Radar Type	Pulse Width (µsec)	PRI (µsec)	Pulse per Hop (kHz)	Hopping Rate (kHz)	Hopping Sequence Length (msec)	Minimum Percentage of Successful Detection	Minimum Number of Trials
6	1	333	9	0.333	300	70%	30

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4.3 Test Location

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	FCC	IC Registration	Width x Depth x	Size of	Other
	Registration Number	Number	Height (m)	reference ground plane (m) / horizontal conducting plane	rooms
No.1 semi-anechoic chamber	313583	2973C-1	19.2 x 11.2 x 7.7m	7.0 x 6.0m	No.1 Power source room
No.2 semi-anechoic chamber	655103	2973C-2	7.5 x 5.8 x 5.2m	4.0 x 4.0m	-
No.3 semi-anechoic chamber	148738	2973C-3	12.0 x 8.5 x 5.9m	6.8 x 5.75m	No.3 Preparation room
No.3 shielded room	-	-	4.0 x 6.0 x 2.7m	N/A	-
No.4 semi-anechoic chamber	134570	2973C-4	12.0 x 8.5 x 5.9m	6.8 x 5.75m	No.4 Preparation room
No.4 shielded room	-	-	4.0 x 6.0 x 2.7m	N/A	-
No.5 semi-anechoic chamber	-	-	6.0 x 6.0 x 3.9m	6.0 x 6.0m	-
No.6 shielded room	-	-	4.0 x 4.5 x 2.7m	4.75 x 5.4 m	-
No.6 measurement room	-	-	4.75 x 5.4 x 3.0m	4.75 x 4.15 m	-
No.7 shielded room	-	-	4.7 x 7.5 x 2.7m	4.7 x 7.5m	-
No.8 measurement room	-	-	3.1 x 5.0 x 2.7m	N/A	-
No.9 measurement room	-	-	8.0 x 4.5 x 2.8m	2.0 x 2.0m	-
No.10 measurement room	-	-	2.6 x 2.8 x 2.5m	2.4 x 2.4m	-
No.11 measurement room	-	-	3.1 x 3.4 x 3.0m	2.4 x 3.4m	-

^{*} Size of vertical conducting plane (for Conducted Emission test): 2.0 x 2.0m for No.1, No.2, No.3, and No.4 semi-anechoic chambers and No.3 and No.4 shielded rooms.

4.4 Test set up, Data of DFS test, and Test instruments of DFS

Refer to APPENDIX 1 to 3.

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SECTION 5: Operation of E.U.T. during testing

5.1 Operating Modes

The EUT, which is a Client Device without Radar detection capability, operates over the 5260-5320MHz and 5500-5700MHz.

Power level(EIRP) of the EUT[dBm]

5250-5350	MHz Band*	5470-5725MHz Band*		
Output Power (Min) Output Power(Max)		Output Power (Min) Output Power(Max		
14.30	15.88	15.35	16.06	

^{*}Refer to 29EE0246-HO-01-C, FCC Part 15E (FCC 15.407) report for other parts than DFS.

The lowest antenna assembly gain of all available antenna assemblies is 1.85dBi.

WLAN traffic is generated by streaming the MPEG Test file " $6\frac{1}{2}$ Magic Hours" from the Master to the Client in full motion video mode using the media player with the V2.61 Codec package.

The EUT utilizes the 802.11a architecture, with a nominal channel bandwidth.

The FCC ID for the Master Device used with EUT for DFS testing is LDK102056.

The rated output power of the Master unit is >200mW(23dBm). Therefore the required interference threshold level is -64 dBm. After correction for antenna gain and procedural adjustments, the required conducted threshold at the antenna port is -64 + 1 + 3.5 = -59.5 dBm (threshold level + additional 1dB + antenna gain).

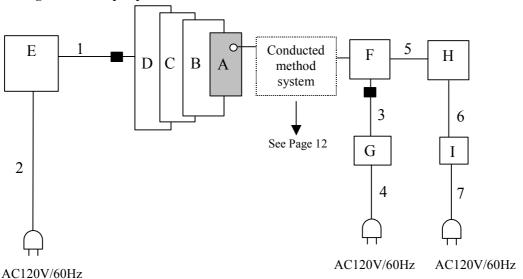
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5.2 Configuration and peripherals



: Standard Ferrite Core

Description of EUT and Support equipment

No.	Item	Model number	Serial number	Manufacturer	Remarks
A	WLAN Module	CH9-1161	ES7006	Canon Inc.	EUT
В	Jig Board	-	-	Canon Inc.	-
С	SDIO Adapter	SK-1520	SK731L0002	MARVELL	-
D	Note PC	PC-LG16FWTGJ	4Z000841A	NEC	-
Е	AC Adapter	ADP-60JH	4903562DD	NEC	-
Е	Wireless LAN access	AIR-AP1242AG-	FTX1045B9L0	Cisco Systems	-
E	point	A-K9			
F	AC adapter	ADP-18PB	PZT0639562214	Cisco Systems	-
G	Personal Computer	T30	97-99D4L	IBM	-
Н	AC adapter	02K6750	11S02K6750Z1Z2U	IBM	-
П			P29A0TJ		

List of cables used

No.	Name	Length (m)	Shield		
			Cable	Connector	
1	DC Cable	1.8	Unshielded	Unshielded	
2	AC Cable	1.8	Unshielded	Unshielded	
3	DC Cable	1.8	Unshielded	Unshielded	
4	AC Cable	1.8	Unshielded	Unshielded	
5	LAN Cable	3.0	Unshielded	Unshielded	
6	DC Cable	1.8	Unshielded	Unshielded	
7	AC Cable	1.0	Unshielded	Unshielded	

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5.3 Test and Measurement System

SYSTEM OVERVIEW

The measurement system is based on a conducted test method.

The software selects waveform parameters from within the bounds of the signal type on a random basis using uniform distribution. The short pulse types 2, 3, and 4, the long pulse type 5, and the frequency hopping type 6 parameters are randomized at run-time.

The signal monitoring equipment consists of a spectrum analyzer with the capacity to display 8192 bins on the horizontal axis. A time-domain resolution of 2 msec/bin is achievable with a 16 second sweep time, meeting the 10 seconds short pulse reporting criteria. The aggregate ON time is calculated by multiplying the number of bins above a threshold during a particular observation period by the dwell time per bin, with the analyzer set to peak detection. A time-domain resolution of 3 msec/bin is achievable with a 24 second sweep time, meeting the 22 second long pulse reporting criteria and allowing a minimum of 10 seconds after the end of the long pulse waveform.

FREQUENCY HOPPING RADAR WAVEFORM GENERATING SUBSYSTEM

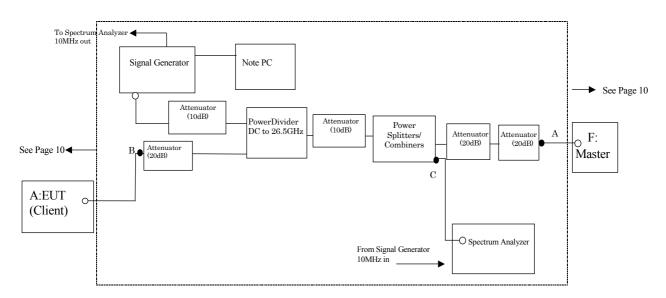
The first 100 frequencies are selected out of the hopping sequence of the randomized 475 hop frequencies. Only a *Burst* that has the frequency falling within the receiver bandwidth of the tested U-NII device is selected among those frequencies. (Frequency-domain simulation). The radar waveform generated at the start time of the selected *Burst* (Time-domain simulation) is download to the Signal Generator.

If all of the randomly selected 100 frequencies do not fall within the receiver bandwidth of the U-NII device, the radar waveform is not used for the test.

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CONDUCTED METHODS SYSTEM BLOCK DIAGRM



MEASUREMENT SYSTEM FREQUENCY REFERENCE

Lock the signal generator and the spectrum analyzer to the same reference sources as follows: Connect the 10MHz OUT on the signal generator to the 10MHz IN on the spectrum analyzer and set the spectrum analyzer 10MHz In to On.

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SYSTEM CALIBRATION

Step 1: Set the system as shown in Figure 3 of FCC 06-96 7.2.1.

Step 2: Adjust each attenuator to fulfill the following three conditions:

- WLAN can be communicated, and
- Rader detection threshold level is bigger than Client Device traffic level on the spectrum analyzer, and
- Master Device traffic level is not displayed on the spectrum analyzer.

Step 3: Terminate 50 ohm at B and C points, and connect the spectrum analyzer to the point A. (See the figure on page 12)

At the point A, adjust the signal generator and spectrum analyzer to the center frequency of the channel to be measured.

Download the applicable radar waveforms to the signal generator. Select the radar waveform, trigger a burst manually and measure the amplitude on the spectrum analyzer. Readjust the amplitude of the signal generator as required so that the peak level of the waveform is at a displayed level equal to the required or desired interference detection threshold.

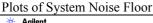
Separate signal generator amplitude settings are determined as required for each radar type.

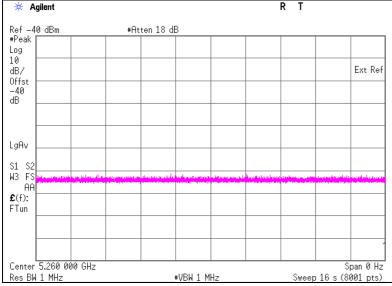
Step 4: Without changing any of the instrument settings, restore the system setting to Step 2 and adjust the Reference Level Offset of the spectrum analyzer to the level at Step 3.

By taking the above steps 1 to 4, the spectrum analyzer displays the level of the signal generator as received at the antenna ports of the Master Device.

See Clause 5.4 for Plots of Noise, Rader Waveforms, and WLAN signals.

5.4 Plots of Noise, Rader Waveforms, and WLAN signals





It was confirmed that the EUT did not transmit before having received appropriate control signals from a Master Device.

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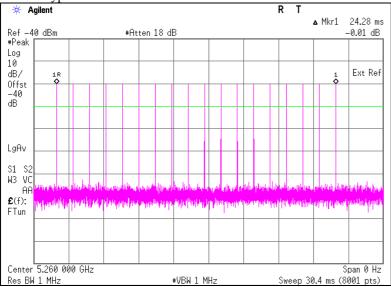
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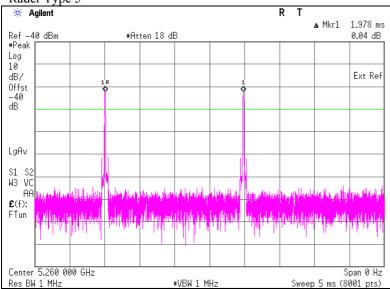
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Plots of Radar Waveforms

Rader Type 1





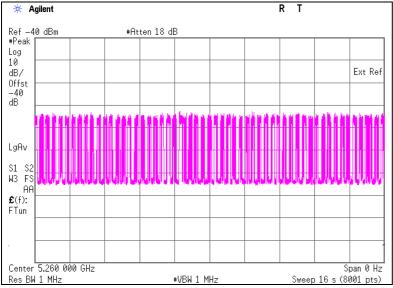


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Plots of WLAN Traffic



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<u>SECTION 6: In-Service Monitoring for Channel Move Time, Channel Closing Transmission</u> Time

6.1 Operating environment

Test place : No.11 measurement room

Temperature : 21 deg.C. Humidity : 39 %

6.2 Test Procedure

Stream the MPEG test file from the Master Device to the Client Device on the test Channel for the entire period of the test.

The Radar Waveform generator sends a Burst of pulses for one of the Short Pulse Radar Types 1-4 at levels defined, on the Operating Channel. An additional 1 dB is added to the radar test signal to ensure it is at or above the DFS Detection Threshold, accounting for equipment variations/errors.

Observe the transmissions of the EUT at the end of the radar Burst on the Operating Channel for duration greater than 10 seconds.

6.3 Test data

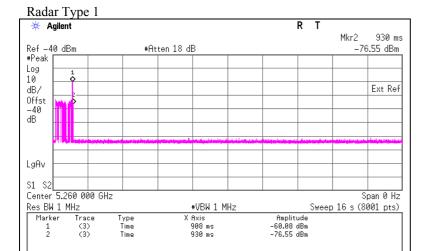
Test Item	Unit	Measurement Time	Limit	Results
Channel Move Time *1)	[sec]	0.022	10.000	Pass
Channel Closing				
Transmission Time *2)	[msec]	0	60	Pass

*1) Channel Move Time is calculated as follows: (Channel Move Time) = (End of Transmission) - (End of Burst) = 0.930 - 0.908

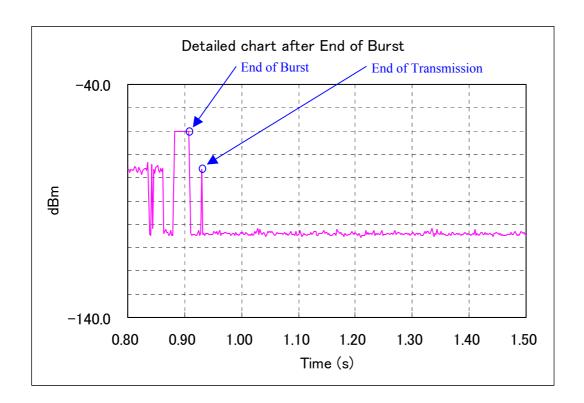
*2) Channel Closing Transmission Time is calculated from (End of Burst + 200msec) to (End of Burst + 10sec) (Channel Closing Transmission Time) = (Number of analyzer bins showing transmission) * (dwell time per bin) = 0 * 2(msec)

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Marker 1 : End of Burst : 908 ms Marker 2 : End of Transmission : 930 ms



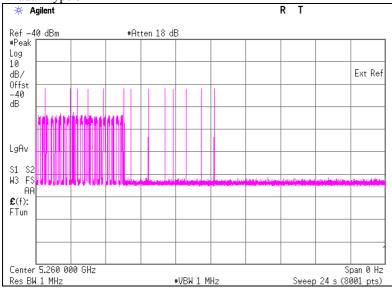
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6.4 Test result

Test result: Pass

Date: March 27, 2009 Test engineer: Takahiro Hatakeda

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SECTION 7: In-Service Monitoring for Non-Occupancy Period

7.1 Operating environment

Test place : No.11 measurement room

Temperature : 21 deg.C. Humidity : 39 %

7.2 Test Procedure

The following two tests are performed:

1). Stream the MPEG test file from the Master Device to the Client Device on the test Channel for the entire period of the test.

The Radar Waveform generator sends a Burst of pulses for one of the Radar Types 1-6 at levels defined on the Operating Channel. An additional 1 dB is added to the radar test signal to ensure it is at or above the DFS Detection Threshold, accounting for equipment variations/errors.

Observe the transmissions of the EUT after the Channel Move Time on the Operating Channel for duration greater than

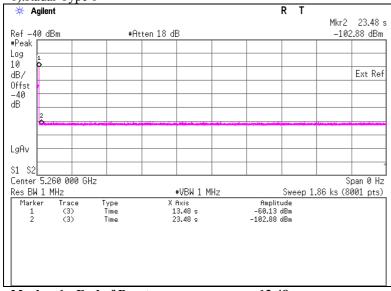
30 minutes.

2). Stream the MPEG test file from the Master Device to the Client Device on the test Channel for the entire period of the test

Observe the transmissions of the EUT on the Operating Channel for duration greater than 30 minutes after the Master Device is shut off.

7.3 Test data





Marker 1 : End of Burst : 13.48 sec Marker 2 : End of Burst +10sec : 23.48 sec

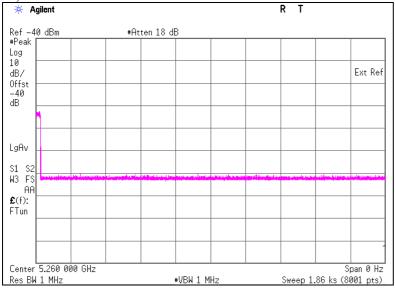
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2). Master is shut off



7.4 Test result

Test result: Pass

Date: March 27, 2009 Test engineer: Takahiro Hatakeda

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