

## **EXHIBITS**

### **Internal Photographs**

(Pursuant to FCC Part 2.1033(b)(7), 2.1033(c)(12) and RSP 100 Annex C)

The following photographs are attached.

Exhibit

- |   |   |
|---|---|
| 1. Base Assembly Housing (Front and Back)                             | A |
| 2. RF Board (Top Side with shields)                                   | B |
| 3. RF Board (Top Side shields removed)                                | C |
| 4. RF Board (Bottom Side)   | D |
| 5. Base Processor Board Inside Housing                                | E |
| 6. Housing without any Boards   | F |
| 7. RF Board with BLE SOC Marked (shields removed)                     | G |
| 8. Base Processor Board and RF Board with Cable (Base Board Top Side) | H |
| 9. Base Processor Board (Top Side)                                    | I |
| 10. Base Processor Board (Bottom Side)                                | J |
| 11. Base Processor Board and RF Board with Cable (RF Board Top Side)  | K |

Exhibit A



Exhibit B





Exhibit C

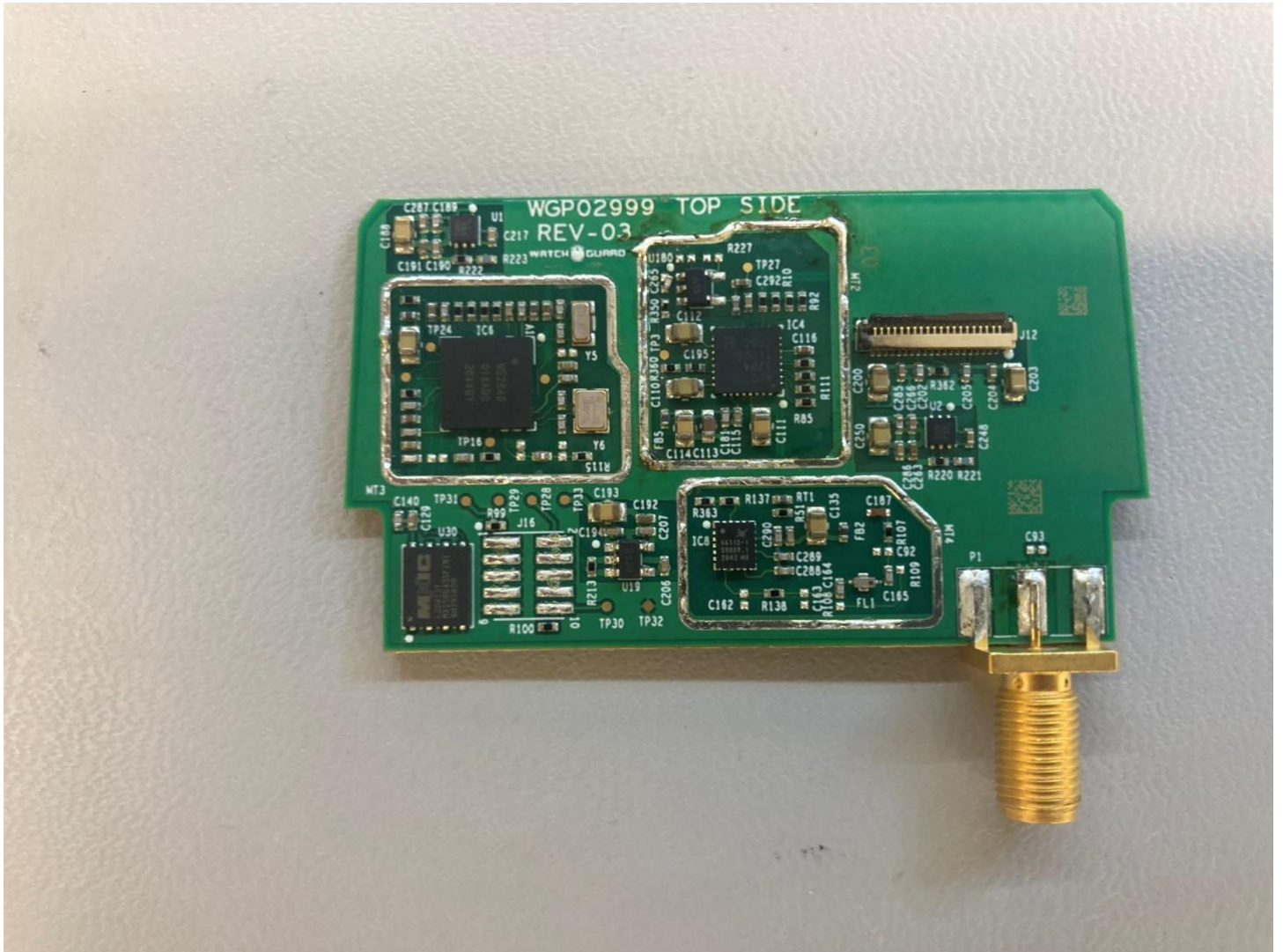


Exhibit D

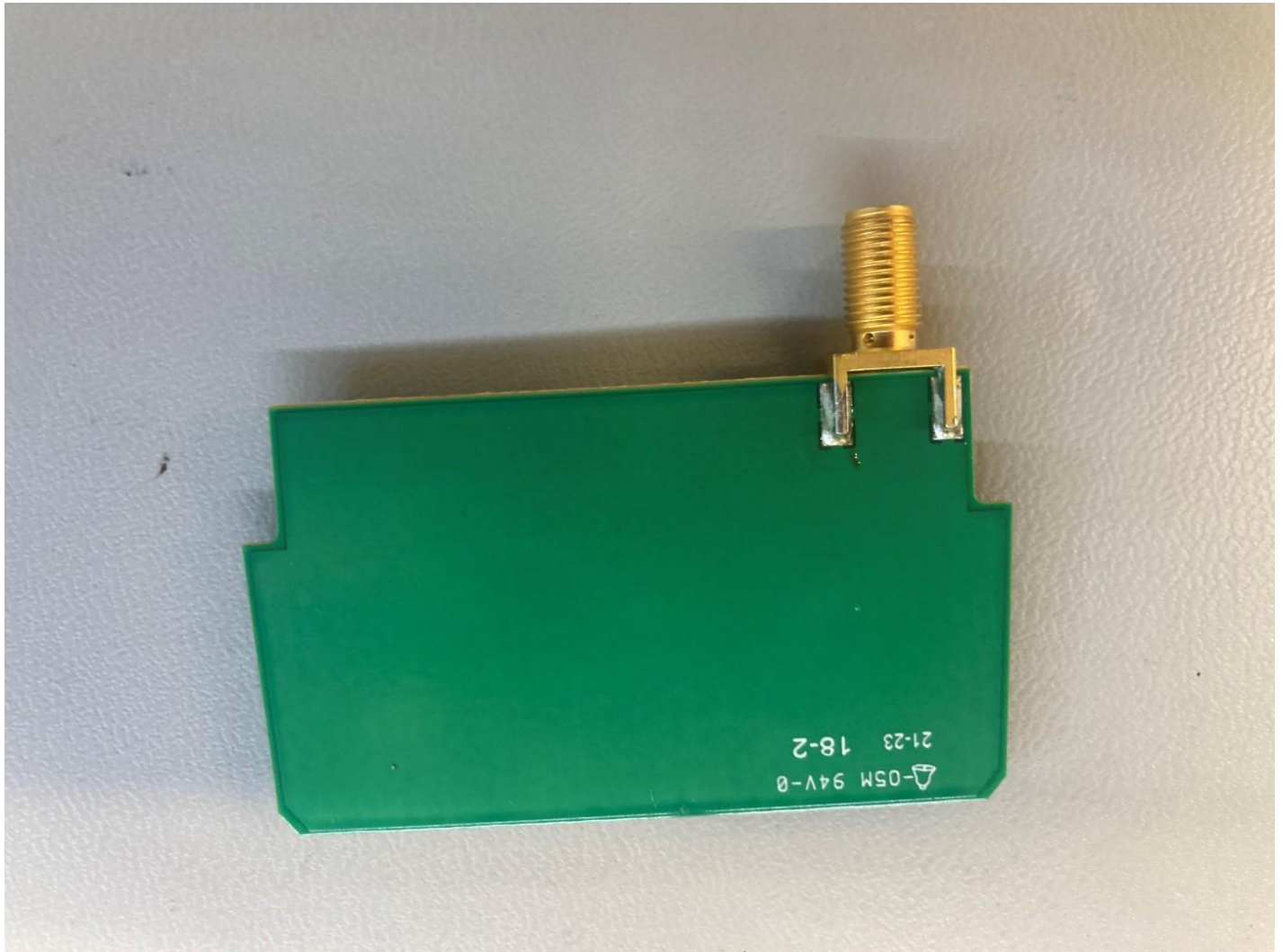


Exhibit E

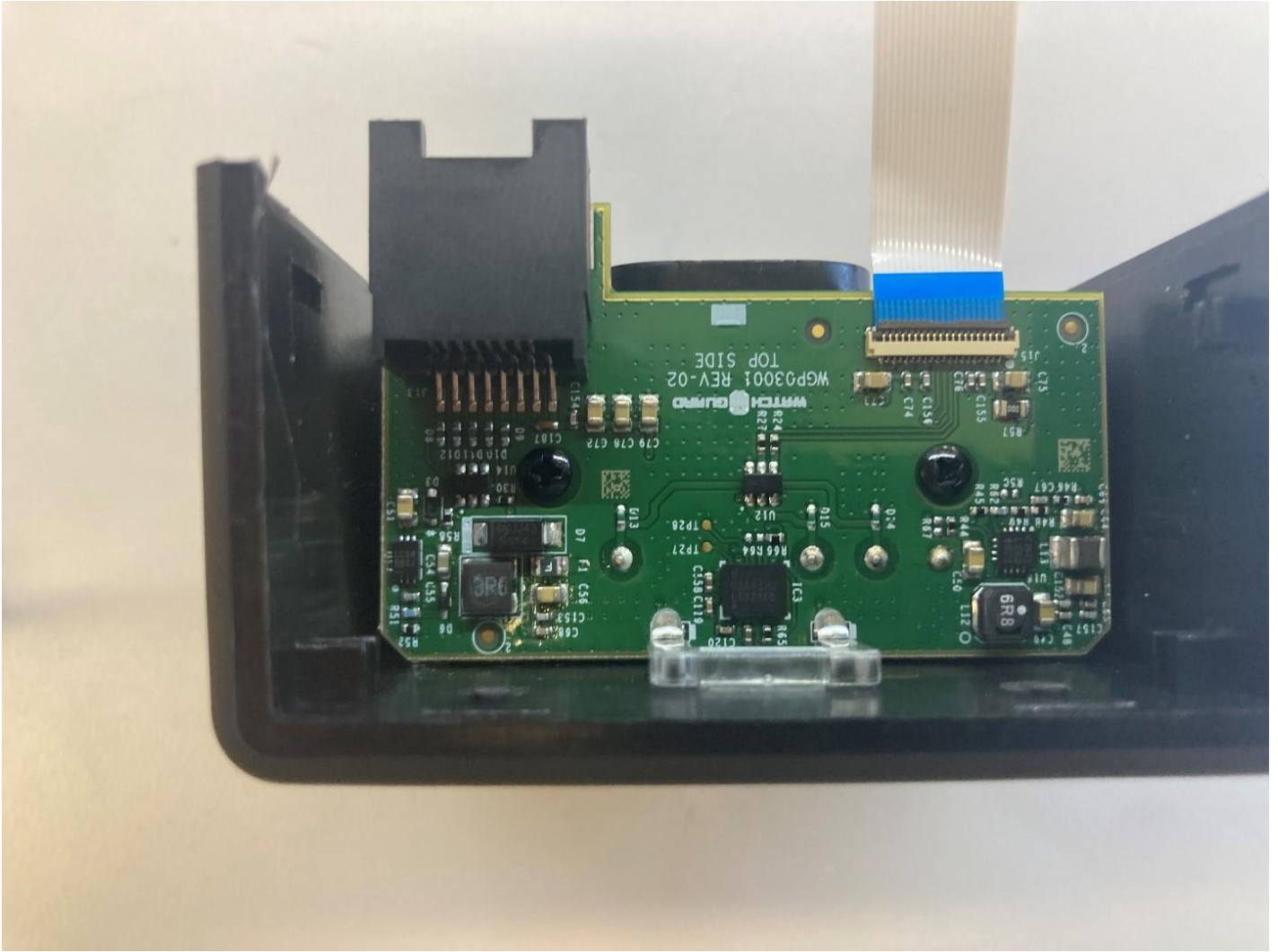




Exhibit F



Exhibit G

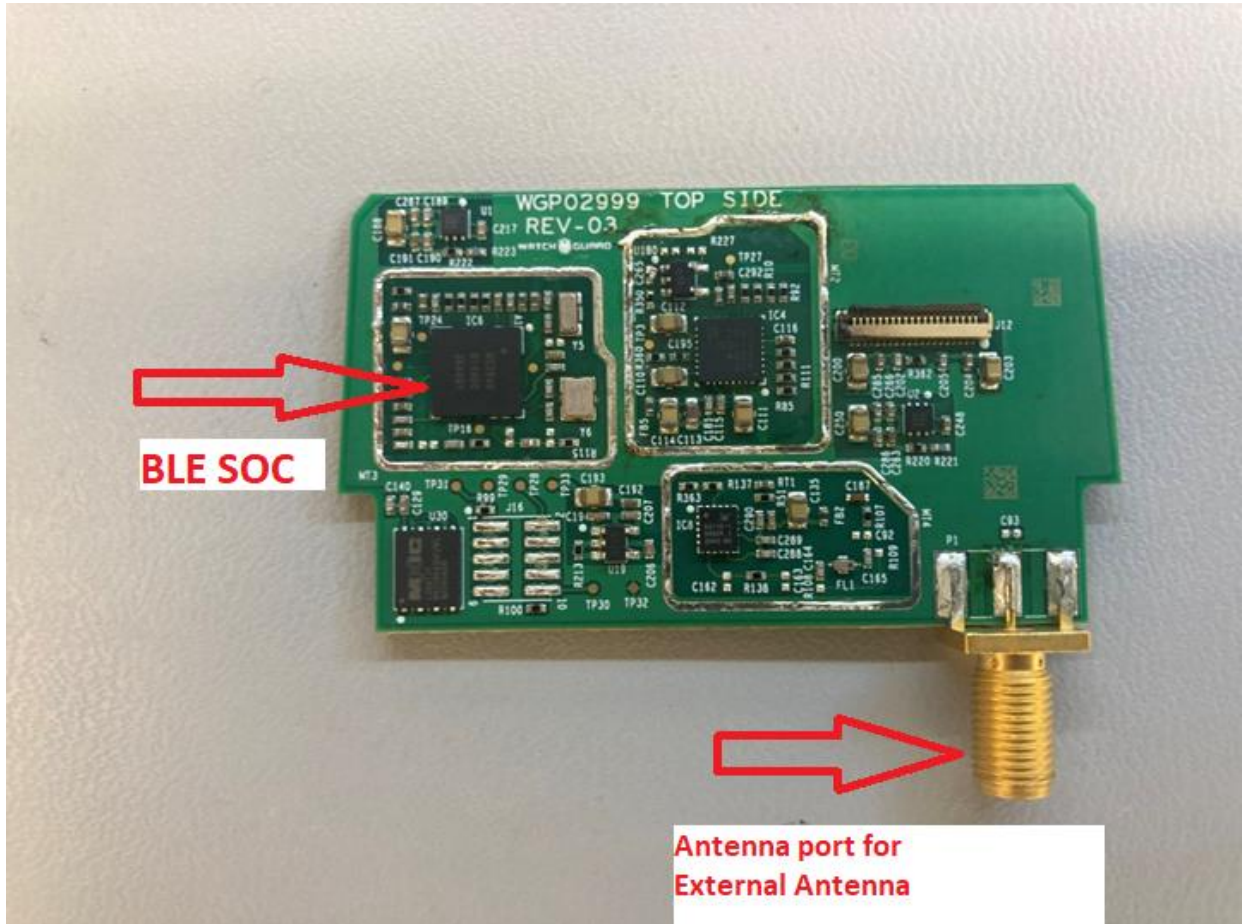




Exhibit H

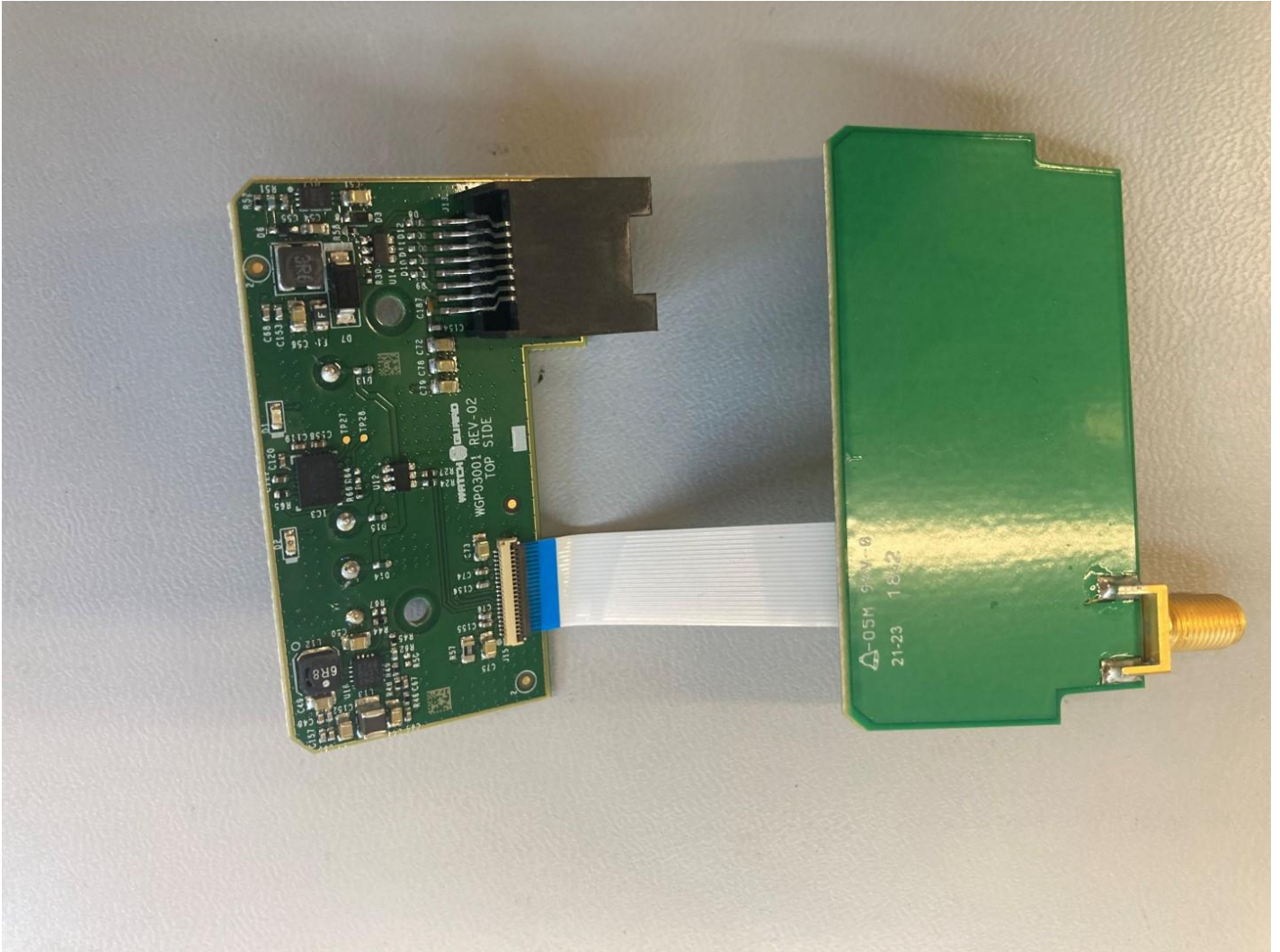


Exhibit I

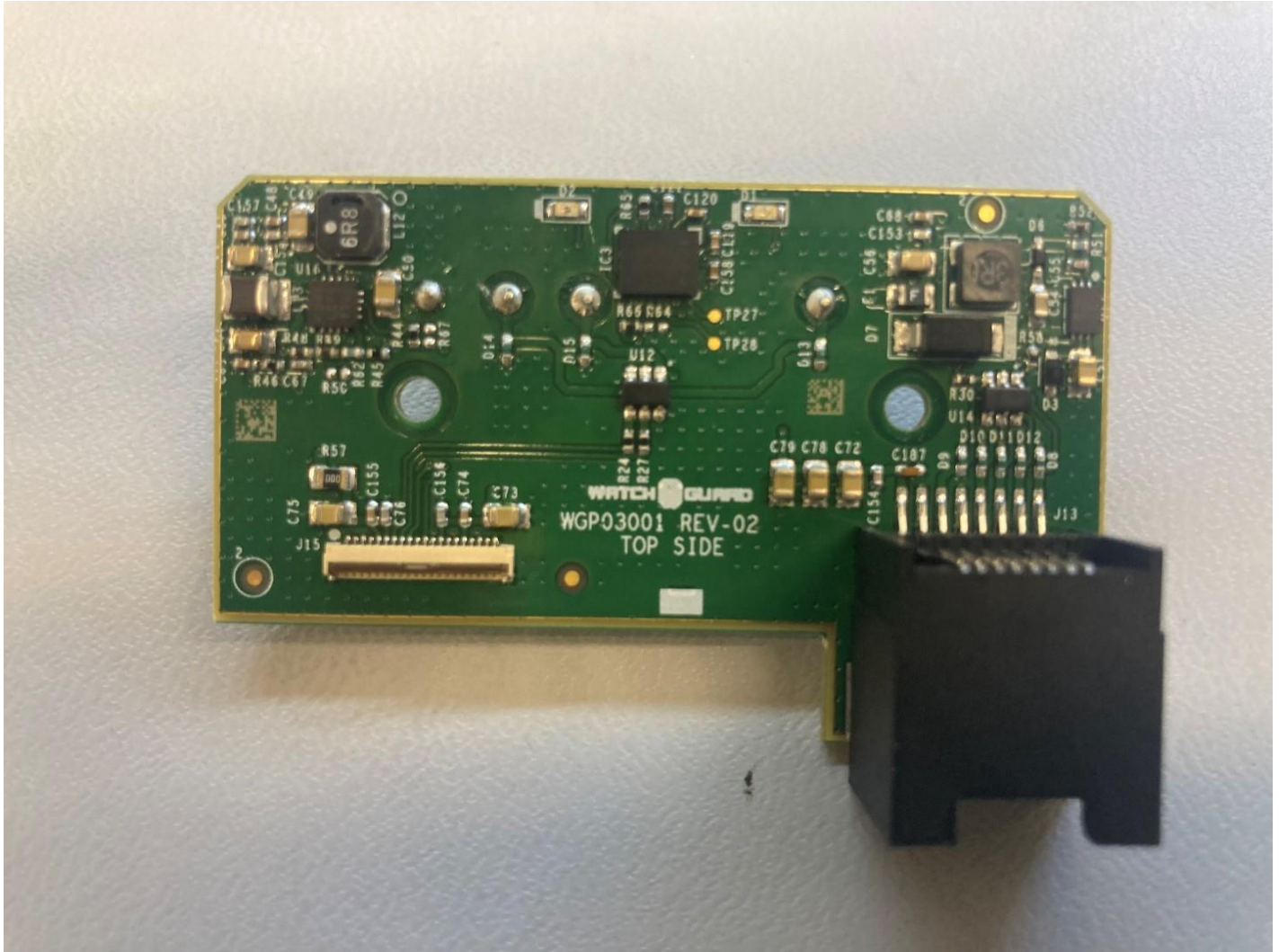
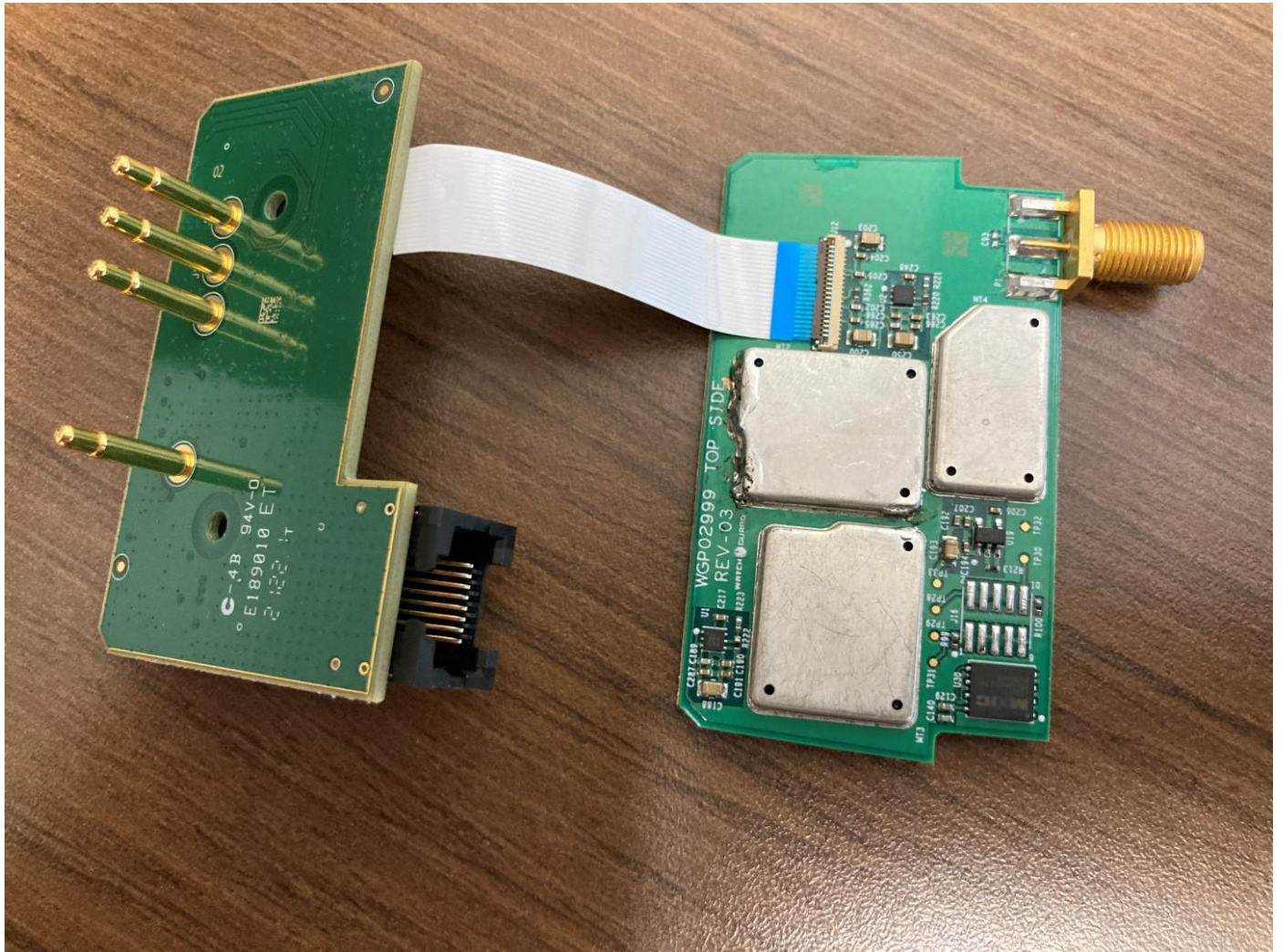


Exhibit J





Exhibit K



\*\*\* END \*\*\*