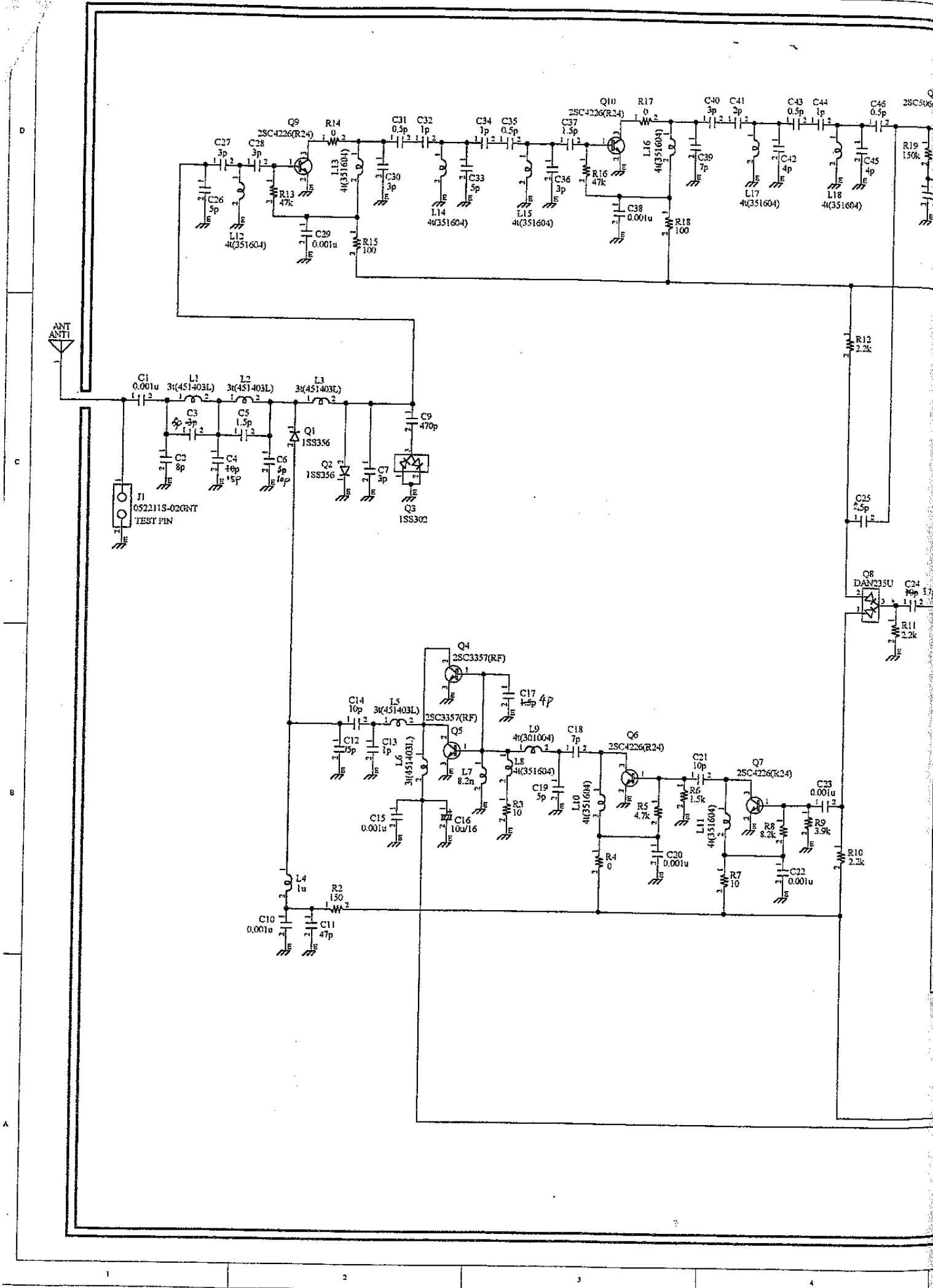
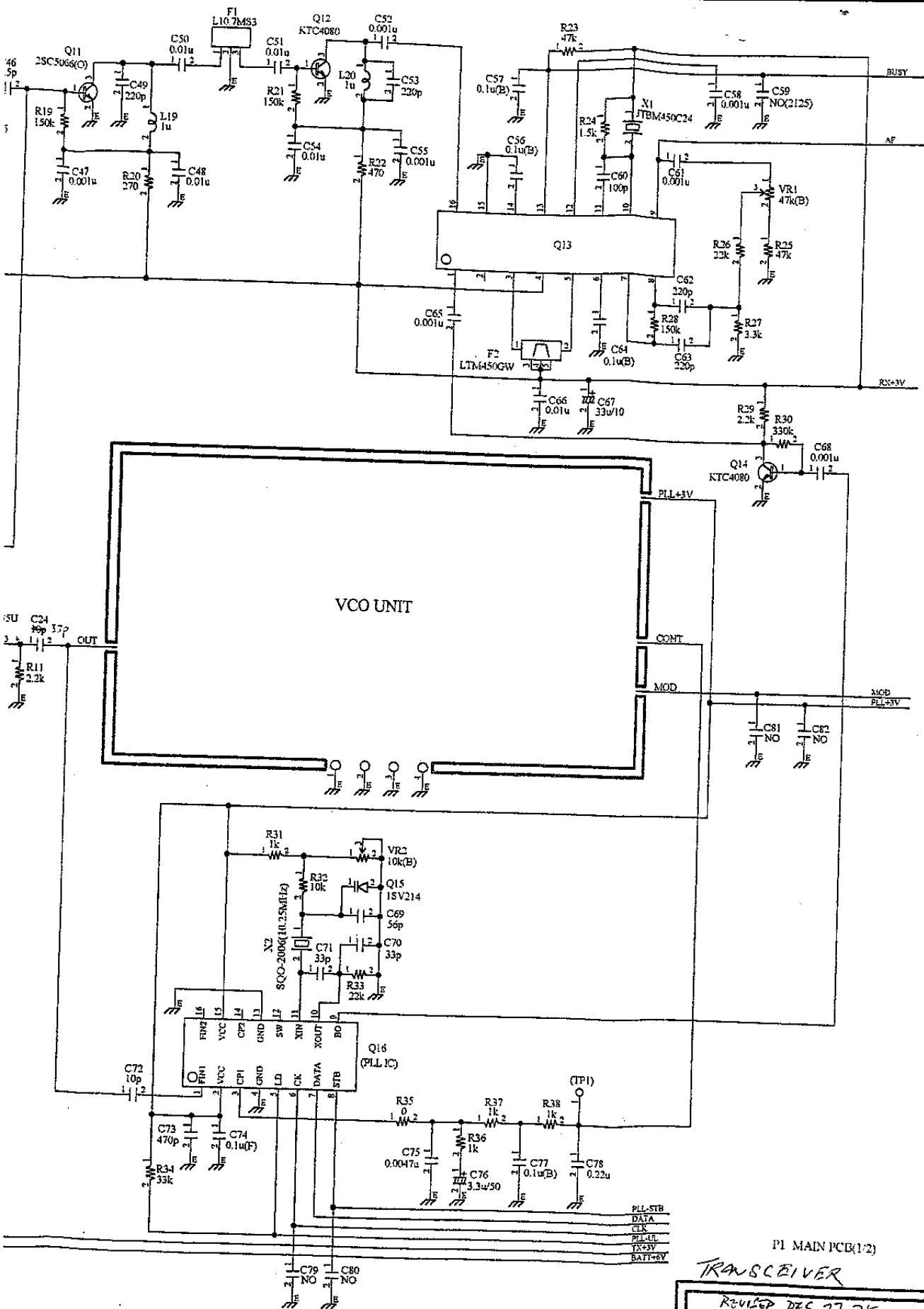






P1 MAIN PCB(1/2)

TRANSCIEVER

REVISED DEC 27 2K

27 DEC 2

DRAWN BY	ONODERA	SCHEMATIC DIAGRAM OF	FRS-80C
DESIGNED BY	KAWASAKI	DATE	SEP-14-2000
APPROVED BY	K. CHIBA		
			FRS-80A MAIN PCB(1
			MUSICAL ELECTRONICS LIMITE