PLL SECTION

* PLL Block of this unit is controlled by the micro-processor which is UP DOWN channel switch which is converted to serial data.

* The PLL operates at the transmit frequency or the required L.O.frequency(receive-10.7MHz) according to whether it is receive or transmit mode.

* The PLL-IC(TB31202F) used in this circuit is a pulse swallow type and fundamental oscillator frequency divider, phase comparator, program counter, prescaler are all included internally.

PLL BLOCK DIAGRAM