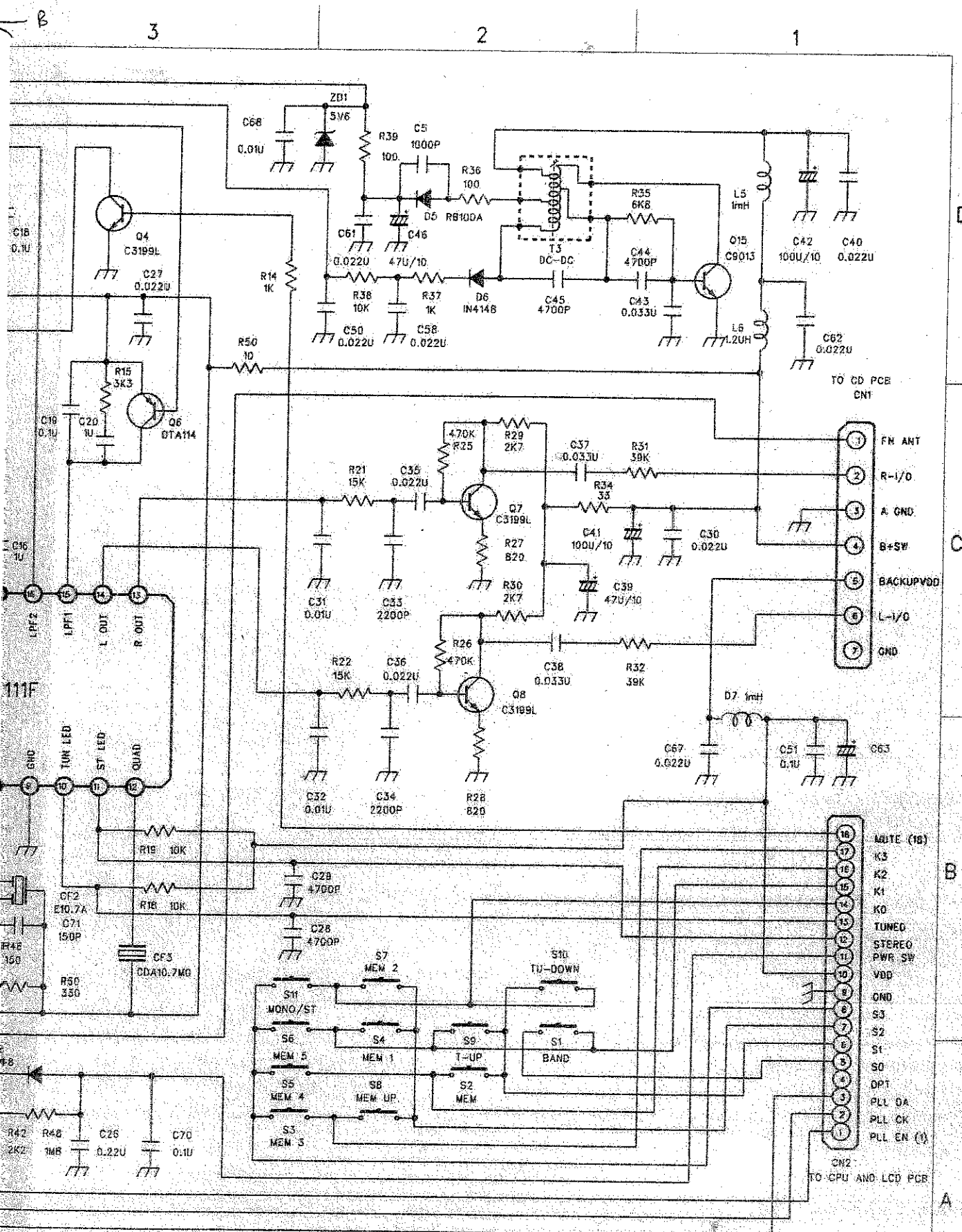




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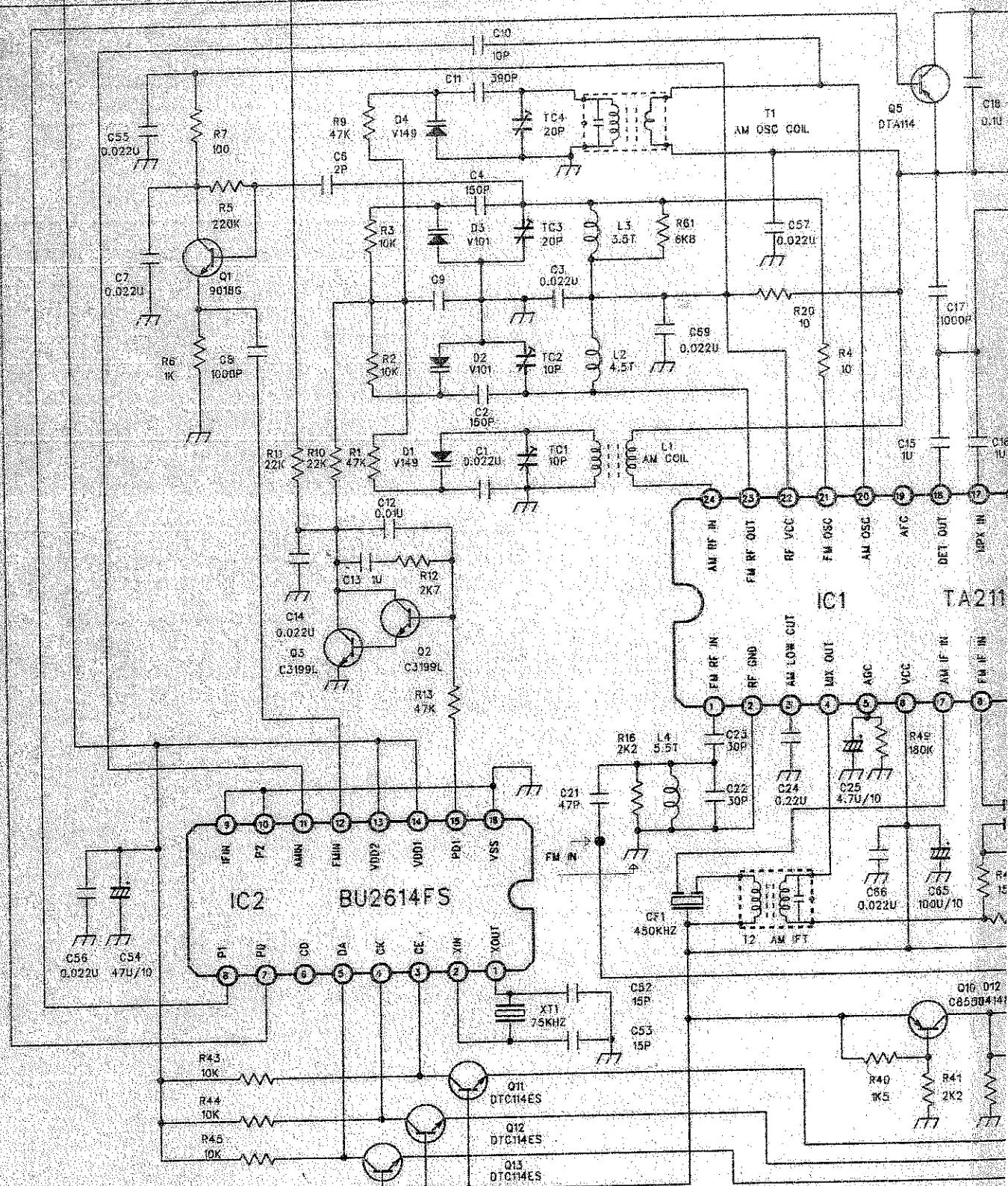
B →

D

C

B

A

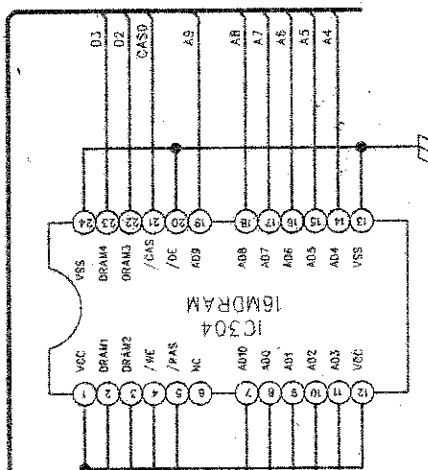


NO.	NAME	REF. NO	DESCRIPTION

REMARK: C68:0.0

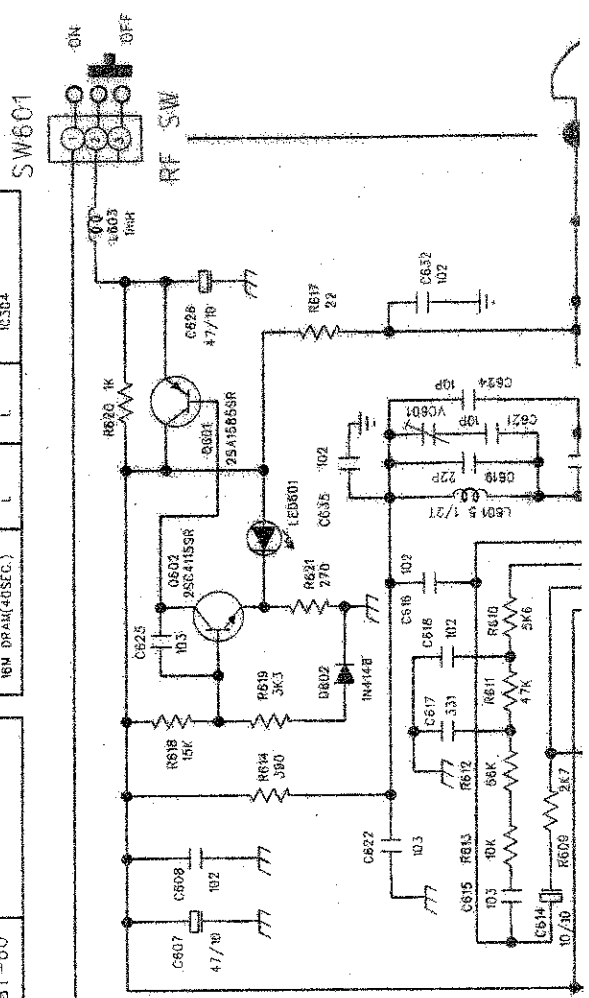
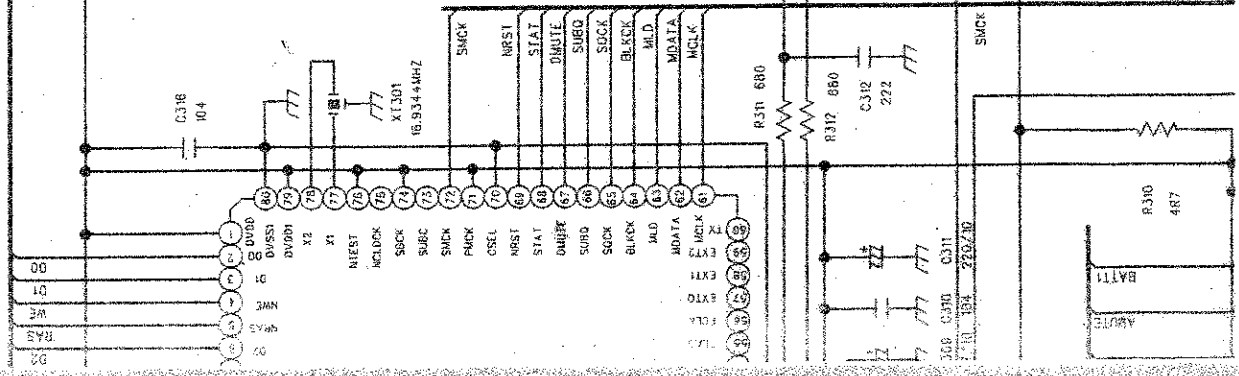
A →

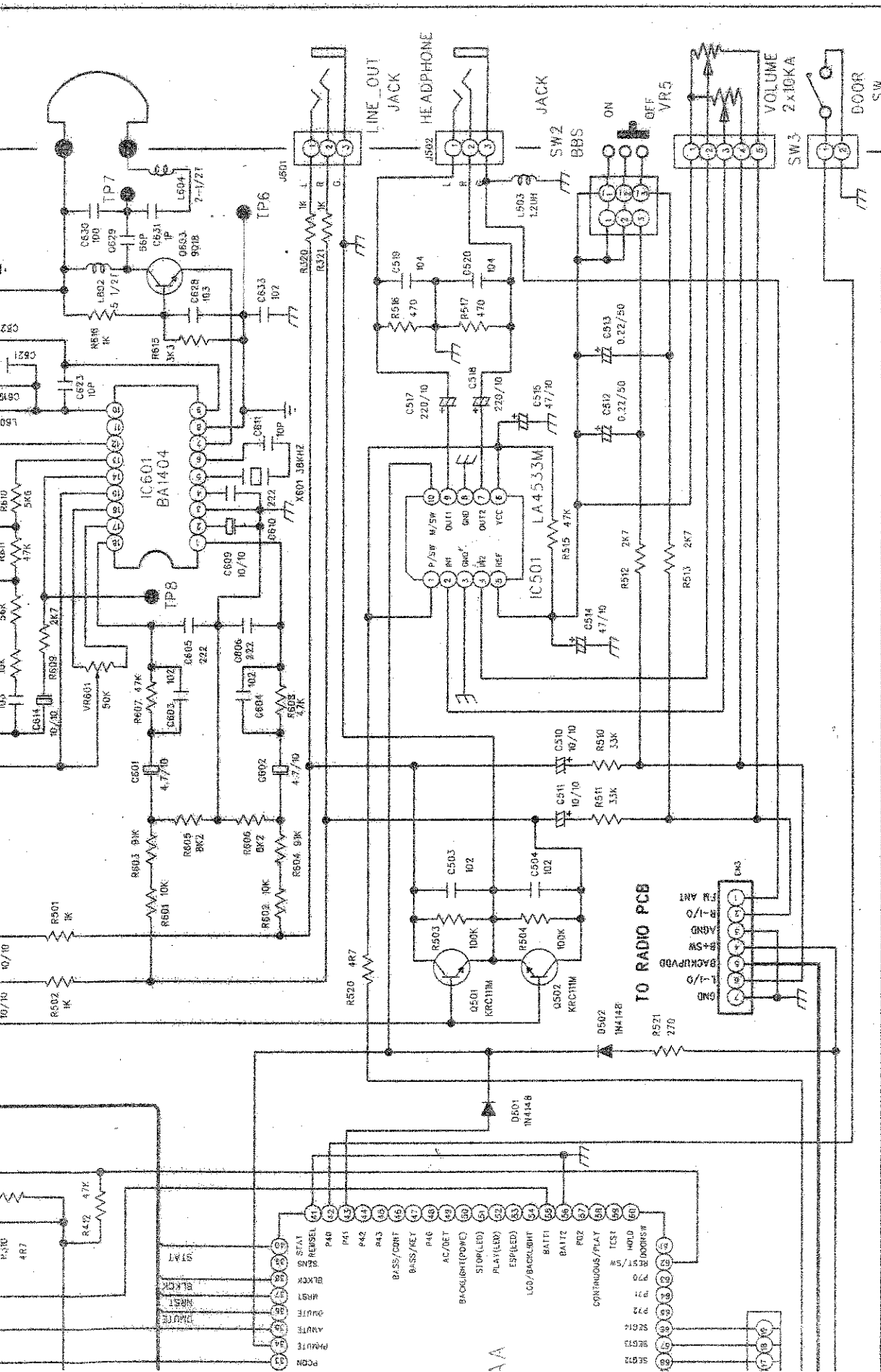
F →



*** DRAM *** ** DRAM SELECTOR OPTION **

DRAM	GM71C4400BJ-60	MN414400BSJ-70	ESP VERSION	SET1	SET2	LOCATION
4MDRAM	GM71C4400BJ-70	MSM51V4400-70	1M DRAM (3SEC)	H	L	IC302
	GM71C4403EJ-70		4M DRAM x 1 (10SEC)	H	L	IC302
16MDRAM	GM71V17400CT-60	HY51V1740BT-60	4M DRAM x 2 (20SEC)	L	H	IC302 & IC303
			16M DRAM (40SEC)	L	L	IC304





SCHEMATIC DIAGRAM

FILE: HD8299C.SCH(CD BOARD)

DRAWN

WELSH & SONS

CHECKED

邵景康

APPROVED

FINES



FINES

NAME _____
S/N _____

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D
V

