

EXHIBIT 14

Section 2.1046 Measurements Required: RF Power Output

This test procedure is a measurement of the single channel RF power transmitted at the PCS-TDMA Dual Radio Module (PDRM) output terminal as shown in the accompanying test set-up diagram. In accordance with ANSI J-STD-010-1996, Section 3.2, the power level shall be maintained within +1 to -3 dB of the manufacturer's rated single channel value across the PCS frequency band 1930.08 – 1989.96 MHz. A single channel was tuned to the center frequency of B-Block, channel 917 at 1957.53 MHz, and the power level set to approximately +15.5 dBm at the PDRM Tx terminal. The carrier was then tuned to 3 frequencies in each PCS Block, corresponding to the lower block edge (le), block center frequency (cf) and the upper edge frequency (ue), and the corresponding power level measured.

PCS Block	PCS Ch. No.	PCS Freq. MHz	PDRM Output dBm
A(le)	2	1930.08	+14.8
A(cf)	250	1937.52	+15.0
A(ue)	498	1944.96	+15.0
D(le)	502	1945.08	+15.0
D(cf)	583	1947.51	+14.9
D(ue)	665	1949.97	+14.8
B(le)	668	1950.06	+14.9
B(cf)	917	1957.53	+14.9
B(ue)	1165	1964.97	+14.9
E(le)	1168	1965.06	+15.1
E(cf)	1250	1967.52	+15.3
E(ue)	1332	1969.98	+14.9
F(le)	1335	1970.07	+15.0
F(cf)	1417	1972.53	+14.9
F(ue)	1498	1974.96	+14.8
C(le)	1502	1975.08	+15.0
C(cf)	1750	1982.52	+15.0
C(ue)	1998	1989.96	+14.3

Results:

Power measurements were made with a Hewlett-Packard E4419A, EPM Series Power Meter and an HP ECP-E18A CW Power Sensor. All measurements are within the required +1 to -3 dB of the rated +15.5 dBm maximum power output.

EXHIBIT 14

Test set-up for measuring the power output from the PCS-TDMA Dual Radio Module transceiver.

FLEXENT™ PCS-TDMA Microcell J41698A-1

- TOM: TDMA Oscillator Module
- TRC: TDMA Radio Controller
- PCU: Power Conversion Unit
- PDRM: PCS-TDMA Dual Radio Module
- LISN: Line Impedance Stabilization Network

