<u>
上時電子股份有限公司</u>
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Federal Communications Commission Authorization and Evaluation Division 7435 Oakland Mills Rd. Columbia, MD. 21046

Date: July 18, 2003

Attention: Authorization and Evaluation Division

Subject: RFI related modifications incorporated into unit COLOR MONITOR (EUT) with - FCC ID: ARSCM793V

Dear Sirs:

This letter serves as our declaration that all modifications listed below were implemented in the sample submitted for testing. We further declare that the same modifications will be implemented into all production units to enhance compliance of the units to FCC limits.

The modifications include the following:

- 1. Added two ferrite cores on the video cable one outside and one inside of the monitor. (see photo 2 & 7)
- 2. Added a ferrite core with eight turns on the safety ground wire. (see photo 7)
- 3. Added aluminum foils from FBT heatsink to chassis's ground.(see photo 6)
- 4. Added metal cover covering CRT board. (see photo $\overline{5}$)
- 5. Added two ferrite cores on G2, G4 and focus wires (one with one turn and one without turn. (see photo 8)
- 6. Added two ferrite cores on the wires with one turn connected between CRT board and mainboard. (see photo 7)
- 7. Added a ferrite core on degaussing coil connected to main board. (see photo 7)
- 8. Added a ferrite core on the wire connected to CRT yoke and mainboard.(see photo 9)
- 9. Added four jump wires and a capacitor on the solder side of main board for electrical improvement. They will be built in to the component side after circuit re-layout. (see photo 12)
- 10. Added a bead core and Y capacitor on the solder side of main board for EMI (see photo12)
- 11. Added two diodes on the solder side of main board for EMI (see photo12)
- 12. Added a gasket on CRT board heatsink. (see photo 13)
- 13. Added two jumper wires , four capacitors and spring fingers on the solder side of main board for EMI (see photo 15)
- 14. Added four ground wires connected from CRT to chassis. (see photo 5 & 10)

If you have any further questions or comments regarding the above, please don't hesitate to contact Mr. Fred Chen of ADT Lab. at fax No.: 886-2-2602-2943 or E-mail: fred@mail.adt.com.tw

Sincerely yours,

george Warg

R& D Project Manager Top Victory Electronics (Taiwan) Co. Ltd.