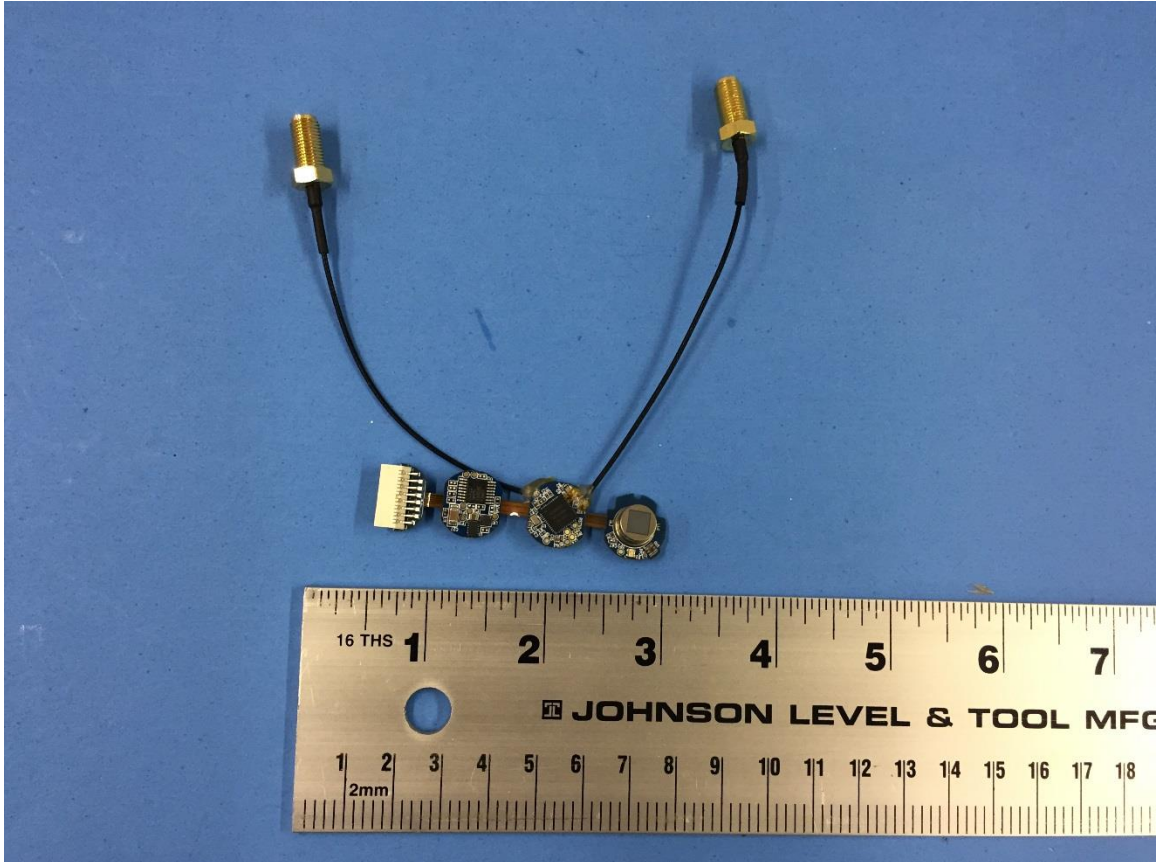
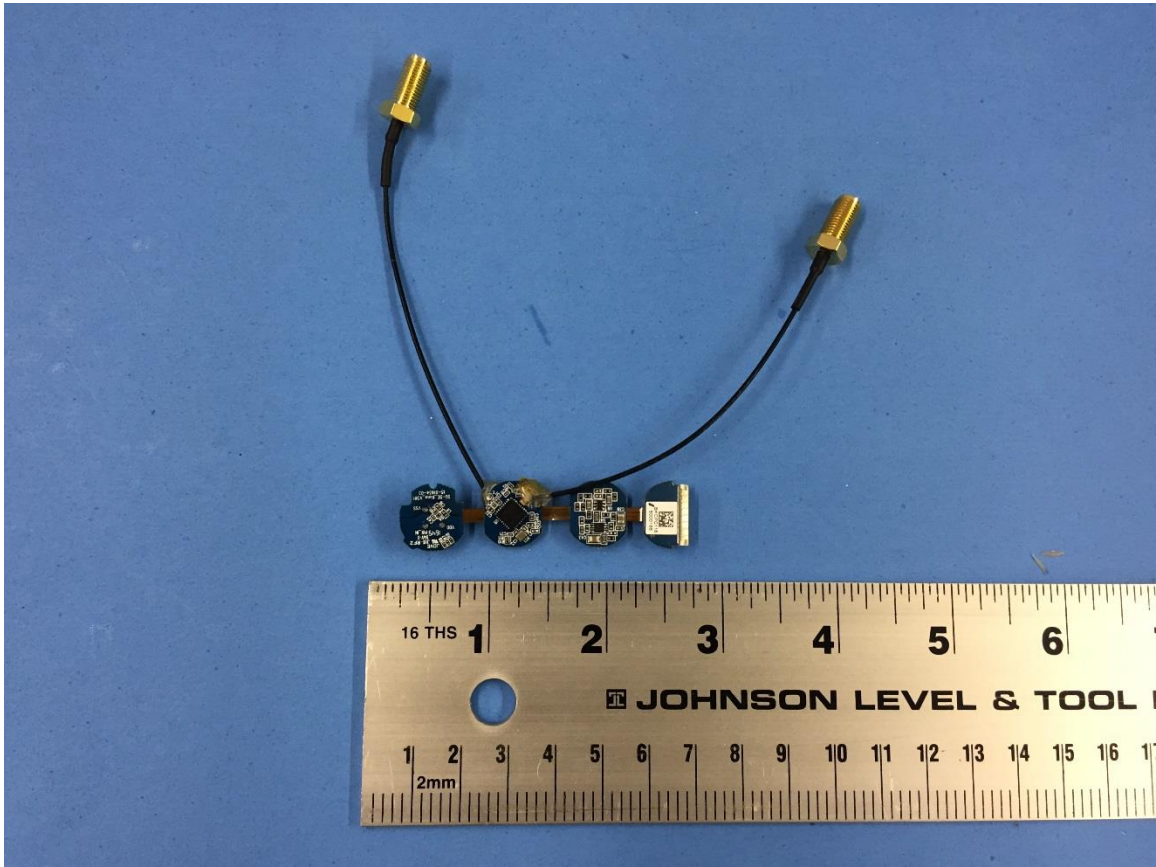


## 1.1 Product Under Test Internal Photo



**Figure 1:** Photo of Conducted Test Sample - PCB (Top view)



**Figure 2:** Photo of Main PCB - Top

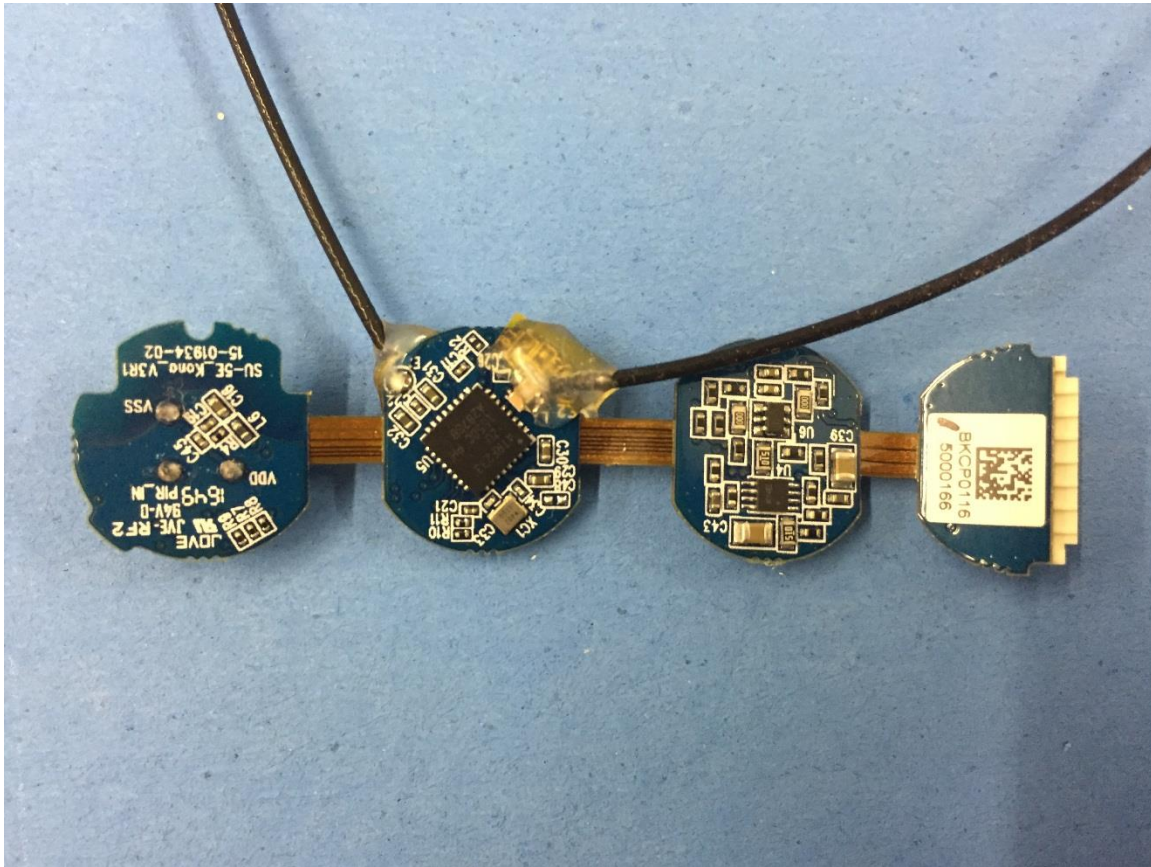
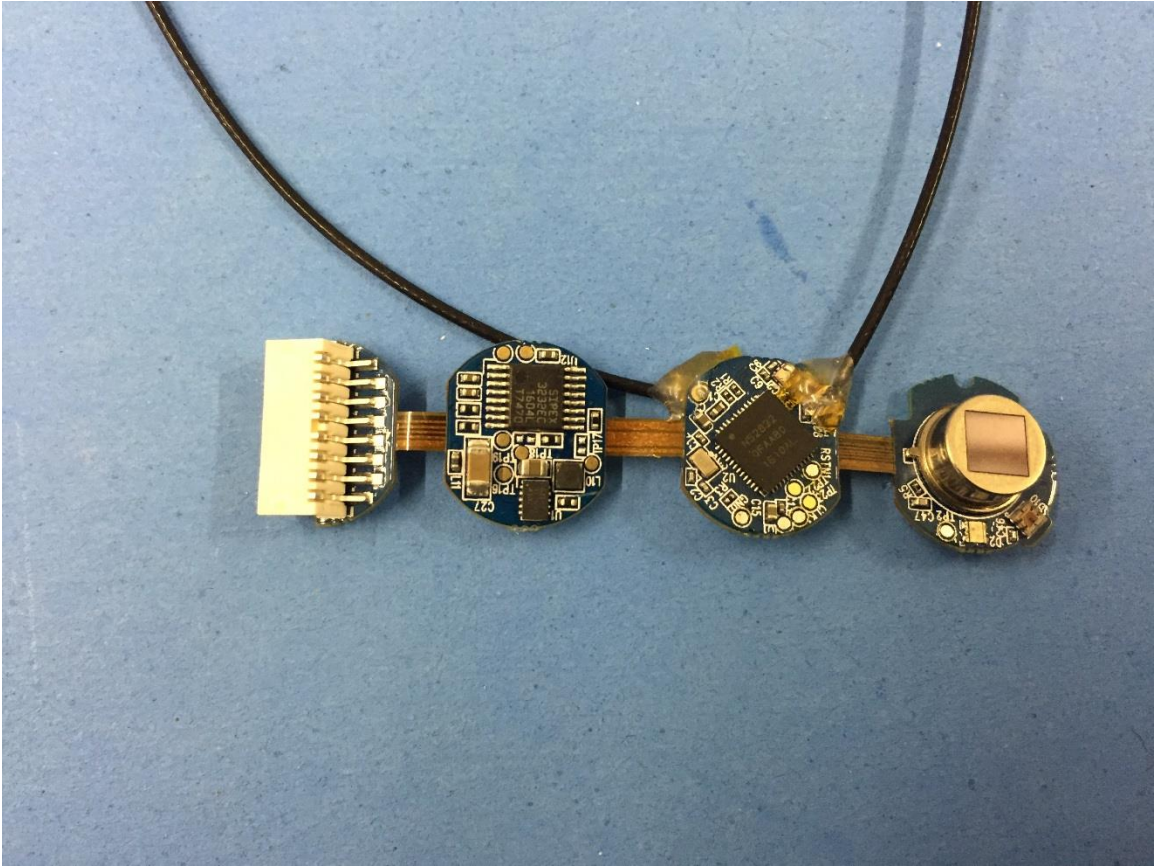


Figure 3: Photo Main PCB - Bottom



**Figure 4:** Photo of FPC4 - Bottom