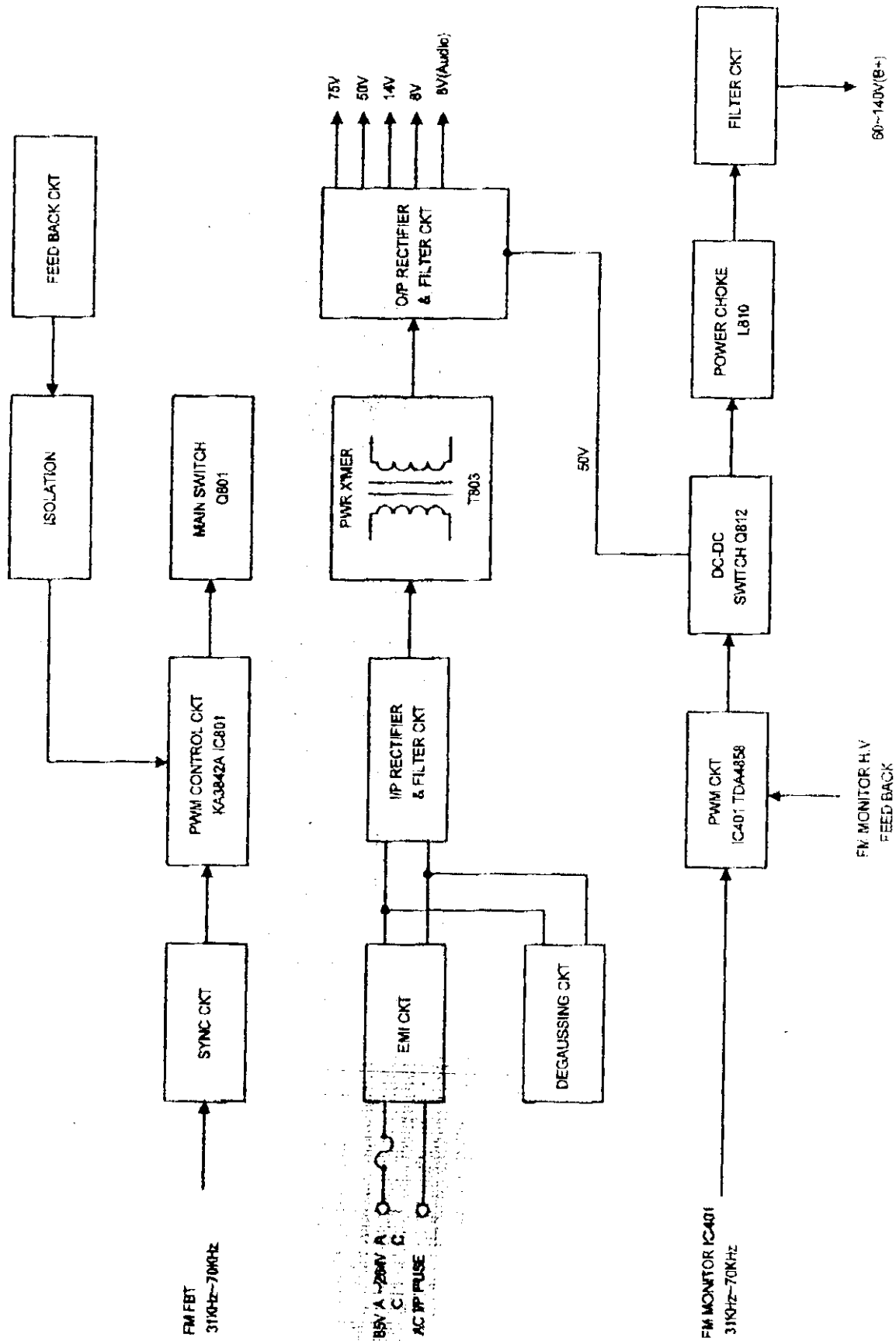


BLOCK DIAGRAM (SPS)



FCC ID: AMPJ51H

The diagram illustrates the Philips 7400 television receiver system, showing the signal path and power supply components. The signal path starts with an I/O cable connected to a CPU (1C101) and a video pre-amp (1C801). The CPU controls the video pre-amp and the combined horizontal/vertical processor (1C401). The video pre-amp outputs a signal to the video output (1C904), which is connected to the CRT. The combined horizontal/vertical processor (1C401) outputs signals to the video output and the deflection yoke. The power supply section includes a switching power supply, a 50V FM SPS, a DC-DC converter, and an HV regulator. The HV regulator is connected to the deflection yoke and the CRT. The diagram also shows the connection of the 7400 to the 7401, which is connected to the 7402, and the 7402 is connected to the 7403.

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