

Page Number

5 of 44. AMENDED October 24, 2003

Subpart 2.1033 (continued)

(c)(8): Voltages & currents in all elements in final RF stage, including final transistor or solid-state device:

Collector Current, A	=	1.8
Collector Voltage, Vdc	=	7.5
Supply Voltage, Vdc	=	7.5

(c)(9): **Tune-Up Procedure:**

Please see attached exhibits

(c)(10): **Circuit Diagram/Circuit Description:**

Including description of circuitry & devices provided for determining and stabilizing frequency, for suppression of spurious radiation, for limiting modulation and limiting power.

Please see attached exhibits

(c)(11): **Label Information:**

Please see attached exhibits

(c)(12): **Photographs:**

Please see attached exhibits

(c)(13): **Digital Modulation Description:**

☐ Attached Exhibits
☒ N/A

(c)(14): **Test and Measurement Data:**

Follows