

M1506GR Label Location (Manual)

M1506GR User Manual

Application Block Diagram

M1506GR is installed into the printed circuit board (PCB) of the application by reflow soldering process. LPWA output ("905M_ANT" pin) is connected to the antenna (SMA Reverse Connector) with 50 Ω line on PCB.

Antenna Information
 Frequency: 860-928 (MHz)
 Antenna type: monopole antenna ($\lambda/2$)
 Maximum absolute gain: 3 dBi or less
 Impedance: 50 Ω
 Connector type: SMA-P (REVERSE)

PCB Information
 Thickness: 1.6 mm (4-Layers)
 Dielectric constant: 4.3

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FCC and IC Regulatory Statements

FCC ID and IC Number

US / FCC	CANADA / IC
AK8M1506GR	409B-M1506GR

The following information must be indicated on the host device of this module;

[for FCC]
 Contains Transmitter Module FCC ID: AK8M1506GR

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

*If it is difficult to describe this statement on the host product due to the size, please describe in the User's manual.

[for ISCED]
 Contains IC: 409B-M1506GR

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FCC ID and IC Number

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M1506GR Label Location (Packaging)

