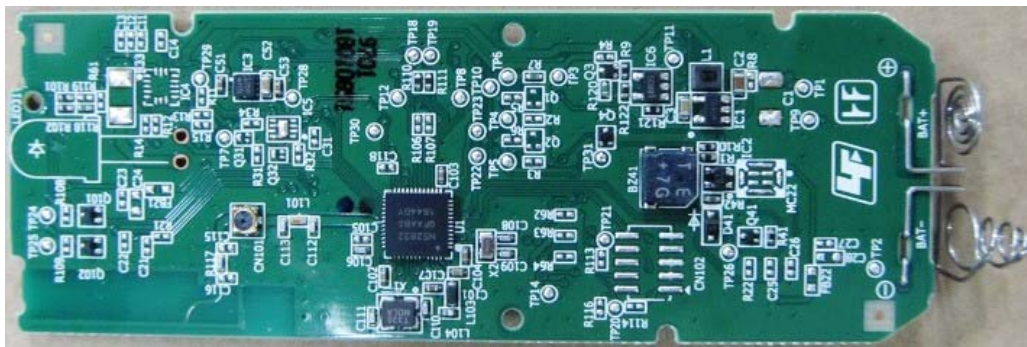
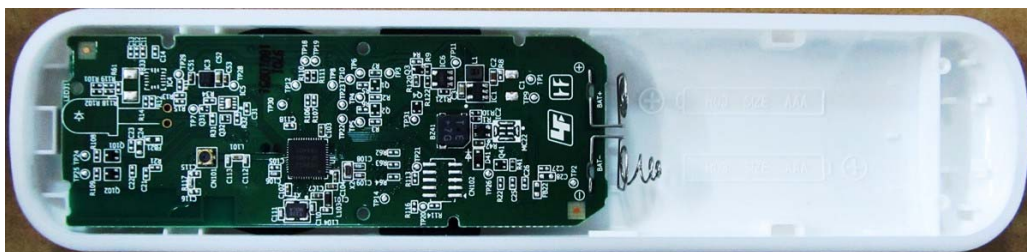


PCB (L1)



PCB (L2)



Location of PCB