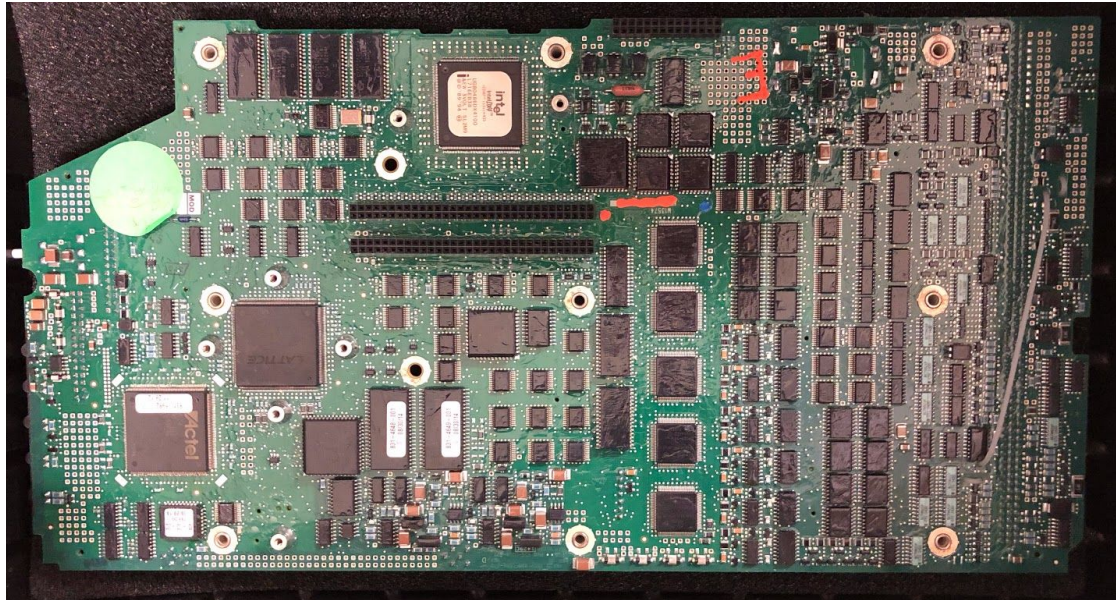
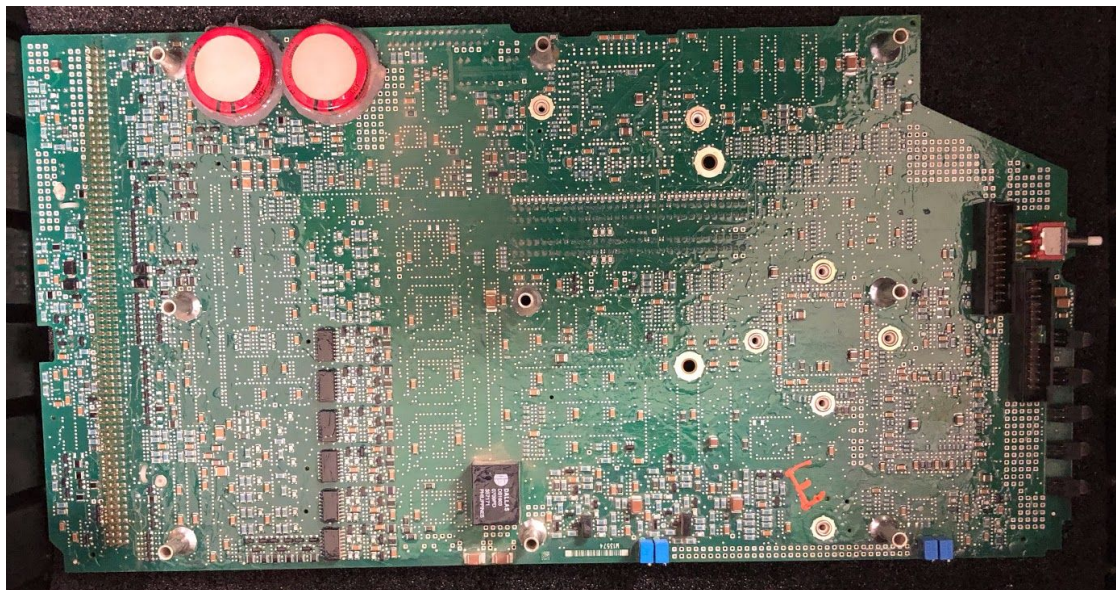


6. Programmed Assembly (CPU) - A4

i. Top View

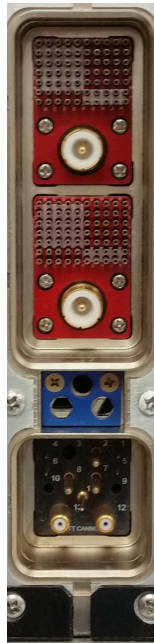


ii. Bottom View



7. Rear Interconnect (IO) - A5

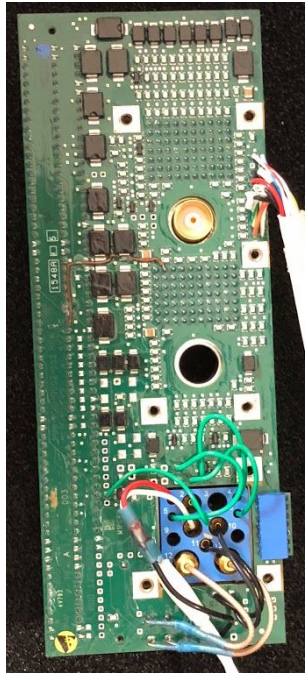
i. Top View



Top Antenna

Bottom Antenna

ii. Bottom View



8. Filter Assembly - FL1

