

Circuit Description for Receiver

Q1 is a Common Base RF amplifier used as part of a super regenerative receiver. L1 and C2 comprise the tuned circuit set for 49.86 MHz.. IC1 (RX5C) contains all the demodulator and motor driver electronics. Three motor are driven by three diferent H pattern transistor driver circuits. Energy is supplied to the receiver and motors via a 6V battery.

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