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Circuit Description

Prepared by	
Approved by	
Rev.	A

Cat No.: 33-1197(T#760)

Description: 900 MHz Rechargeable Analog Wireless Stereo Headphone

Receiver Section

Slide switch SW2 is power switch. SW1 is Battery selector, when you use Rechargeable batteries, the switch is set to Charge and if the Alkaline battery is used, you must set the switch to Alk.

U104 and C137 are used to build the regulator circuit. When the SW2 is set to ON, the Power LED lights on. R52 R53 Q15 and Q16 are the battery detect circuit. If the batteries voltage is low, the Power LED1 will be flash, you must replace fresh battery or recharge for the batteries.

U105 is the AF power circuit. VR101 is volume control knob. Q18 Q19 VR2 Q20 and Q17 are the mute circuit which control the AF PA U105.

U103 is the Expander to improve the SNR.

Q1 and Q2 are used to build the LNA circuit. Fil1 is the band passed filter for image rejection. Q3 is the mixer circuit. U1 is PLL IC which is controlled by the MCU U2. Q4 L8 and VD1 are used to build the VCO circuit, Q5 and Q6 are the LO buffer, C16 C17 R14 R15 C18 are used to build the passive LPF. Q8 Q9 Q10 Q11 and Q12 are used to build the IF AMP circuit, Fil2 Fil3 Fil4 and Fil5 are ceramic filter which improve the 1st image rejection and adjacent channel selection. U106 L10 and L11 are the demodulation circuit to generate the LR stereo signal. Q22 Q21 VR4 and VR3 are used to build the AF AMP circuit and produce a suitable AF signal to the next stage expander.