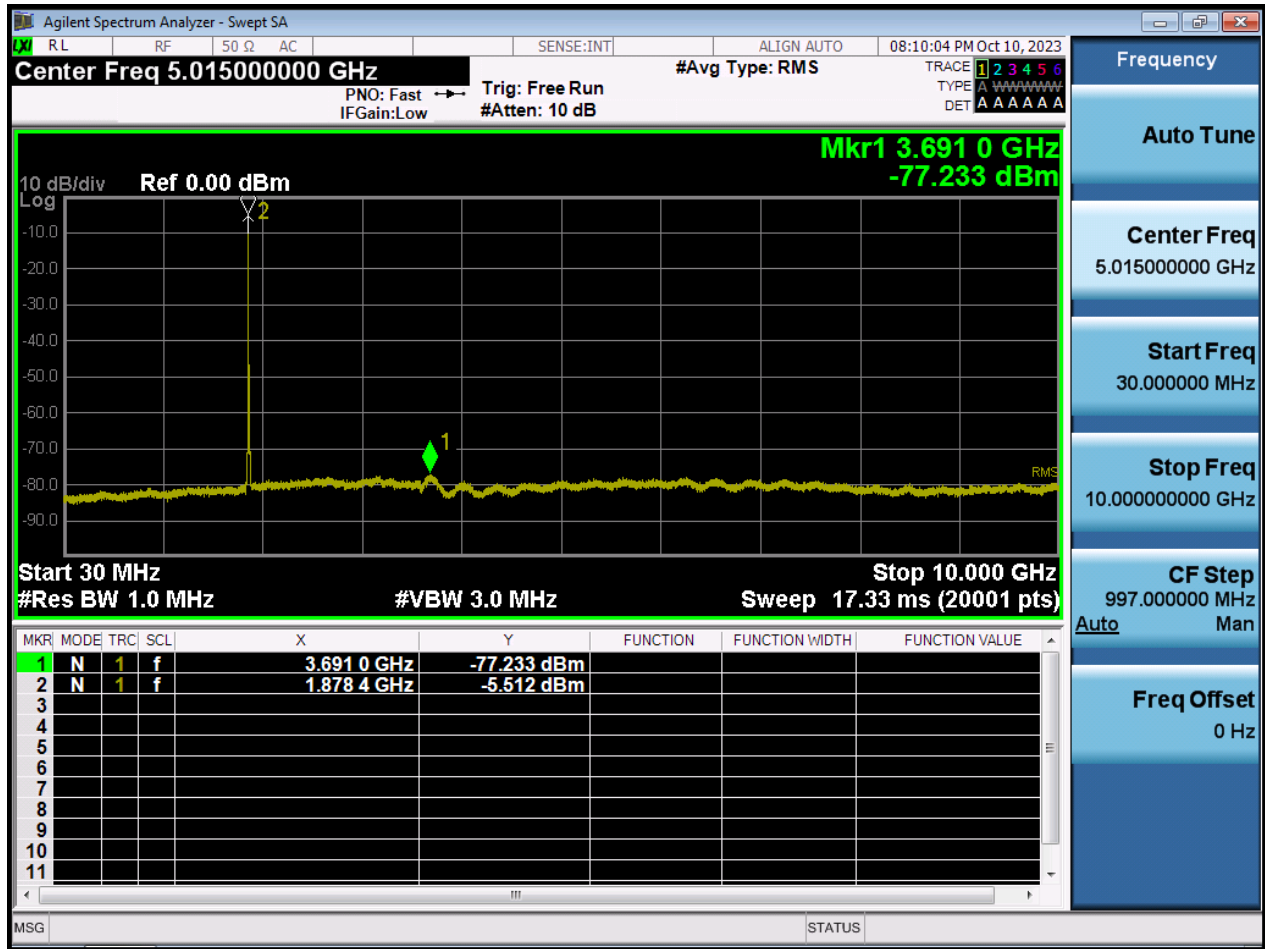
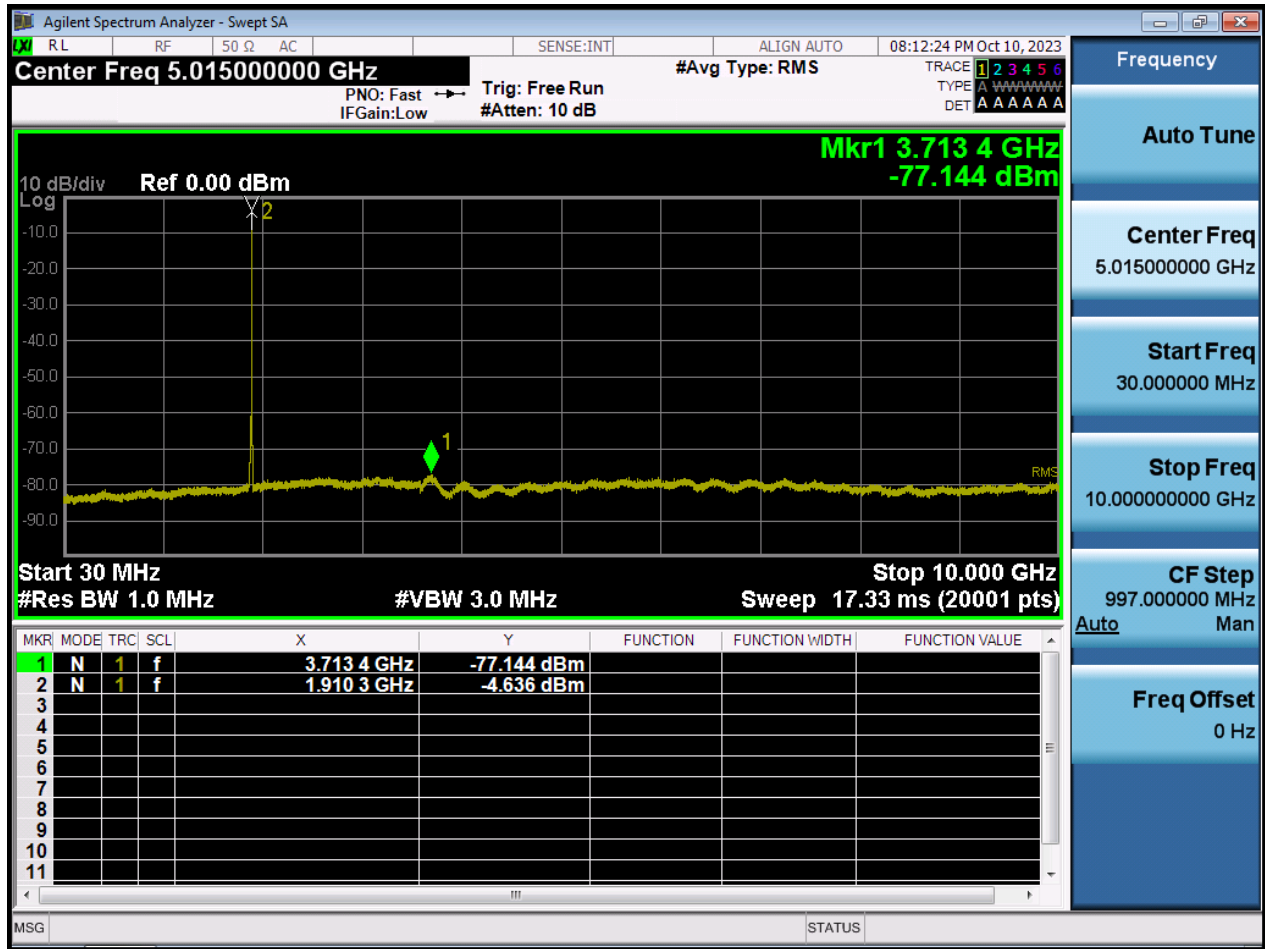


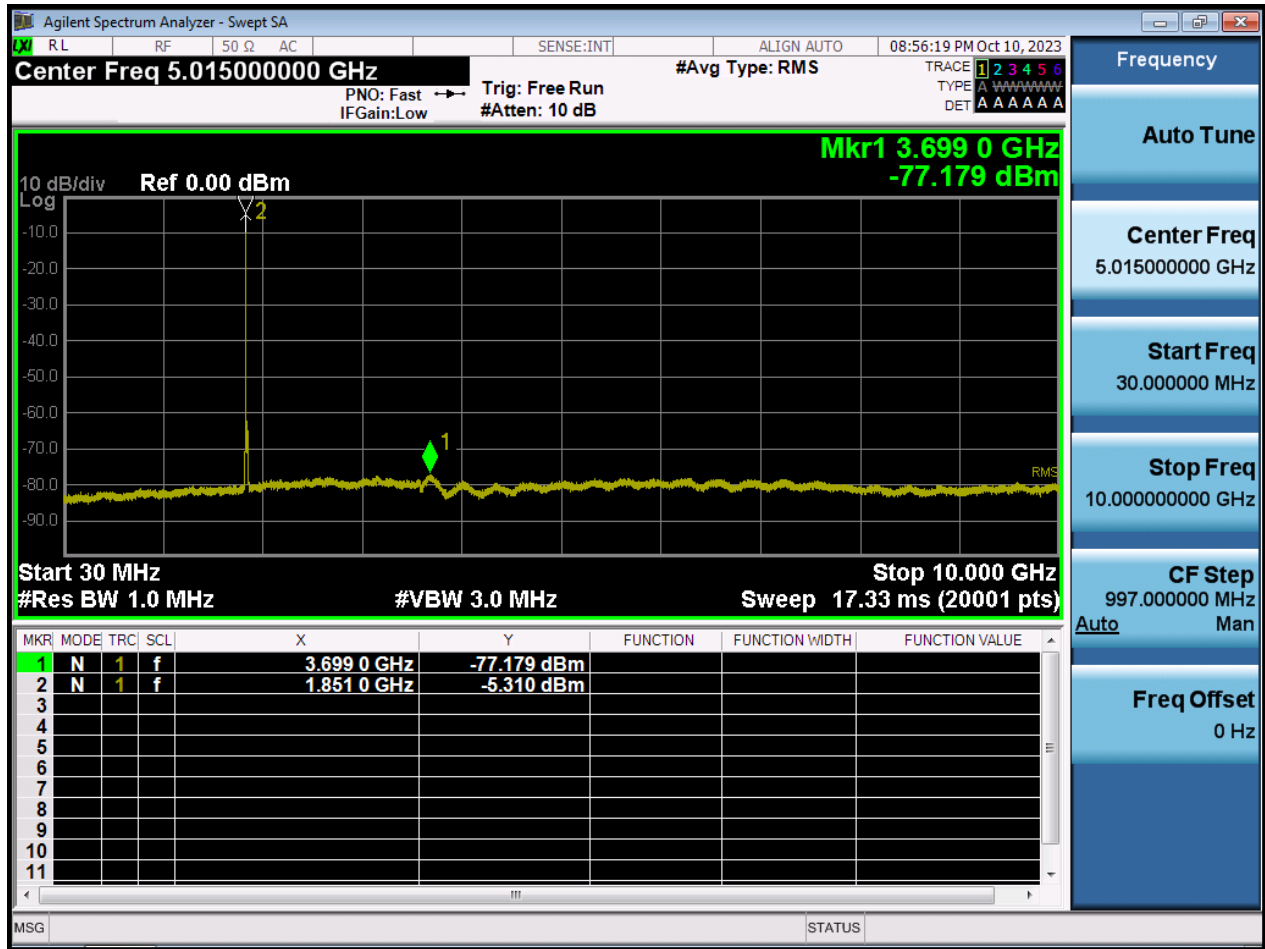
BW5 M_CSE(30 M-10 G)_Middle Channel_QPSK_1RB



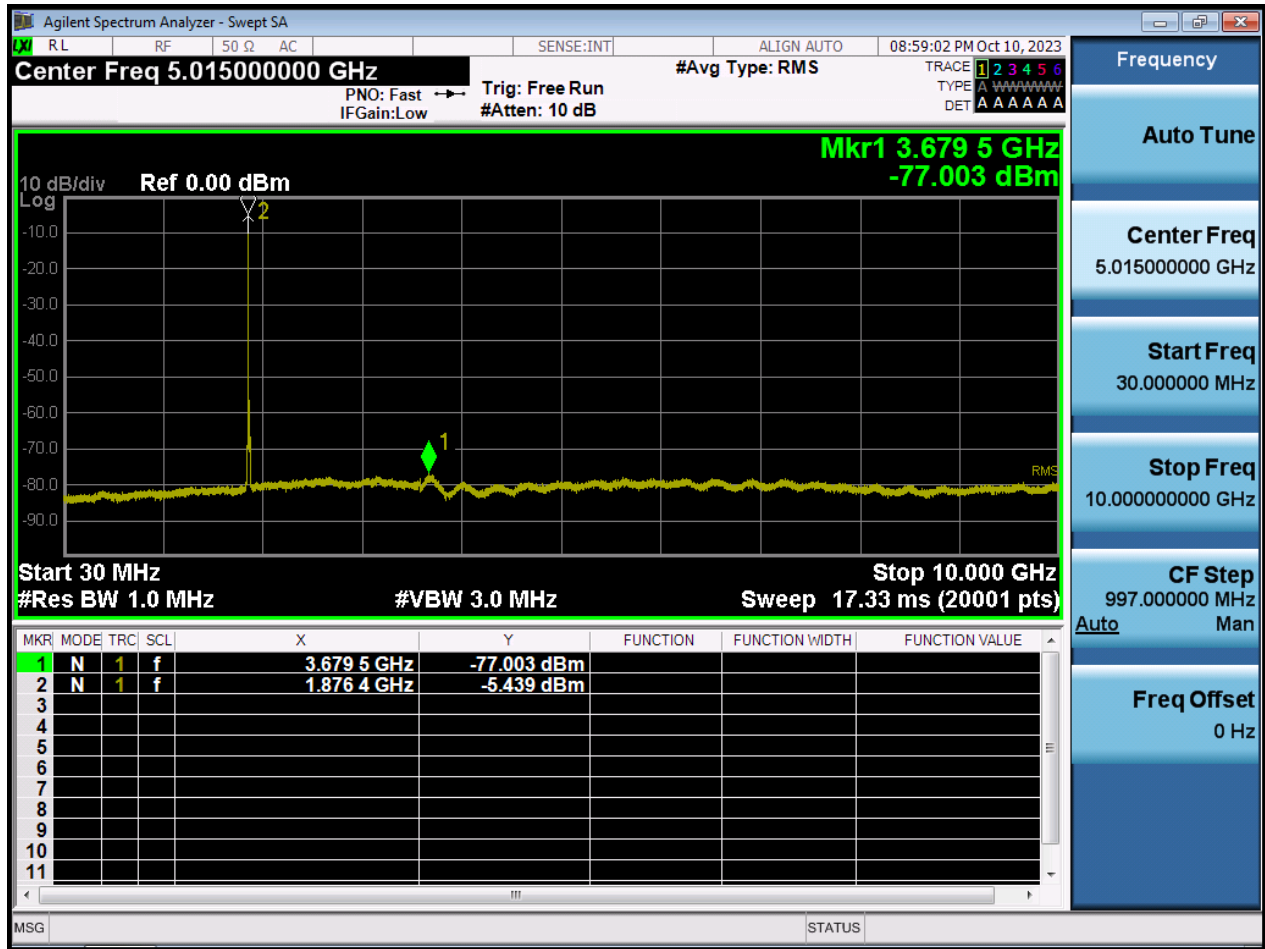
BW5 M_CSE(30 M-10 G)_Highest Channel_QPSK_1RB



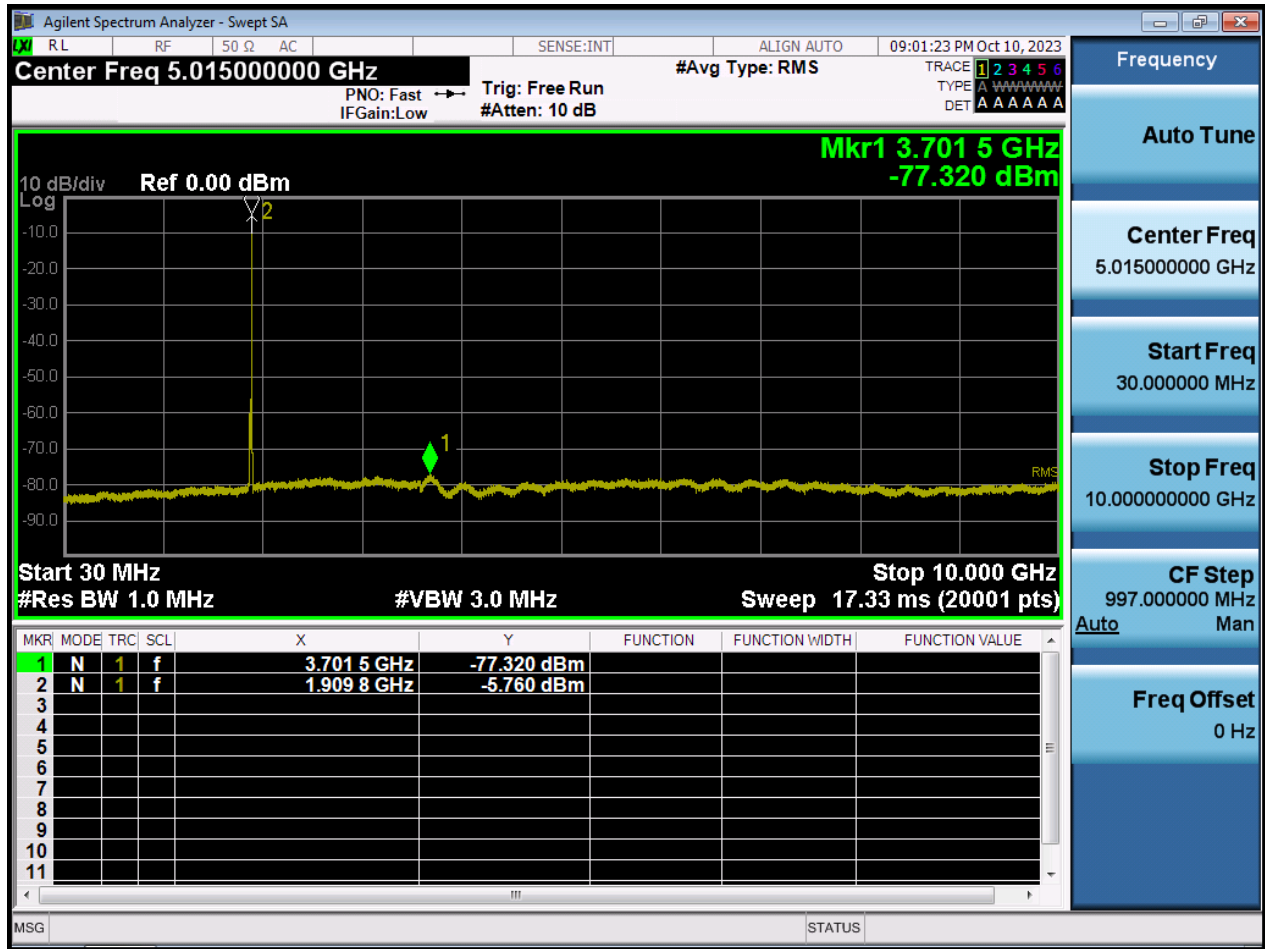
BW10 M_CSE(30 M-10 G)_Lowest Channel_QPSK_1RB



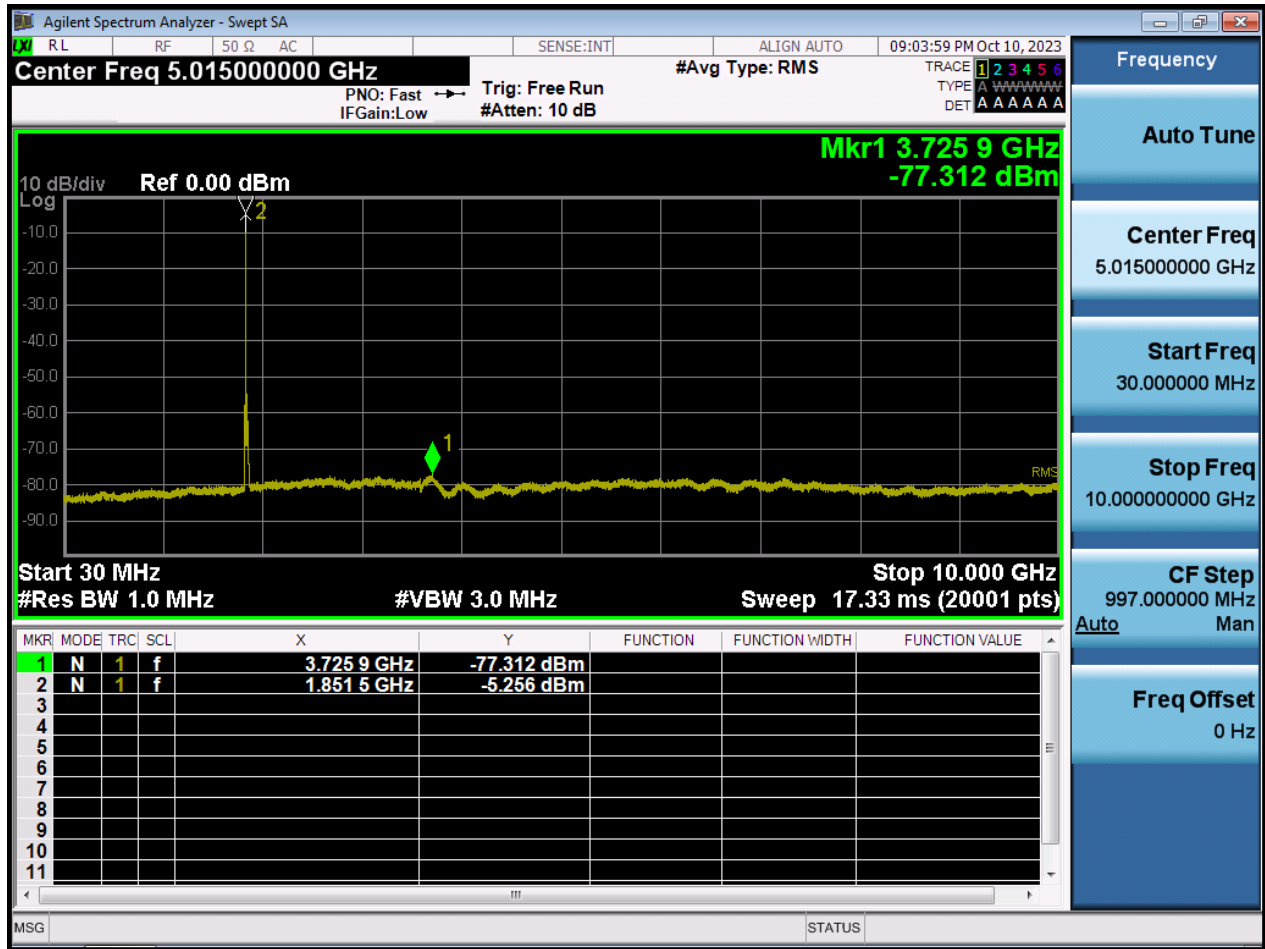
BW10 M_CSE(30 M-10 G)_Middle Channel_QPSK_1RB



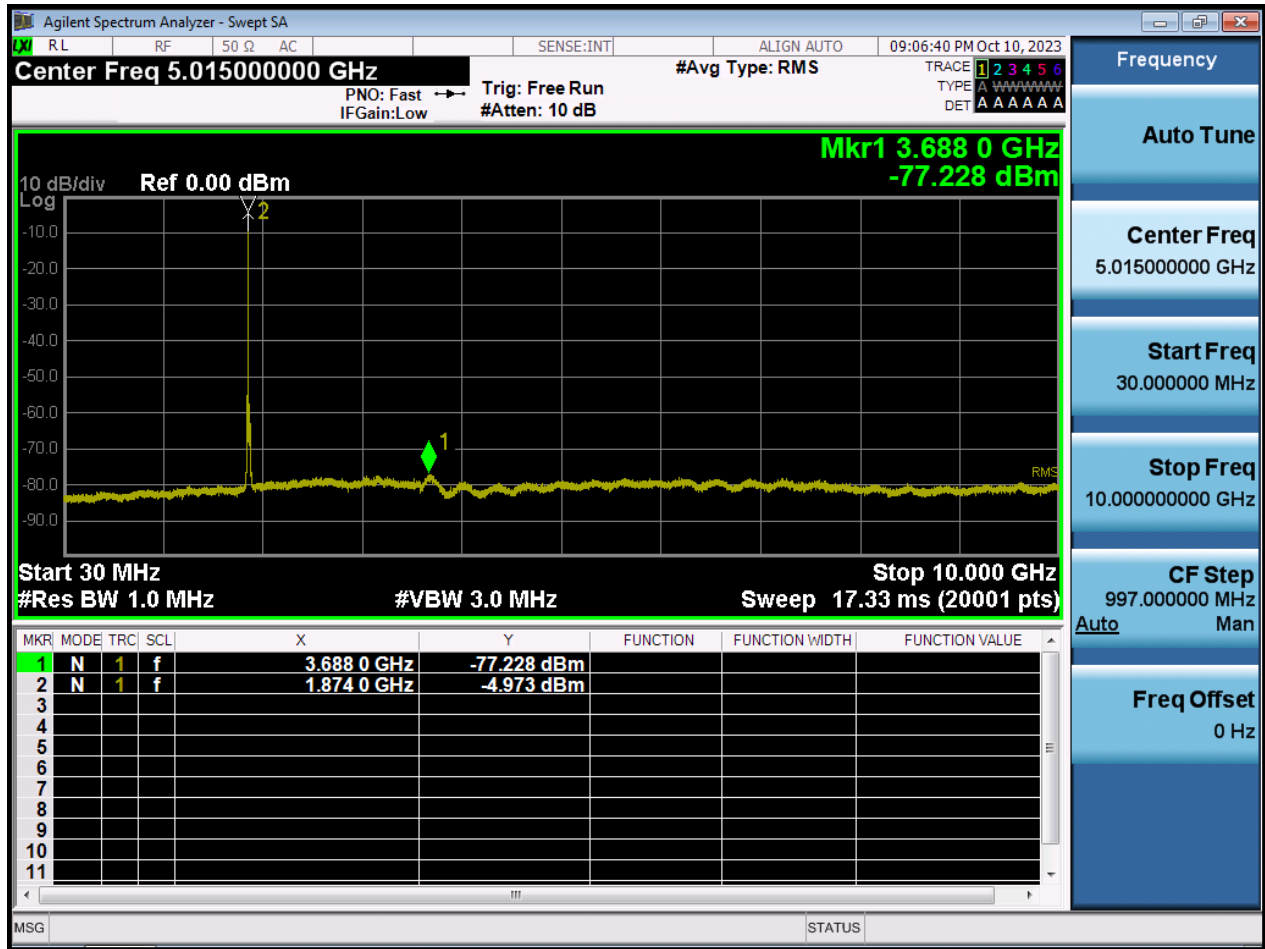
BW10 M_CSE(30 M-10 G)_Highest Channel_QPSK_1RB



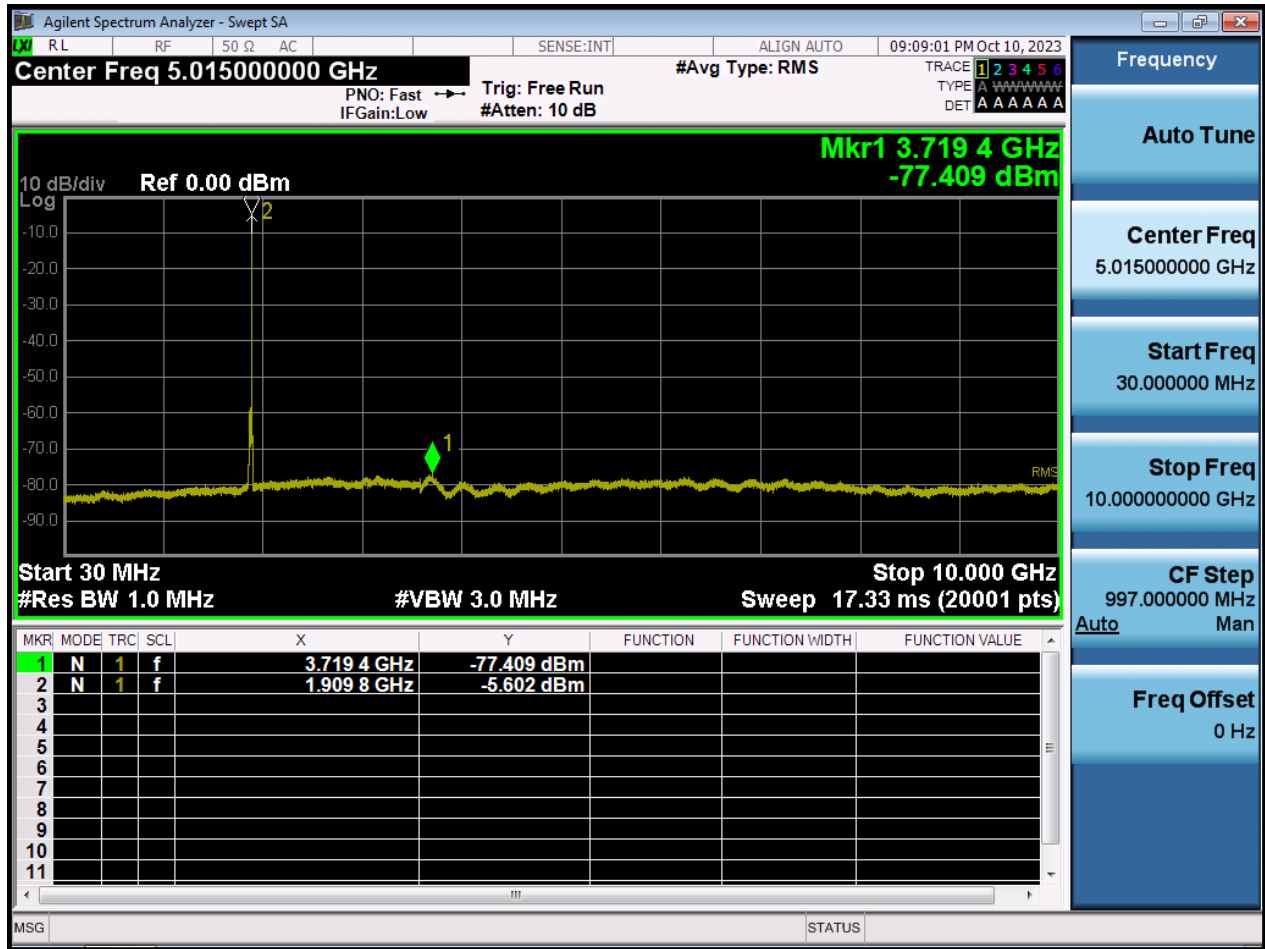
BW15 M_CSE(30 M-10 G)_Lowest Channel_QPSK_1RB



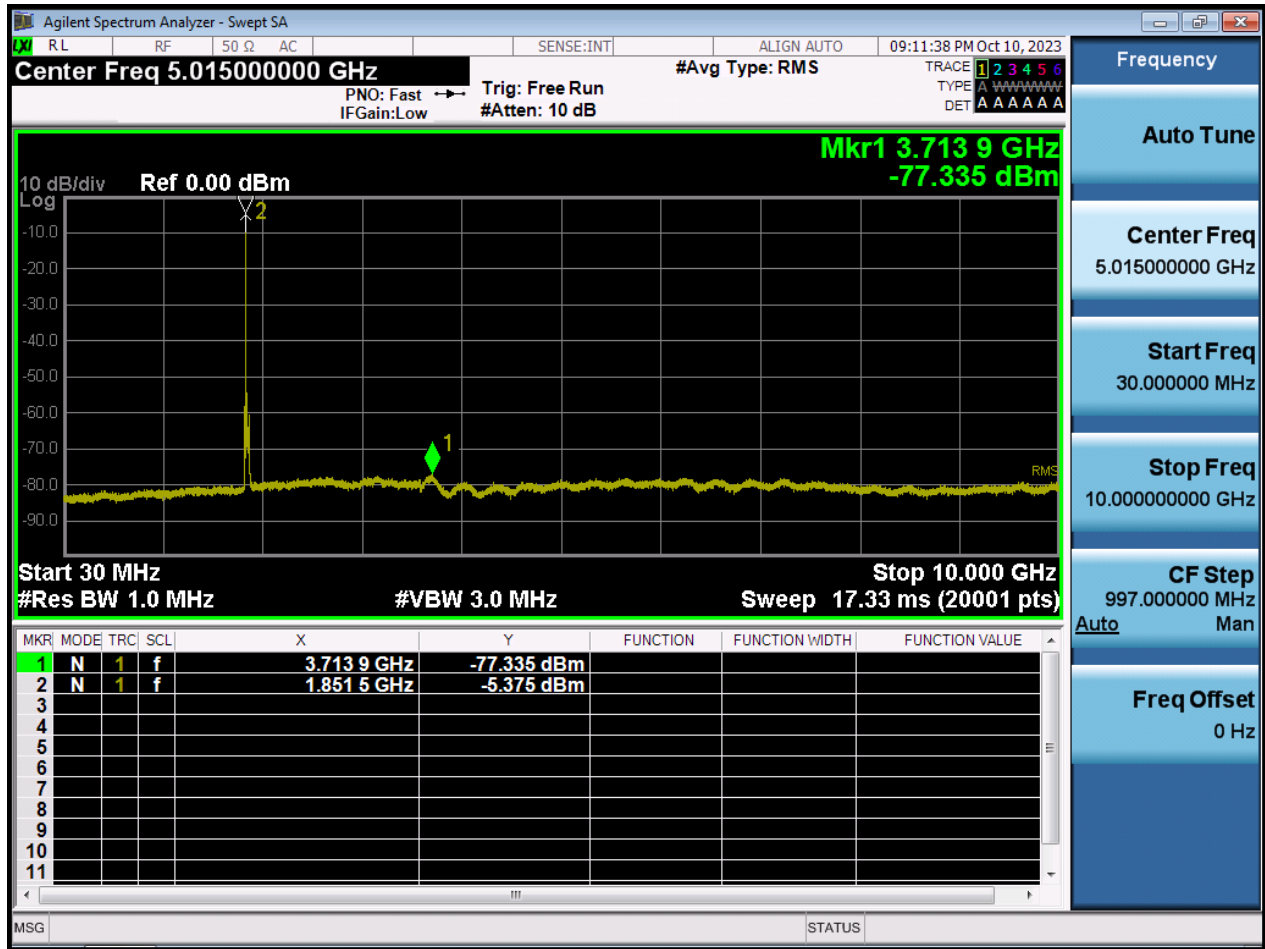
BW15 M_CSE(30 M-10 G)_Middle Channel_QPSK_1RB



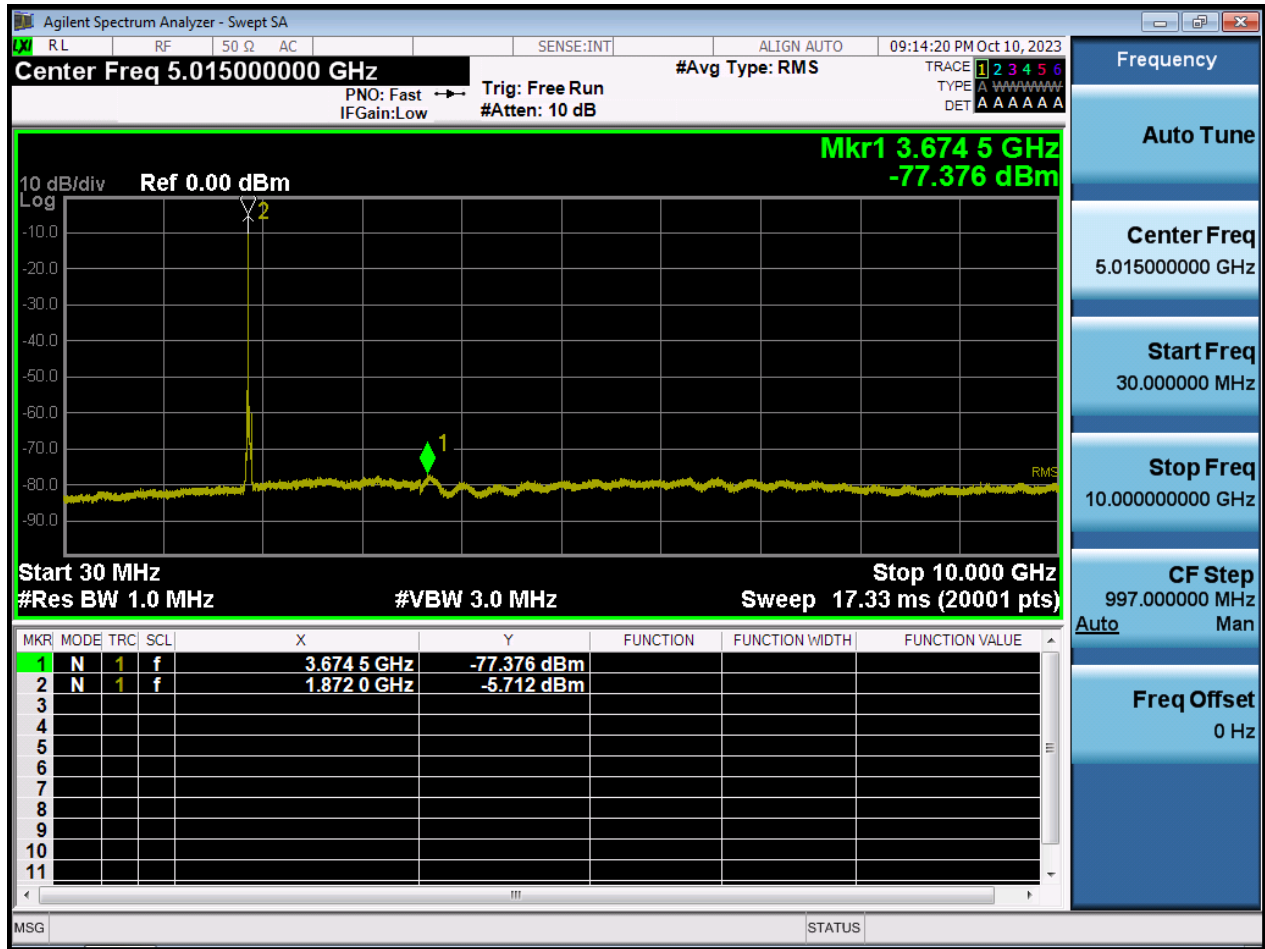
BW15 M_CSE(30 M-10 G)_Highest Channel_QPSK_1RB



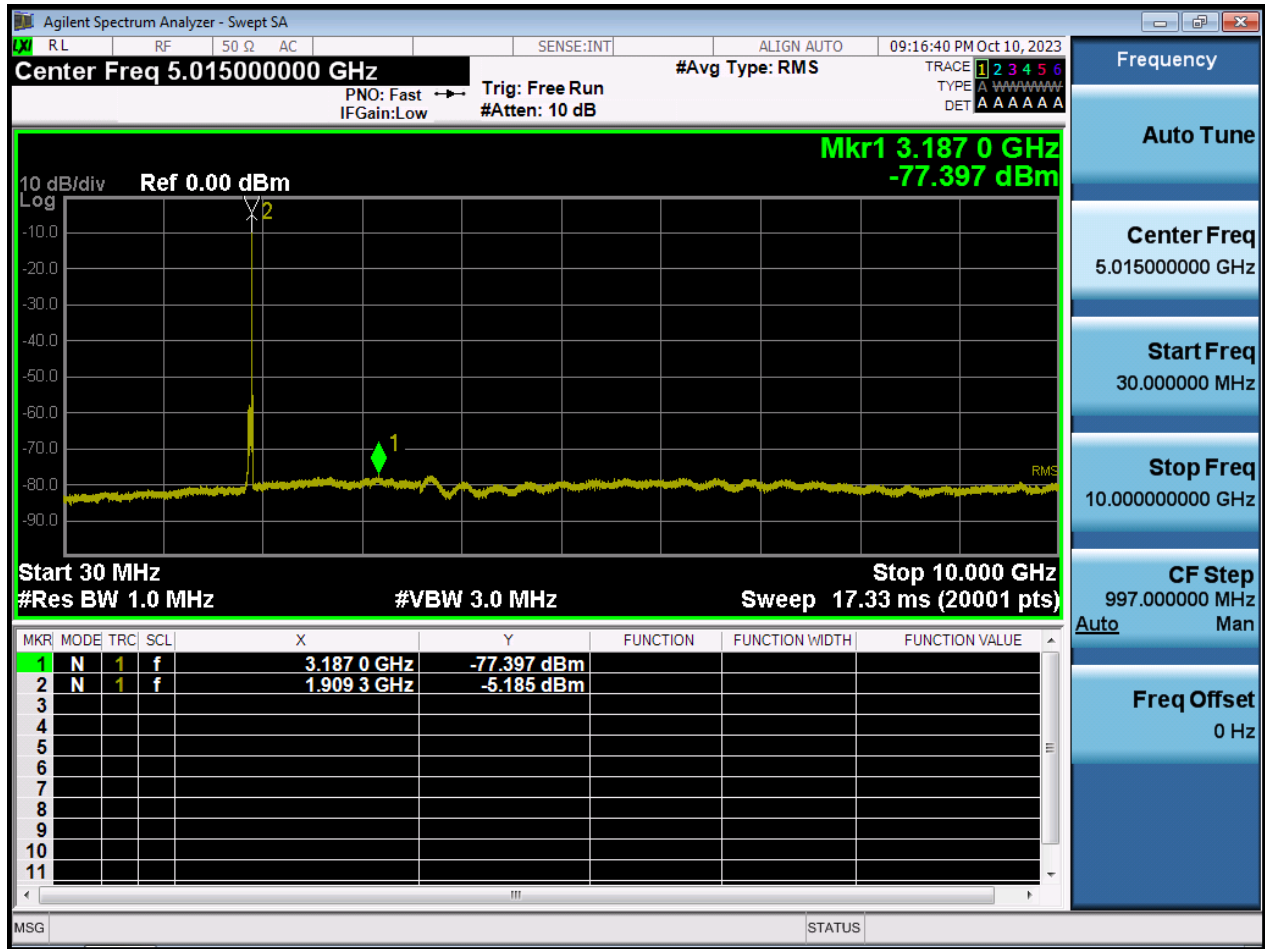
BW20 M_CSE(30 M-10 G)_Lowest Channel_QPSK_1RB



BW20 M_CSE(30 M-10 G)_Middle Channel_QPSK_1RB



BW20 M_CSE(30 M-10 G)_Highest Channel_QPSK_1RB



BW1.4 M_CSE(10 G-20 G)_Lowest Channel_QPSK_1RB



BW1.4 M_CSE(10 G-20 G)_Middle Channel_QPSK_1RB



BW1.4 M_CSE(10 G-20 G)_Highest Channel_QPSK_1RB



BW3 M_CSE(10 G-20 G)_Lowest Channel_QPSK_1RB



BW3 M_CSE(10 G-20 G)_Middle Channel_QPSK_1RB



BW3 M_CSE(10 G-20 G)_Highest Channel_QPSK_1RB



BW5 M_CSE(10 G-20 G)_Lowest Channel_QPSK_1RB



BW5 M_CSE(10 G-20 G)_Middle Channel_QPSK_1RB



BW5 M_CSE(10 G-20 G)_Highest Channel_QPSK_1RB



BW10 M_CSE(10 G-20 G)_Lowest Channel_QPSK_1RB



BW10 M_CSE(10 G-20 G)_Middle Channel_QPSK_1RB



BW10 M_CSE(10 G-20 G)_Highest Channel_QPSK_1RB



BW15 M_CSE(10 G-20 G)_Lowest Channel_QPSK_1RB



BW15 M_CSE(10 G-20 G)_Middle Channel_QPSK_1RB



BW15 M_CSE(10 G-20 G)_Highest Channel_QPSK_1RB



BW20 M_CSE(10 G-20 G)_Lowest Channel_QPSK_1RB



BW20 M_CSE(10 G-20 G)_Middle Channel_QPSK_1RB



BW20 M_CSE(10 G-20 G)_Highest Channel_QPSK_1RB

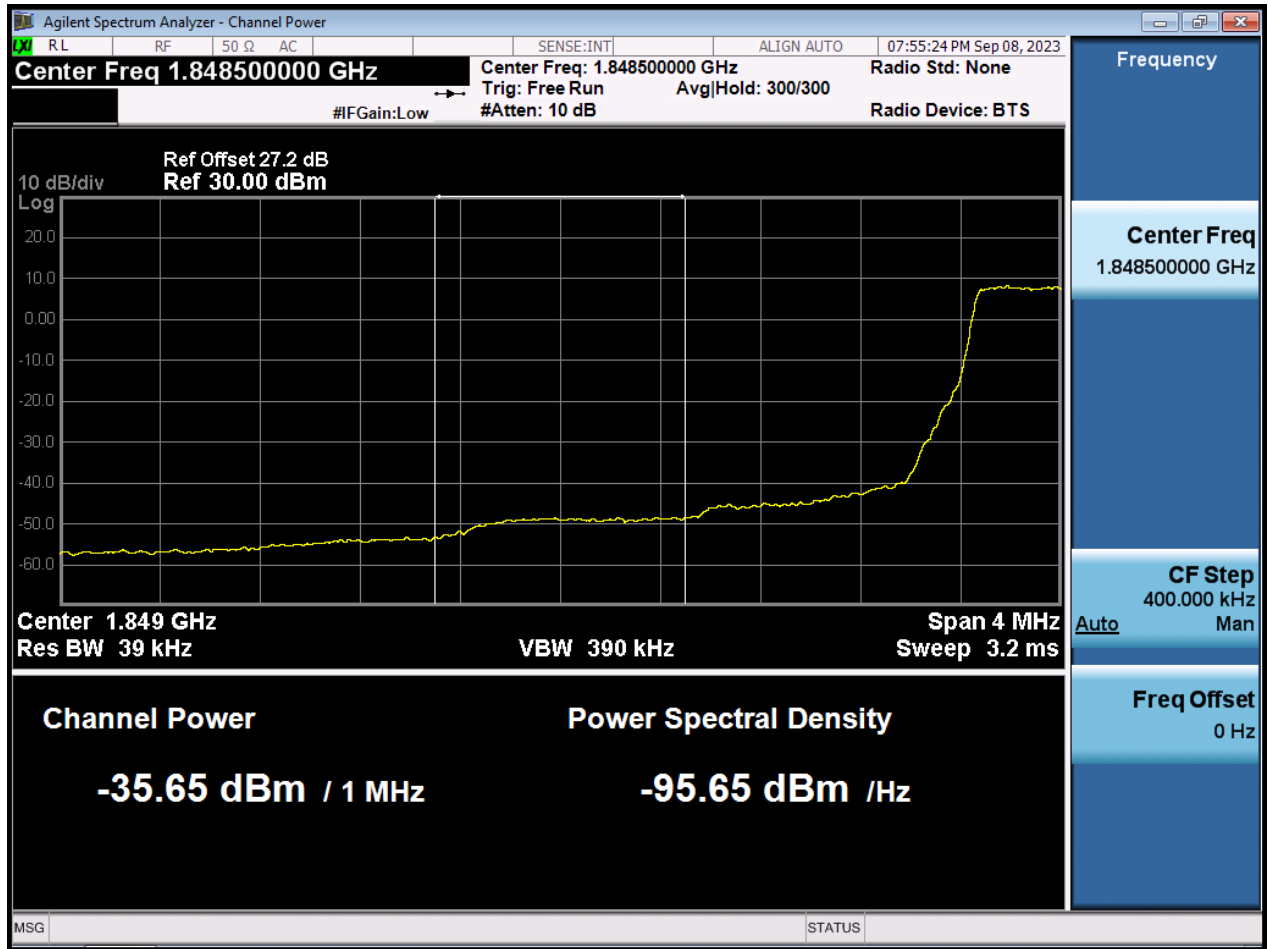


11. TEST PLOTS(Sub 2 Ant)

BW1.4 M_BandEdge_Lowest Channel_QPSK_Full RB(1)



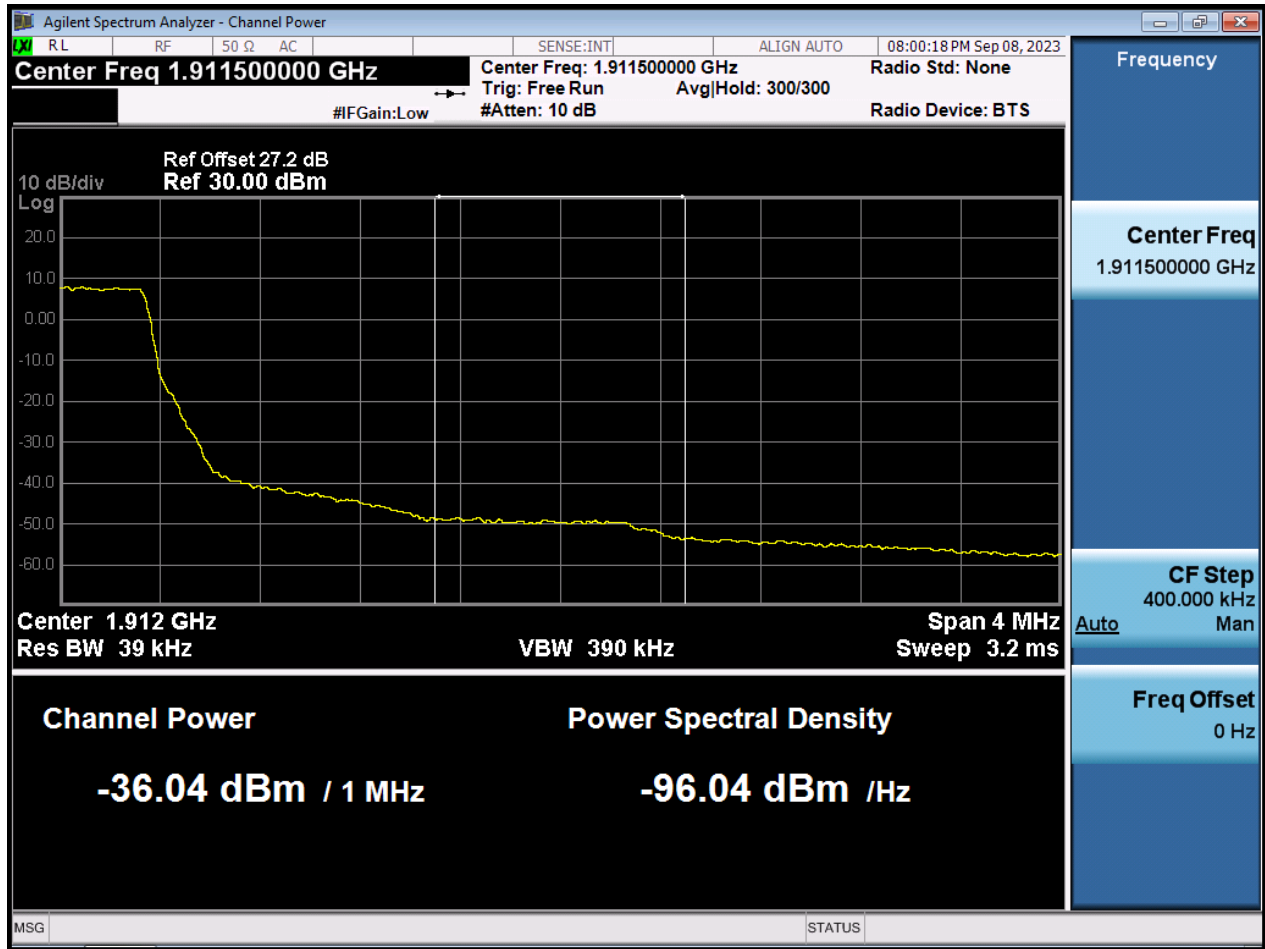
BW1.4 M_BandEdge_Lowest Channel_QPSK_Full RB(2)



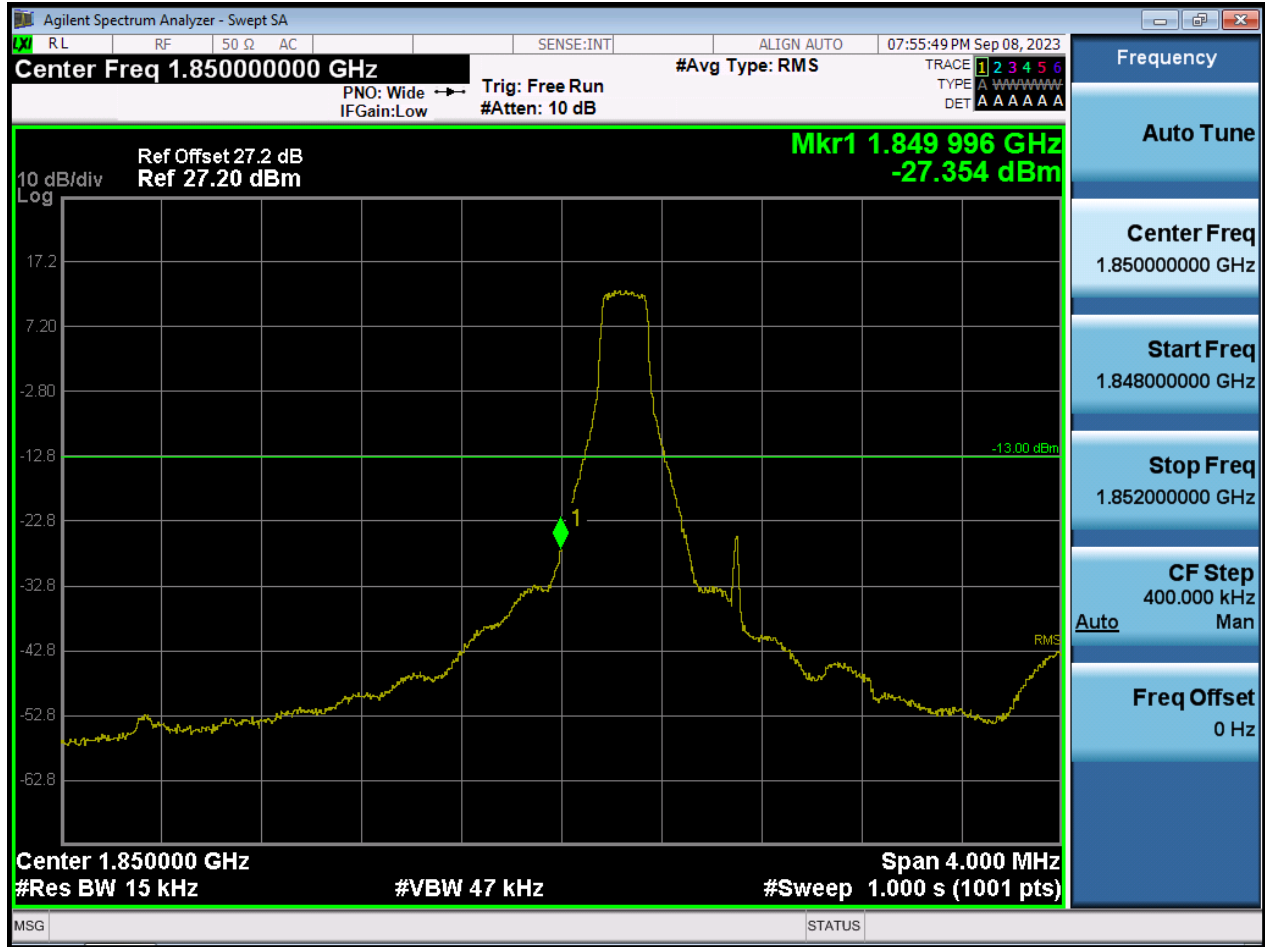
BW1.4 M_BandEdge_Highest Channel_QPSK_Full RB(1)



BW1.4 M_BandEdge_Highest Channel_QPSK_Full RB(2)



BW1.4 M_BandEdge_Lowest Channel_QPSK_1RB



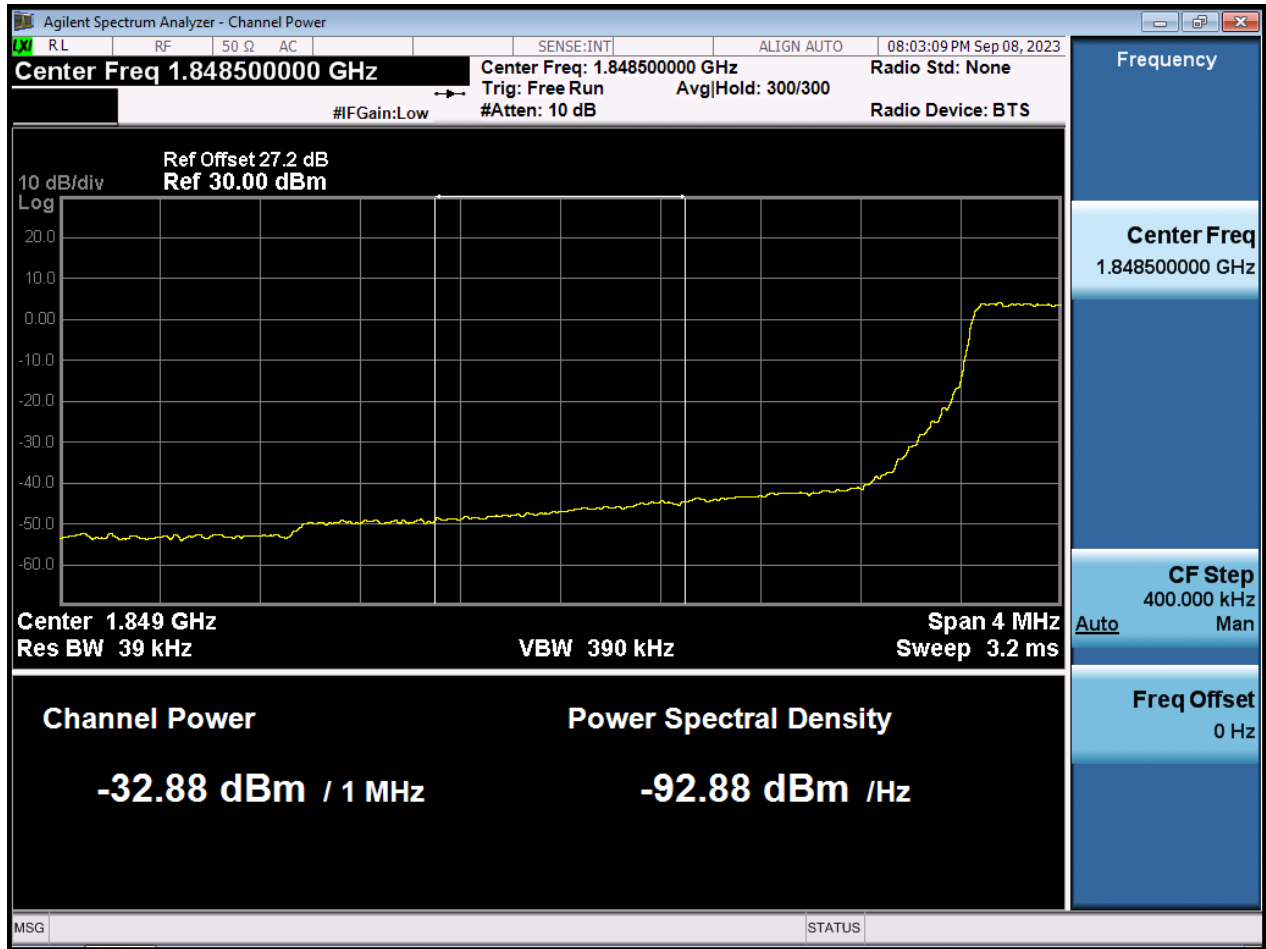
BW1.4 M_BandEdge_Highest Channel_QPSK_1RB



BW3 M_BandEdge_Lowest Channel_QPSK_Full RB(1)



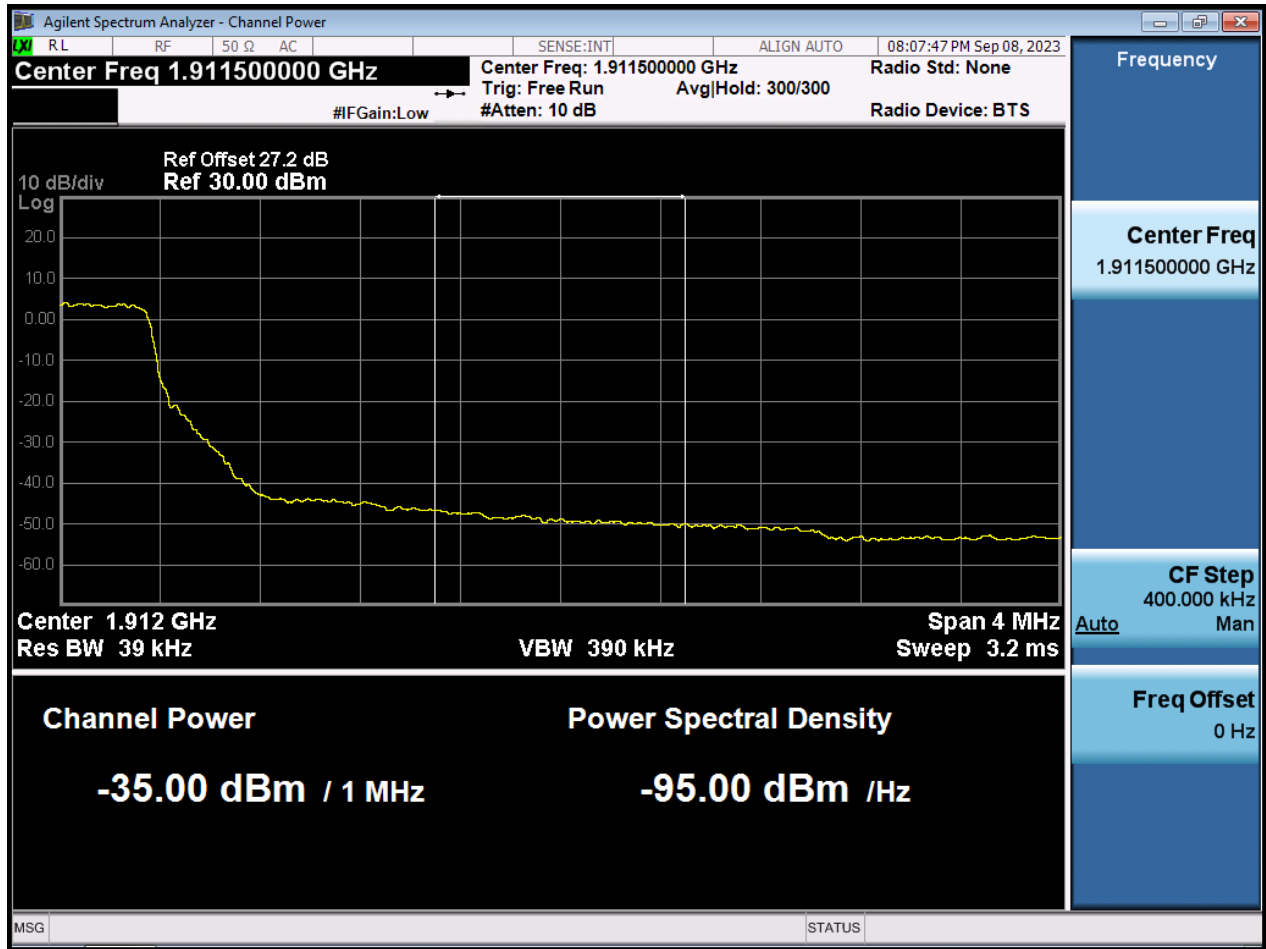
BW3 M_BandEdge_Lowest Channel_QPSK_Full RB(2)



BW3 M_BandEdge_Highest Channel_QPSK_Full RB(1)



BW3 M_BandEdge_Highest Channel_QPSK_Full RB(2)



BW3 M_BandEdge_Lowest Channel_QPSK_1RB



BW3 M_BandEdge_Highest Channel_QPSK_1RB



BW5 M_BandEdge_Lowest Channel_QPSK_Full RB(1)



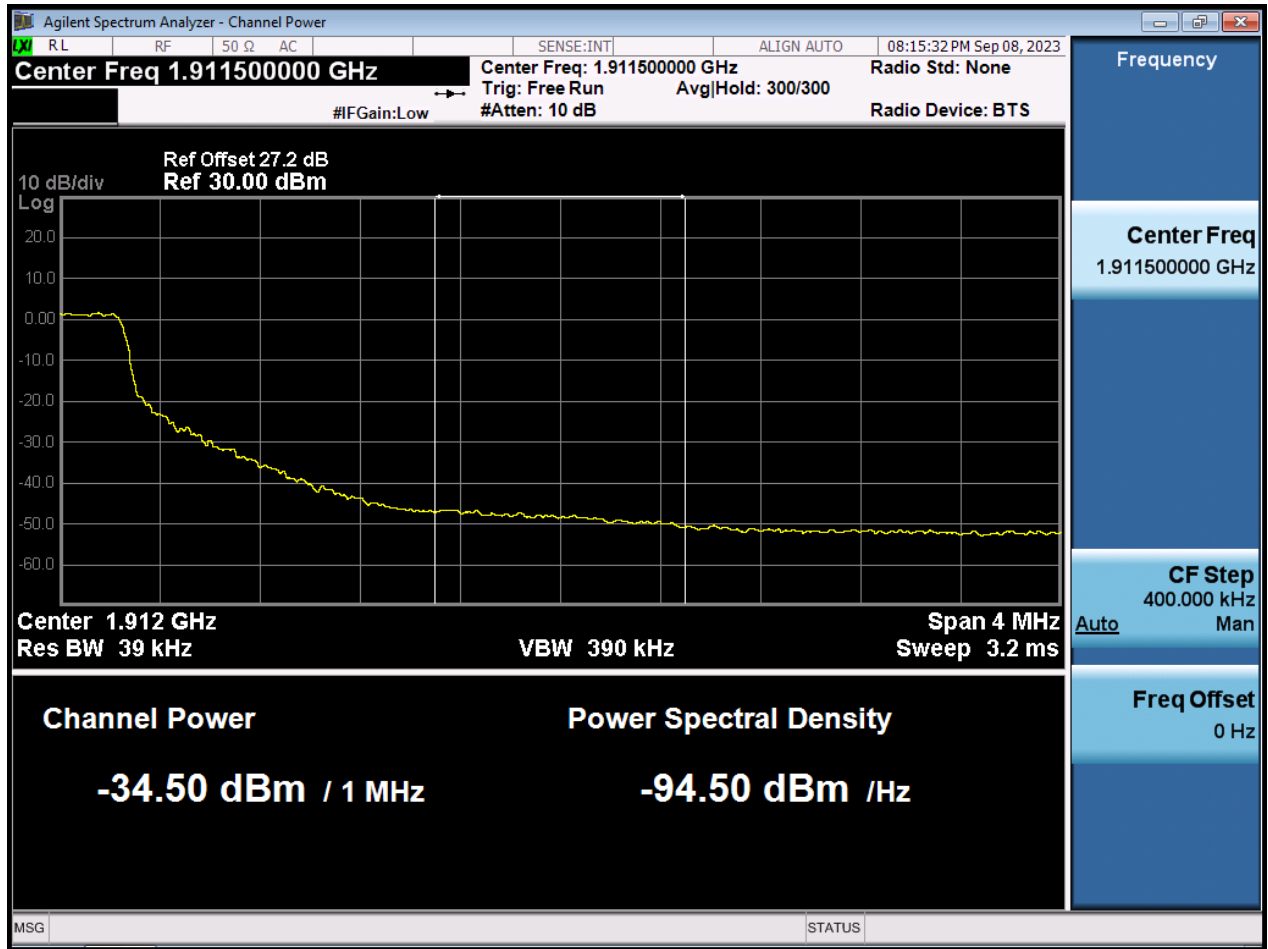
BW5 M_BandEdge_Lowest Channel_QPSK_Full RB(2)



BW5 M_BandEdge_Highest Channel_QPSK_Full RB(1)



BW5 M_BandEdge_Highest Channel_QPSK_Full RB(2)



BW5 M_BandEdge_Lowest Channel_QPSK_1RB



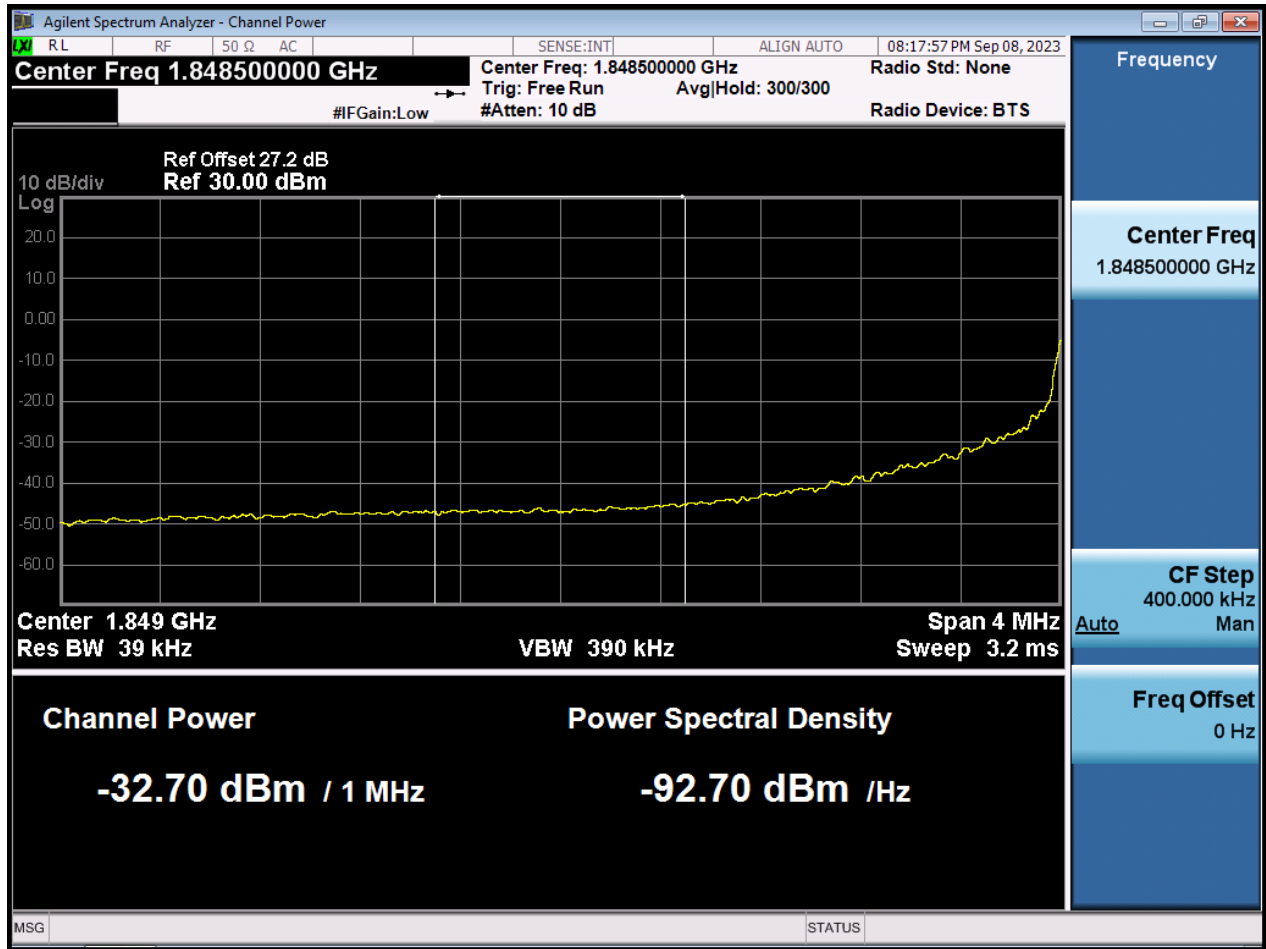
BW5 M_BandEdge_Highest Channel_QPSK_1RB



BW10 M_BandEdge_Lowest Channel_QPSK_Full RB(1)



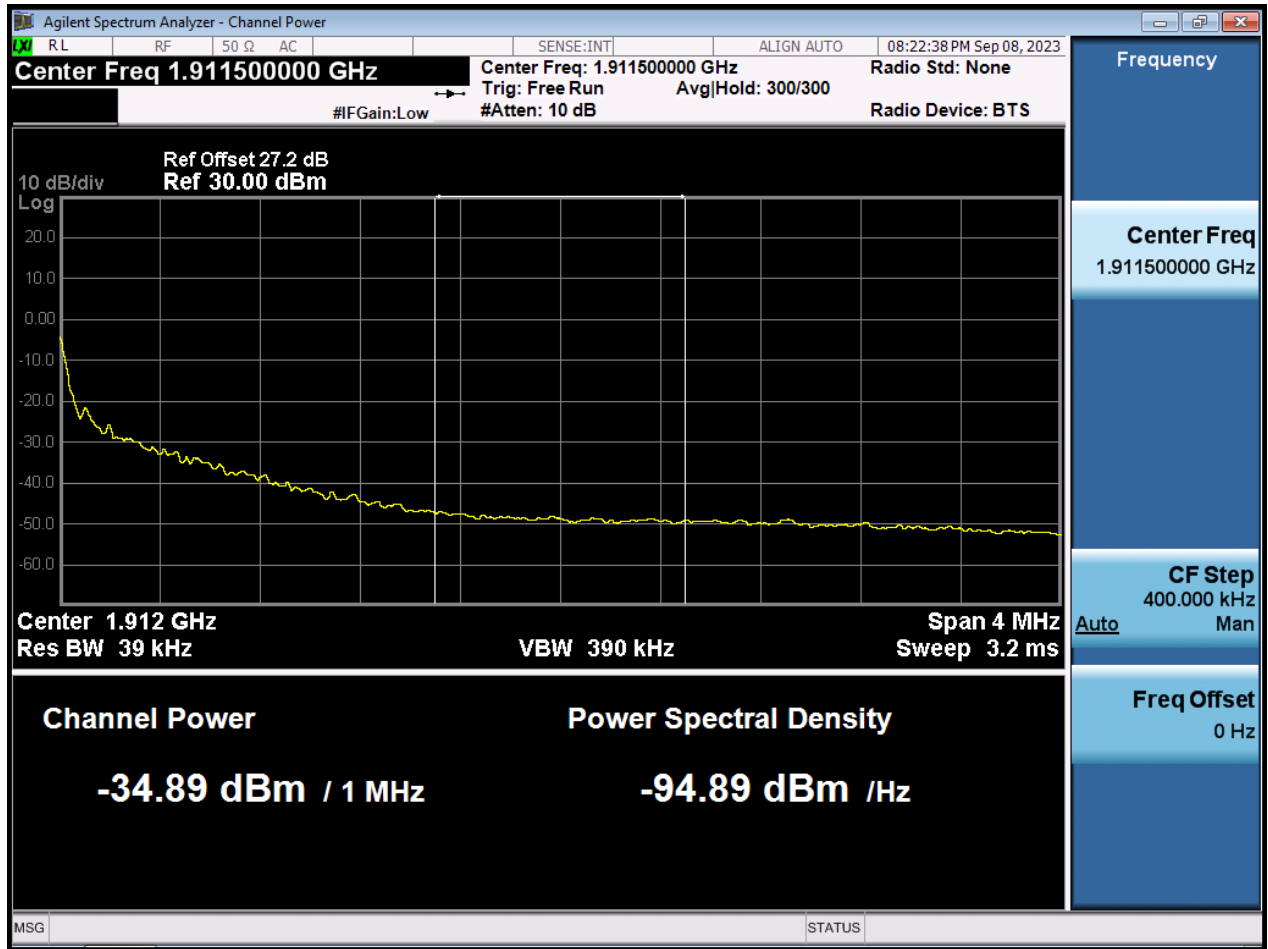
BW10 M_BandEdge_Lowest Channel_QPSK_Full RB(2)



BW10 M_BandEdge_Highest Channel_QPSK_Full RB(1)



BW10 M_BandEdge_Highest Channel_QPSK_Full RB(2)



BW10 M_BandEdge_Lowest Channel_QPSK_1RB



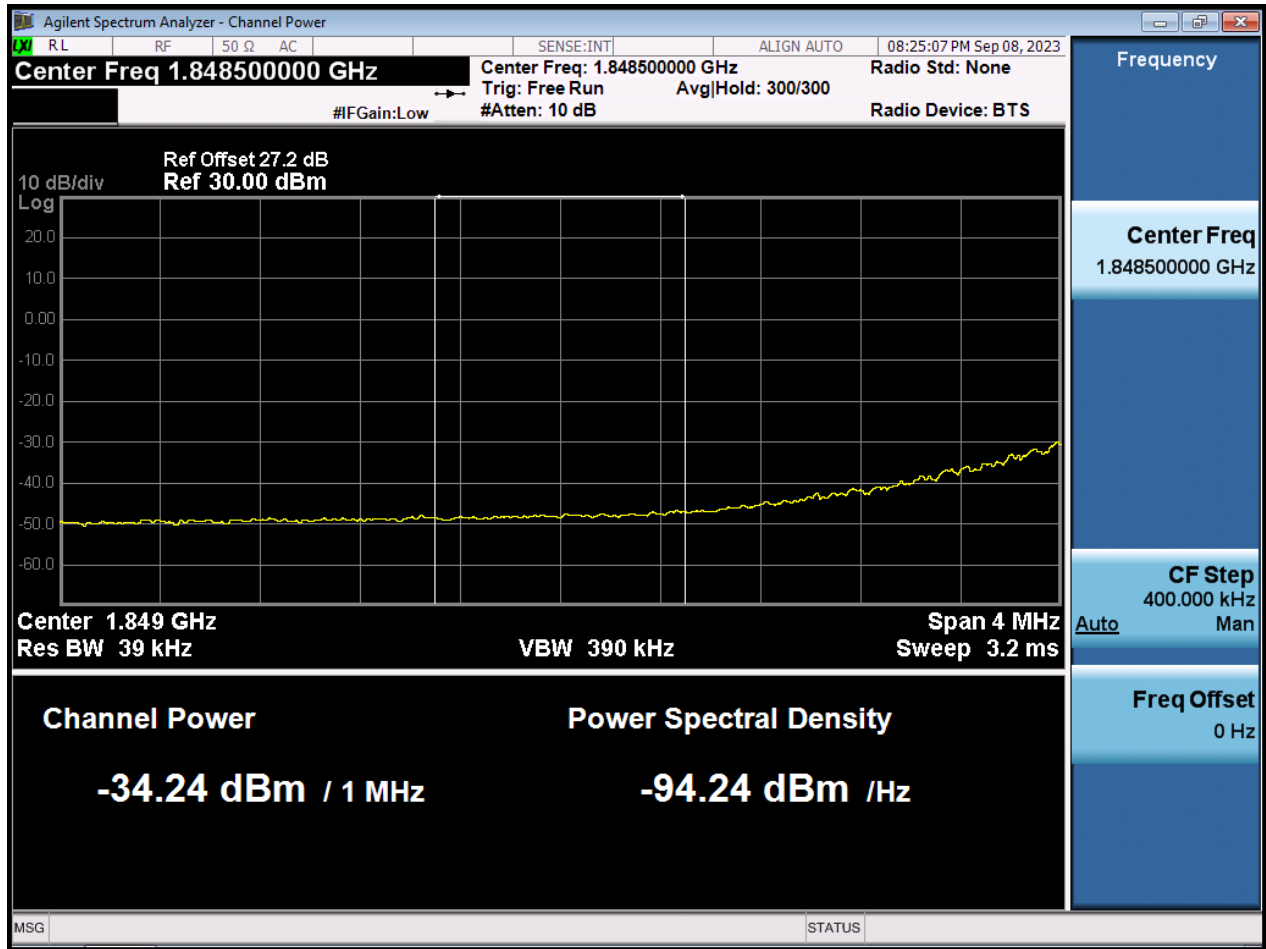
BW10 M_BandEdge_Highest Channel_QPSK_1RB



BW15 M_BandEdge_Lowest Channel_QPSK_Full RB(1)



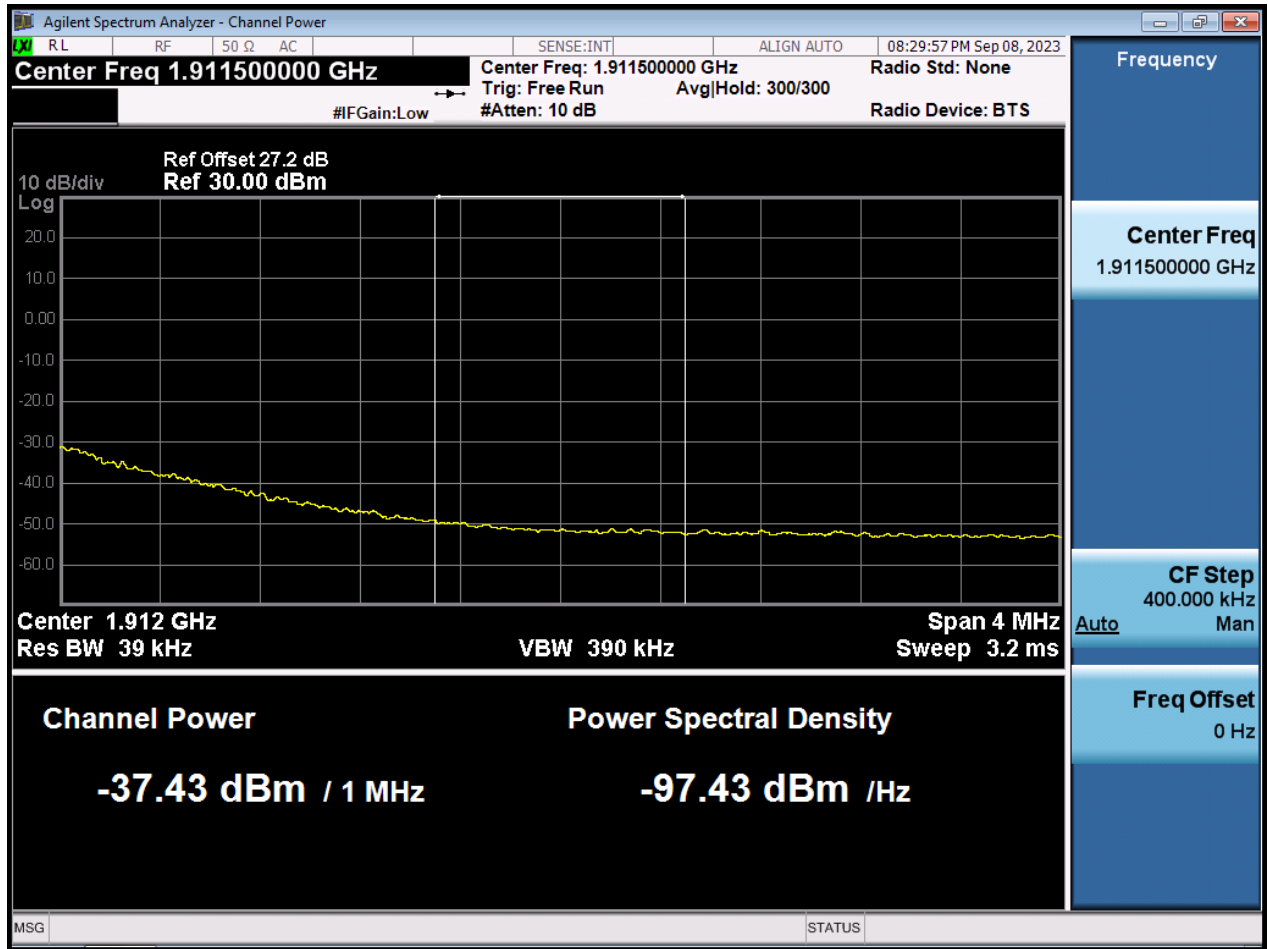
BW15 M_BandEdge_Lowest Channel_QPSK_Full RB(2)



BW15 M_BandEdge_Highest Channel_QPSK_Full RB(1)



BW15 M_BandEdge_Highest Channel_QPSK_Full RB(2)



BW15 M_BandEdge_Lowest Channel_QPSK_1RB



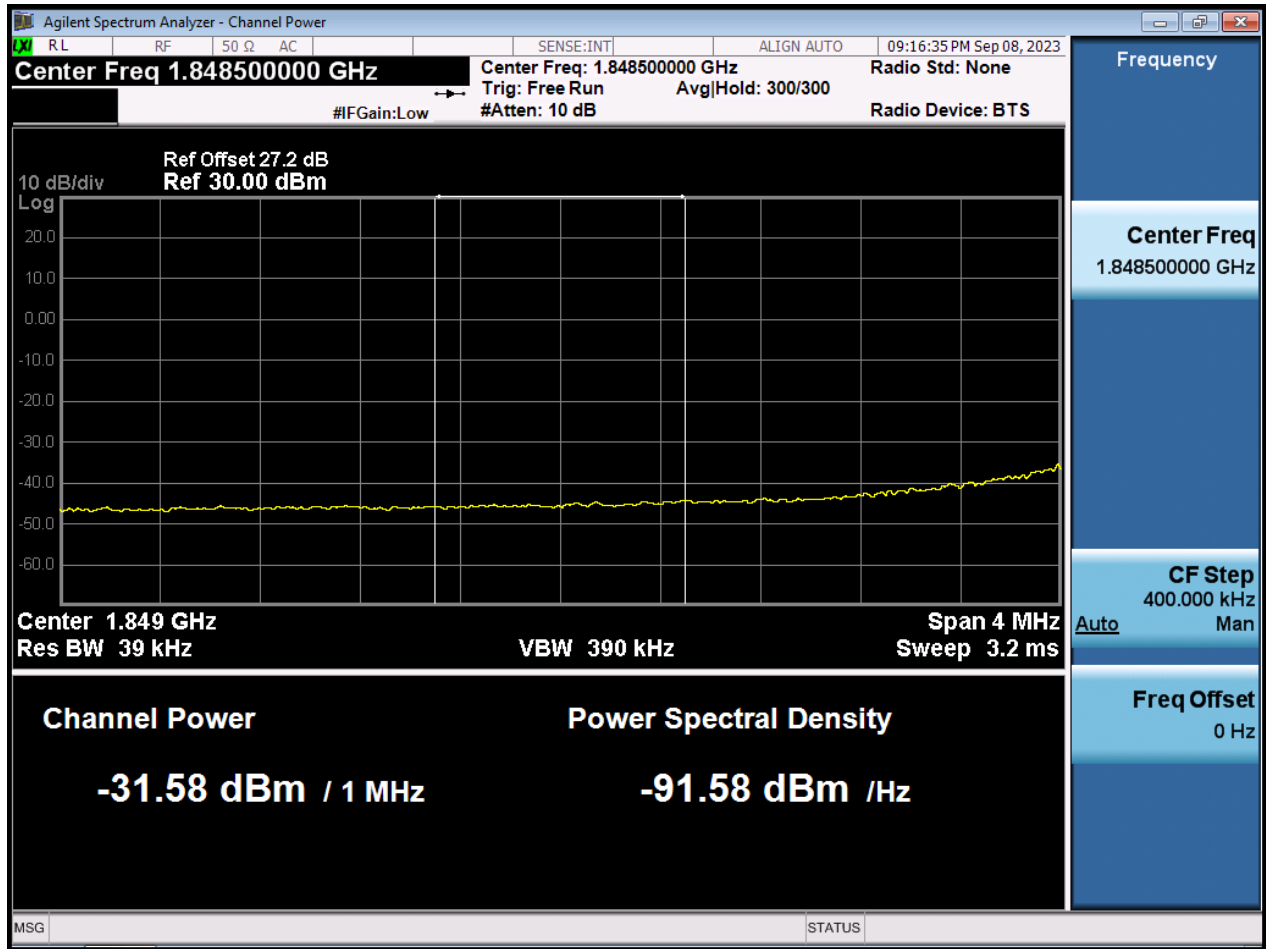
BW15 M_BandEdge_Highest Channel_QPSK_1RB



BW20 M_BandEdge_Lowest Channel_QPSK_Full RB(1)



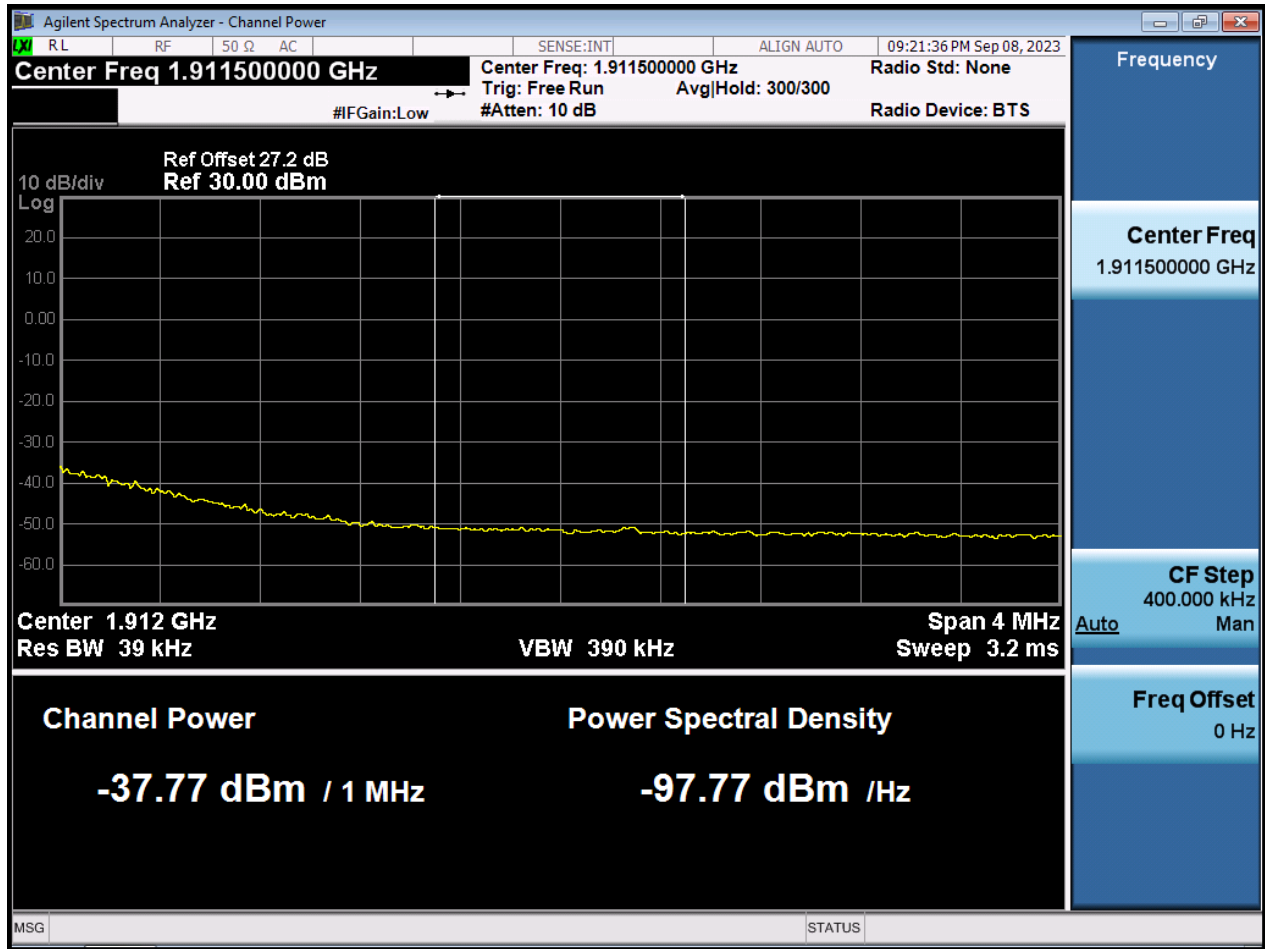
BW20 M_BandEdge_Lowest Channel_QPSK_Full RB(2)



BW20 M_BandEdge_Highest Channel_QPSK_Full RB(1)



BW20 M_BandEdge_Highest Channel_QPSK_Full RB(2)



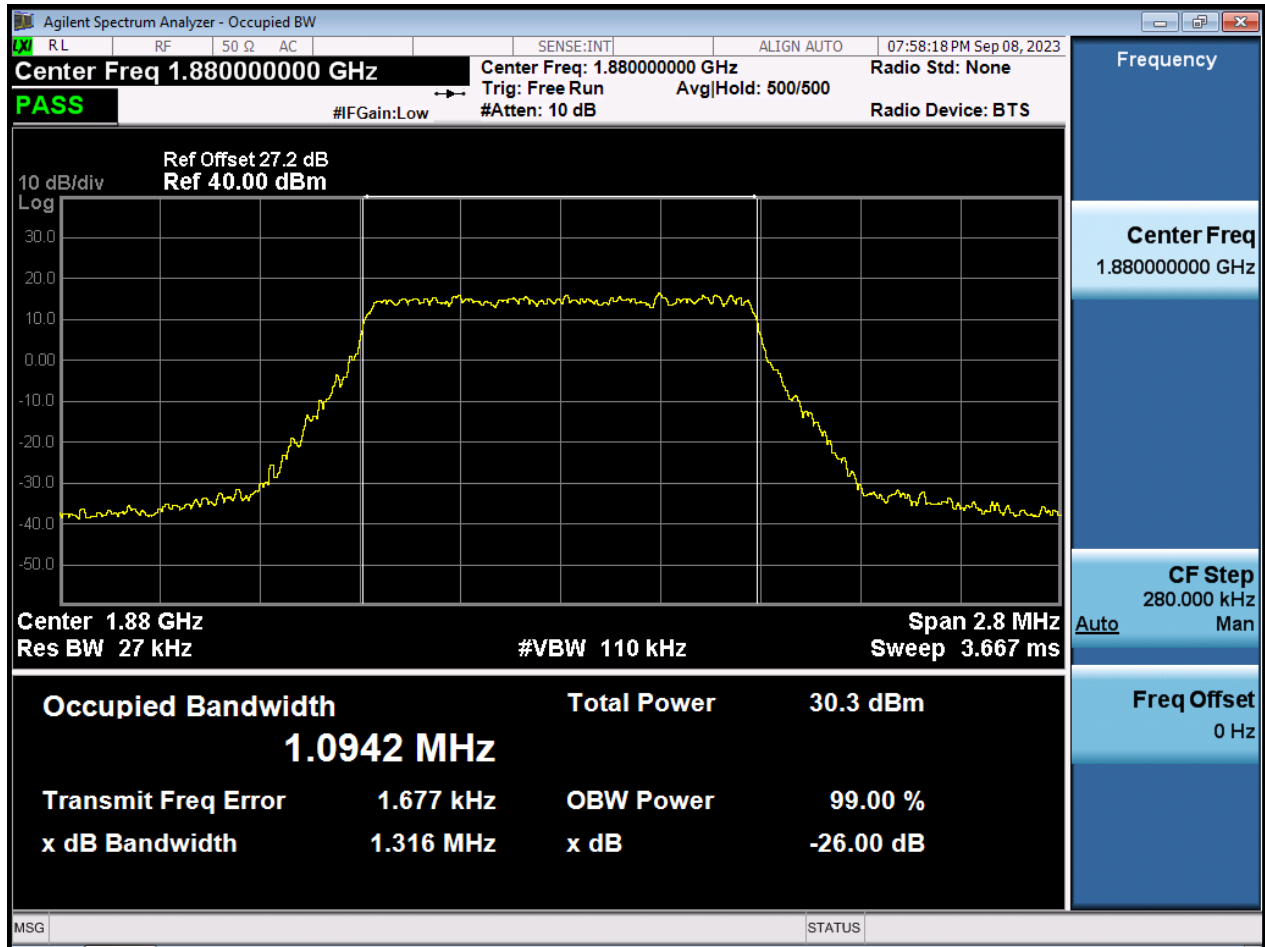
BW20 M_BandEdge_Lowest Channel_QPSK_1RB



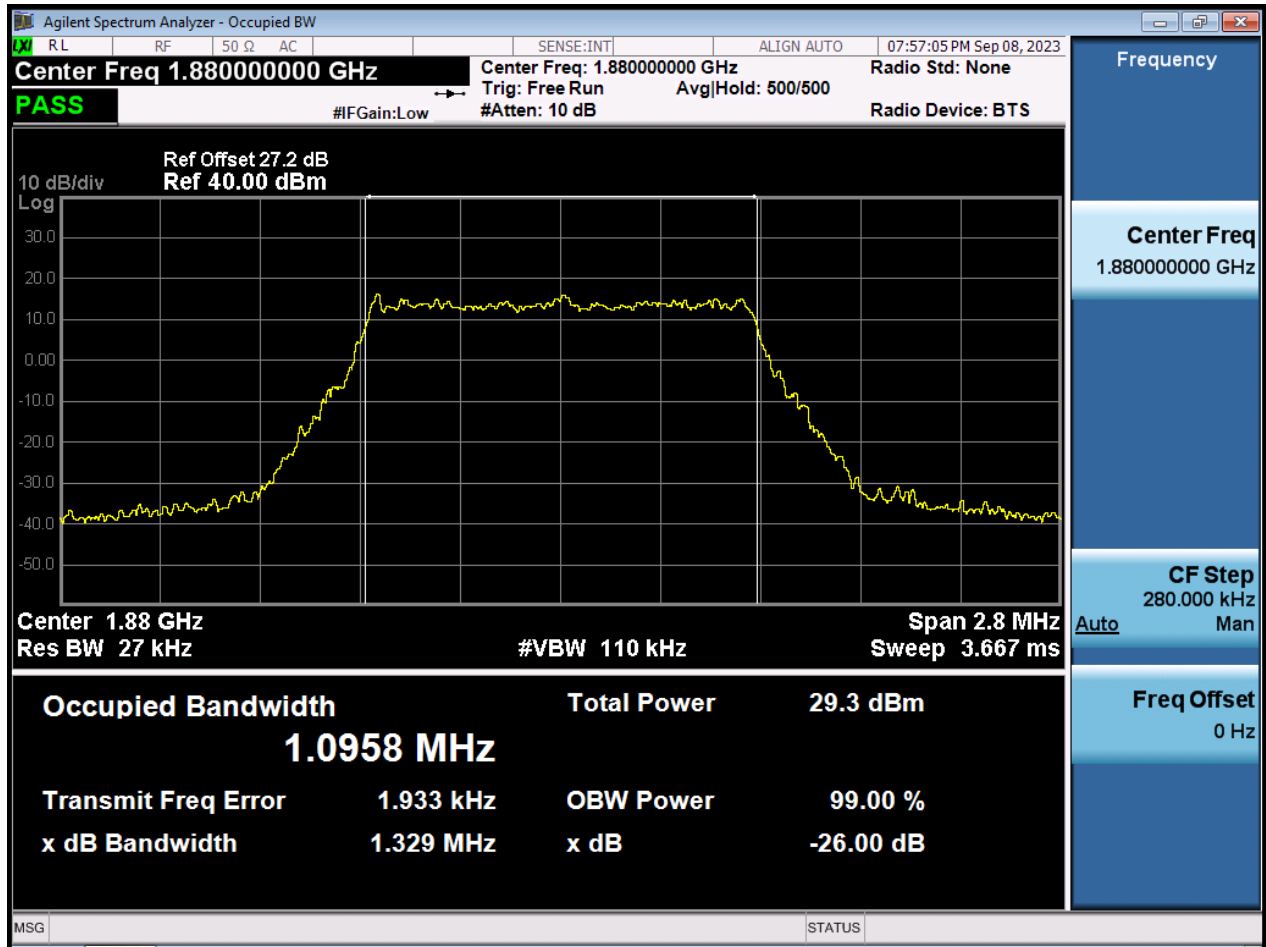
BW20 M_BandEdge_Highest Channel_QPSK_1RB



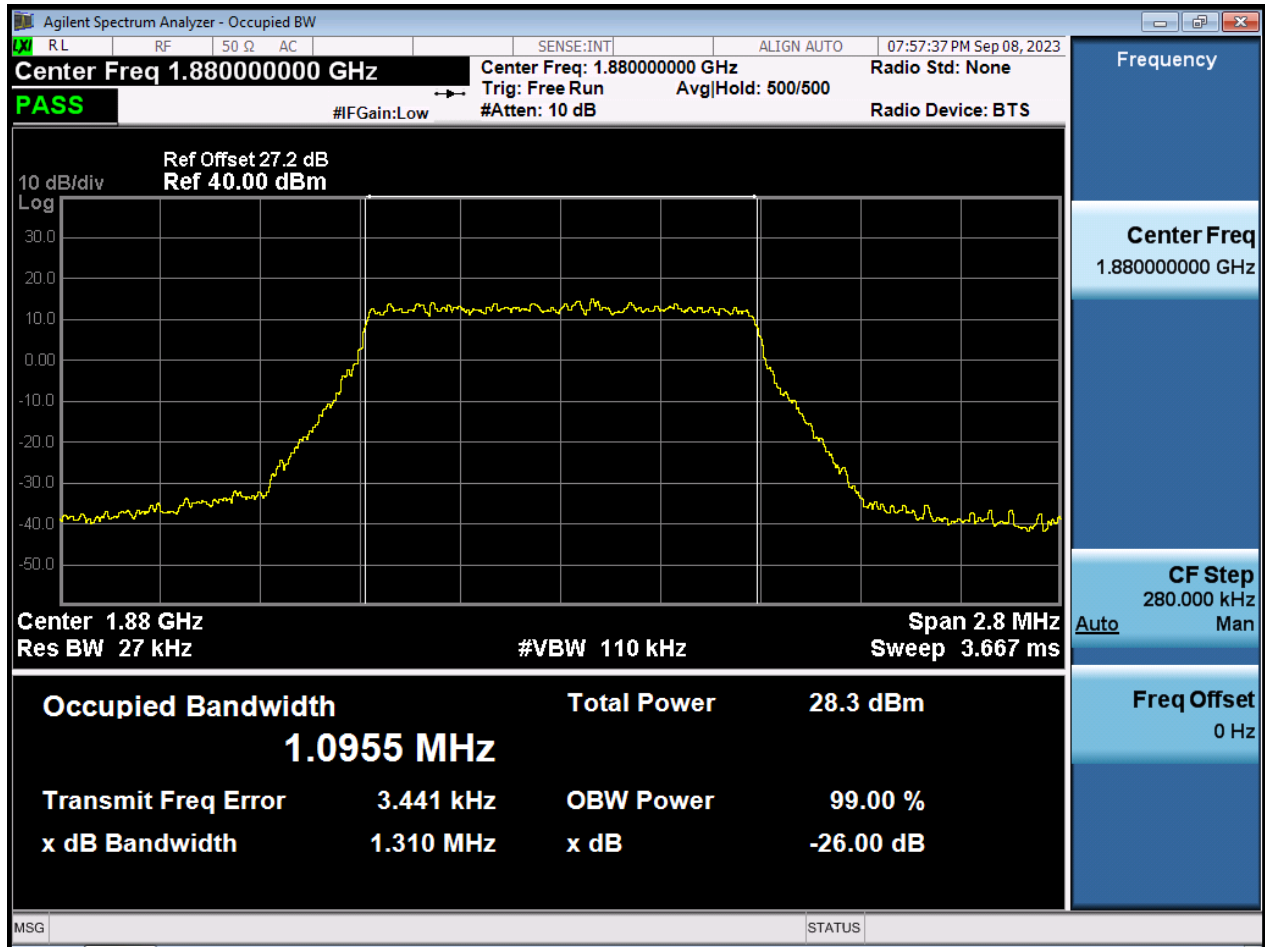
BW1.4 M_OBW_Middle Channel_QPSK_Full RB



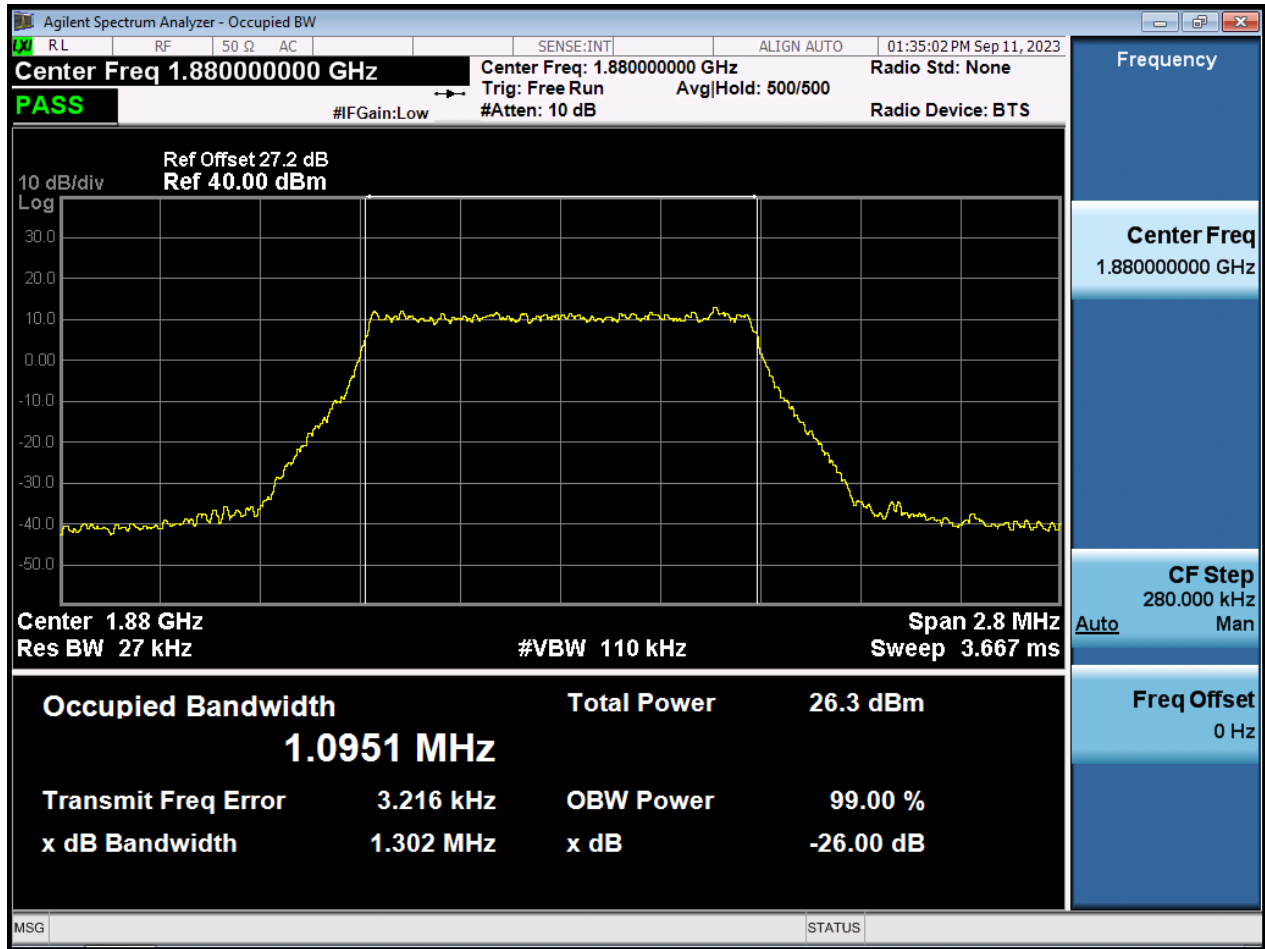
BW1.4 M_OBW_Middle Channel_16QAM_Full RB



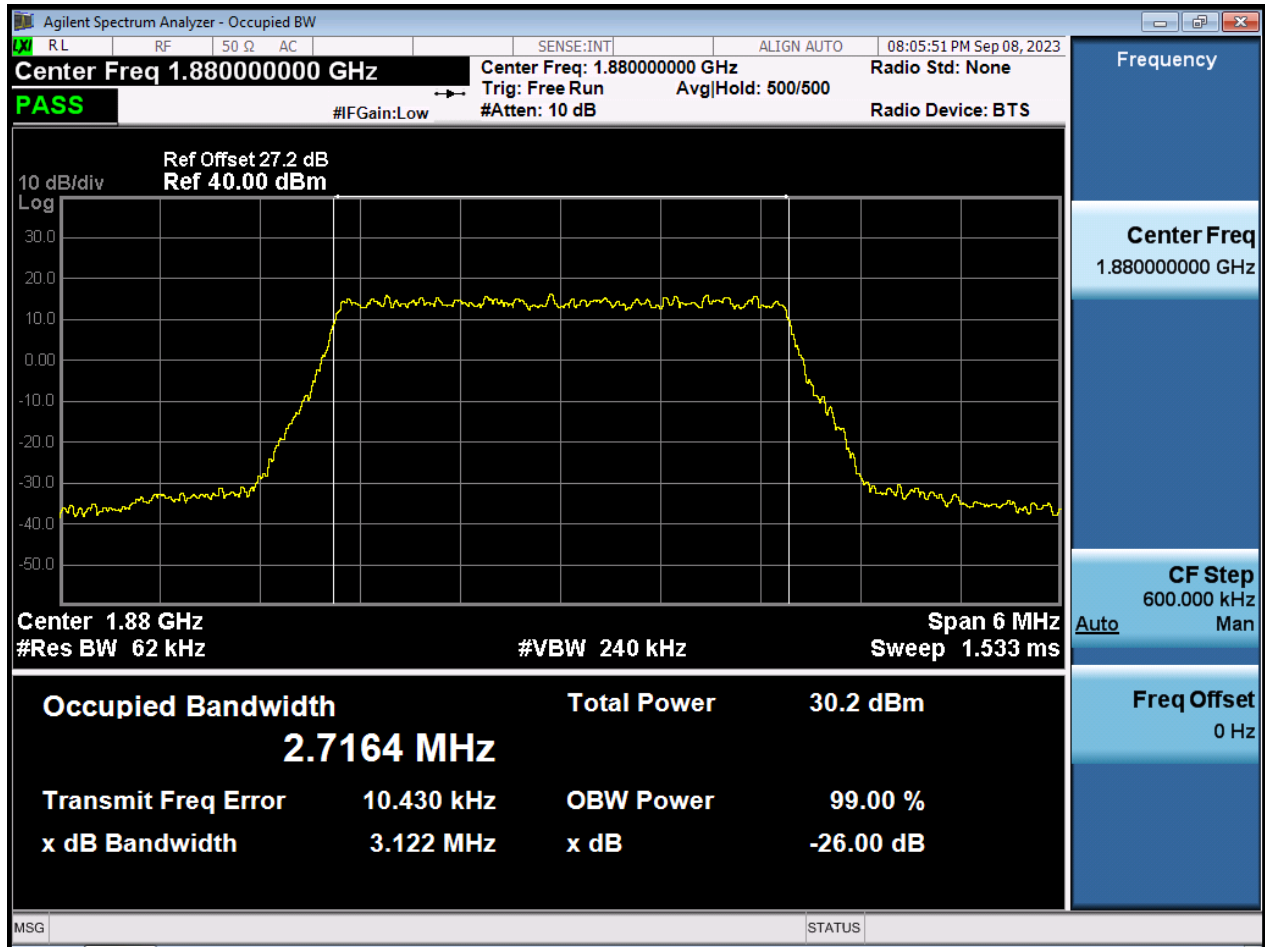
BW1.4 M_OBW_Middle Channel_64QAM_Full RB



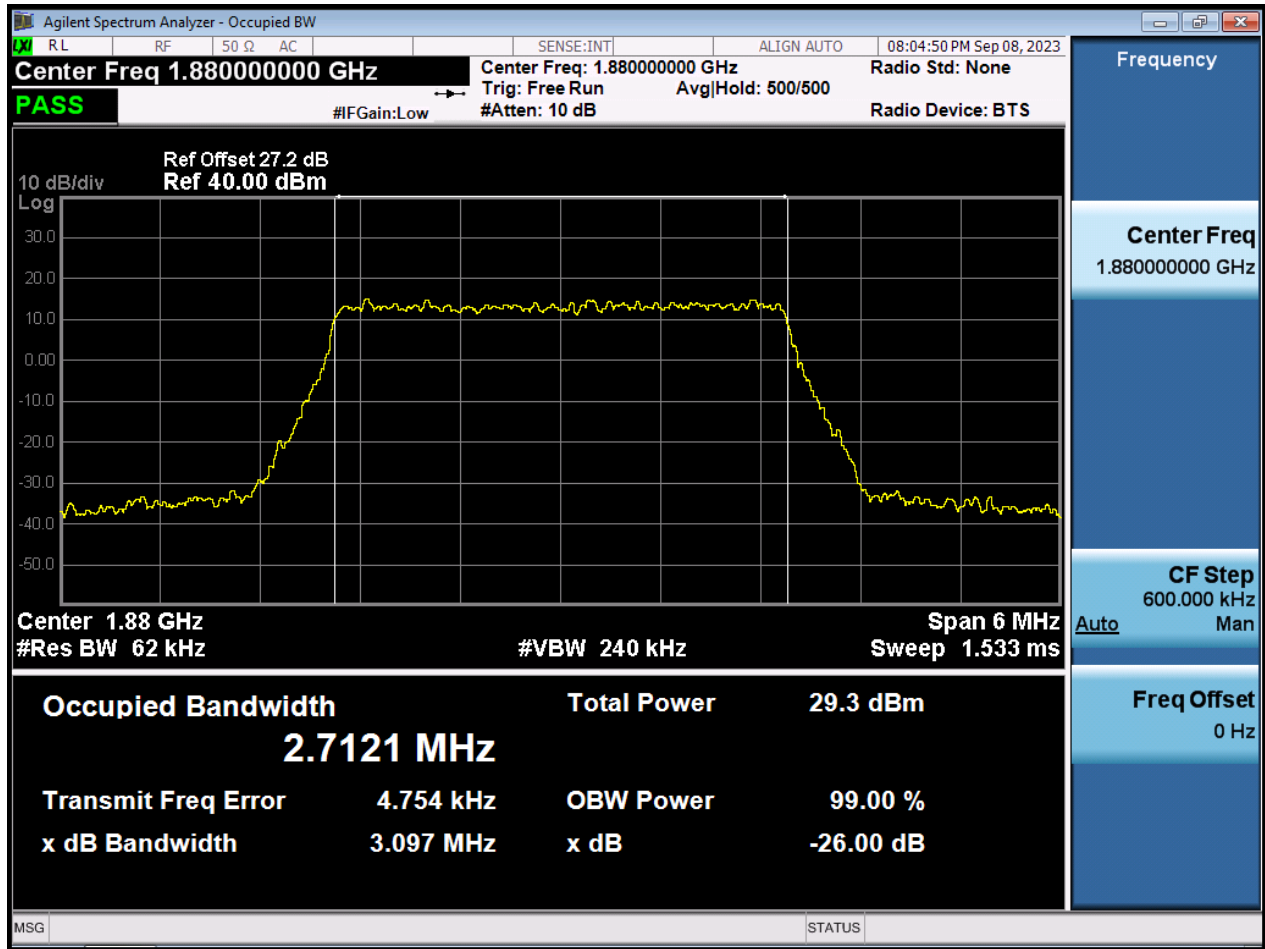
BW1.4 M_OBW_Middle Channel_256QAM_Full RB



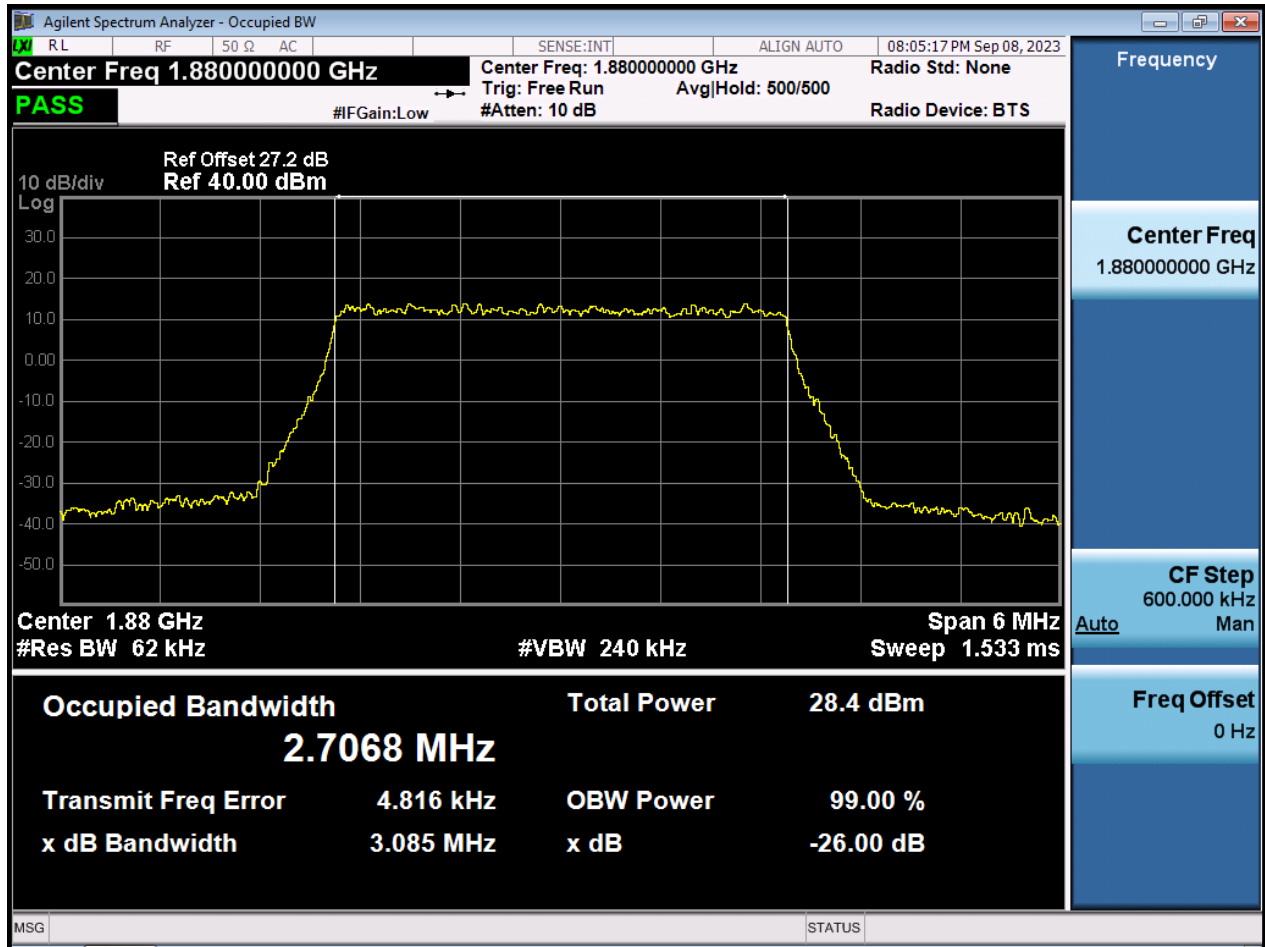
BW3 M_OBW_Middle Channel_QPSK_Full RB



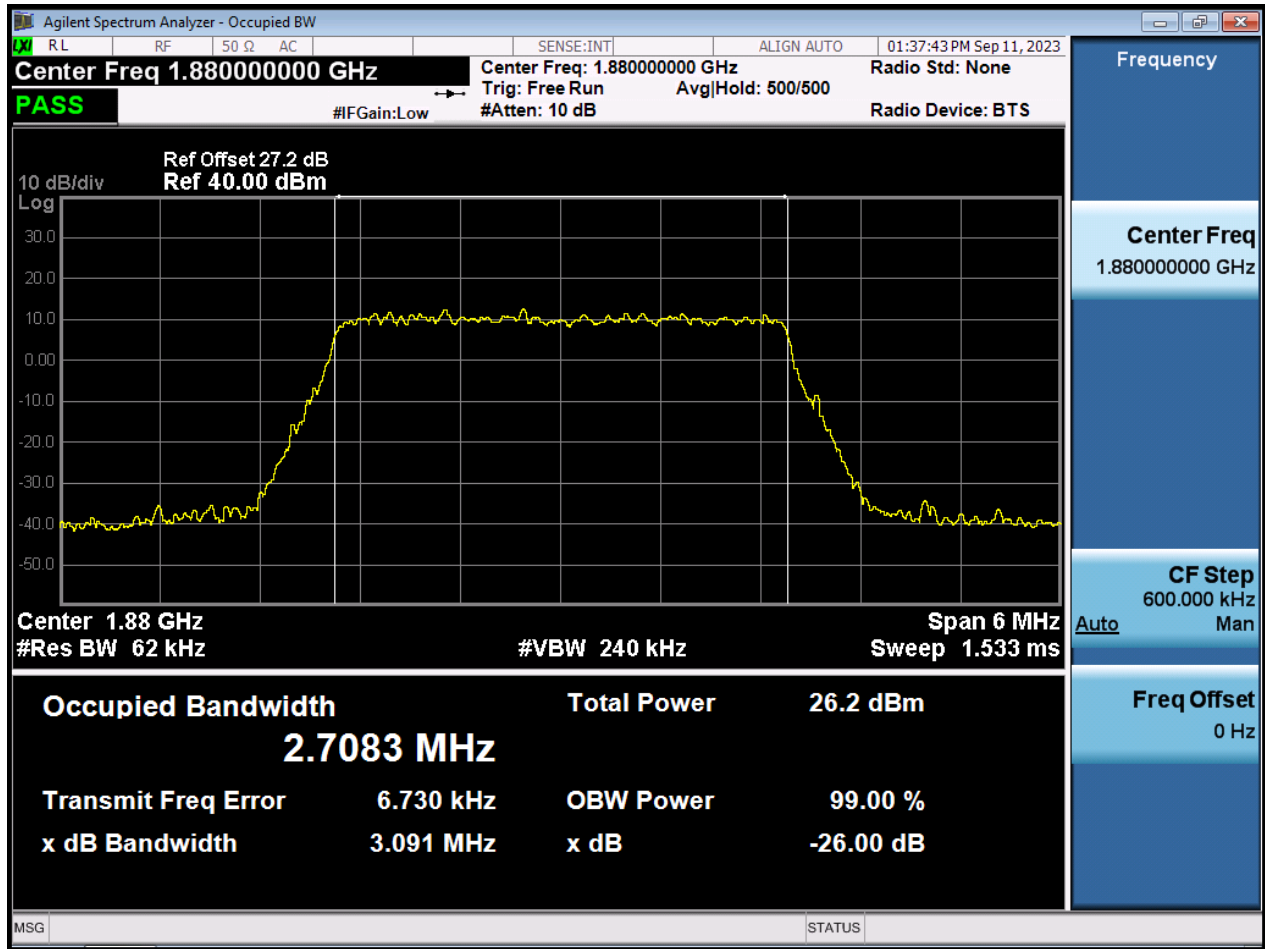
BW3 M_OBW_Middle Channel_16QAM_Full RB



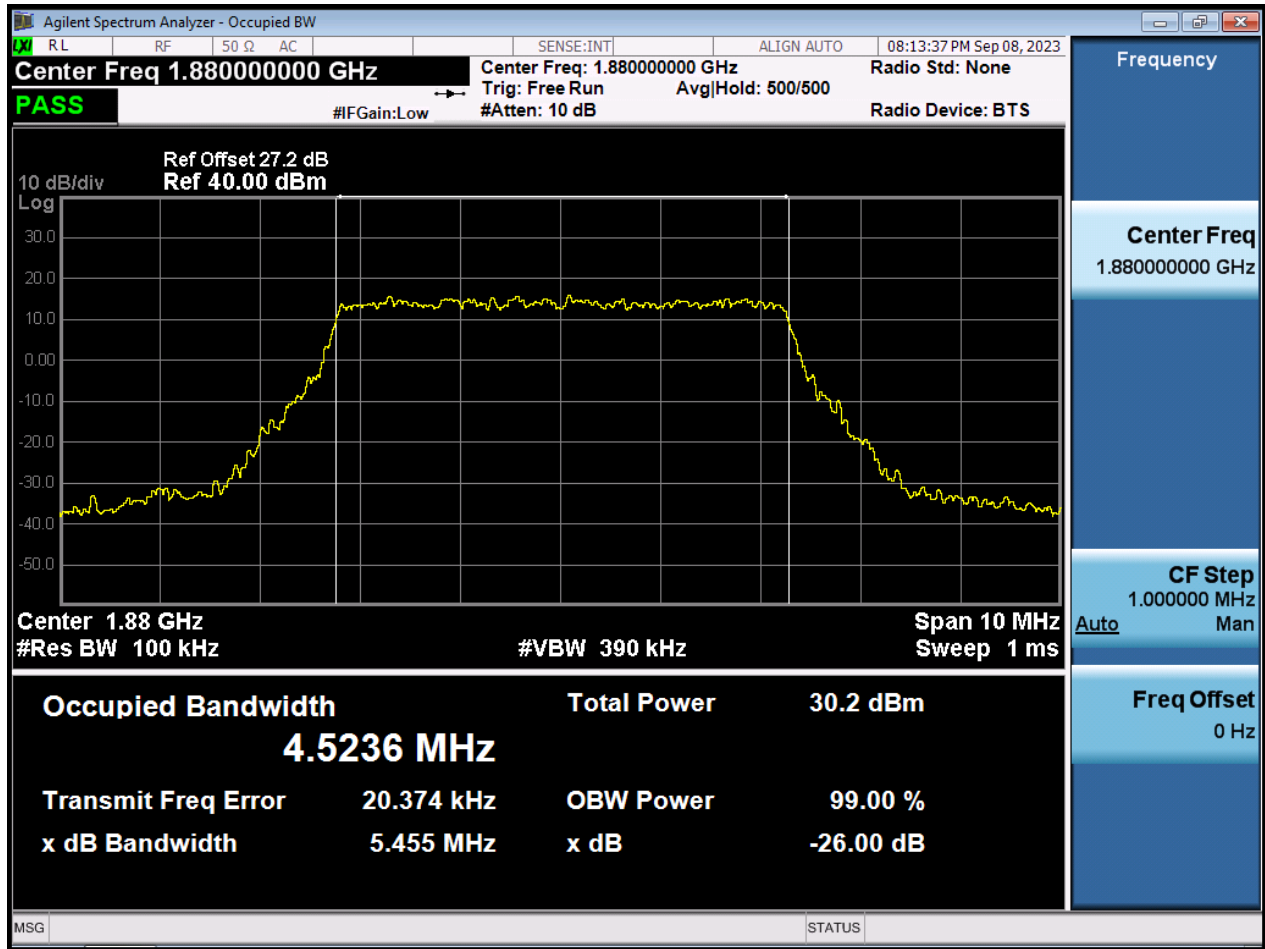
BW3 M_OBW_Middle Channel_64QAM_Full RB



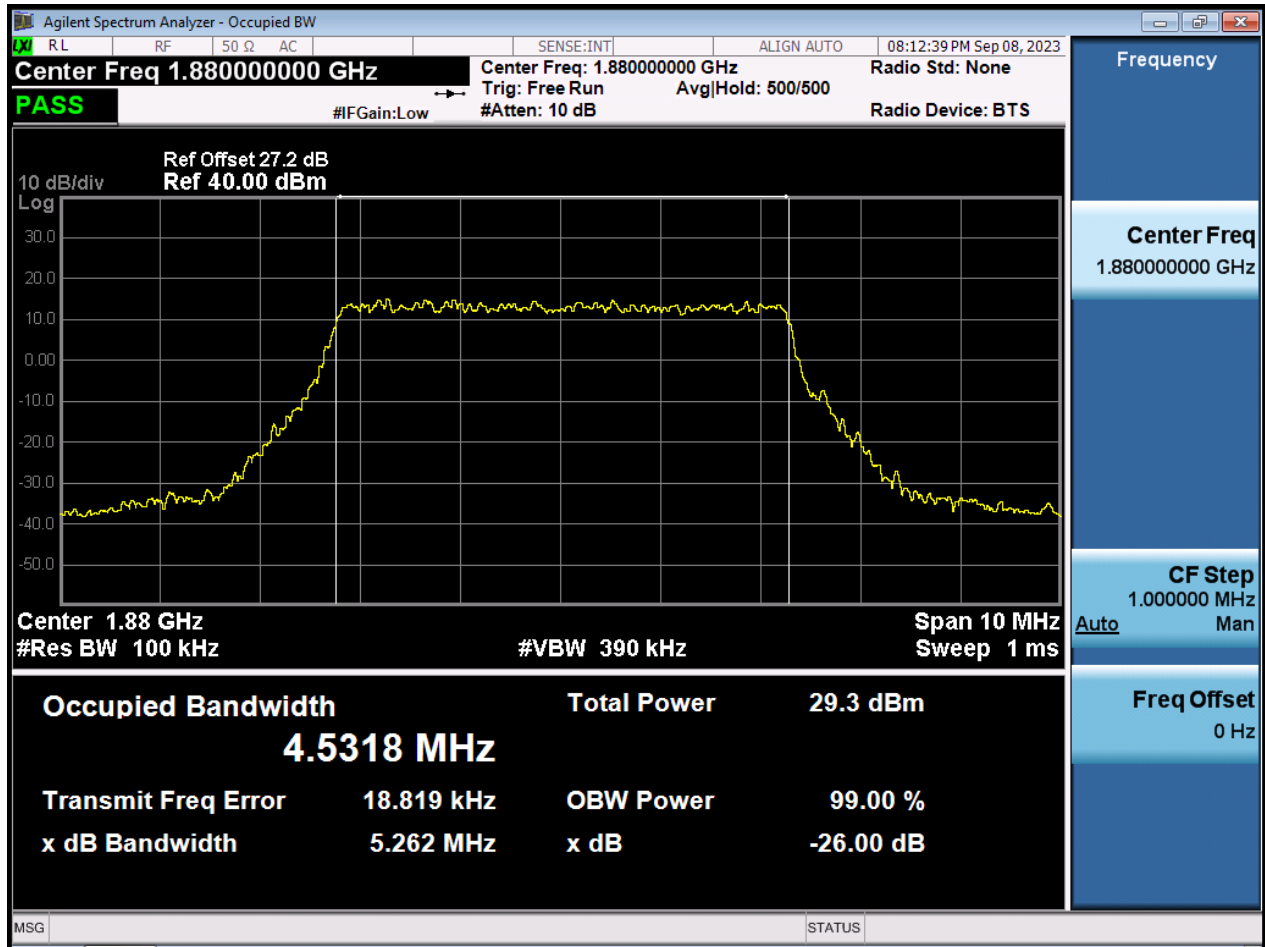
BW3 M_OBW_Middle Channel_256QAM_Full RB



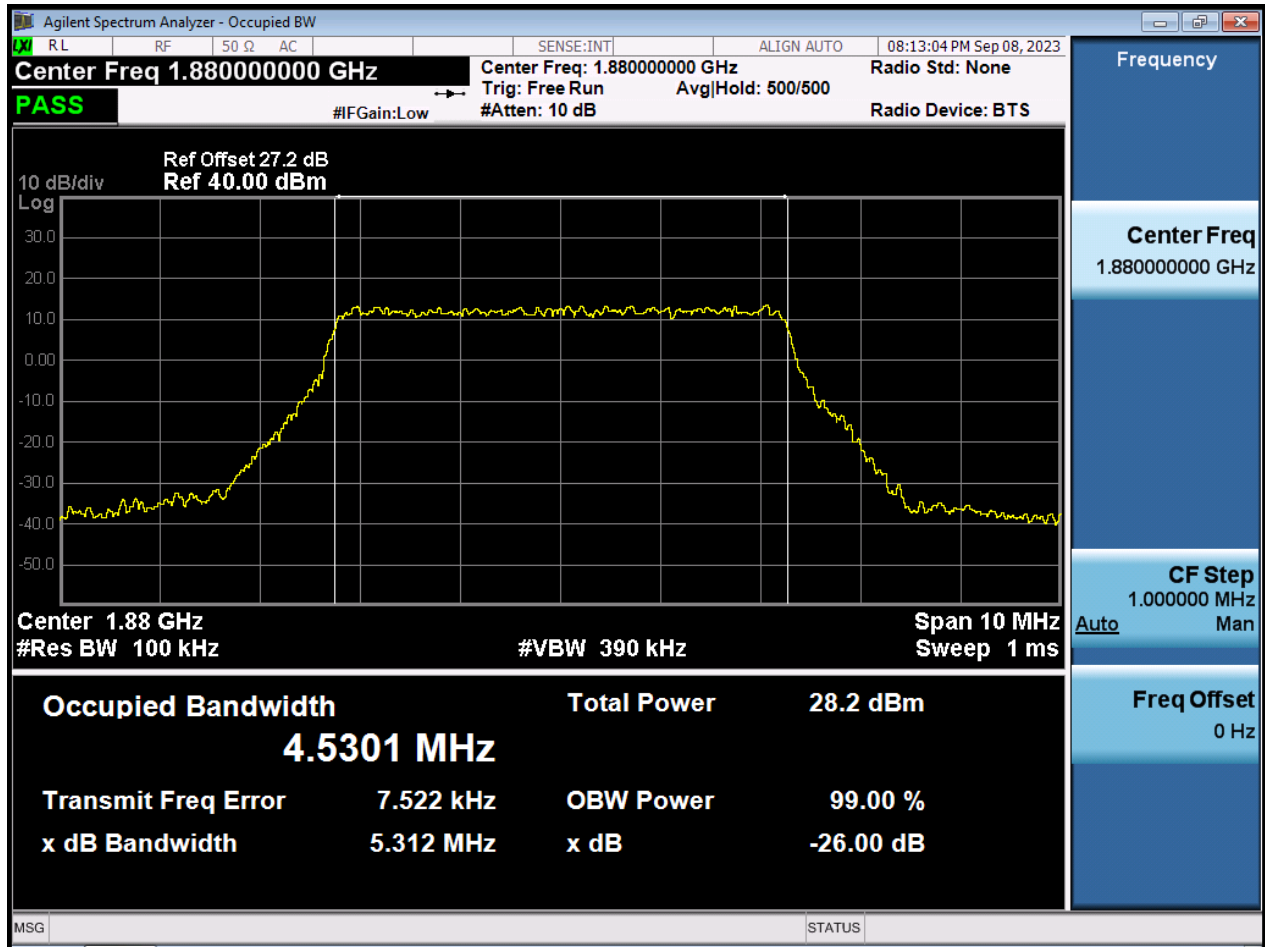
BW5 M_OBW_Middle Channel_QPSK_Full RB



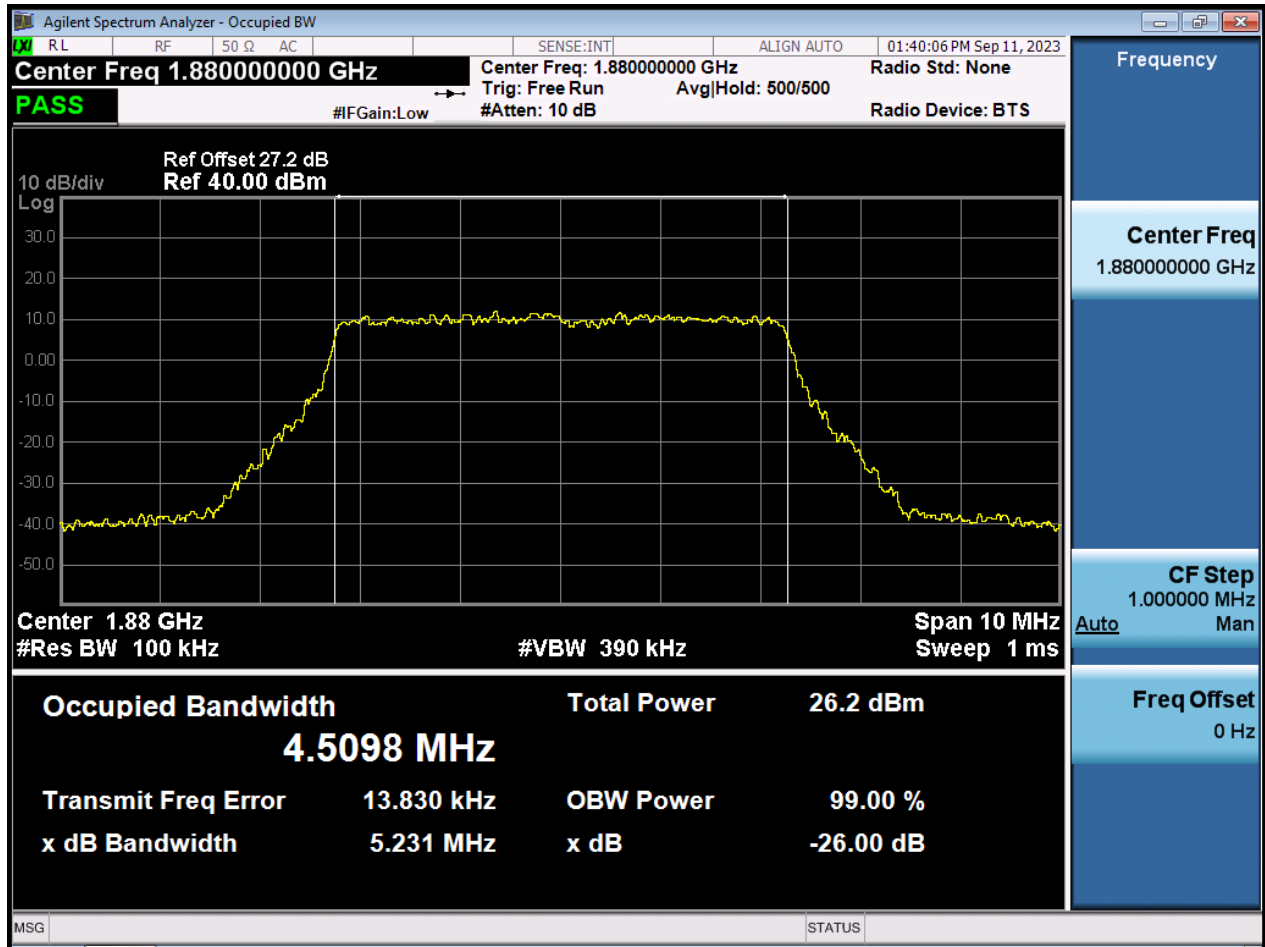
BW5 M_OBW_Middle Channel_16QAM_Full RB



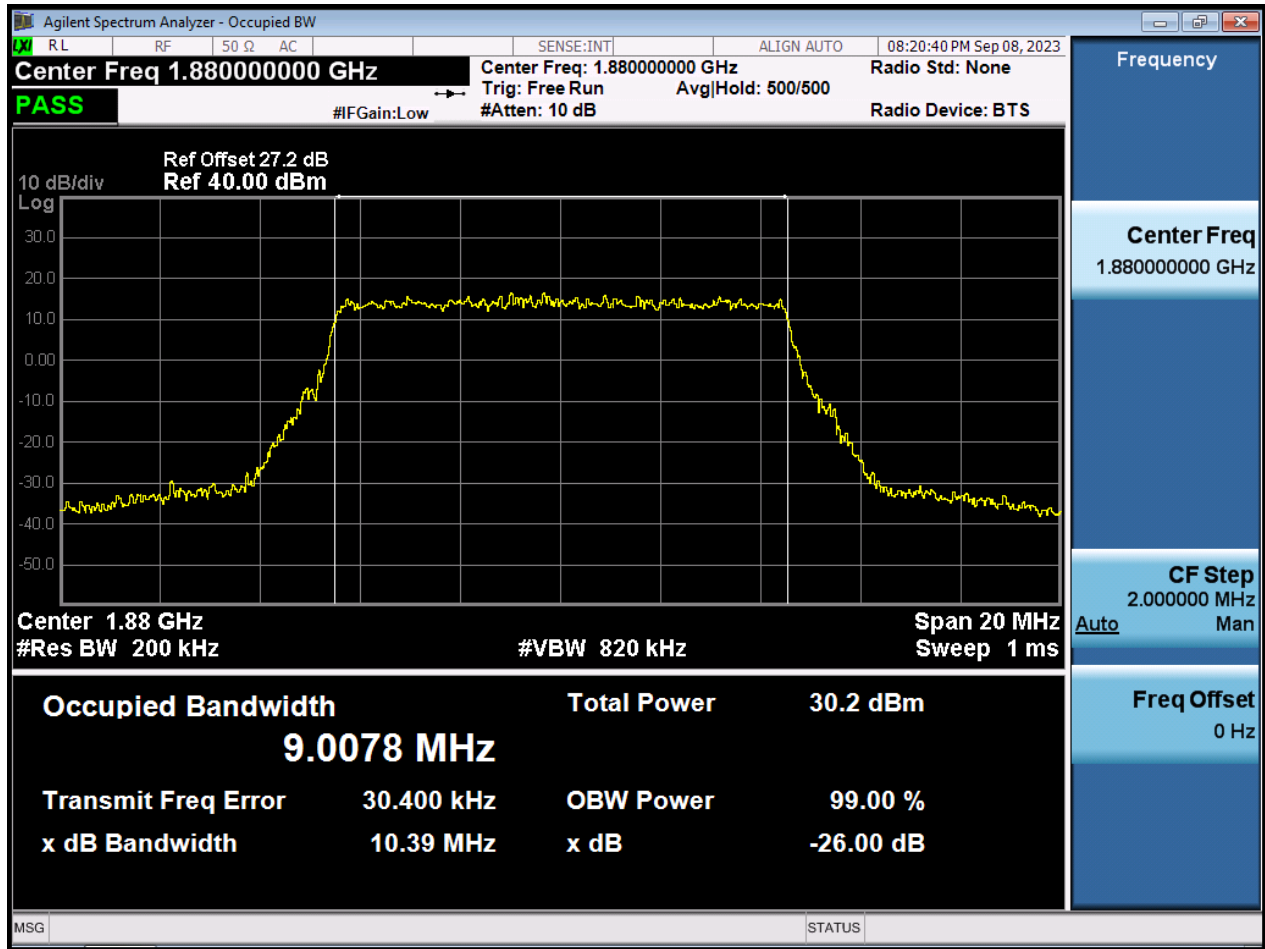
BW5 M_OBW_Middle Channel_64QAM_Full RB



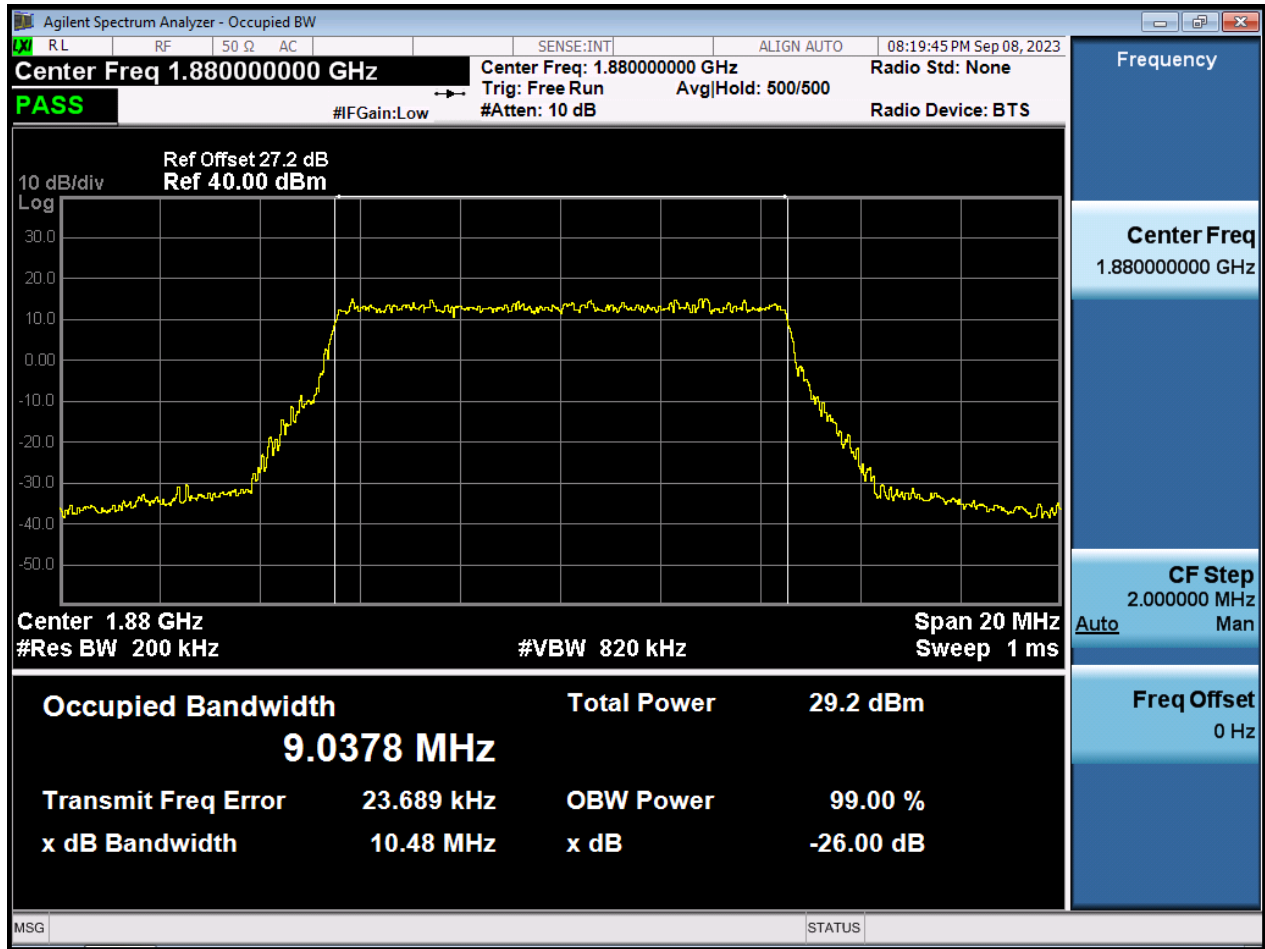
BW5 M_OBW_Middle Channel_256QAM_Full RB



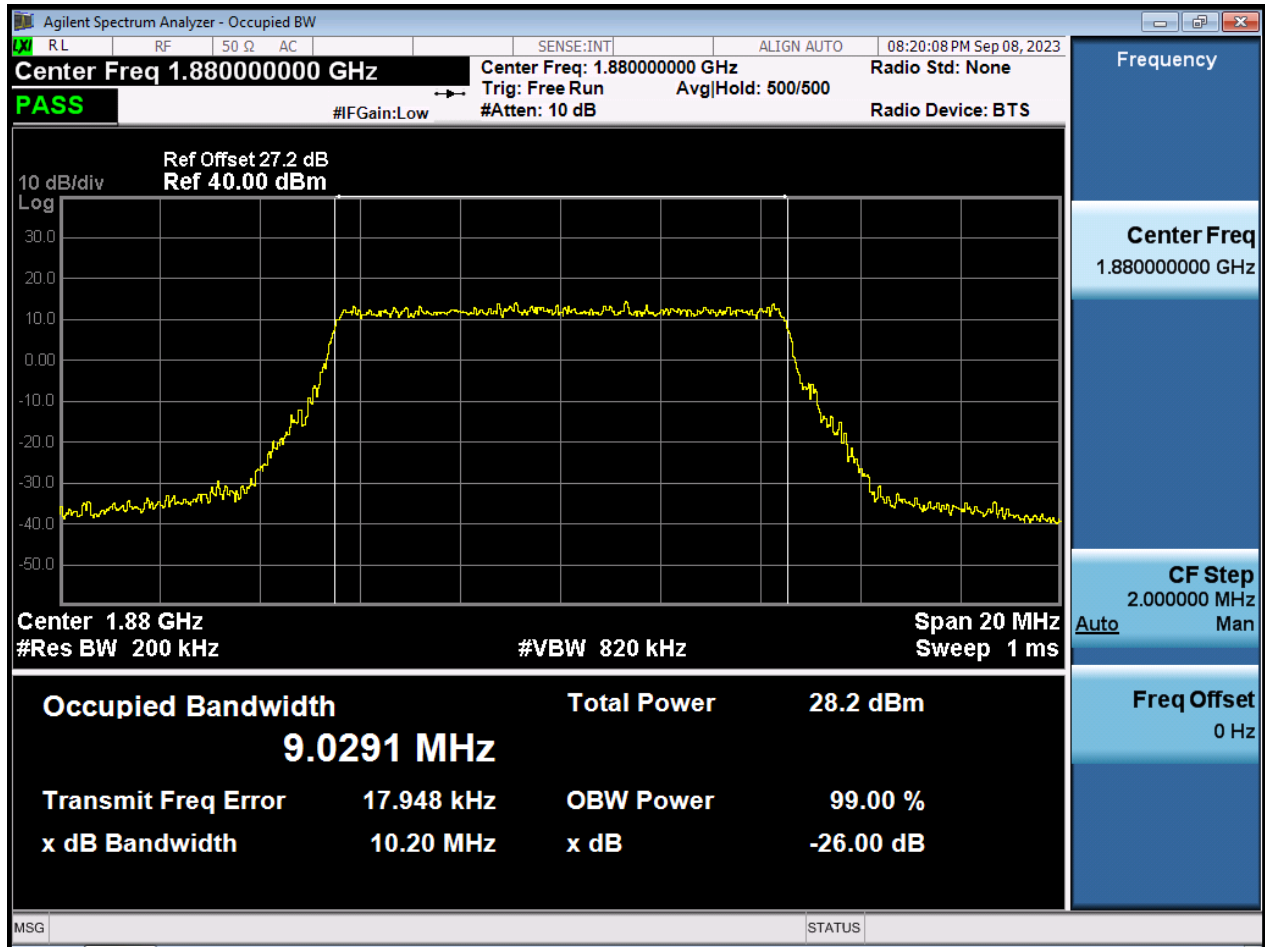
BW10 M_OBW_Middle Channel_QPSK_Full RB



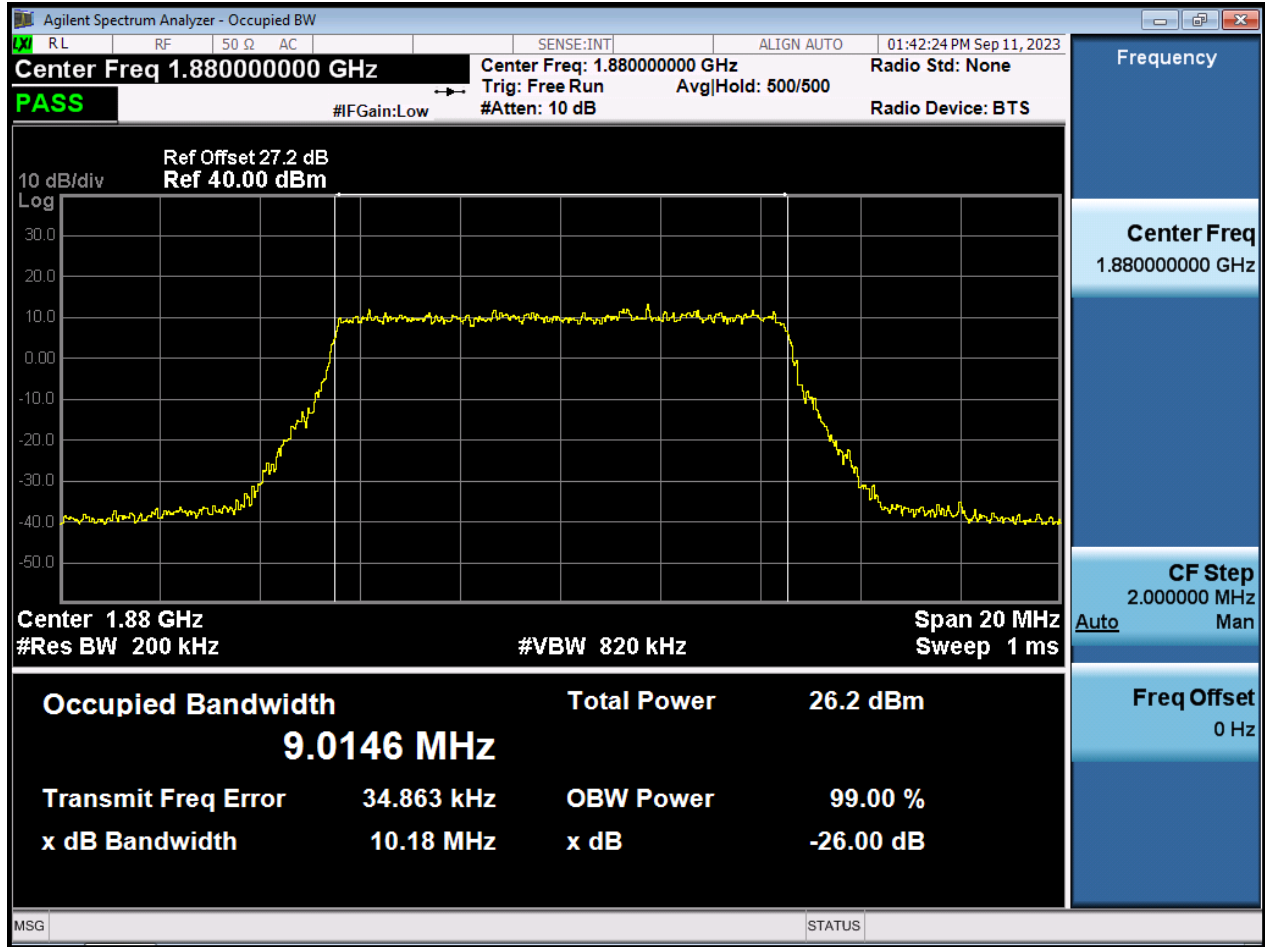
BW10 M_OBW_Middle Channel_16QAM_Full RB



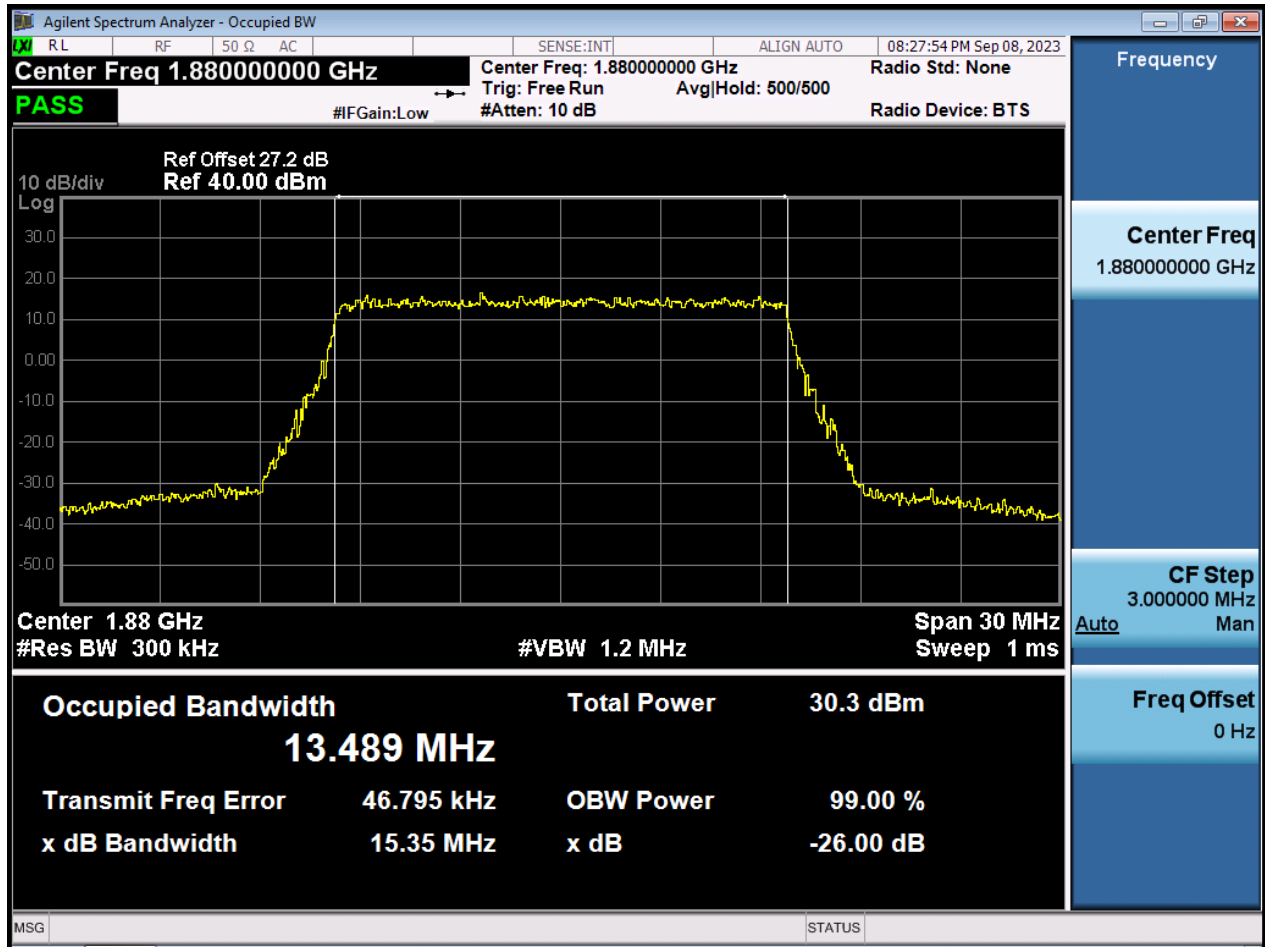
BW10 M_OBW_Middle Channel_64QAM_Full RB



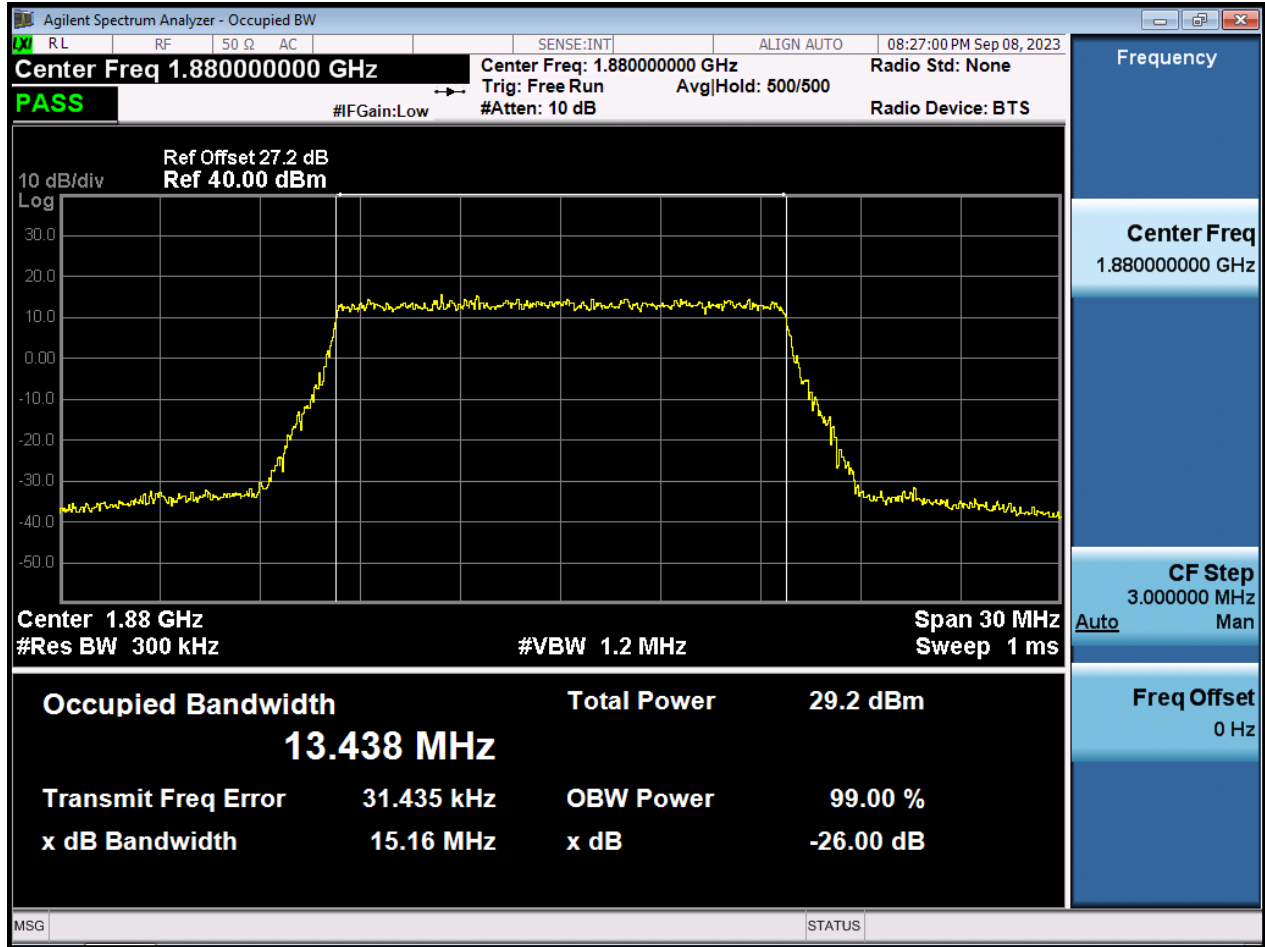
BW10 M_OBW_Middle Channel_256QAM_Full RB



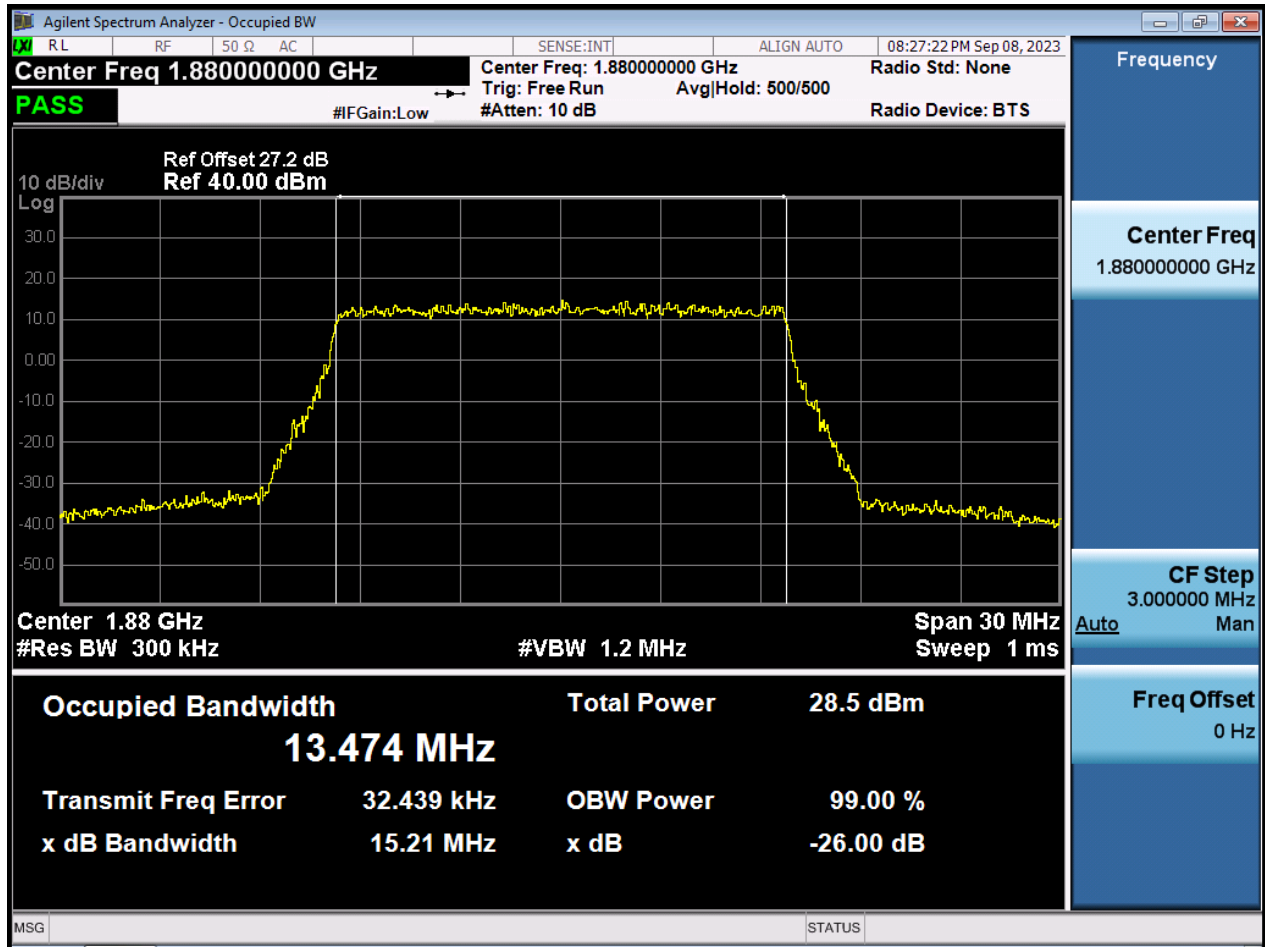
BW15 M_OBW_Middle Channel_QPSK_Full RB



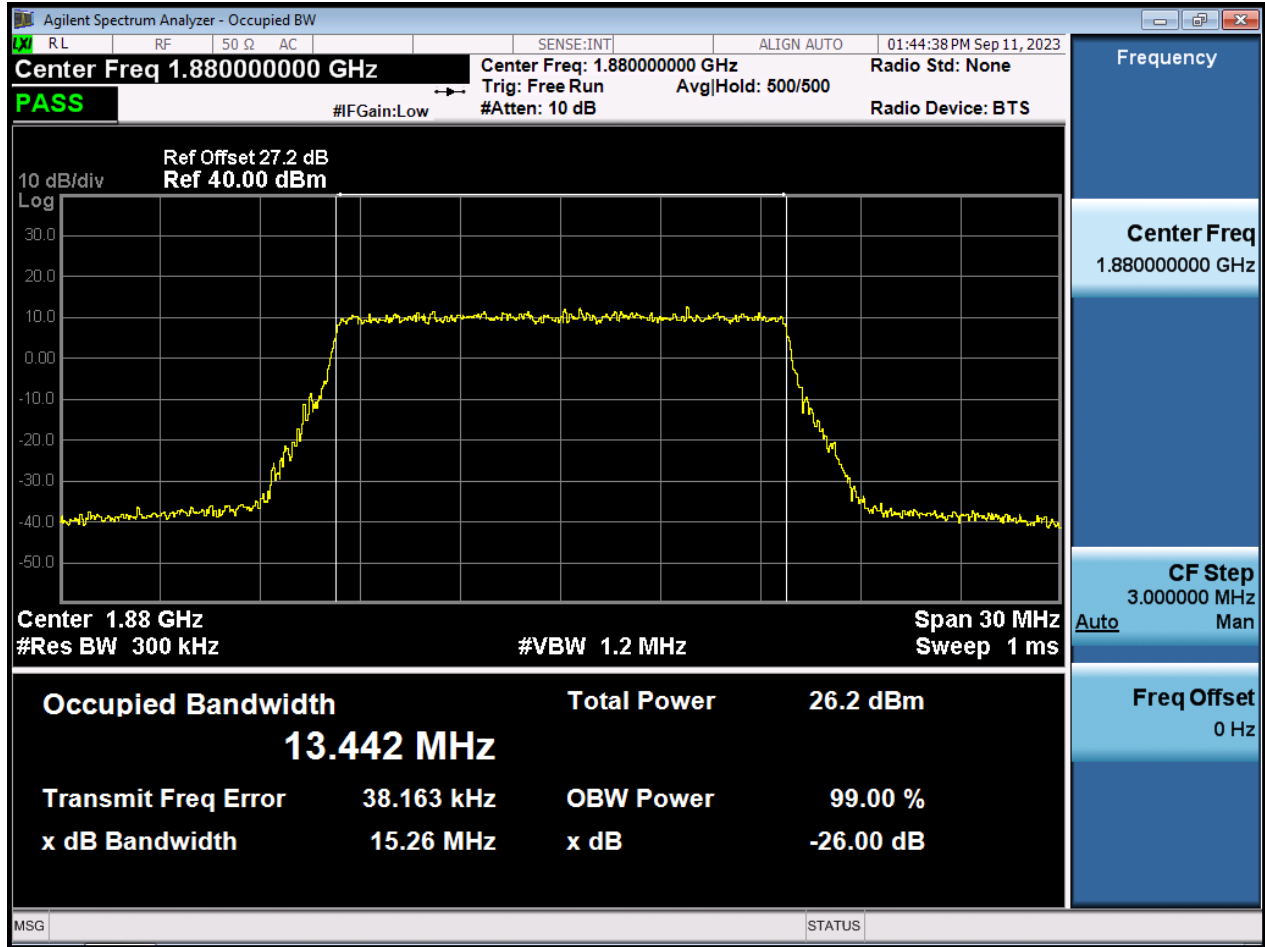
BW15 M_OBW_Middle Channel_16QAM_Full RB



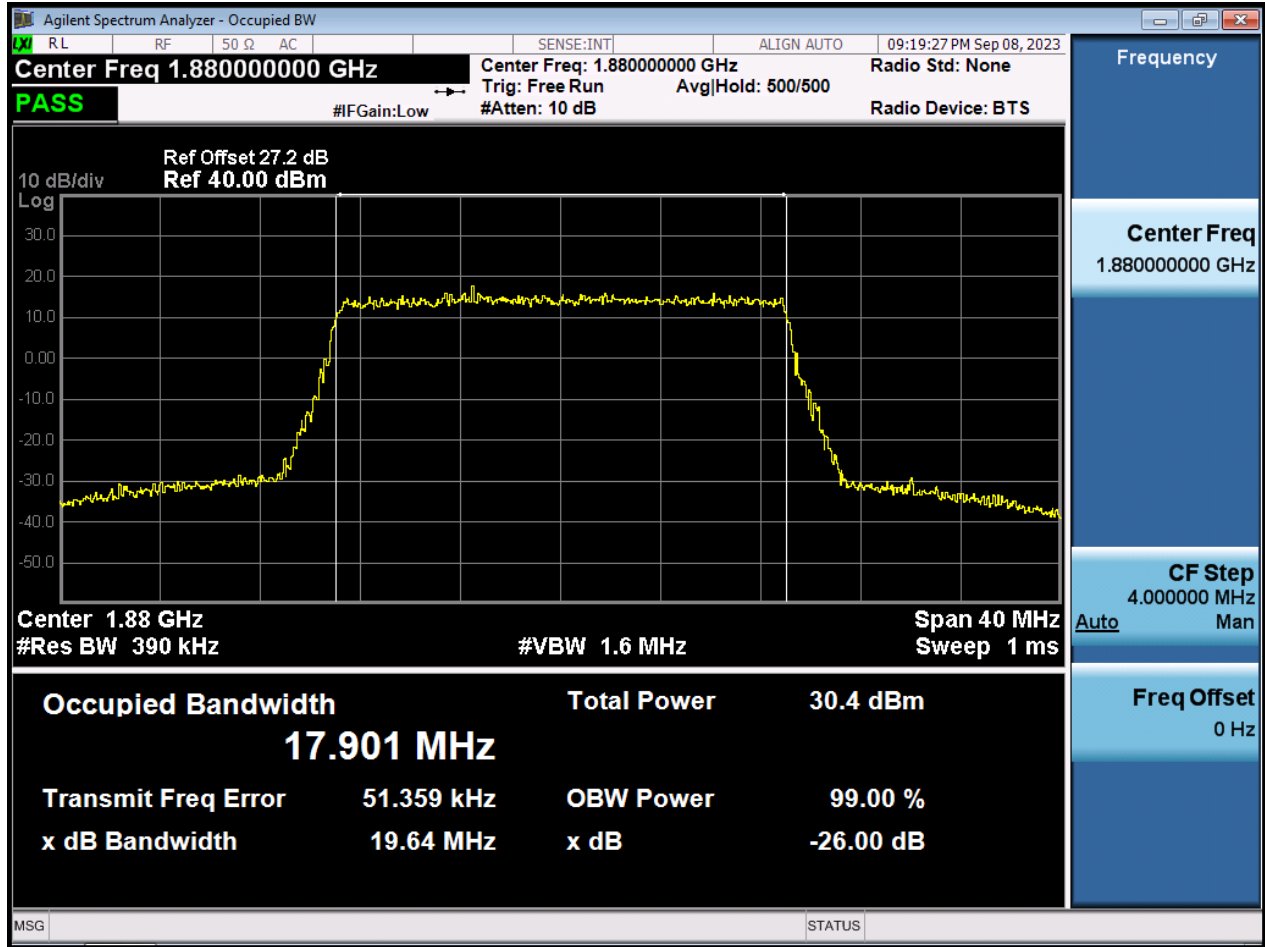
BW15 M_OBW_Middle Channel_64QAM_Full RB



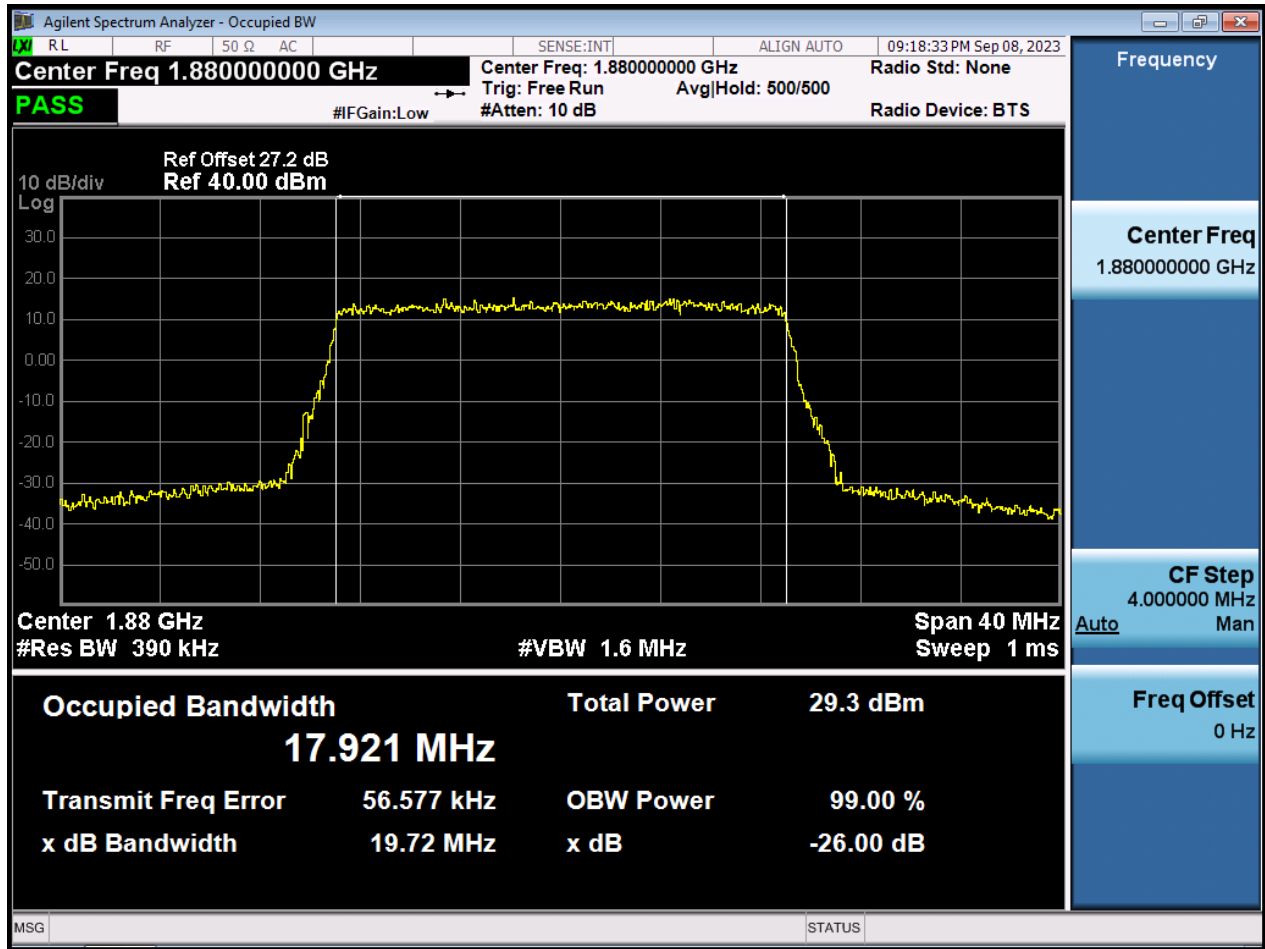
BW15 M_OBW_Middle Channel_256QAM_Full RB



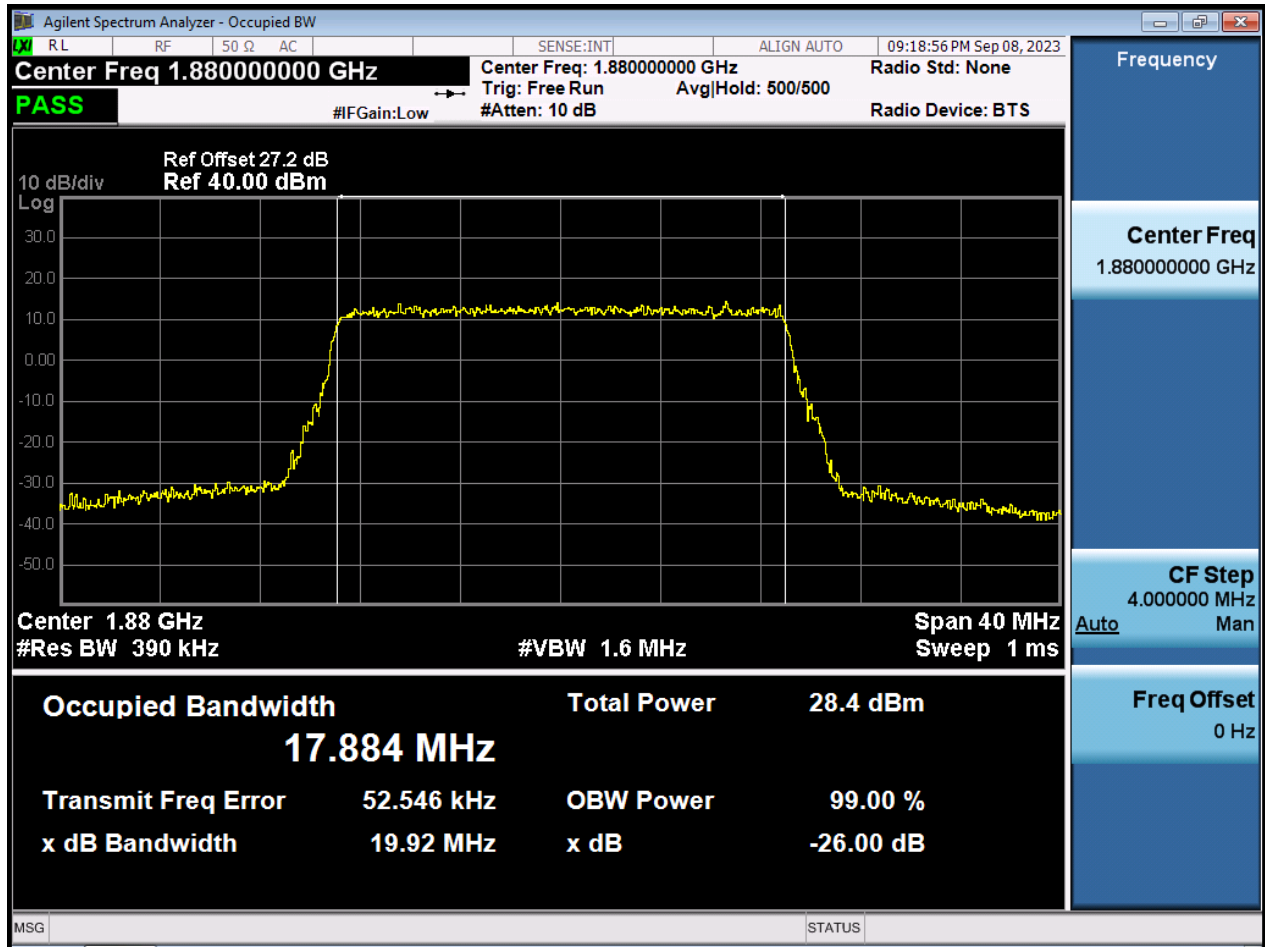
BW20 M_OBW_Middle Channel_QPSK_Full RB



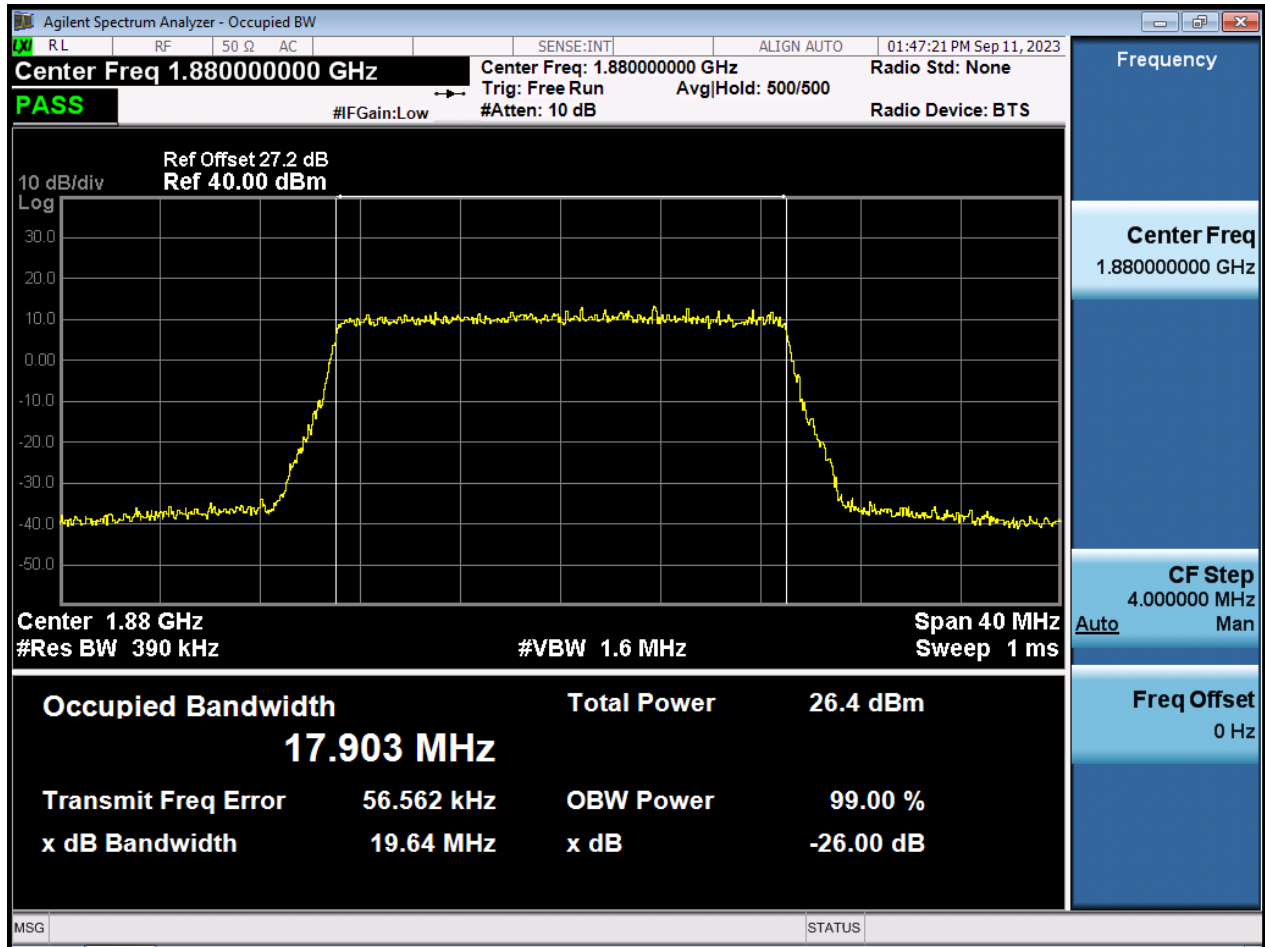
BW20 M_OBW_Middle Channel_16QAM_Full RB



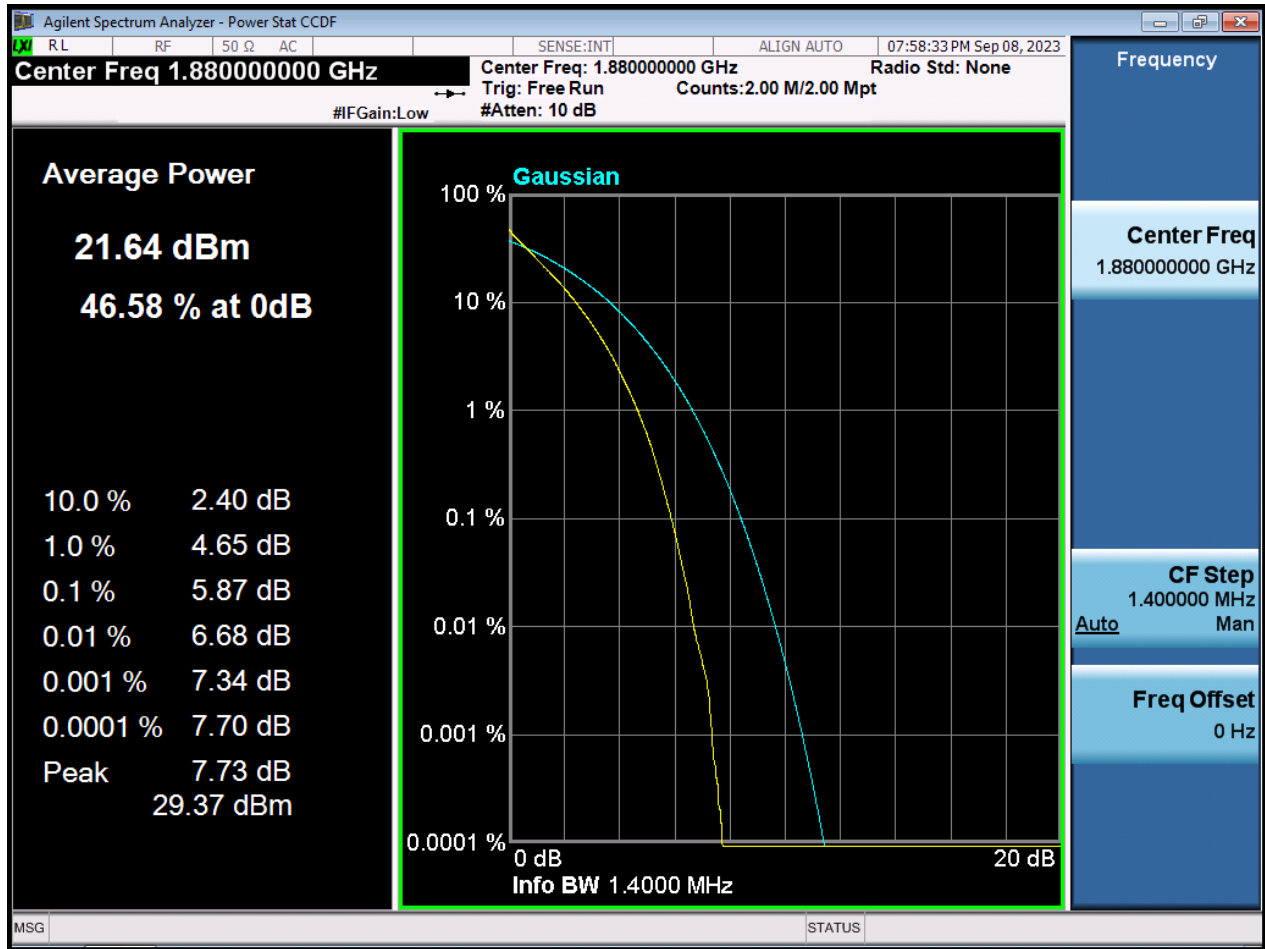
BW20 M_OBW_Middle Channel_64QAM_Full RB



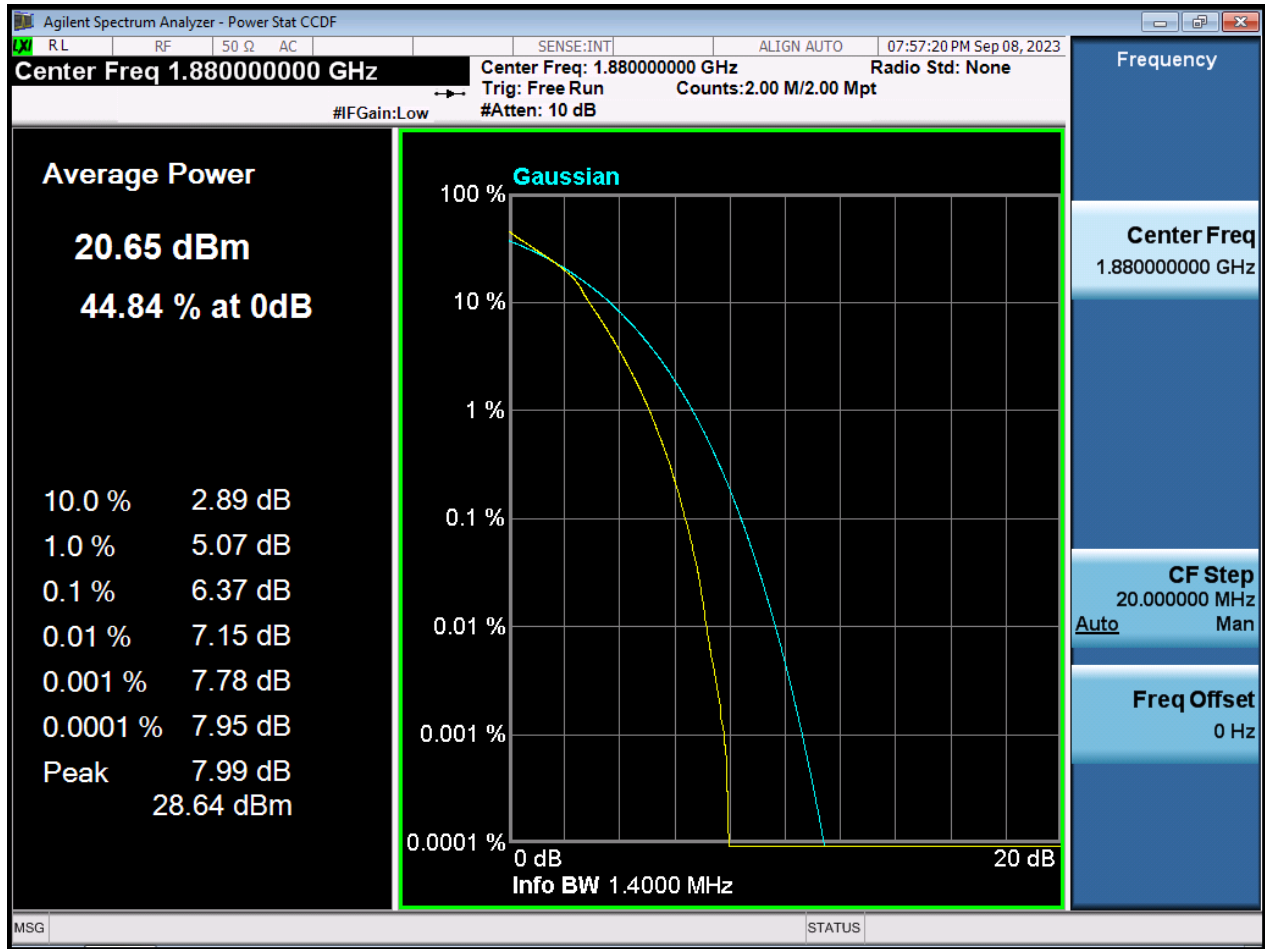
BW20 M_OBW_Middle Channel_256QAM_Full RB



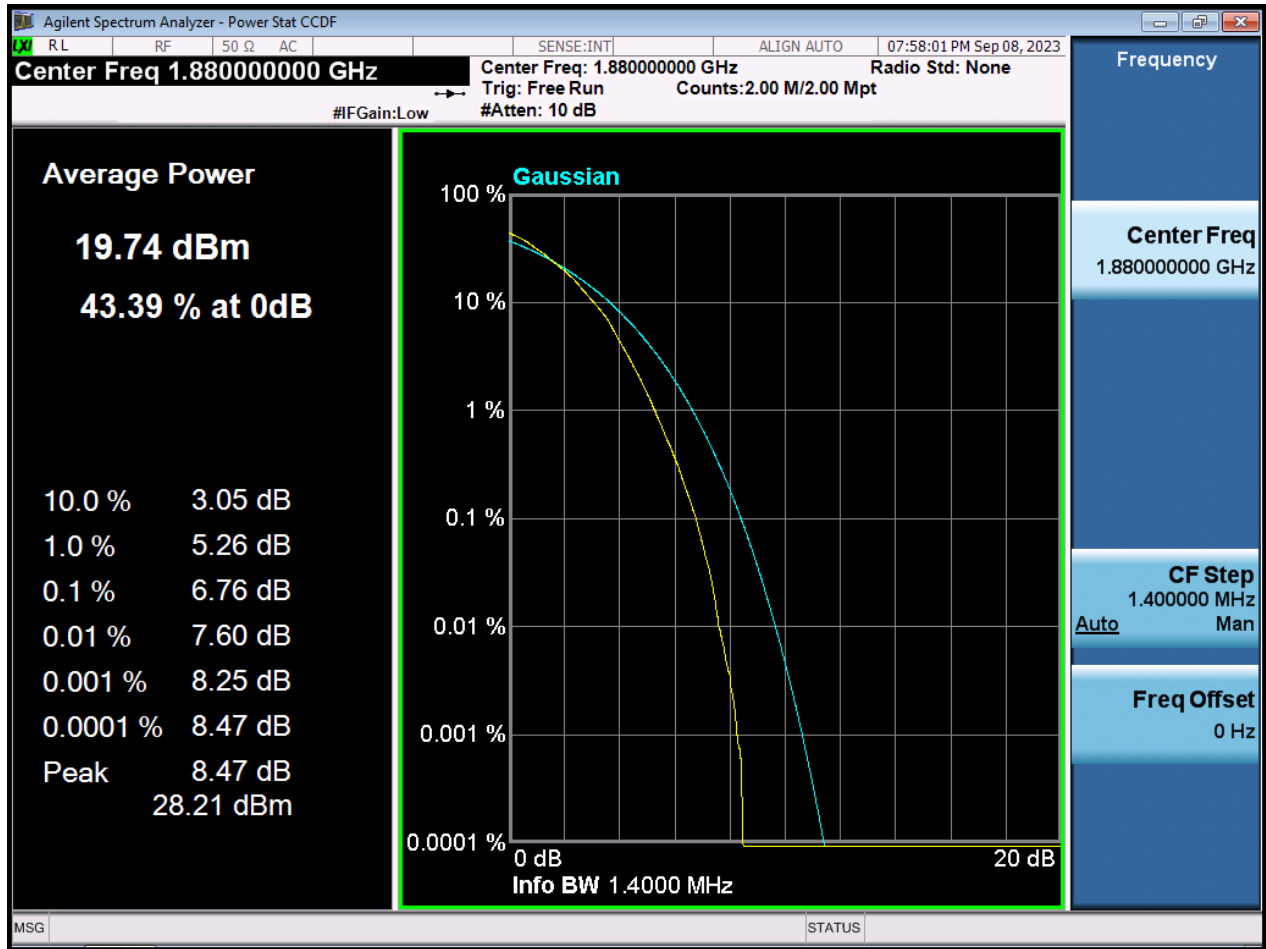
BW1.4 M_PAR_Middle Channel_QPSK_Full RB



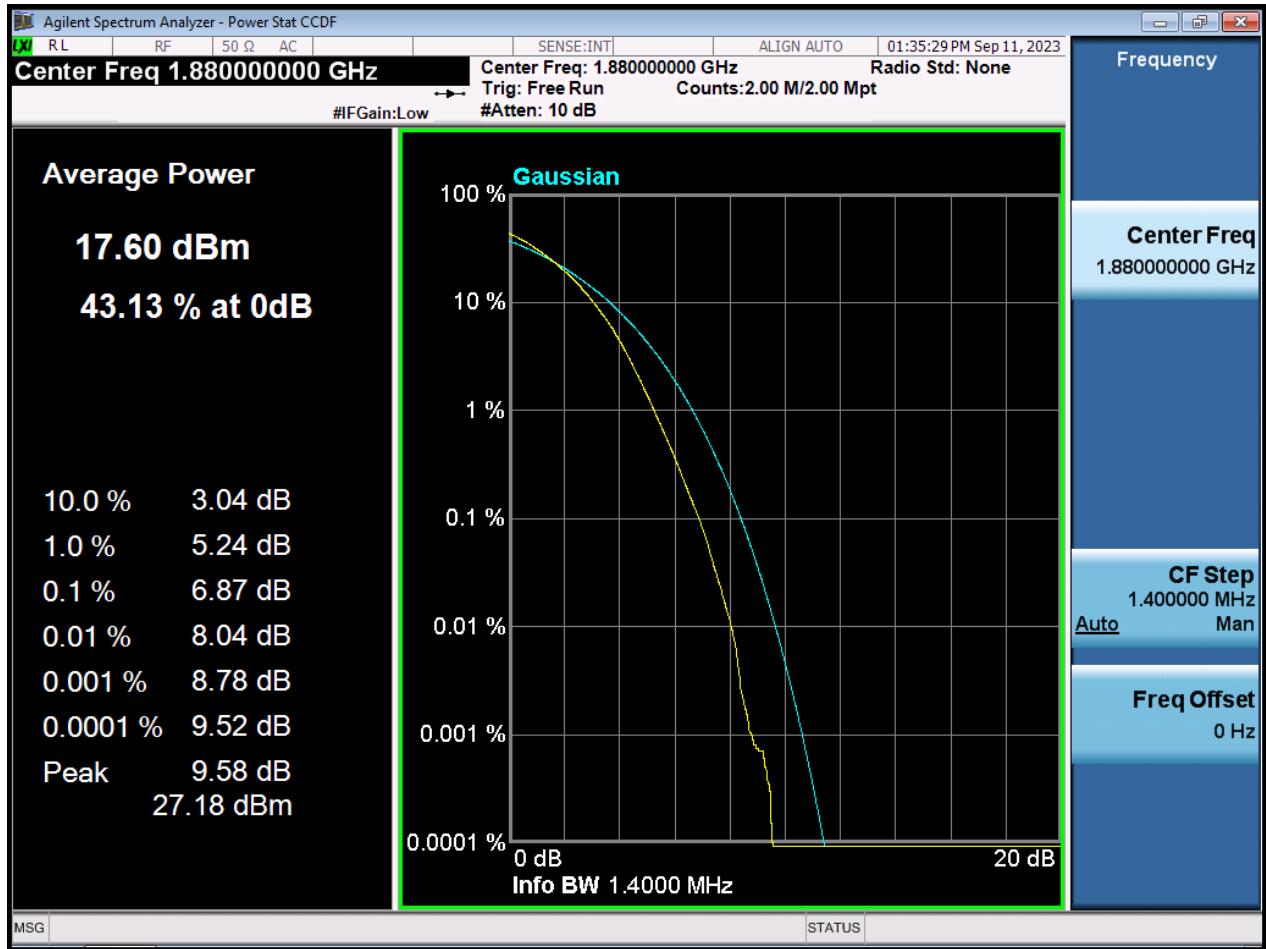
BW1.4 M_PAR_Middle Channel_16QAM_Full RB



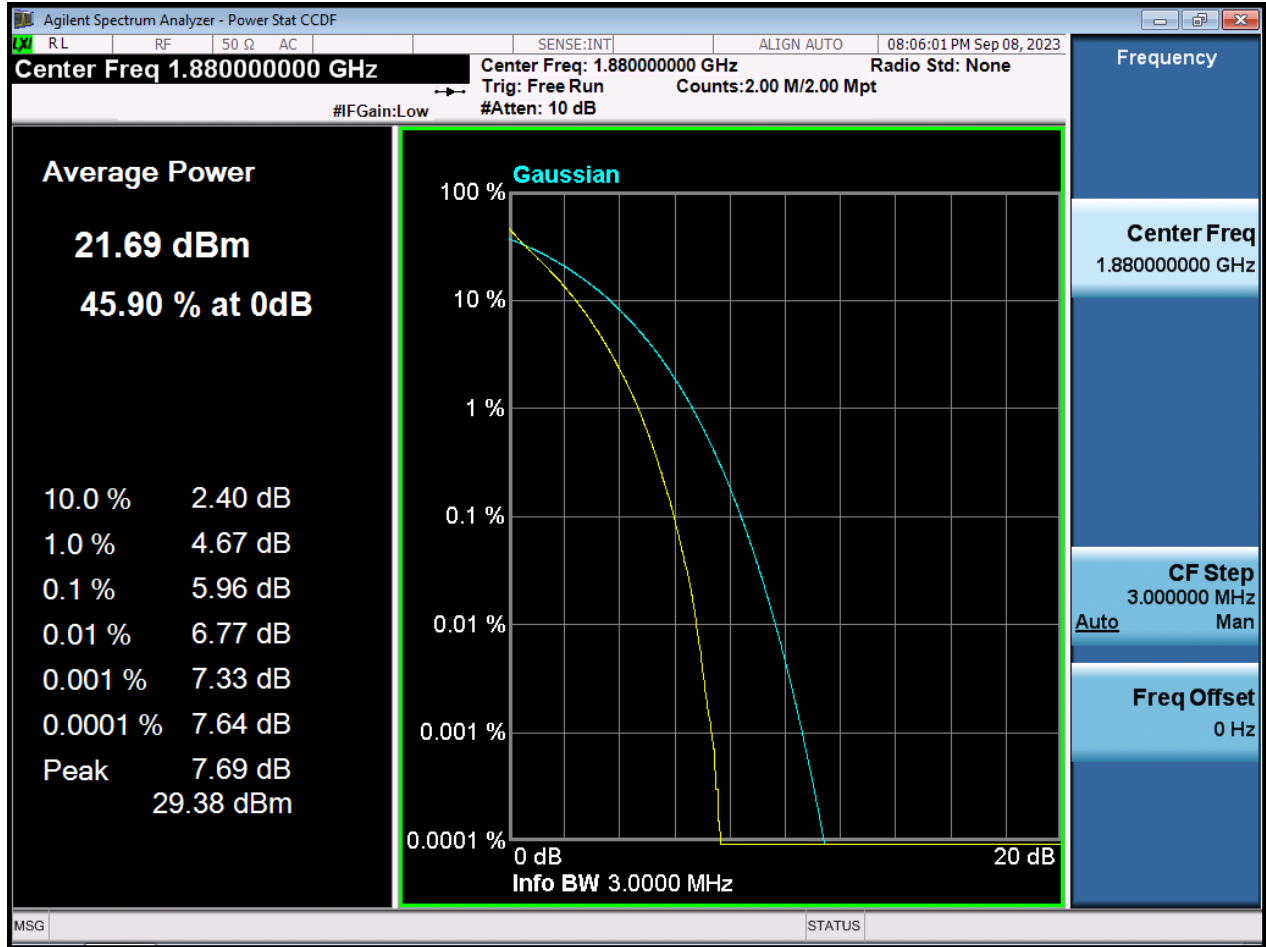
BW1.4 M_PAR_Middle Channel_64QAM_Full RB



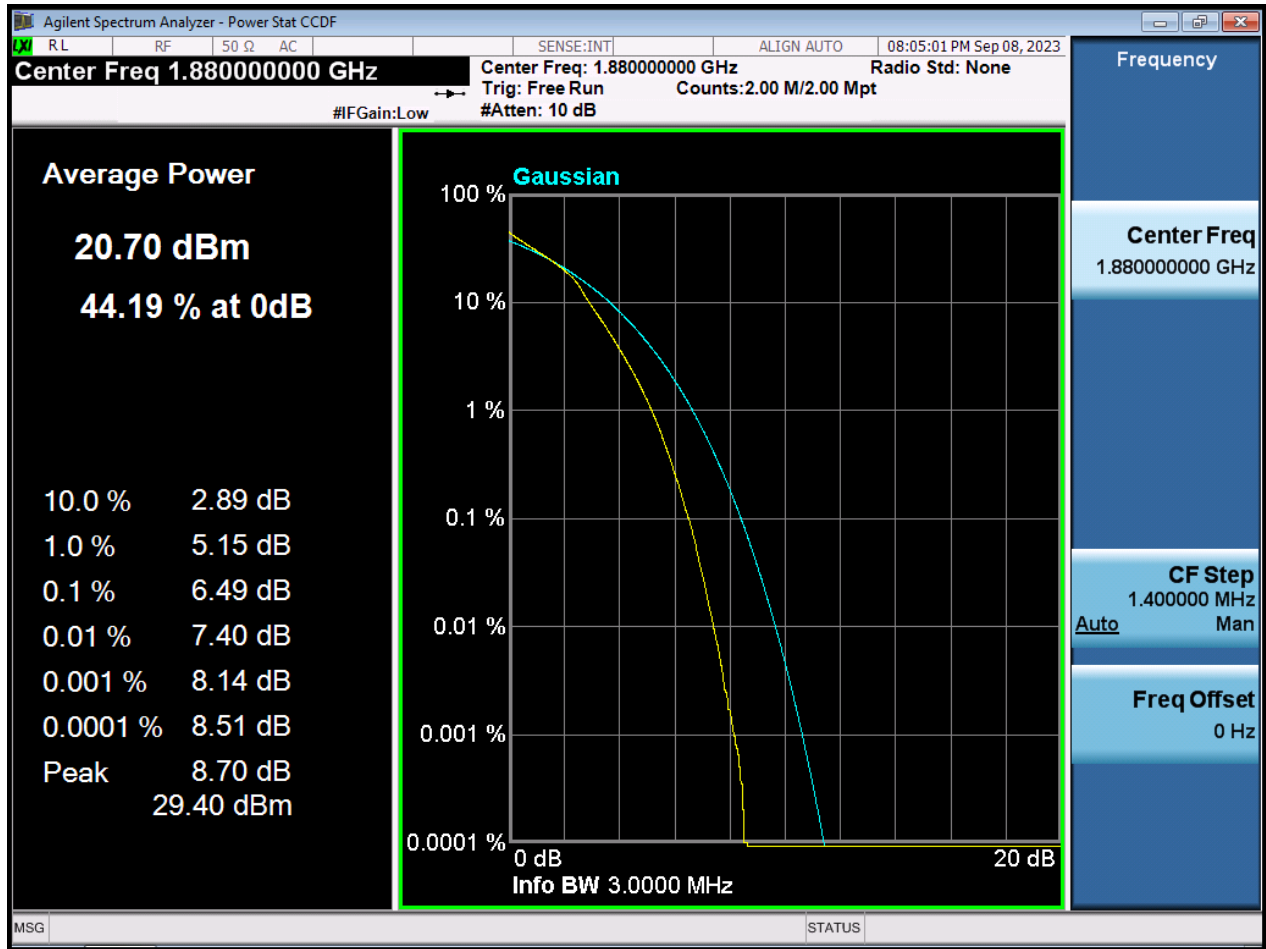
BW1.4 M_PAR_Middle Channel_256QAM_Full RB



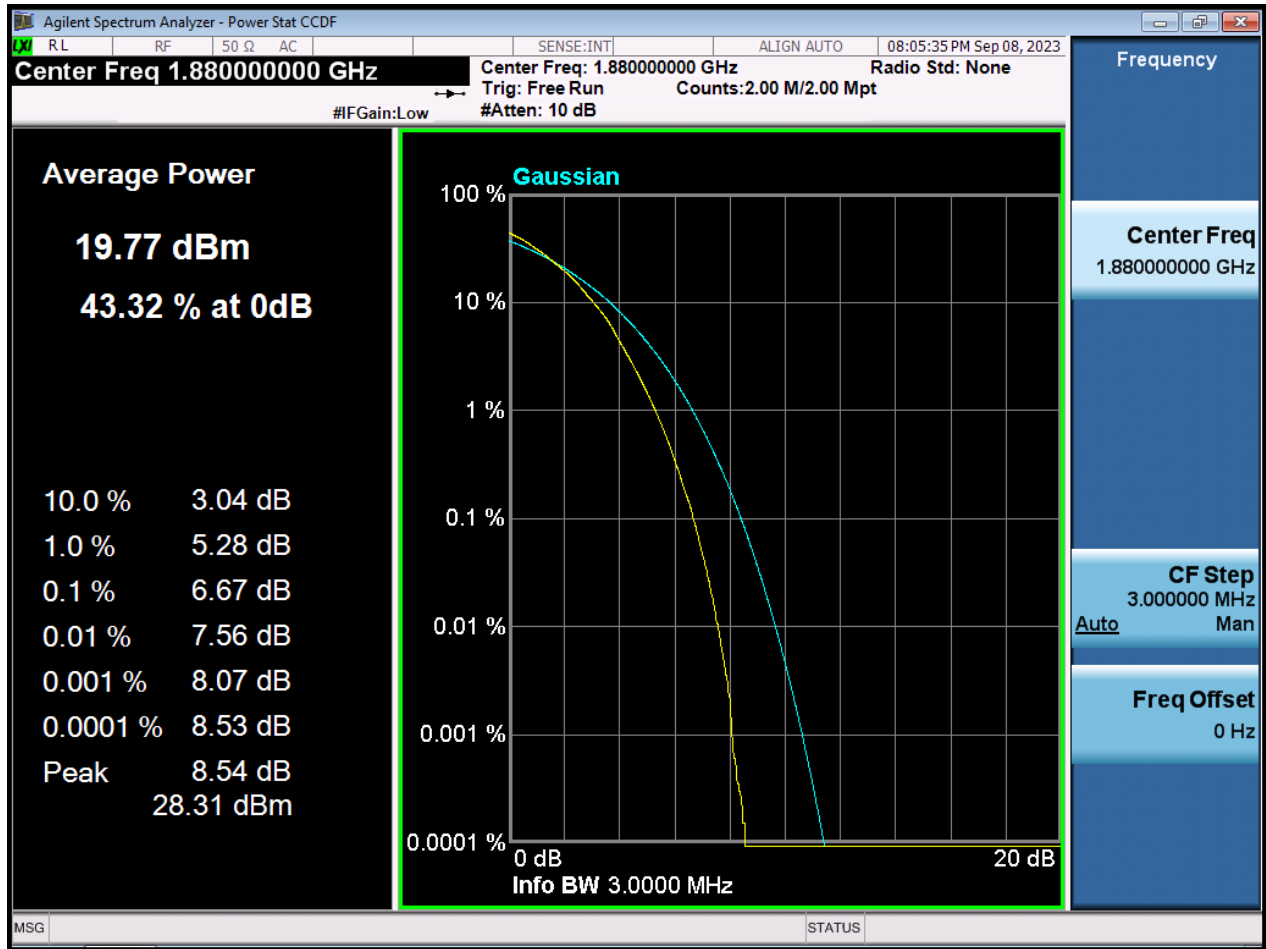
BW3 M_PAR_Middle Channel_QPSK_Full RB



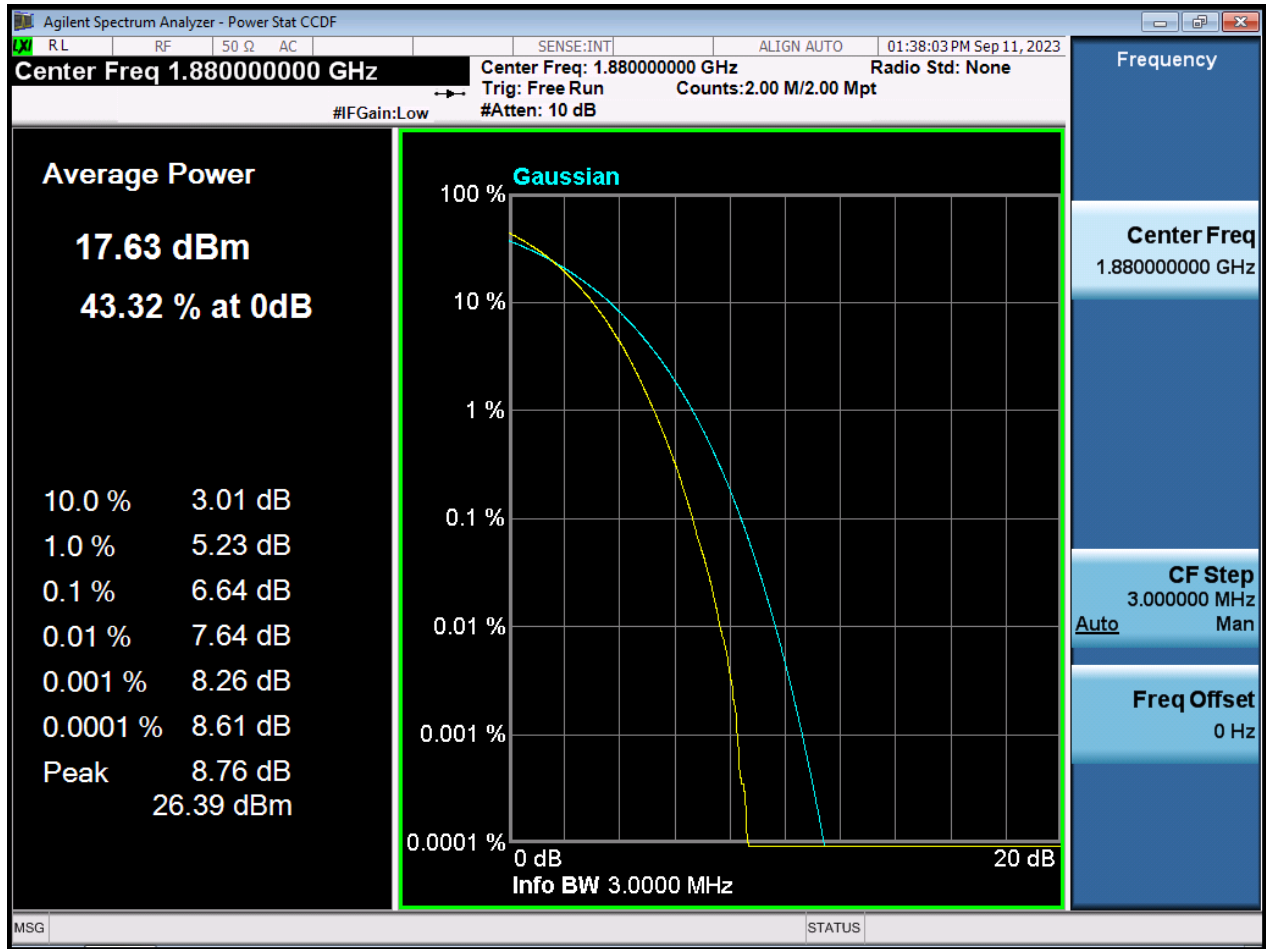
BW3 M_PAR_Middle Channel_16QAM_Full RB



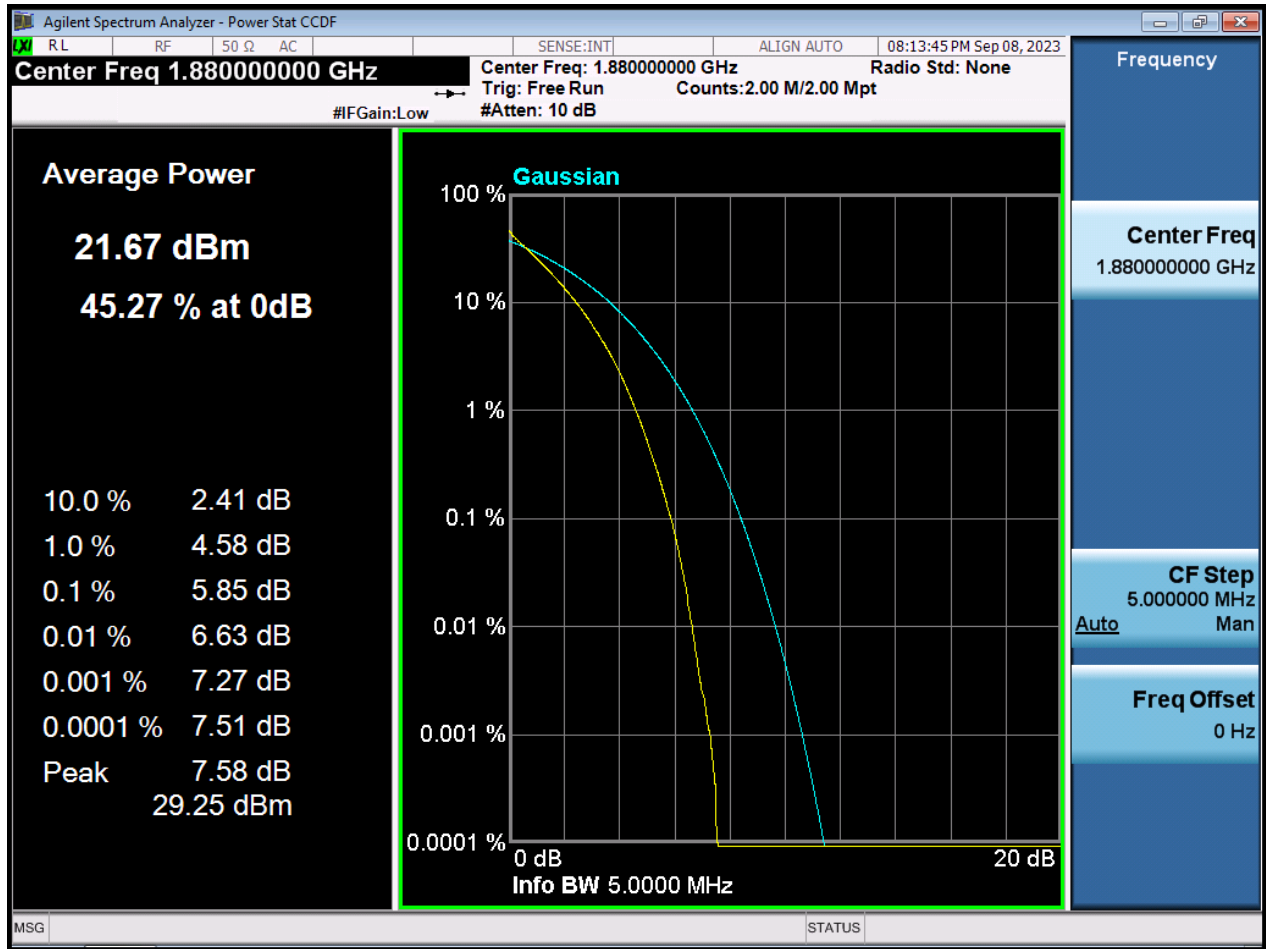
BW3 M_PAR_Middle Channel_64QAM_Full RB



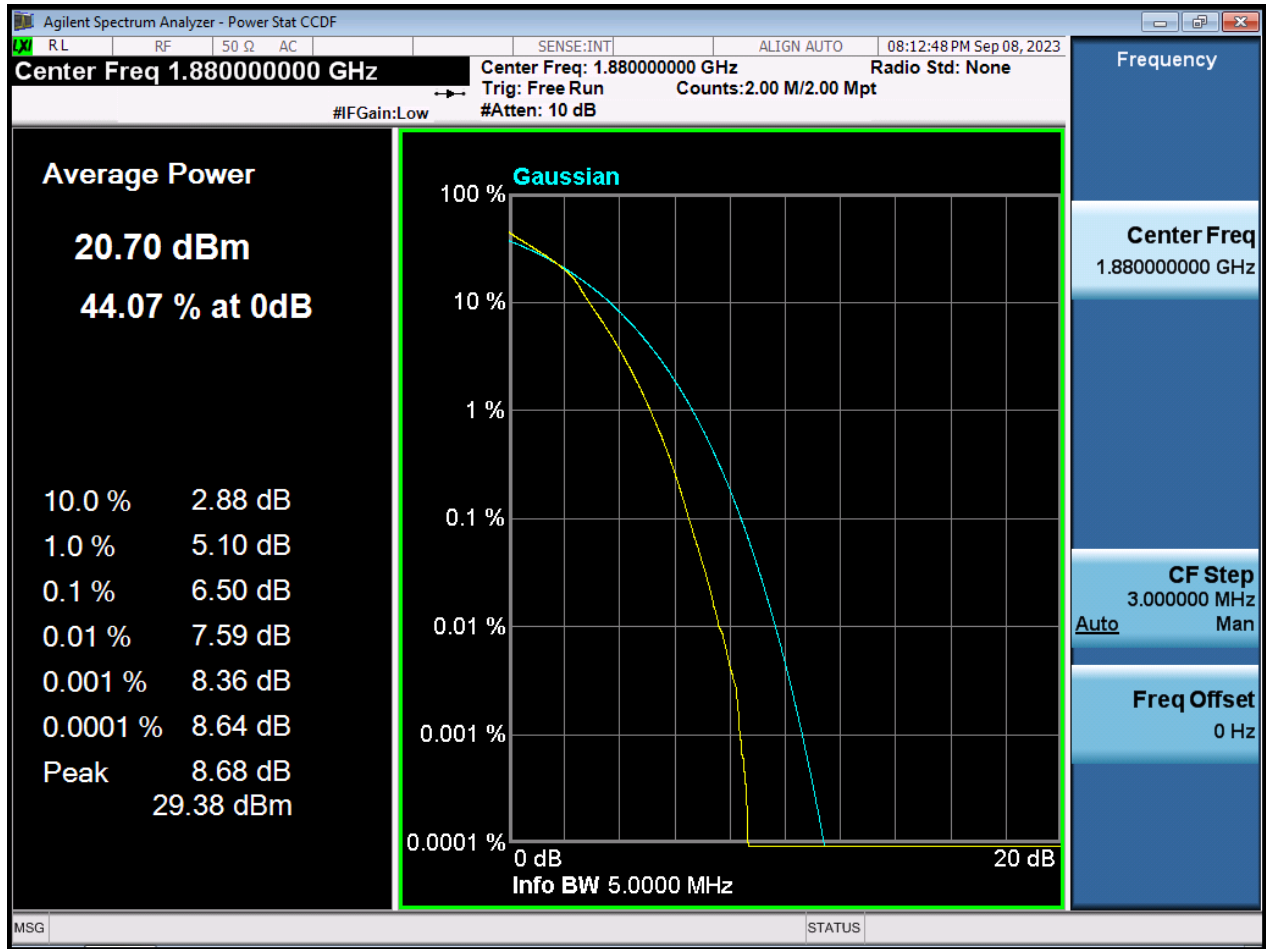
BW3 M_PAR_Middle Channel_256QAM_Full RB



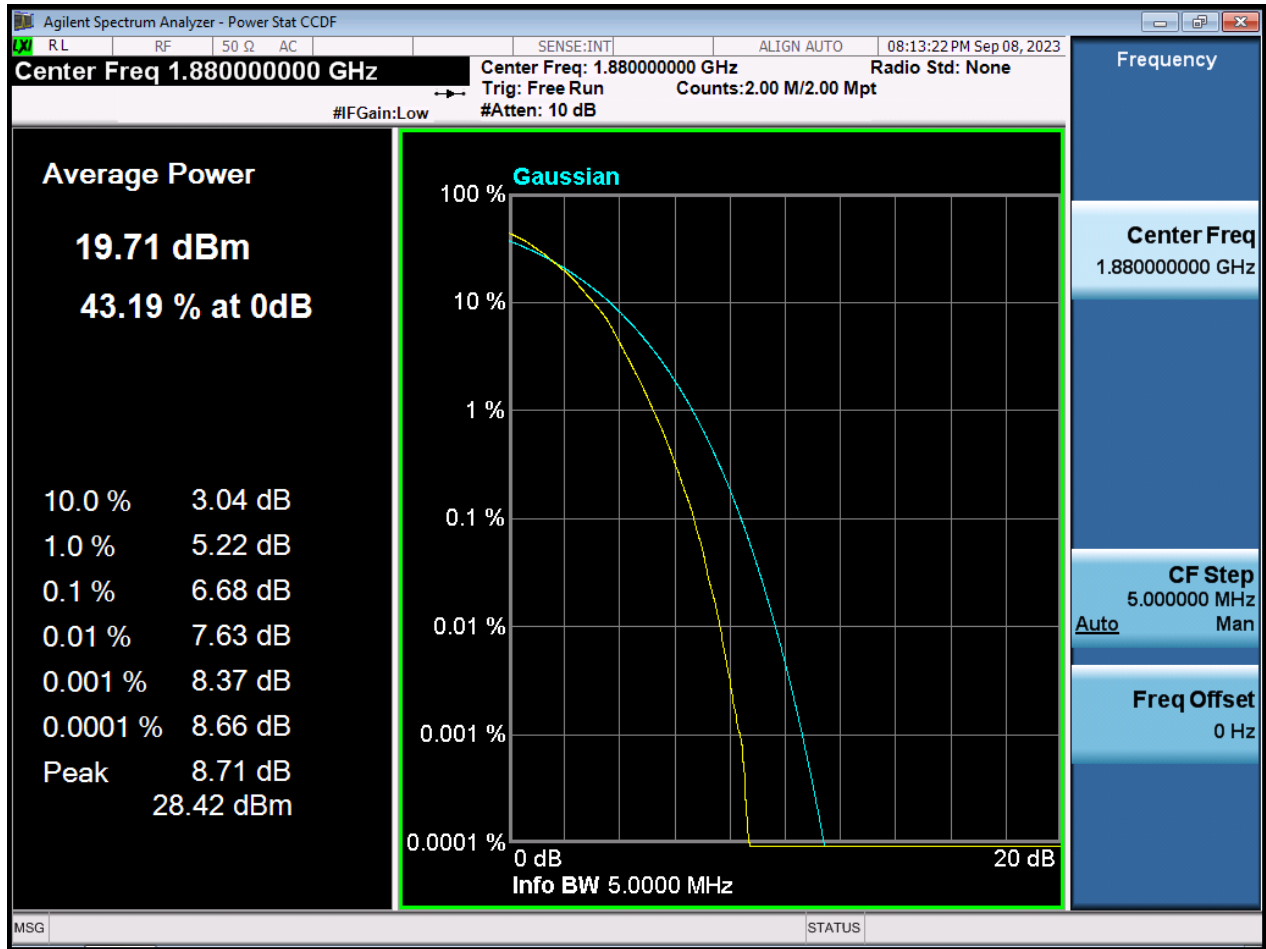
BW5 M_PAR_Middle Channel_QPSK_Full RB



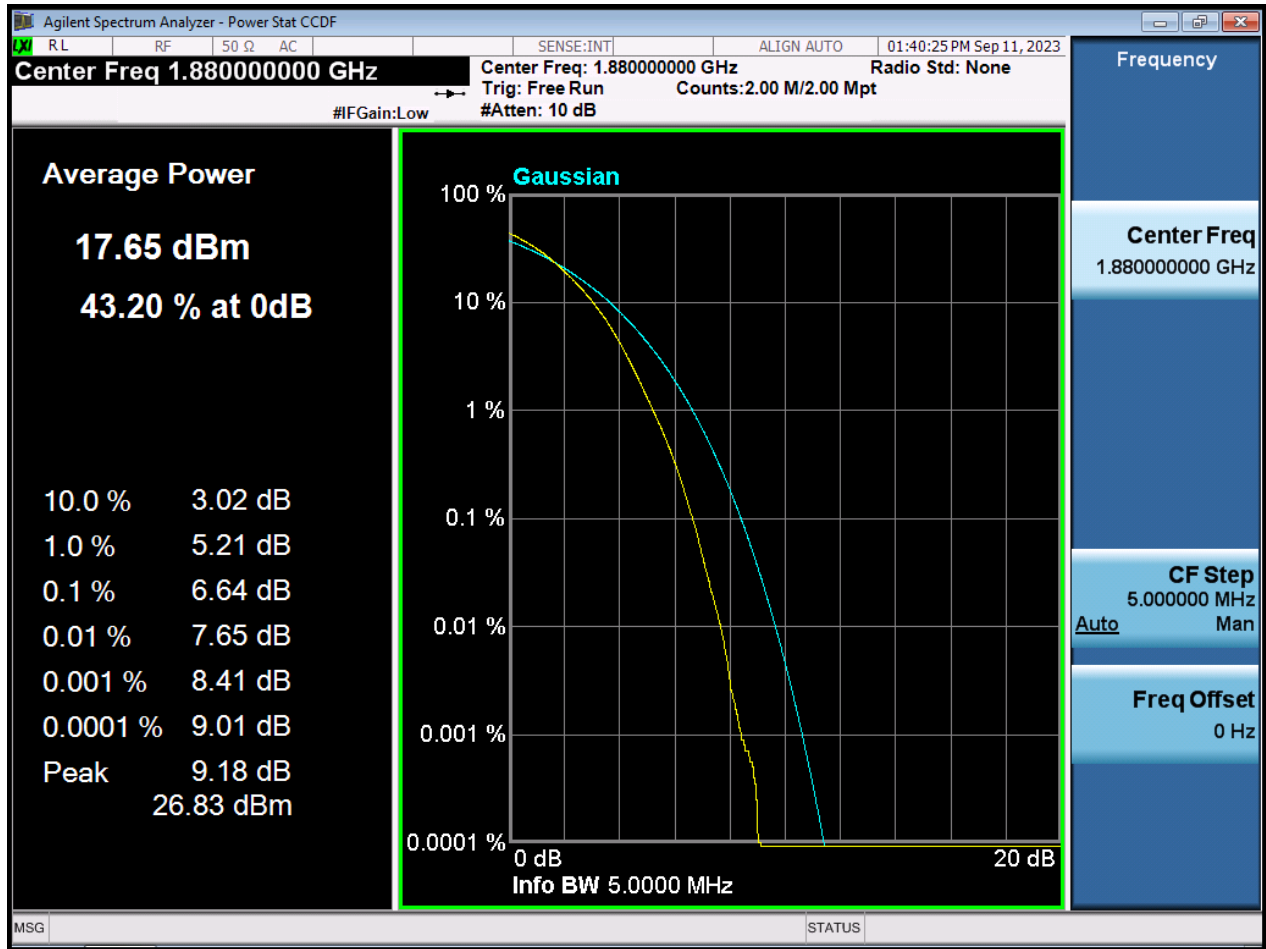
BW5 M_PAR_Middle Channel_16QAM_Full RB



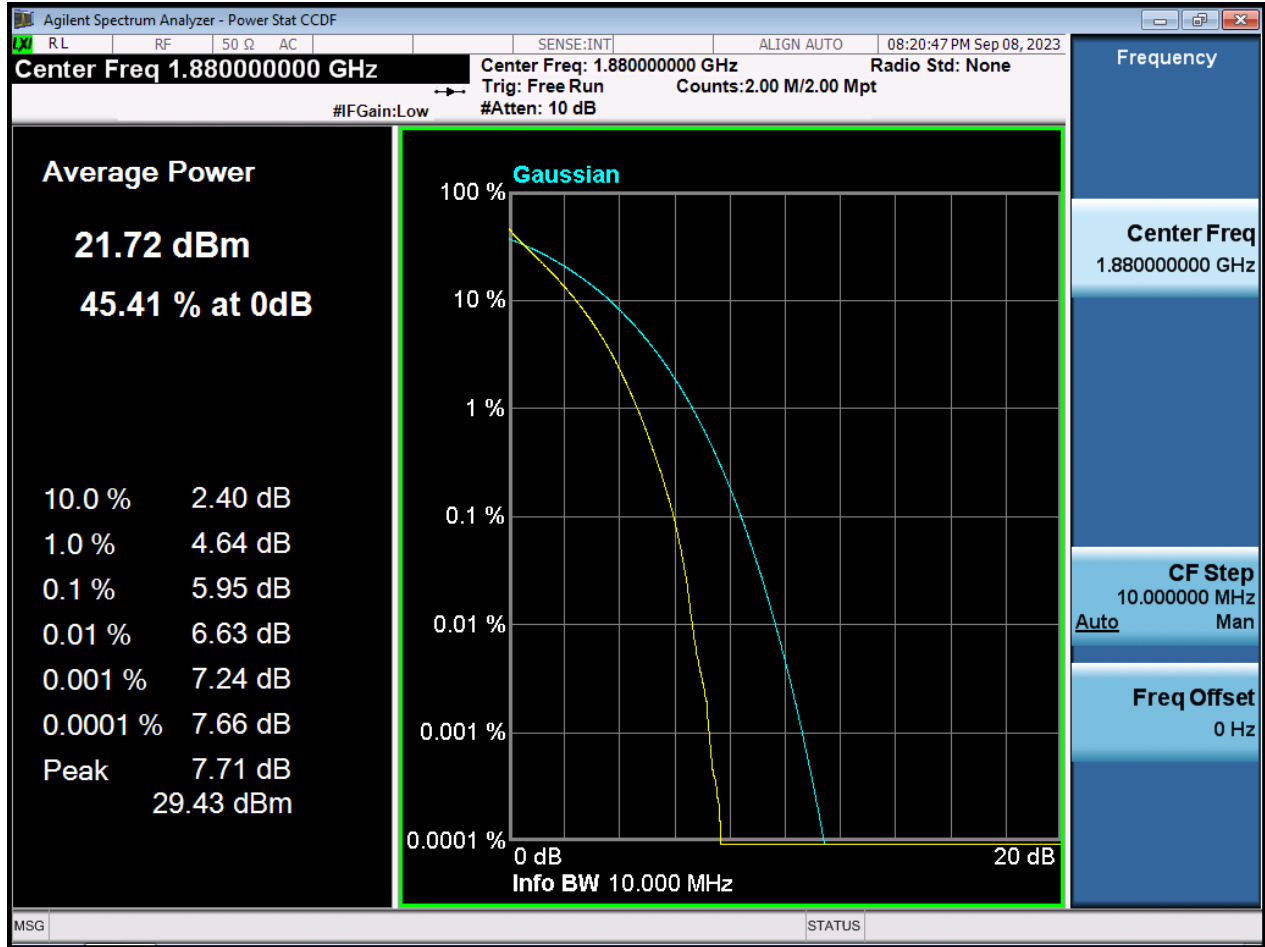
BW5 M_PAR_Middle Channel_64QAM_Full RB



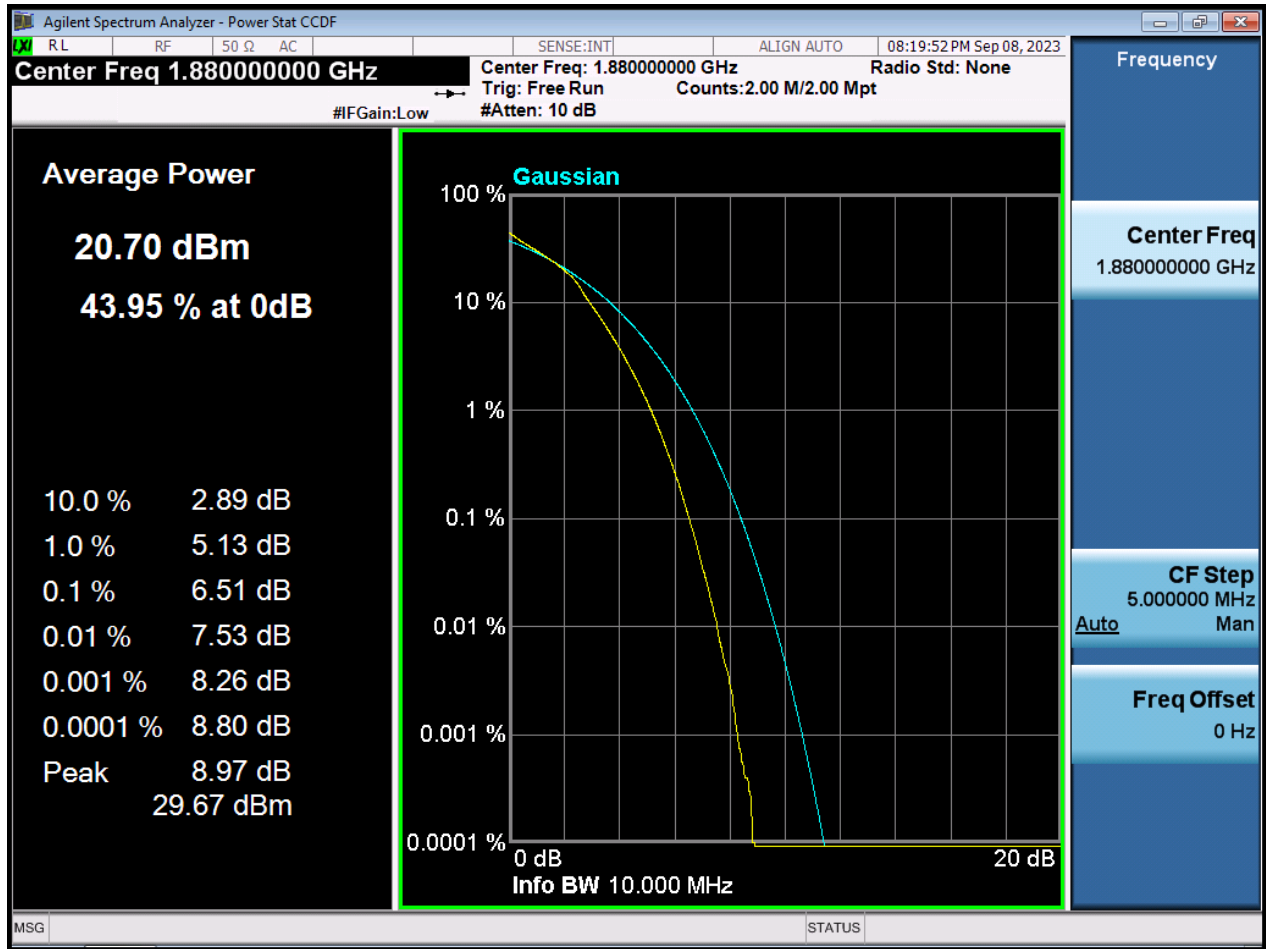
BW5 M_PAR_Middle Channel_256QAM_Full RB



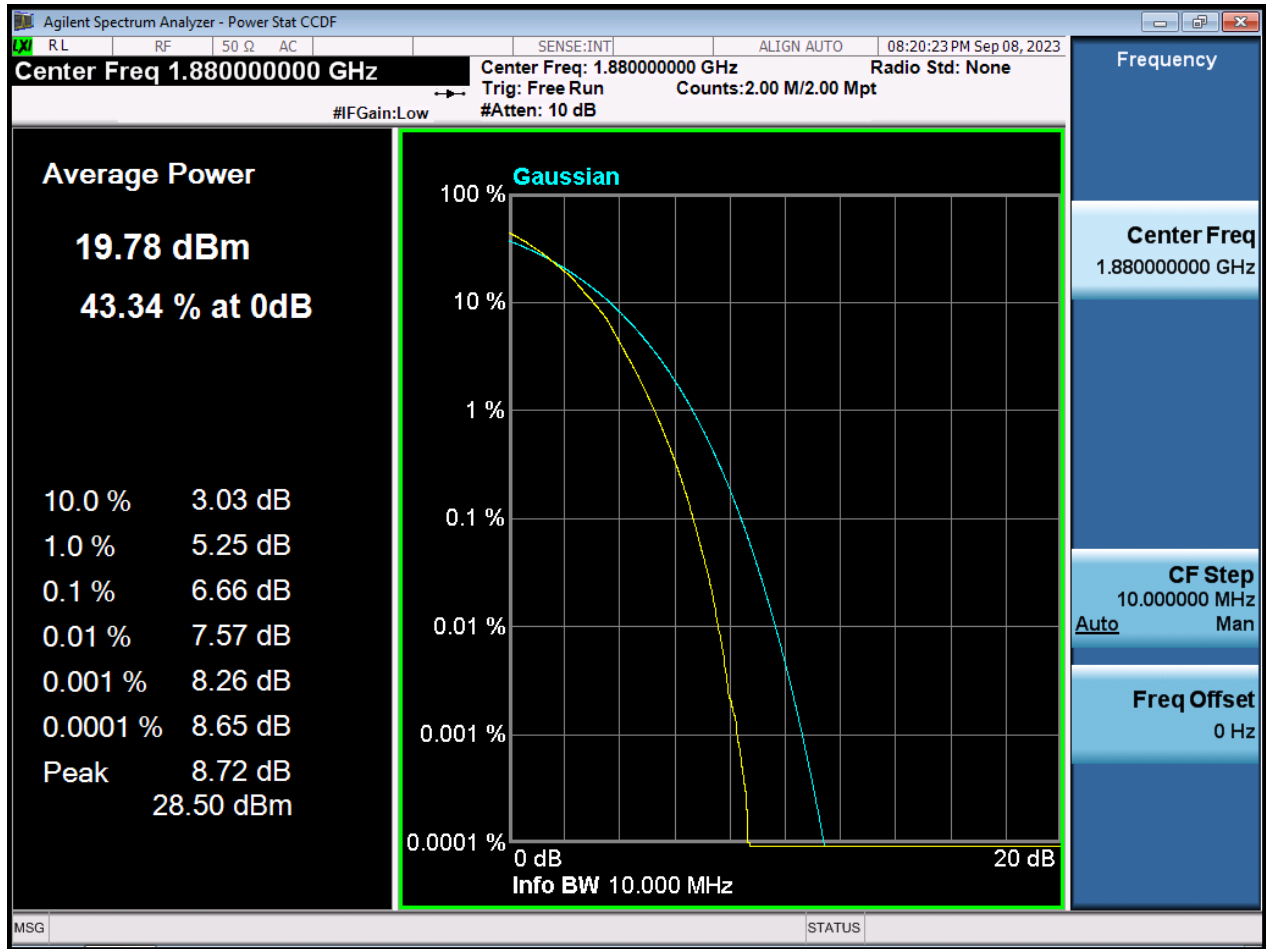
BW10 M_PAR_Middle Channelz_QPSK_Full RB



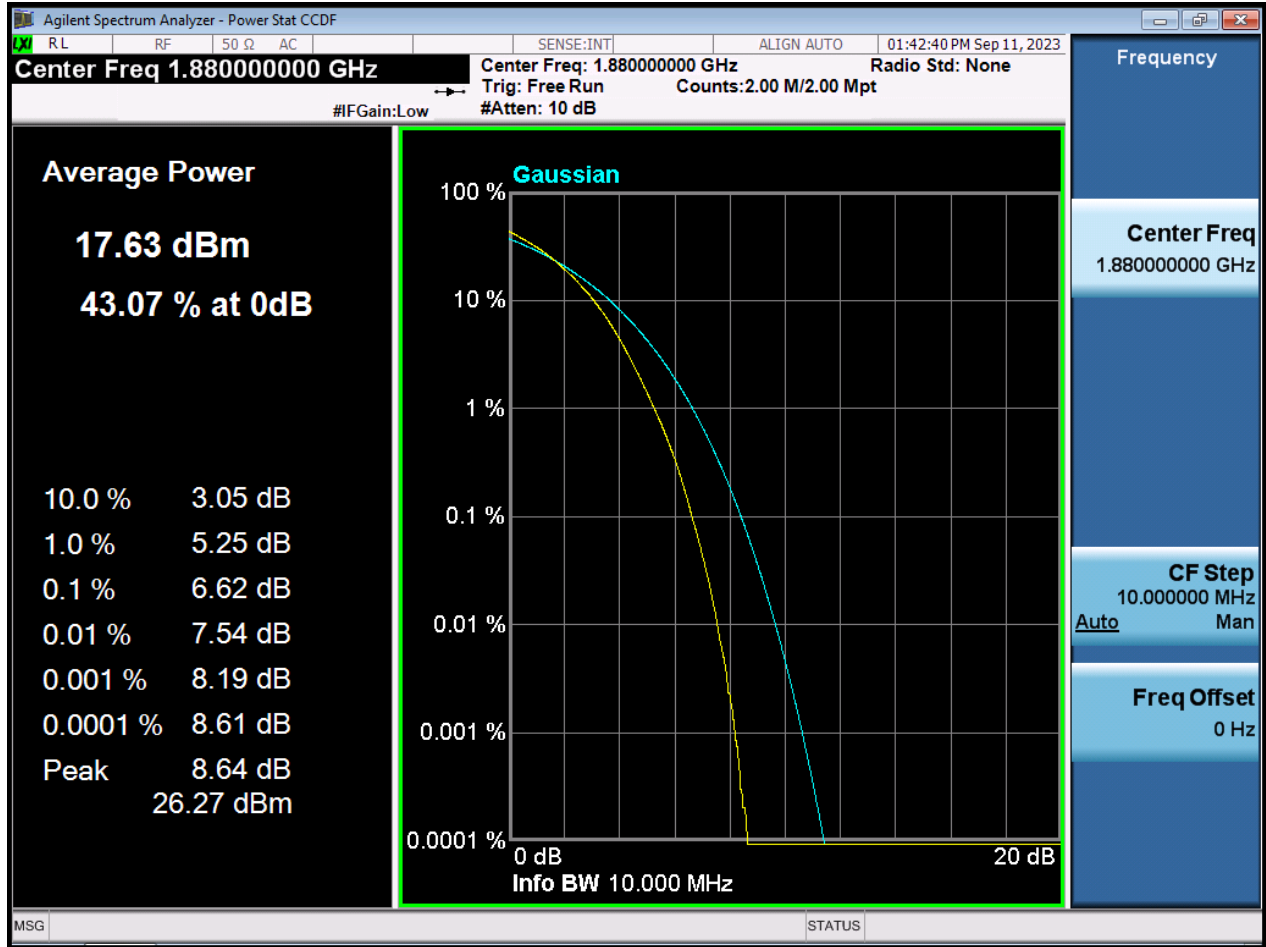
BW10 M_PAR_Middle Channel_16QAM_Full RB



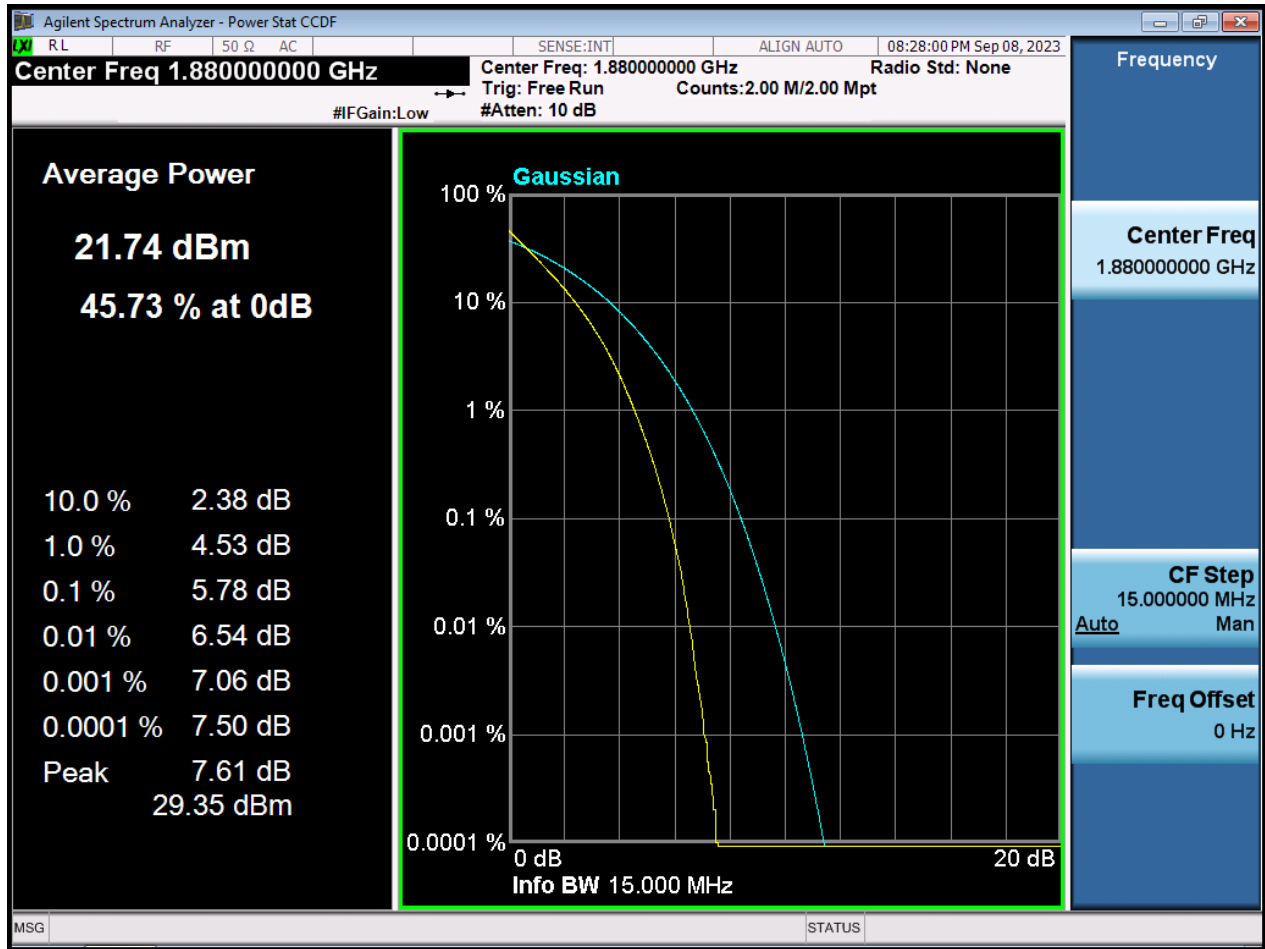
BW10 M_PAR_Middle Channel_64QAM_Full RB



BW10 M_PAR_Middle Channel_256QAM_Full RB



BW15 M_PAR_Middle Channel_QPSK_Full RB



BW15 M_PAR_Middle Channel_16QAM_Full RB

