Introduction of multiple chipset version test report

Basic		
Model	SM-S906B	
FCC ID	A3LSMS906B	

In order to use both of Renesas, S.LSI MFC IC chipset simultaneously, there are some detailed changes and you can find the specific information in the operational description exhibits. These changes have been approved as within the scope of permissive changes for this FCC ID via KDB inquiry.

Version 1	PCB Rev0.7 - Renesas (P9320)
Version 2	PCB Rev 0.7A - Renesas (P9320)
Version 3	PCB Rev 0.7A - S.LSI (S2MIW04)

All compliance tests were performed on the device with the WPT PCB Version 1. Preliminary tests on the device with version 2 and with version 3 PCBs determined that these changes only impacted the WPT(equipment code DCD), digital device(JBP) and SAR RF exposure values in some bands. The WPT EMC and RF exposure test reports have been revised to include data for all three versions and the JBP filing includes a report for version 1 and a report for versions 2 and 3 as detailed below. The SAR report has also been updated. Spot checks on the SAR and IPD values for the 6GHz Wi-Fi bands was checked and confirmed that the different versions did not increase the

Additional issued & Updated test report

values reported for the test on versions 1.

	Report number	Note.
Additional issued	4790089626-E12 FCC Report Part 15B(JBP)	Version 2 & 3 tested (Version 1 test report: LBE20210654)
Updated	4790089626-E9 FCC Report WPT	Updated Version 2 & 3 tested data
	4790089626-E10 FCC Report MPE	Updated Version 2 & 3 tested data
		Spot check data highlighting bands with higher SAR values for version 2 and 3 are added in section 12 and test data for these bands with higher SAR have been added to section 10. The simultaneous analysis in section 12 has been updated to use the highest SAR values across the three versions.