

n77(3700~3980 MHz)_40 M_PAR_Mid_QPSK_FullRB



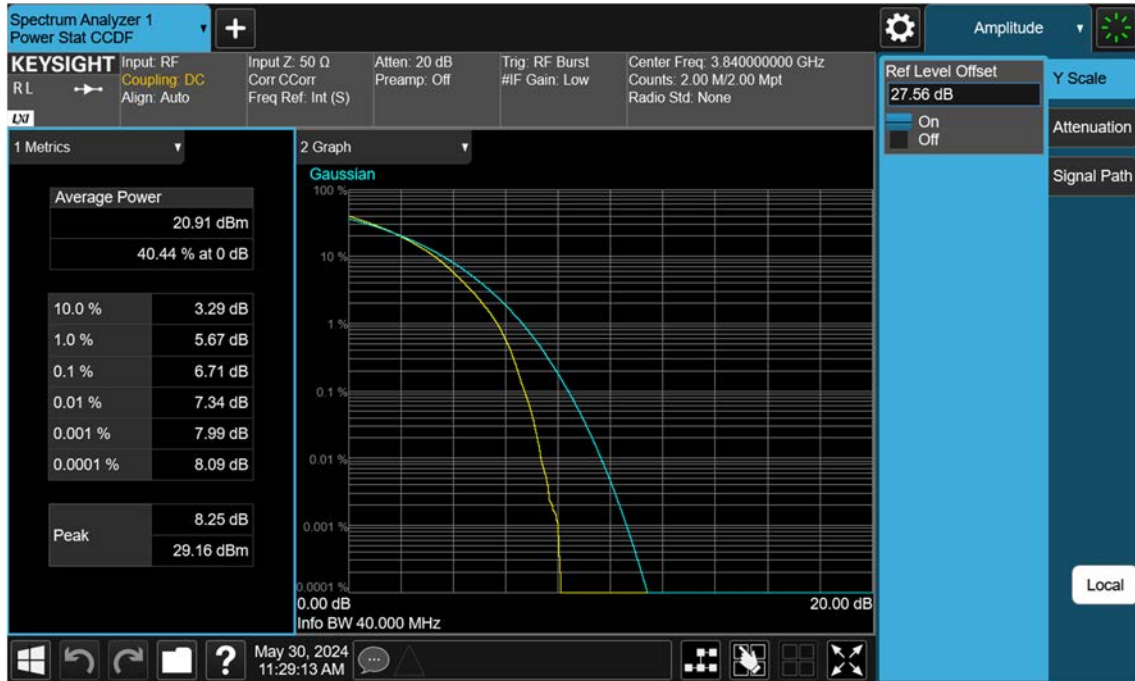
n77(3700~3980 MHz)_40 M_PAR_Mid_16QAM_FullRB



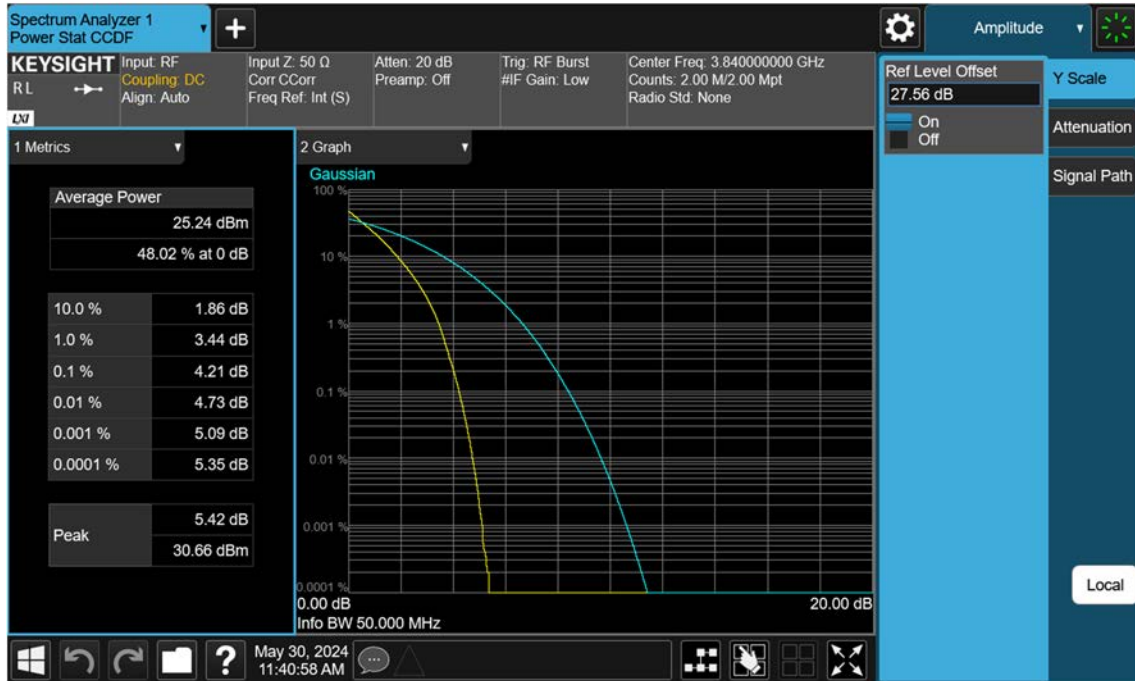
n77(3700~3980 MHz)_40 M_PAR_Mid_64QAM_FullRB



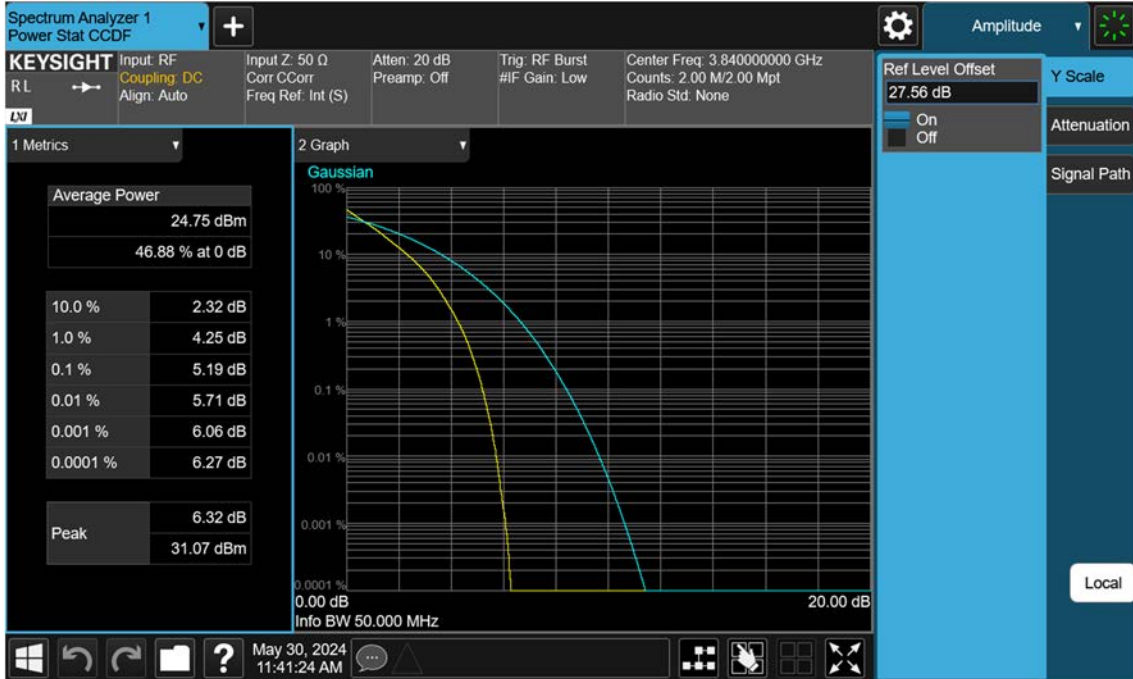
n77(3700~3980 MHz)_40 M_PAR_Mid_256QAM_FullRB



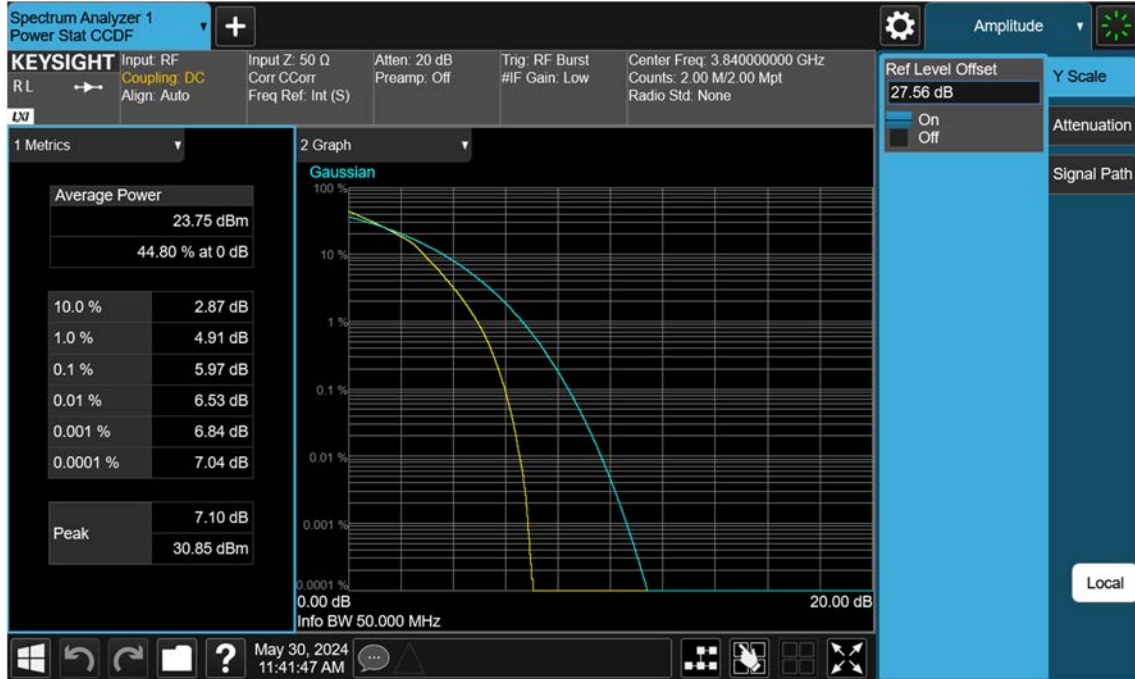
n77(3700~3980 MHz)_50 M_PAR_Mid_BPSK_FullRB



n77(3700~3980 MHz)_50 M_PAR_Mid_QPSK_FullRB



n77(3700~3980 MHz)_50 M_PAR_Mid_16QAM_FullRB



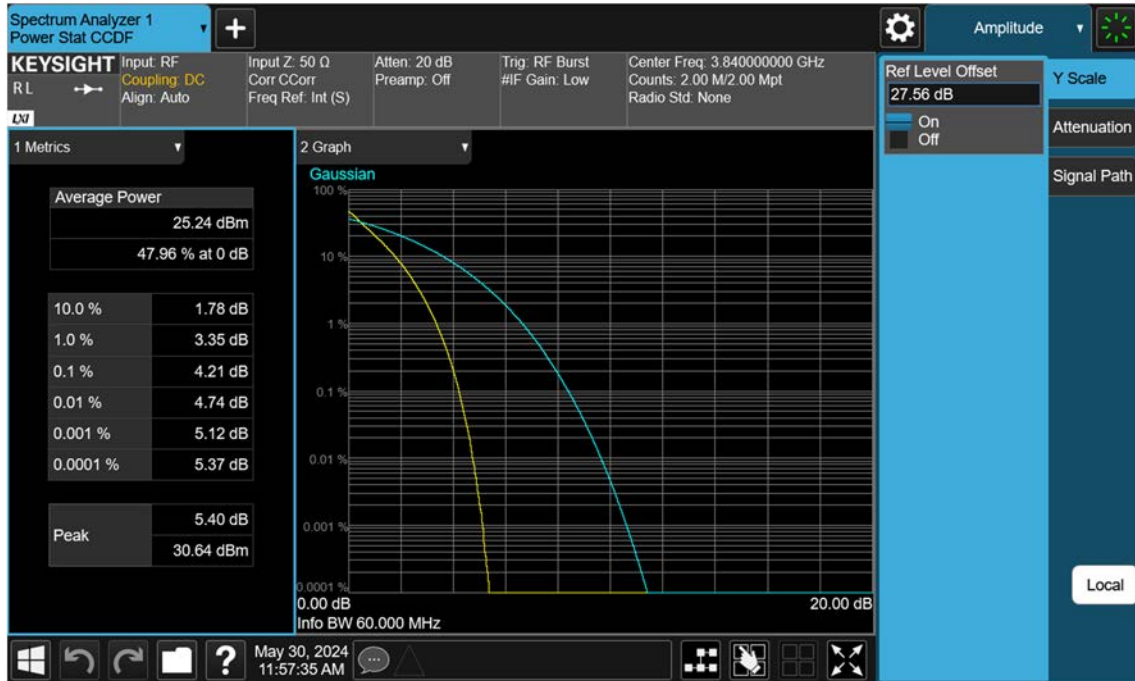
n77(3700~3980 MHz)_50 M_PAR_Mid_64QAM_FullRB



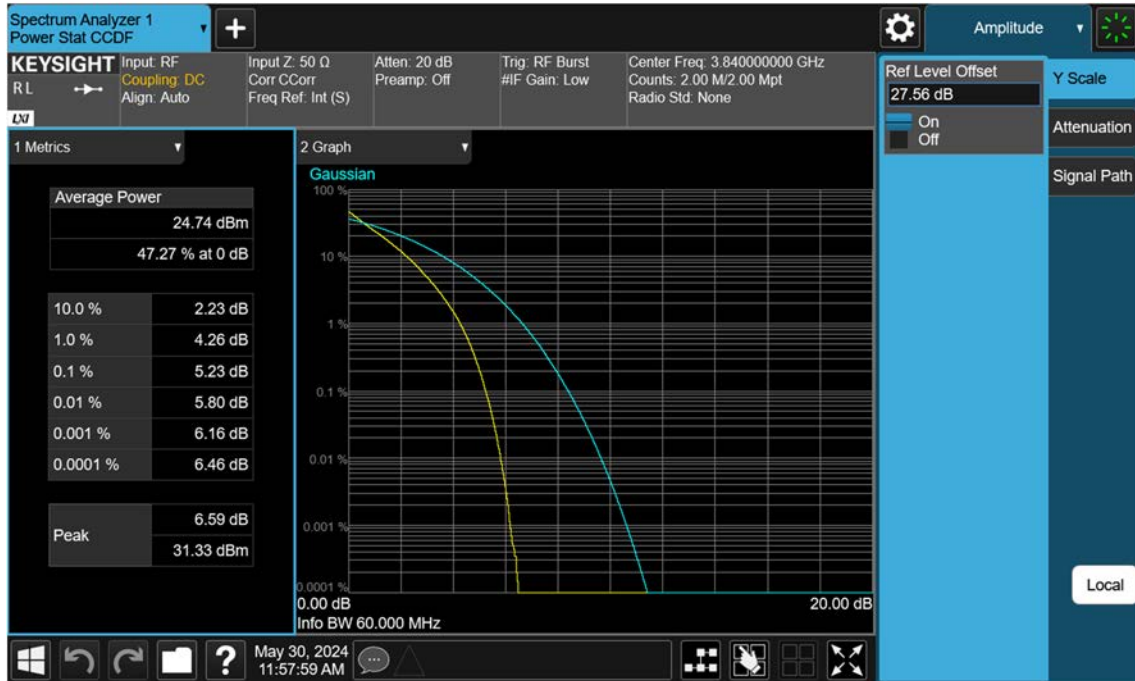
n77(3700~3980 MHz)_50 M_PAR_Mid_256QAM_FullRB



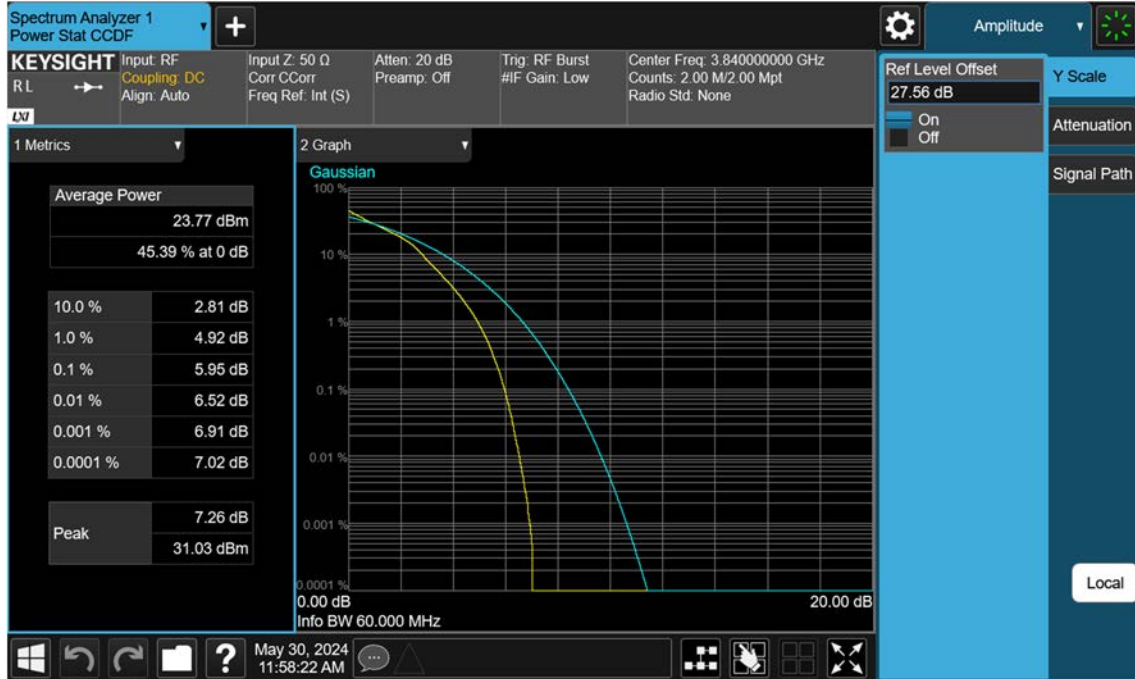
n77(3700~3980 MHz)_60 M_PAR_Mid_BPSK_FullRB



n77(3700~3980 MHz)_60 M_PAR_Mid_QPSK_FullRB



n77(3700~3980 MHz)_60 M_PAR_Mid_16QAM_FullRB



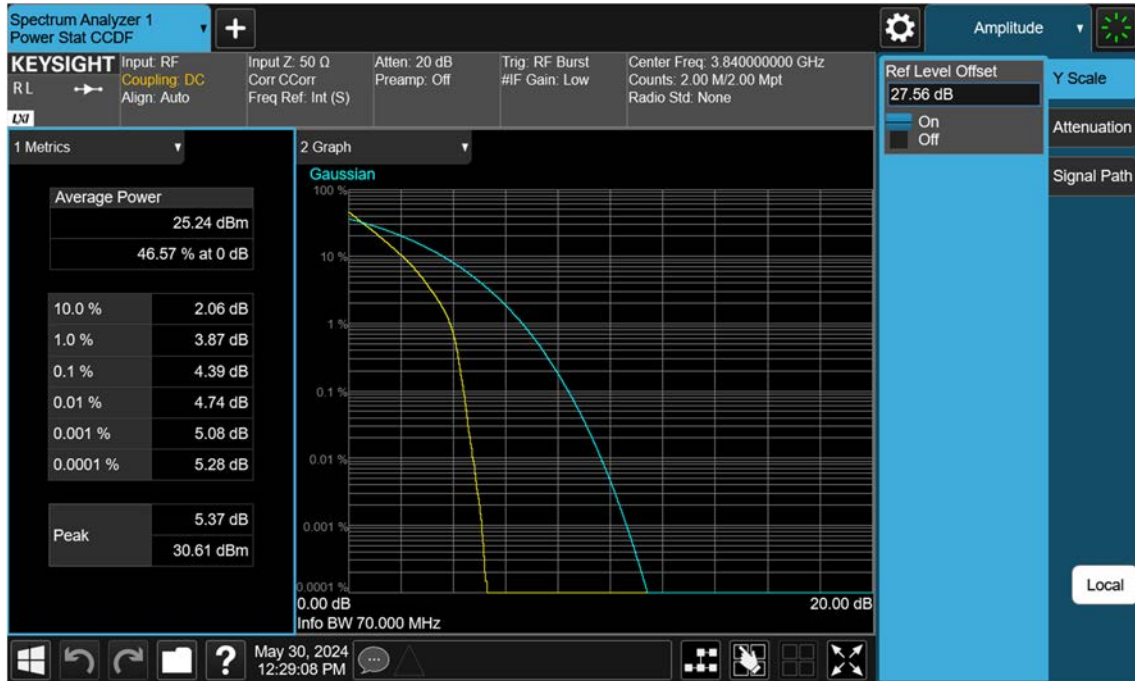
n77(3700~3980 MHz)_60 M_PAR_Mid_64QAM_FullRB



n77(3700~3980 MHz)_60 M_PAR_Mid_256QAM_FullRB



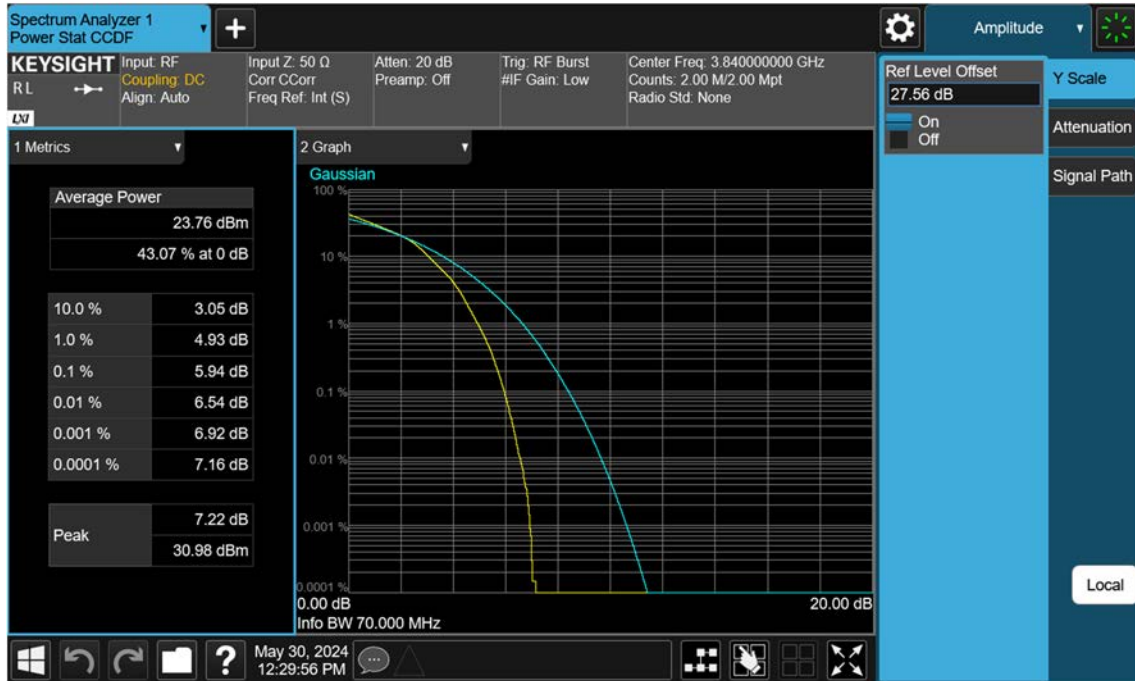
n77(3700~3980 MHz)_70 M_PAR_Mid_BPSK_FullRB



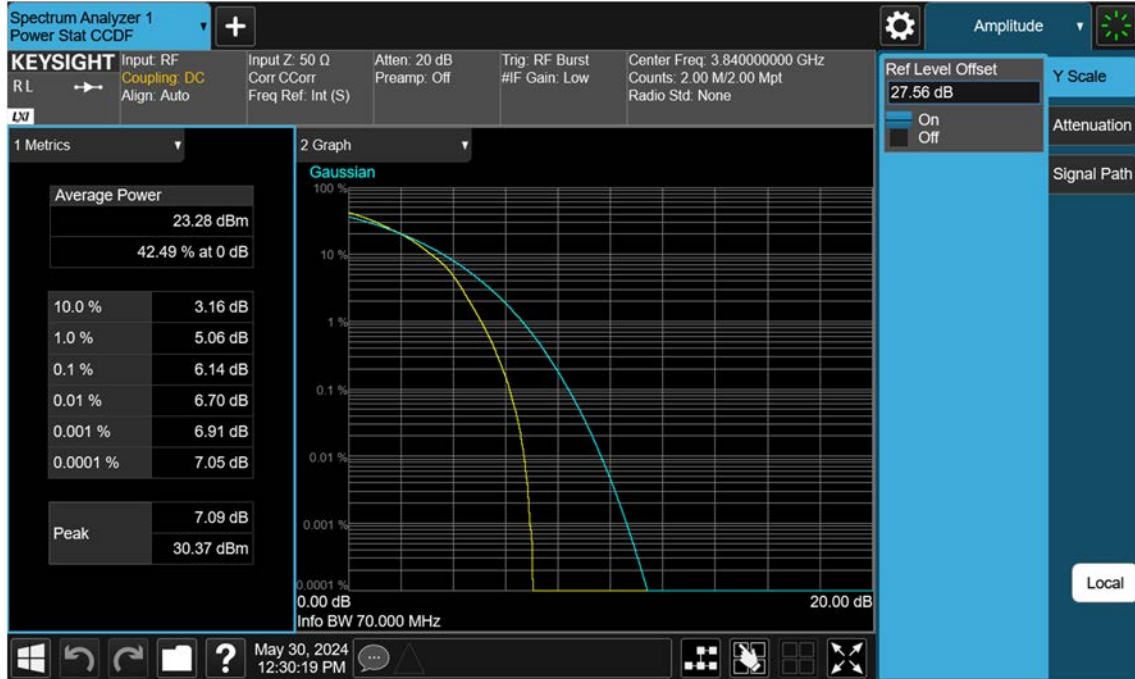
n77(3700~3980 MHz)_70 M_PAR_Mid_QPSK_FullRB



n77(3700~3980 MHz)_70 M_PAR_Mid_16QAM_FullRB



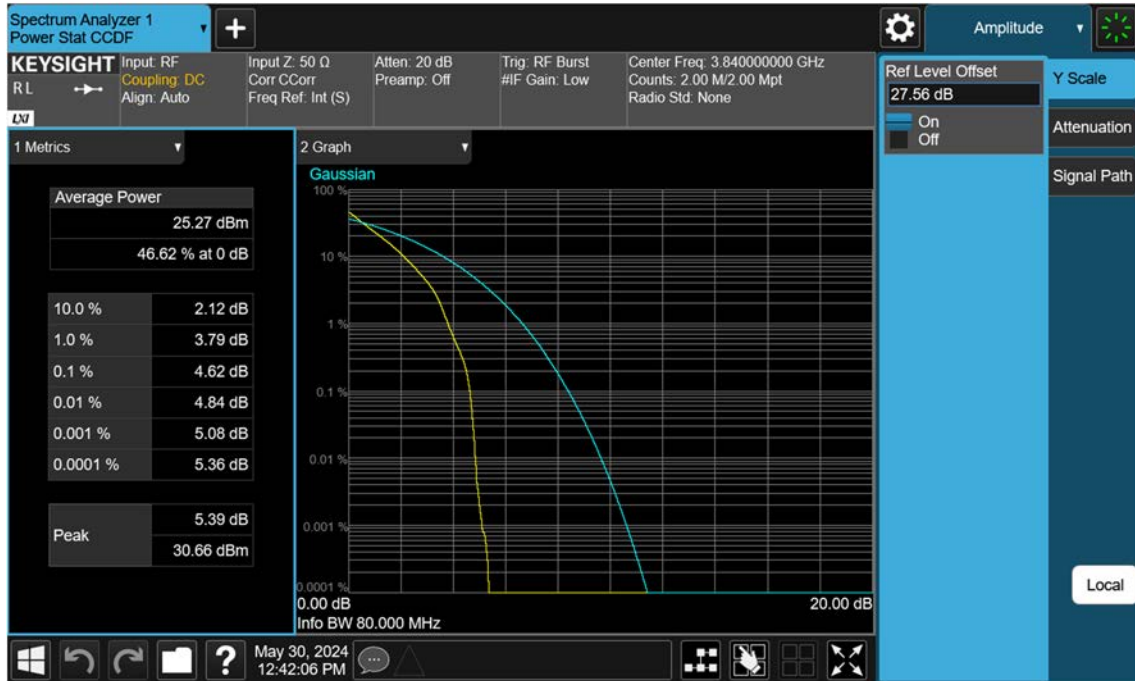
n77(3700~3980 MHz)_70 M_PAR_Mid_64QAM_FullRB



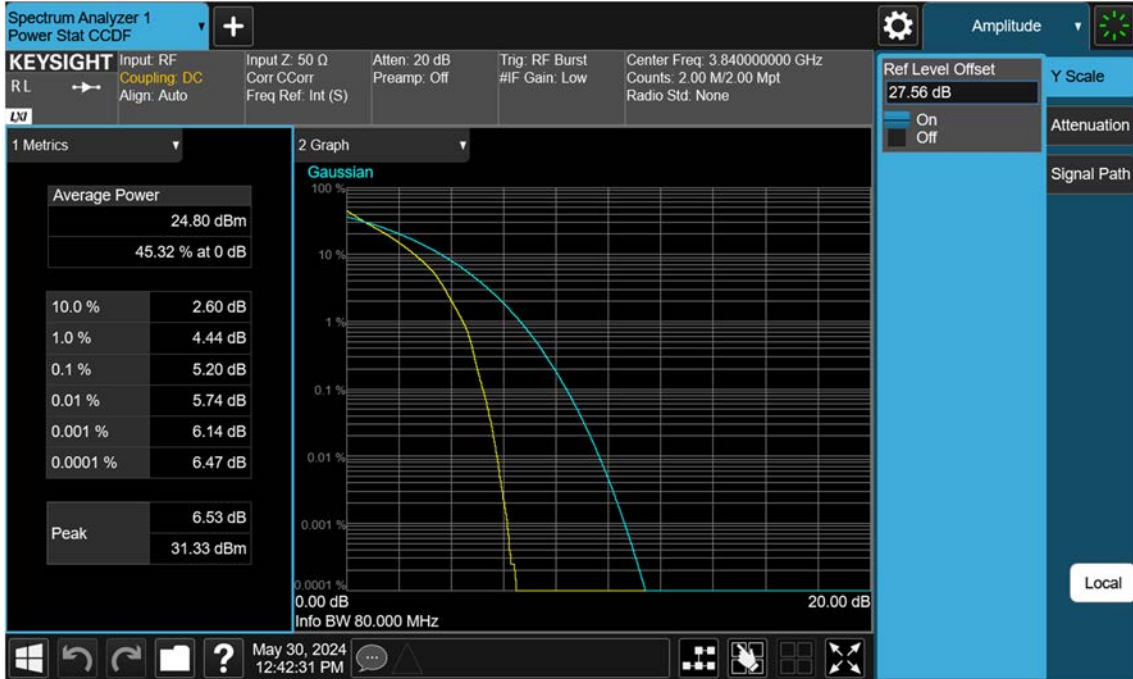
n77(3700~3980 MHz)_70 M_PAR_Mid_256QAM_FullRB



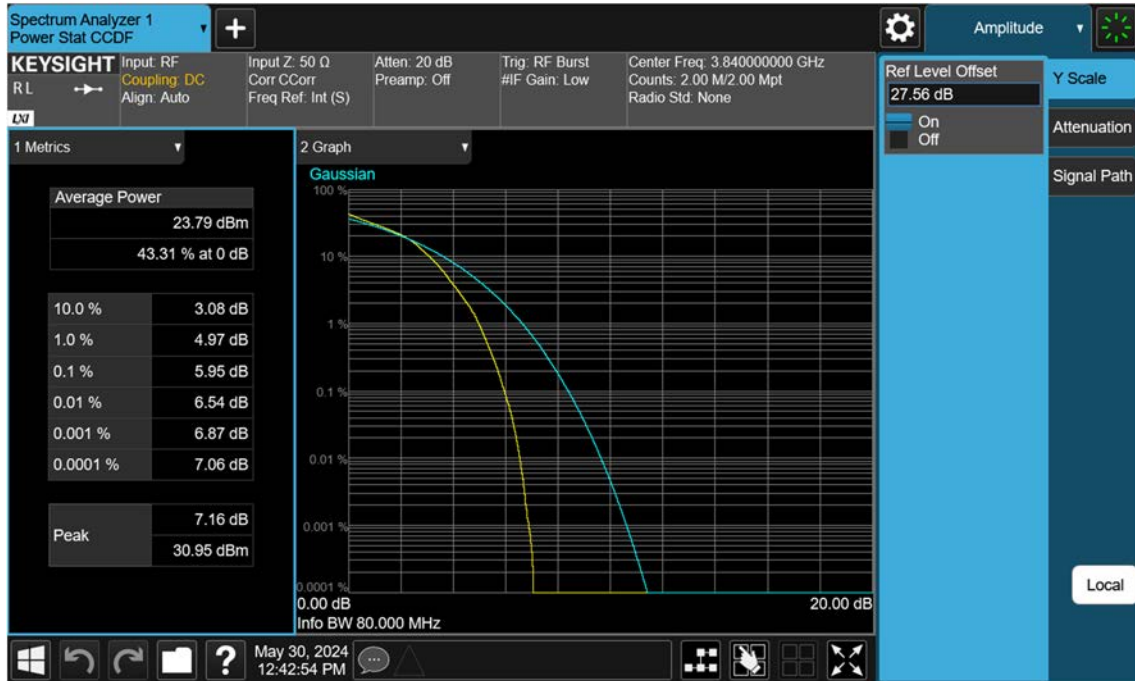
n77(3700~3980 MHz)_80 M_PAR_Mid_BPSK_FullRB



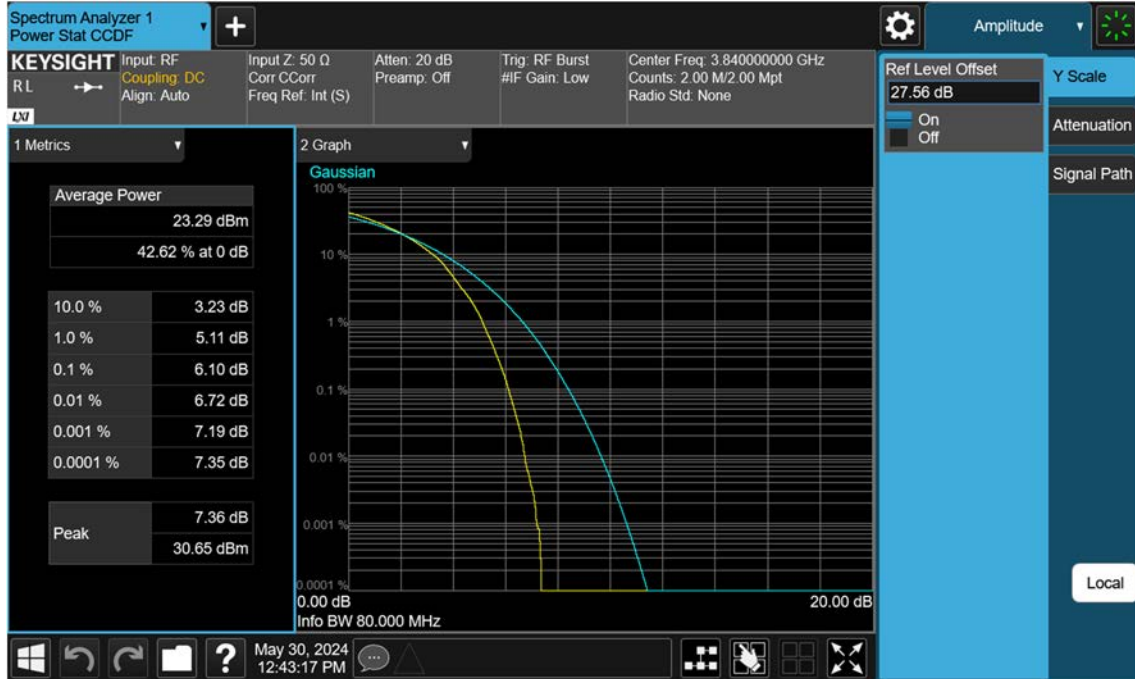
n77(3700~3980 MHz)_80 M_PAR_Mid_QPSK_FullRB



n77(3700~3980 MHz)_80 M_PAR_Mid_16QAM_FullRB



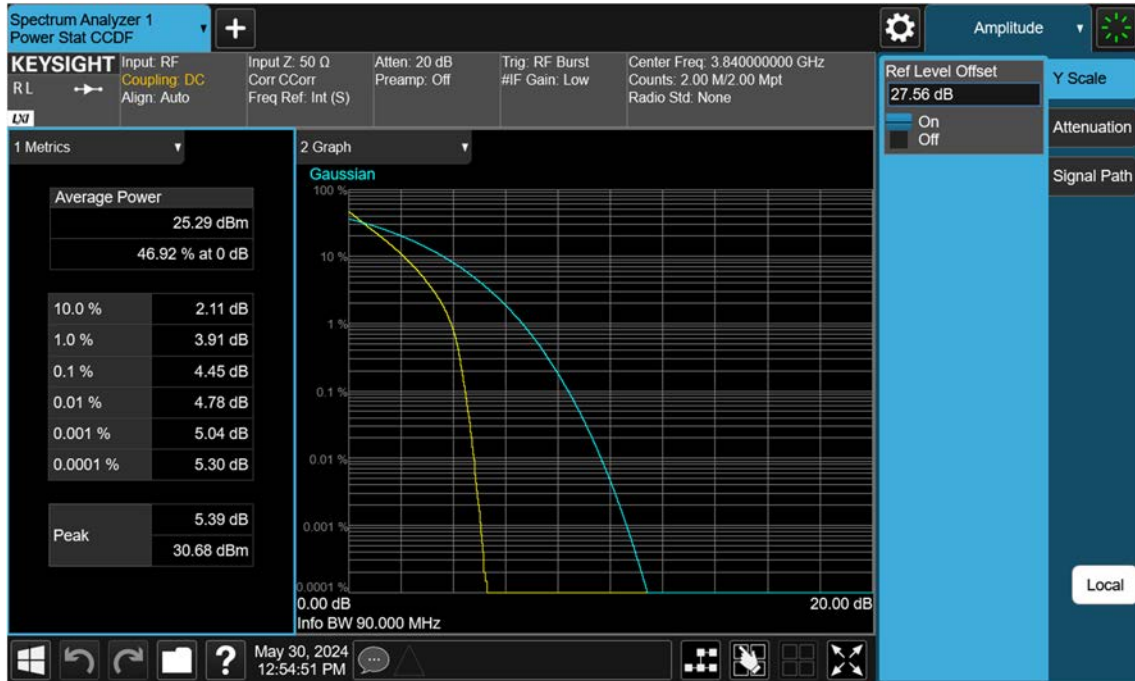
n77(3700~3980 MHz)_80 M_PAR_Mid_64QAM_FullRB



n77(3700~3980 MHz)_80 M_PAR_Mid_256QAM_FullRB



n77(3700~3980 MHz)_90 M_PAR_Mid_BPSK_FullRB



n77(3700~3980 MHz)_90 M_PAR_Mid_QPSK_FullRB



n77(3700~3980 MHz)_90 M_PAR_Mid_16QAM_FullRB



n77(3700~3980 MHz)_90 M_PAR_Mid_64QAM_FullRB



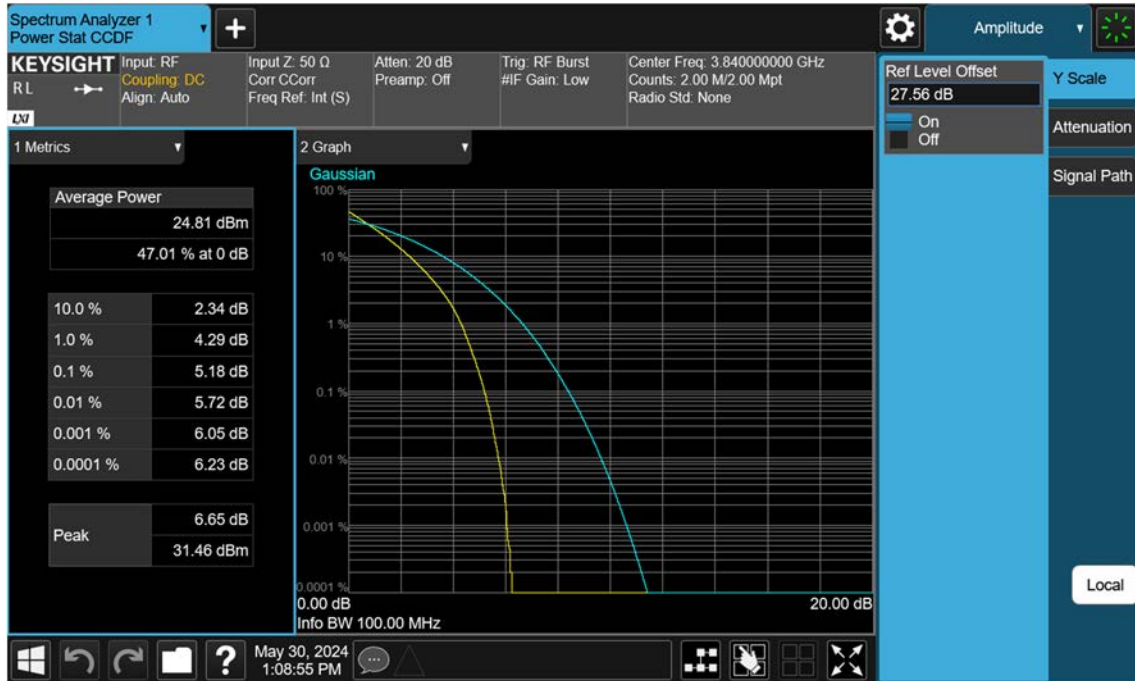
n77(3700~3980 MHz)_90 M_PAR_Mid_256QAM_FullRB



n77(3700~3980 MHz)_100 M_PAR_Mid_BPSK_FullRB



n77(3700~3980 MHz)_100 M_PAR_Mid_QPSK_FullRB



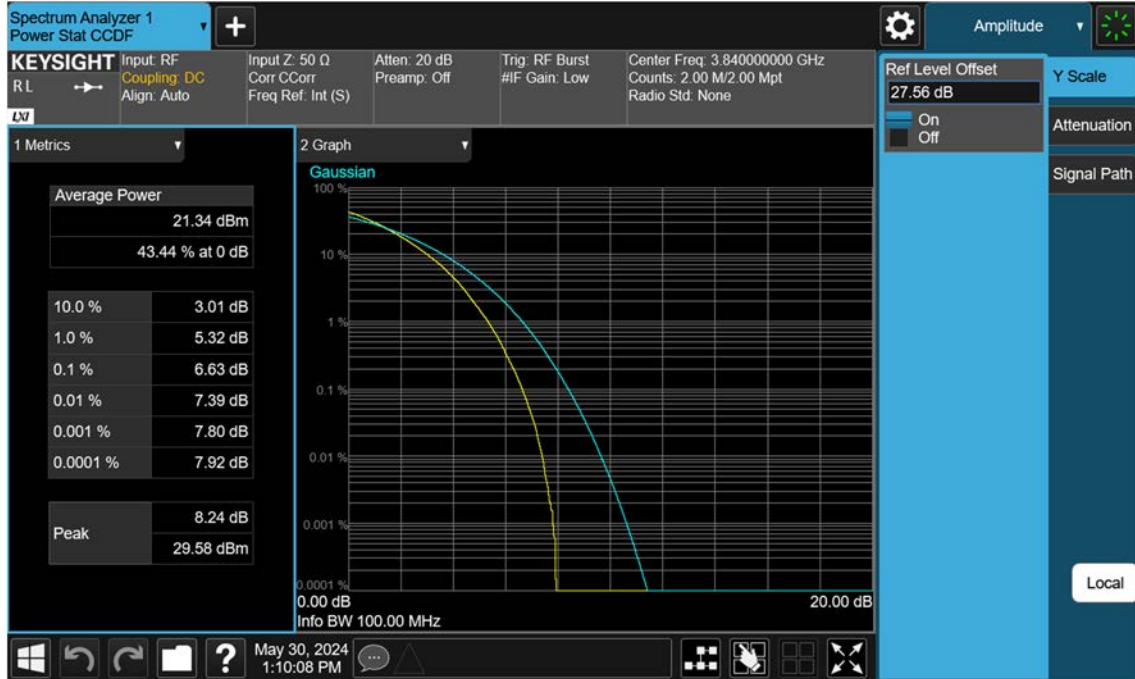
n77(3700~3980 MHz)_100 M_PAR_Mid_16QAM_FullRB



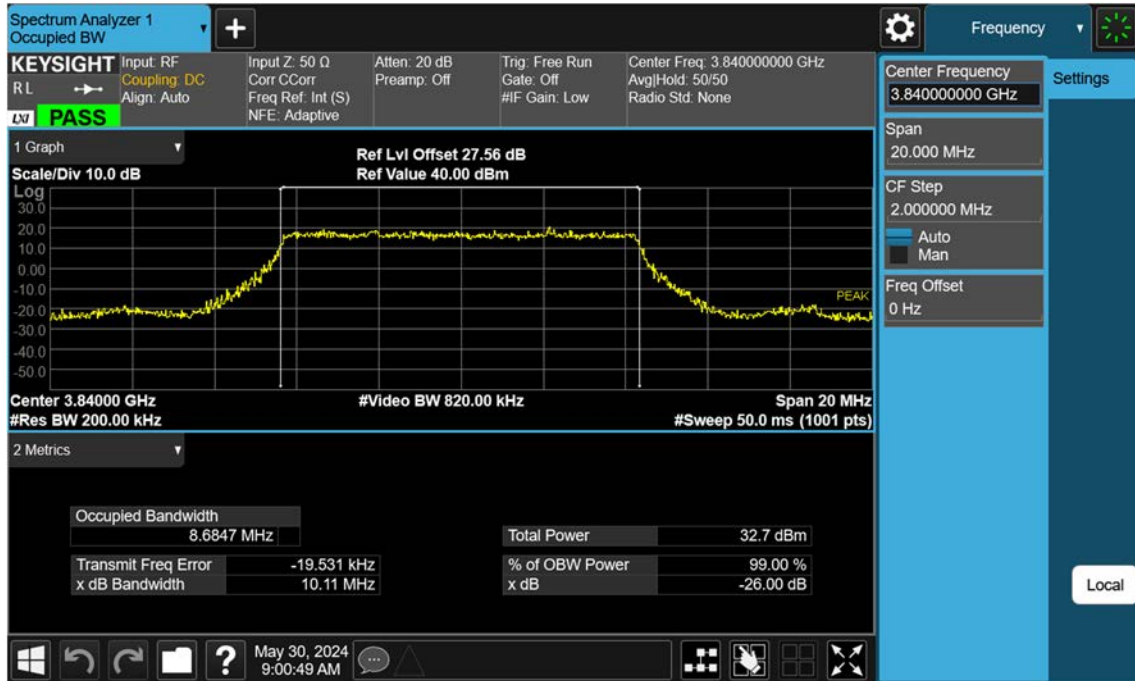
n77(3700~3980 MHz)_100 M_PAR_Mid_64QAM_FullRB



n77(3700~3980 MHz)_100 M_PAR_Mid_256QAM_FullRB



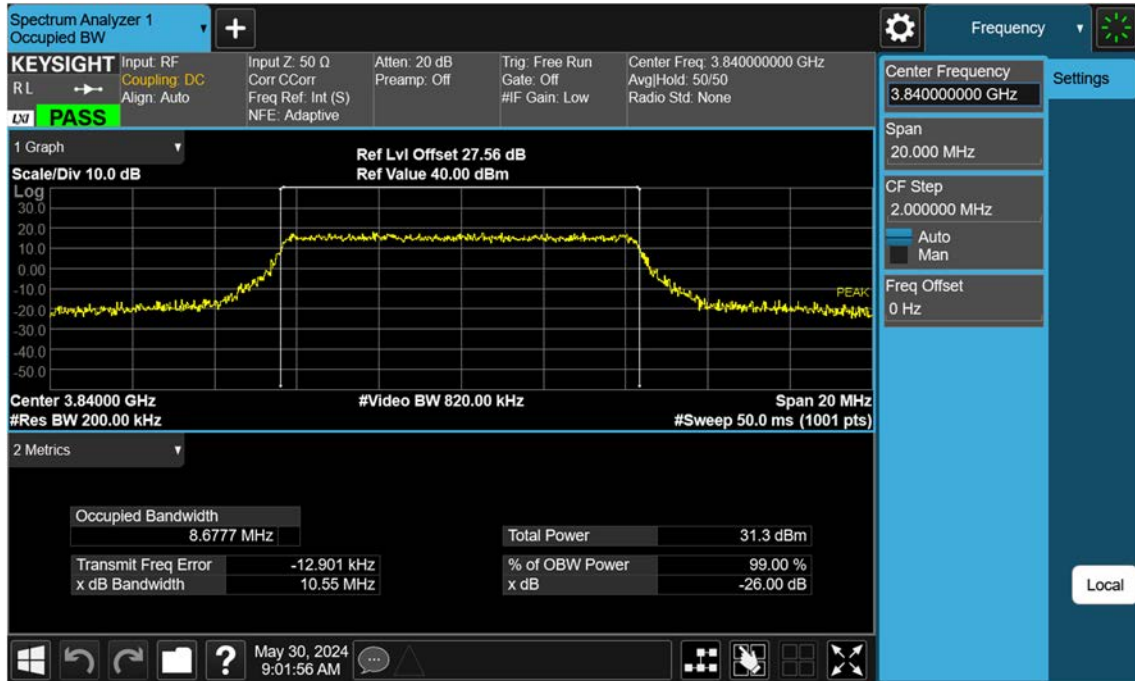
n77(3700~3980 MHz)_10 M_OBW_Mid_BPSK_FullRB



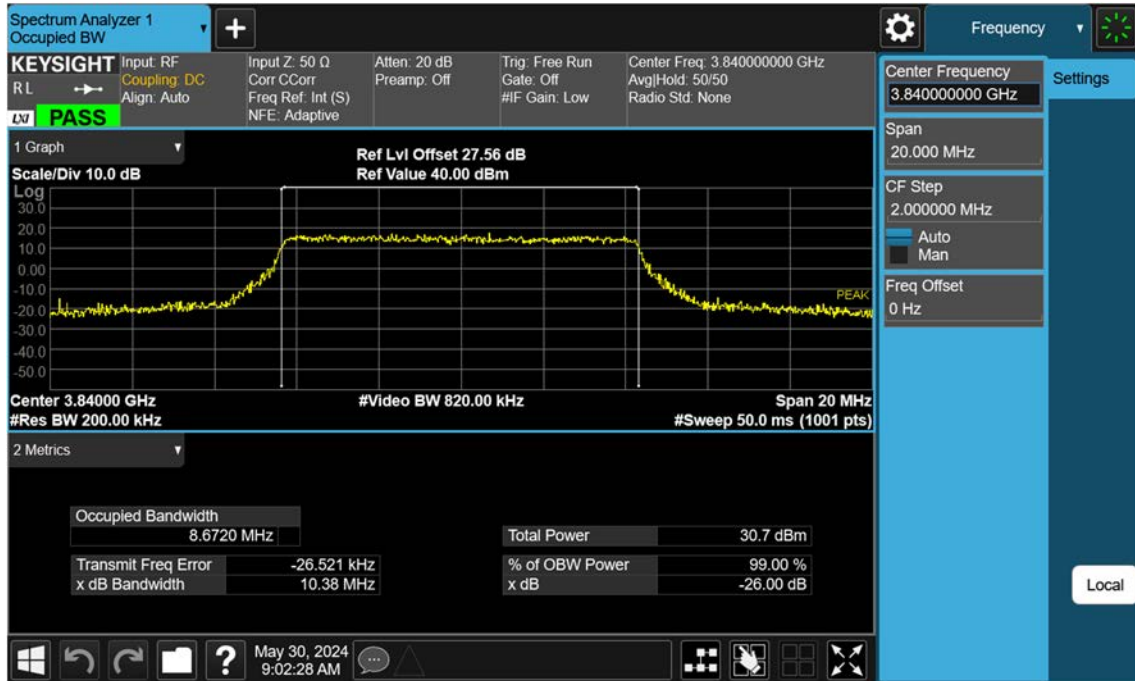
n77(3700~3980 MHz)_10 M_OBW_Mid_QPSK_FullRB



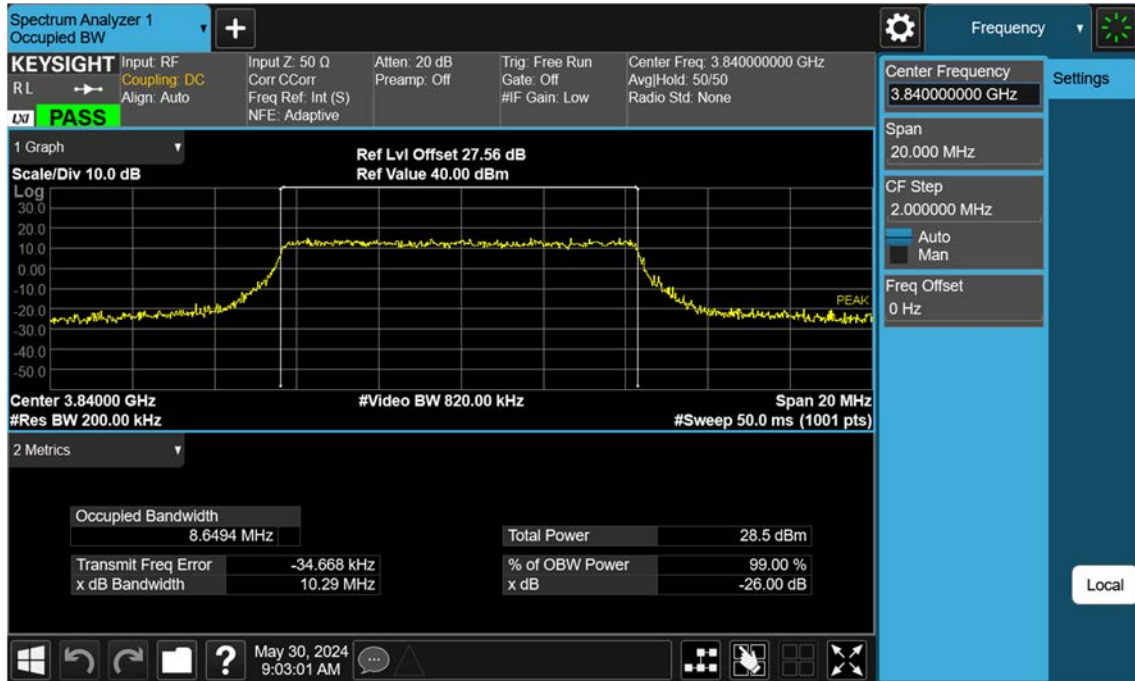
n77(3700~3980 MHz)_10 M_OBW_Mid_16QAM_FullRB



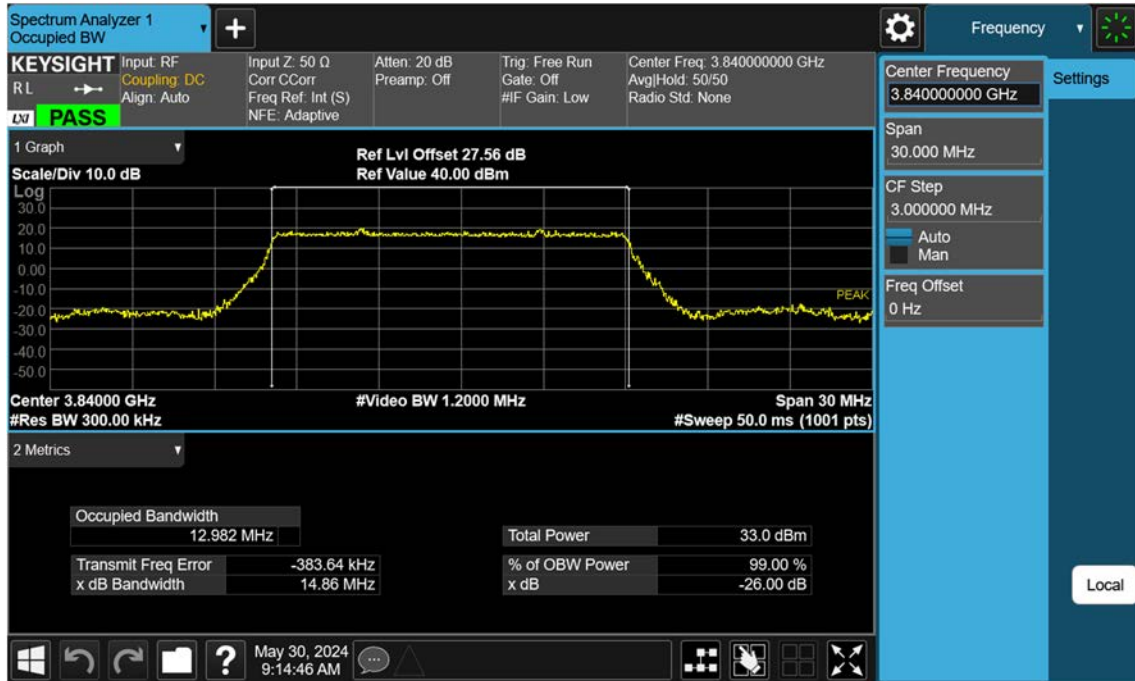
n77(3700~3980 MHz)_10 M_OBW_Mid_64QAM_FullRB



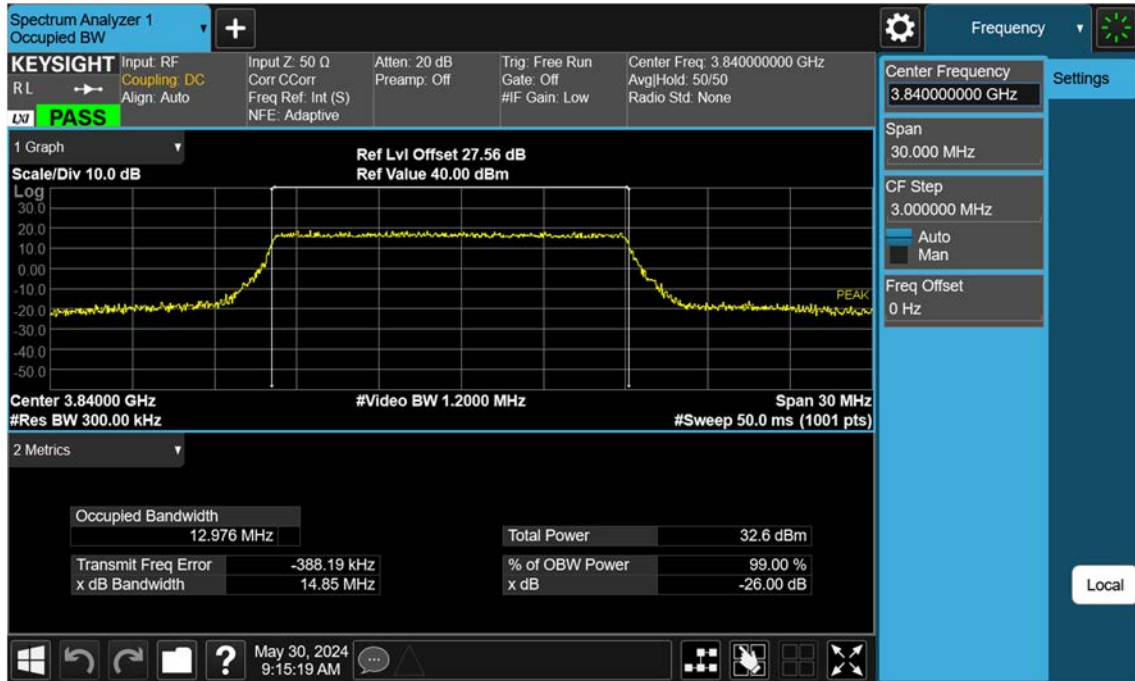
n77(3700~3980 MHz)_10 M_OBW_Mid_256QAM_FullRB



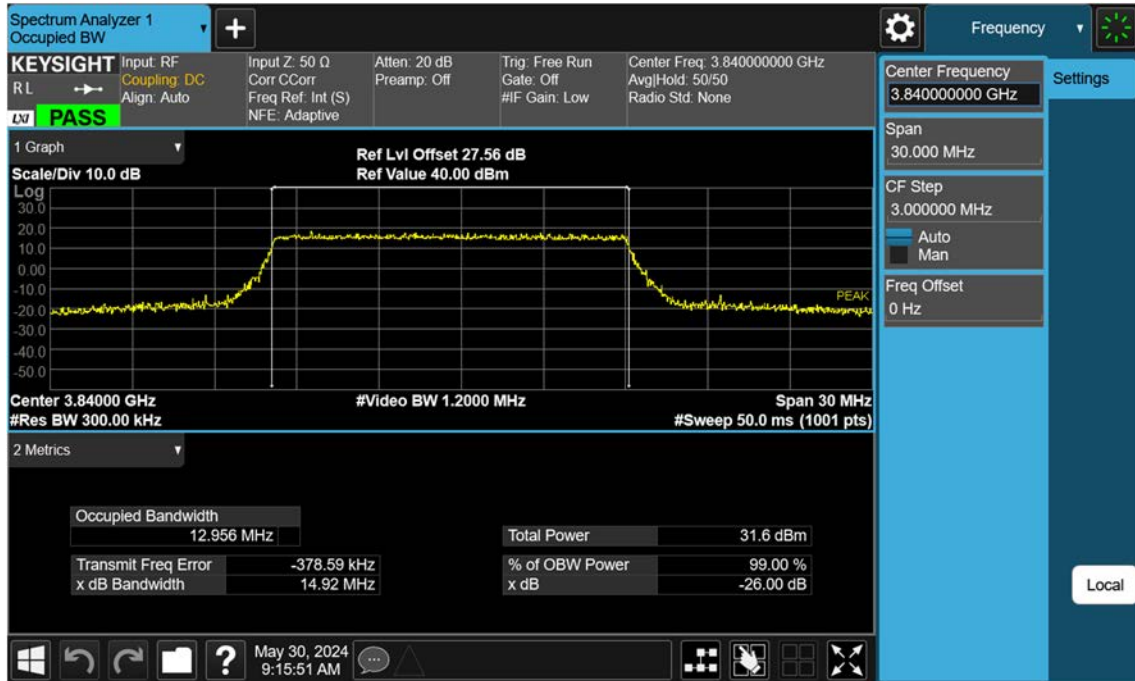
n77(3700~3980 MHz)_15 M_OBW_Mid_BPSK_FullRB



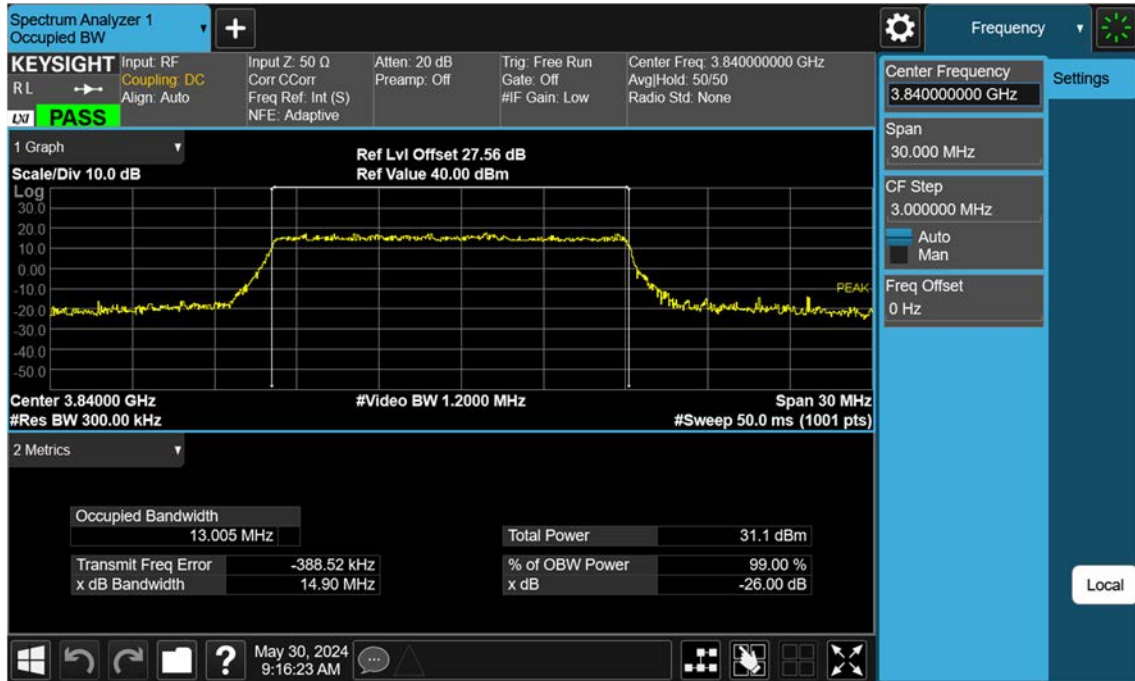
n77(3700~3980 MHz)_15 M_OBW_Mid_QPSK_FullRB



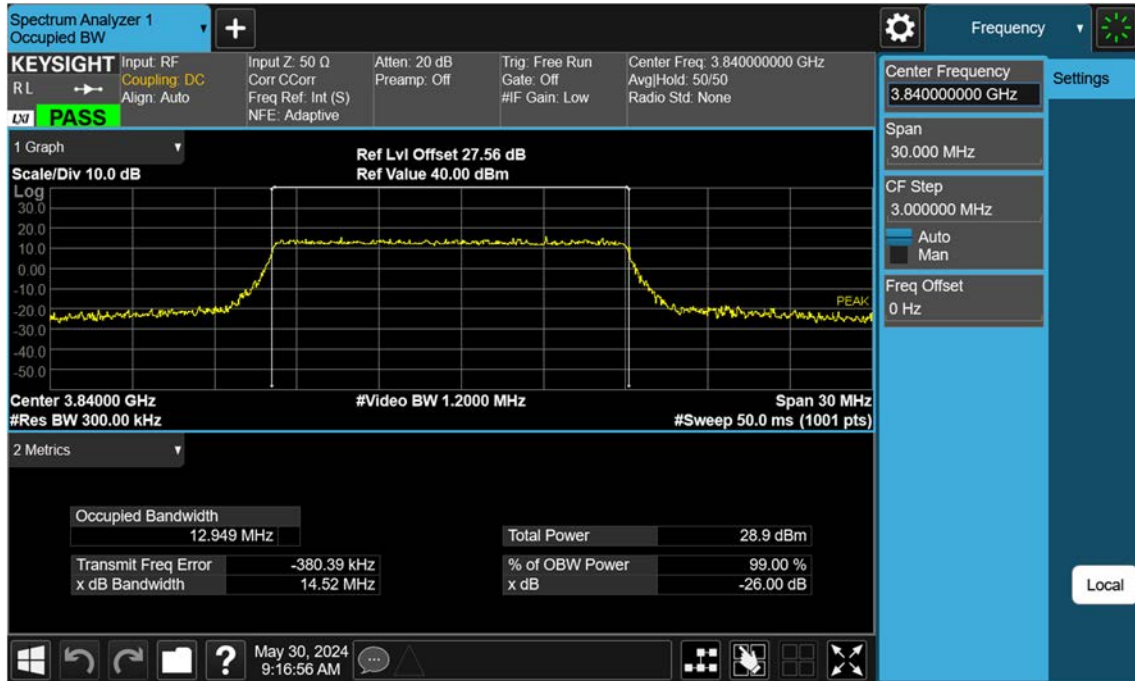
n77(3700~3980 MHz)_15 M_OBW_Mid_16QAM_FullRB



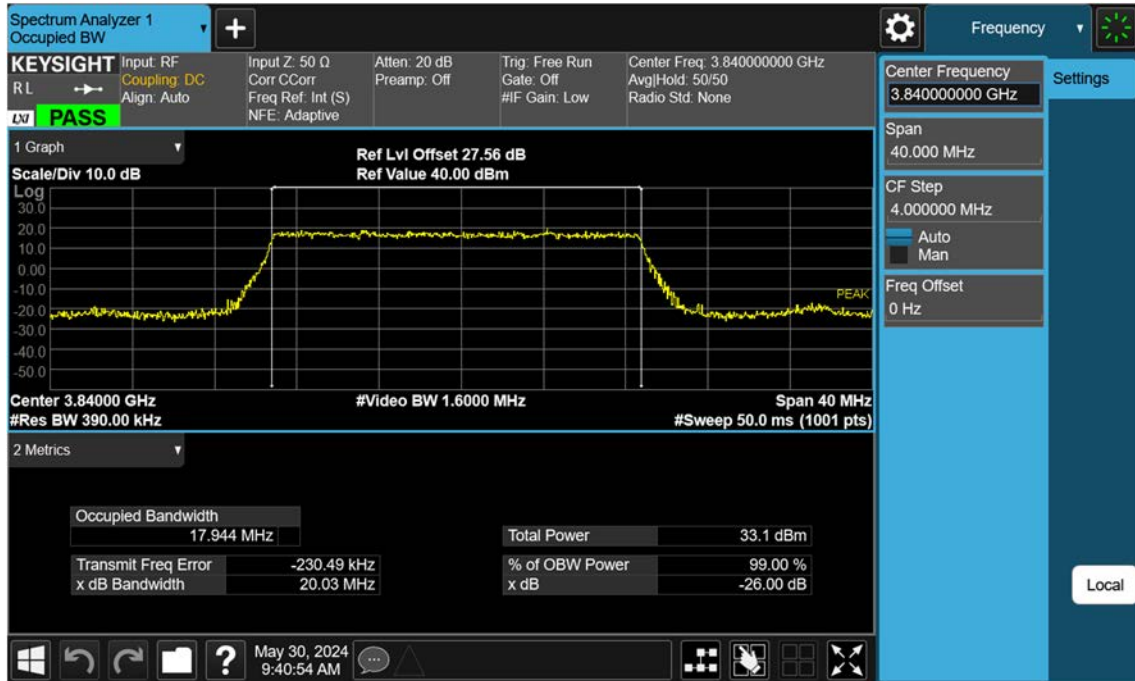
n77(3700~3980 MHz)_15 M_OBW_Mid_64QAM_FullRB



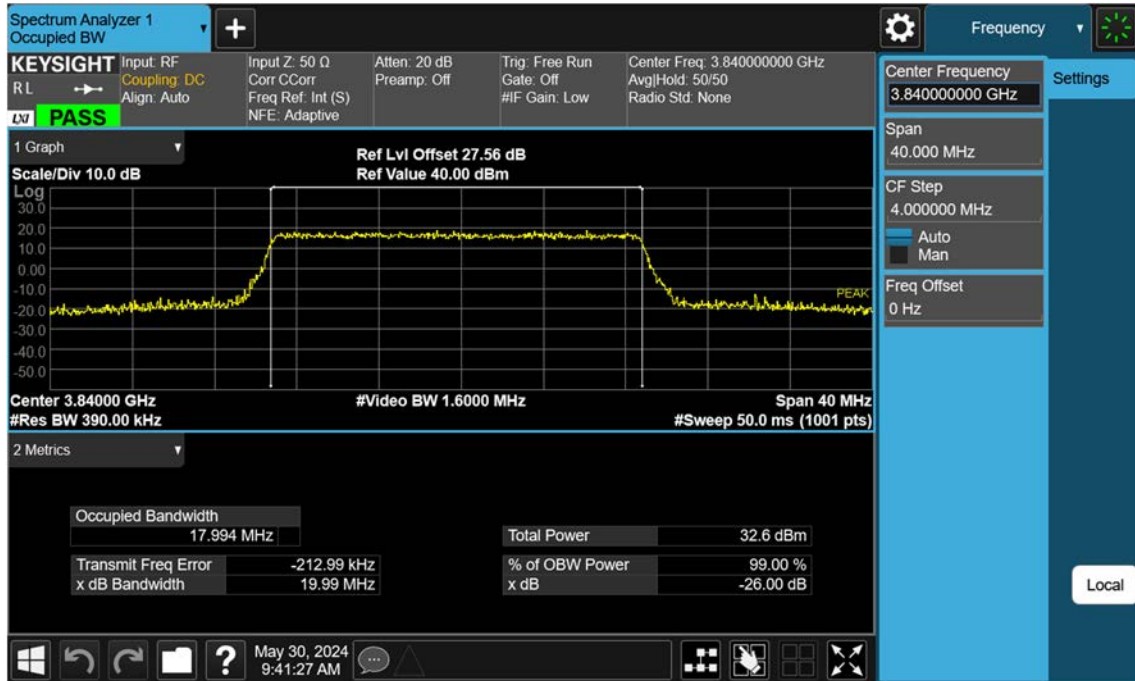
n77(3700~3980 MHz)_15 M_OBW_Mid_256QAM_FullRB



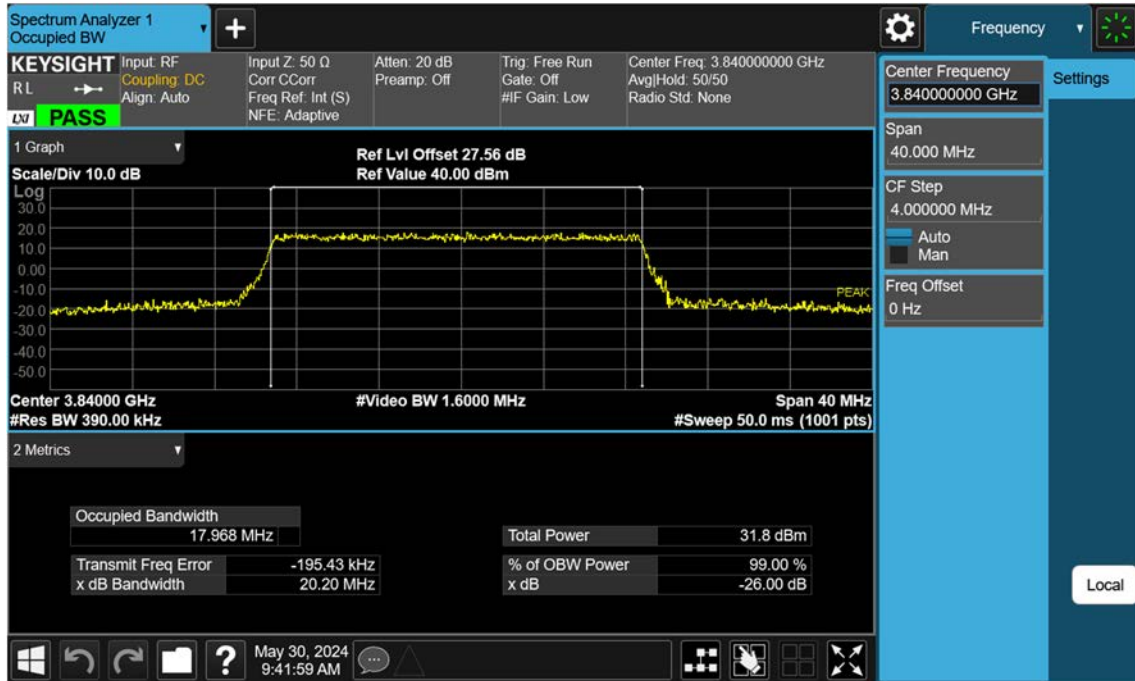
n77(3700~3980 MHz)_20 M_OBW_Mid_BPSK_FullRB



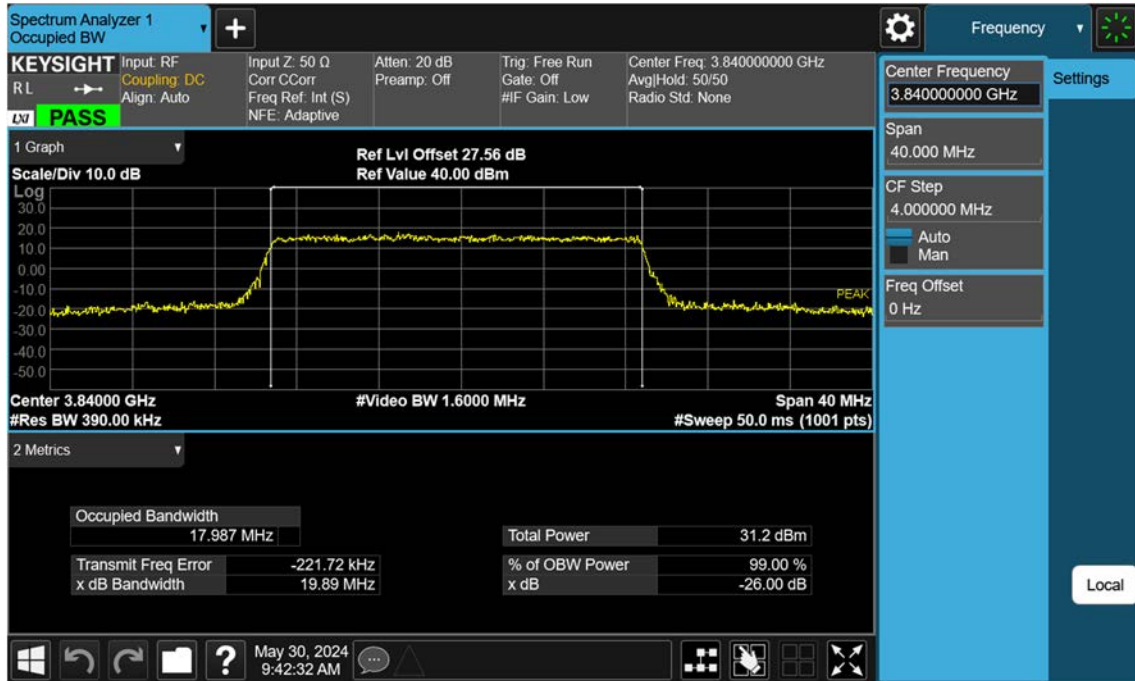
n77(3700~3980 MHz)_20 M_OBW_Mid_QPSK_FullRB



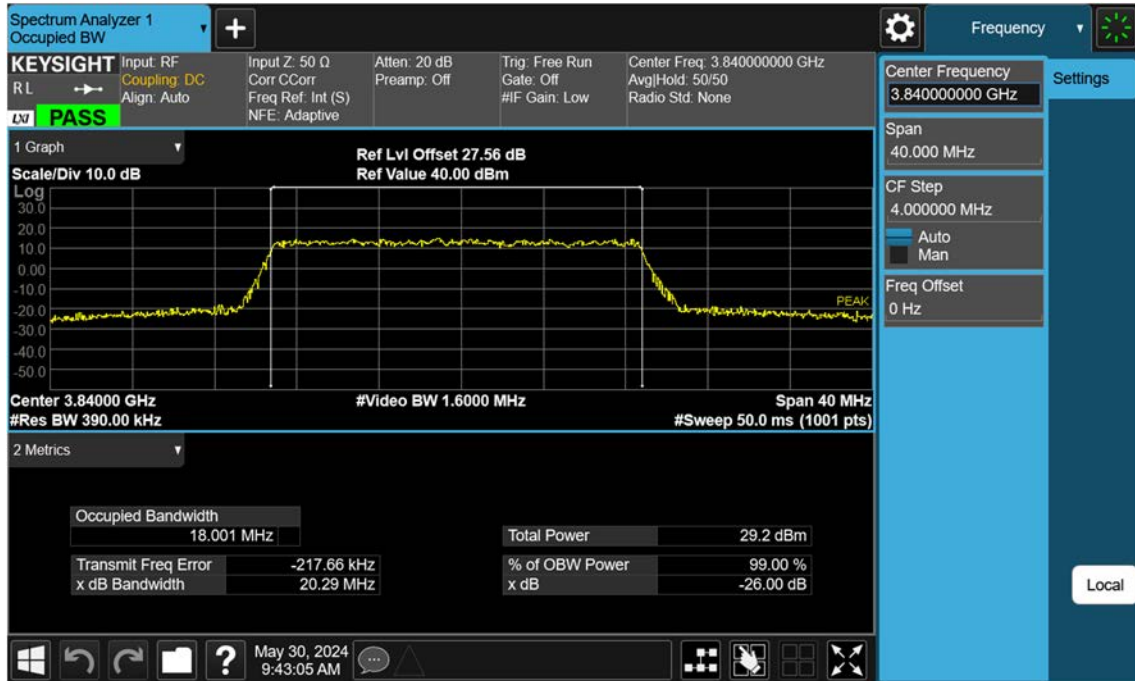
n77(3700~3980 MHz)_20 M_OBW_Mid_16QAM_FullRB



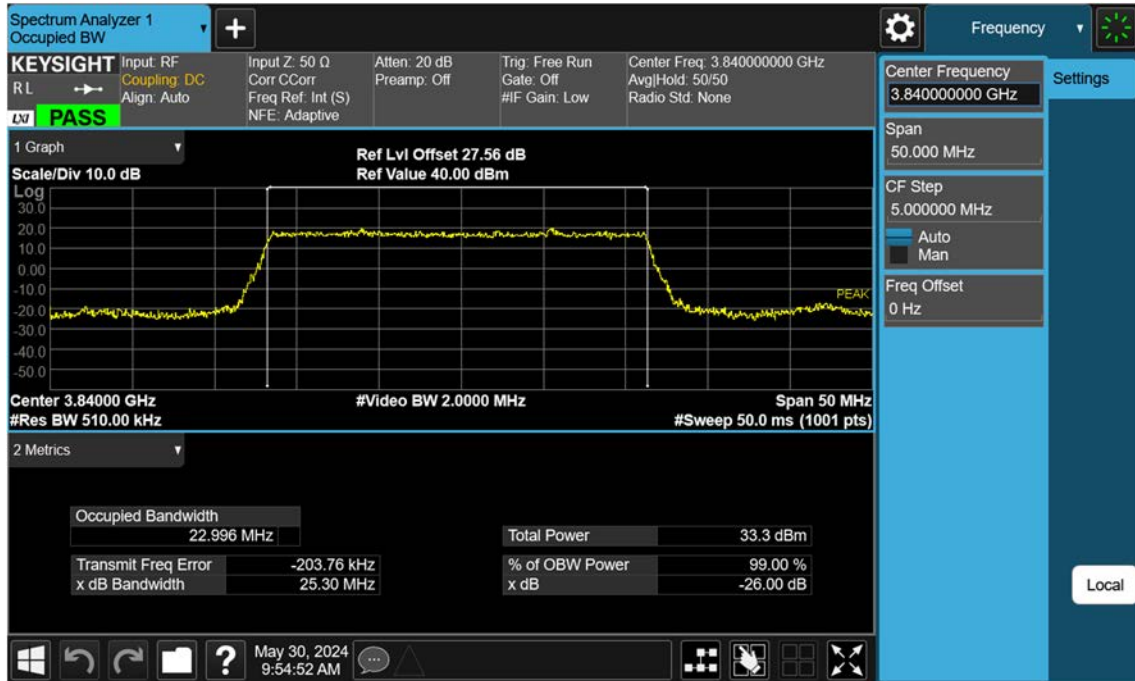
n77(3700~3980 MHz)_20 M_OBW_Mid_64QAM_FullRB



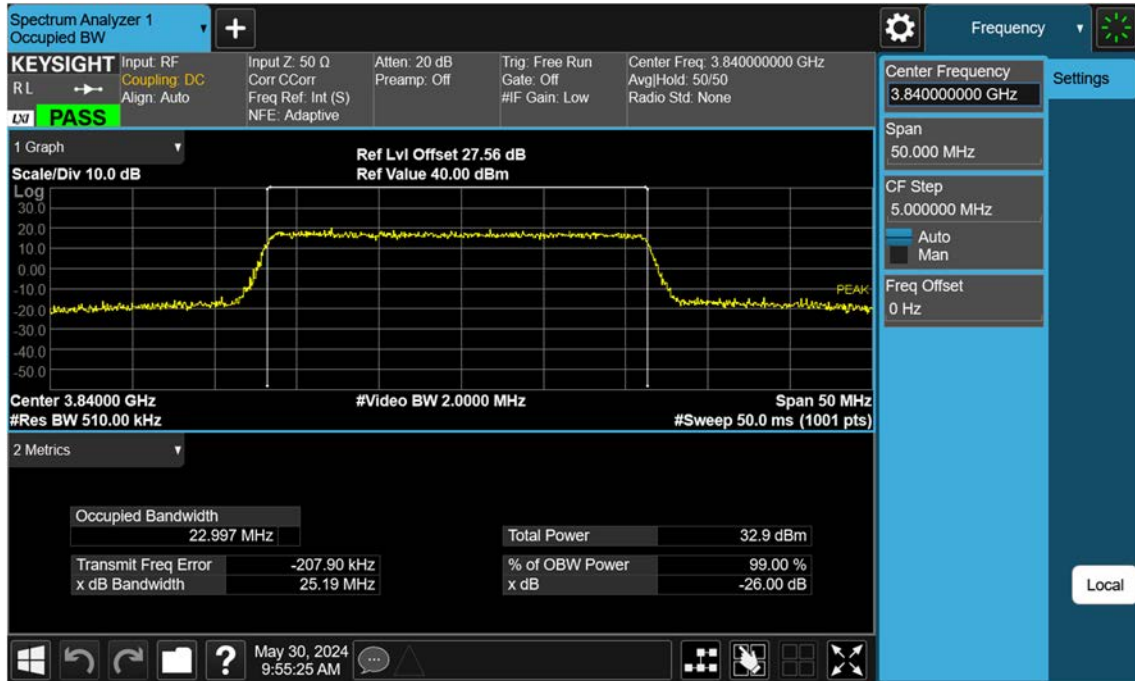
n77(3700~3980 MHz)_20 M_OBW_Mid_256QAM_FullRB



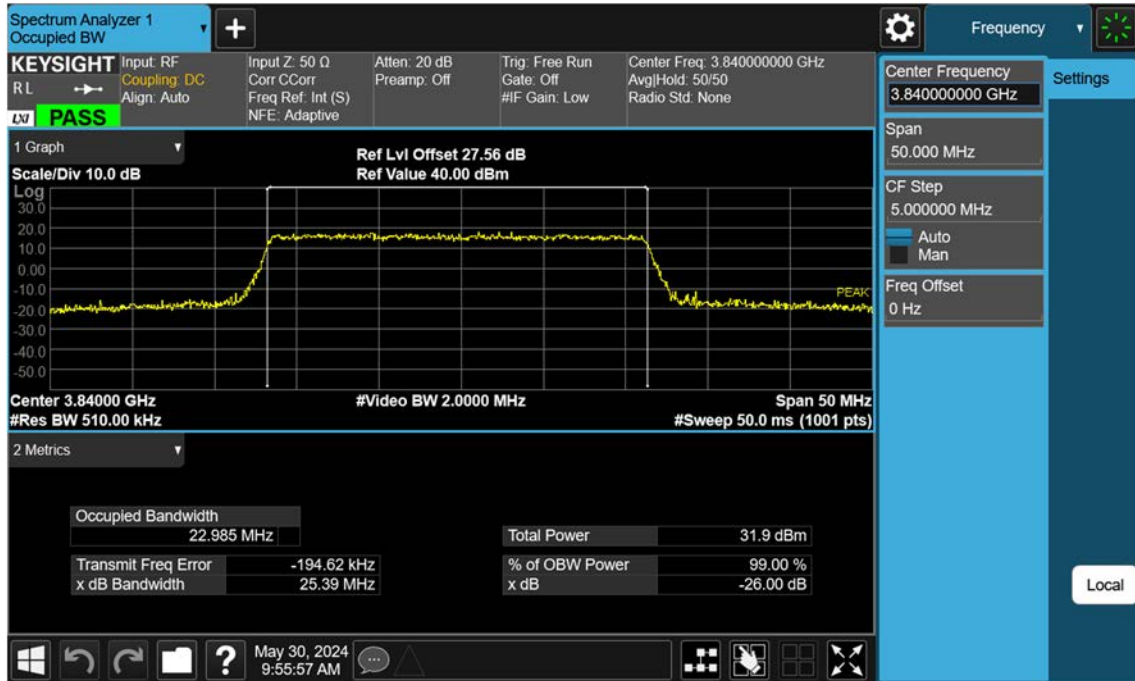
n77(3700~3980 MHz)_25 M_OBW_Mid_BPSK_FullRB



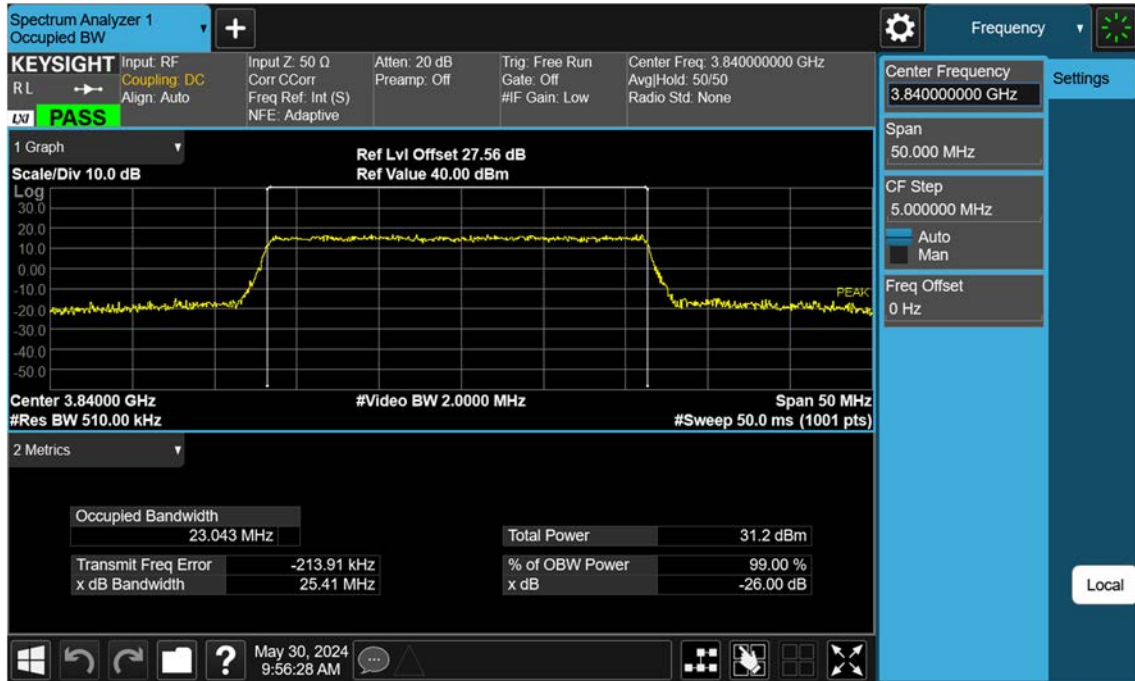
n77(3700~3980 MHz)_25 M_OBW_Mid_QPSK_FullRB



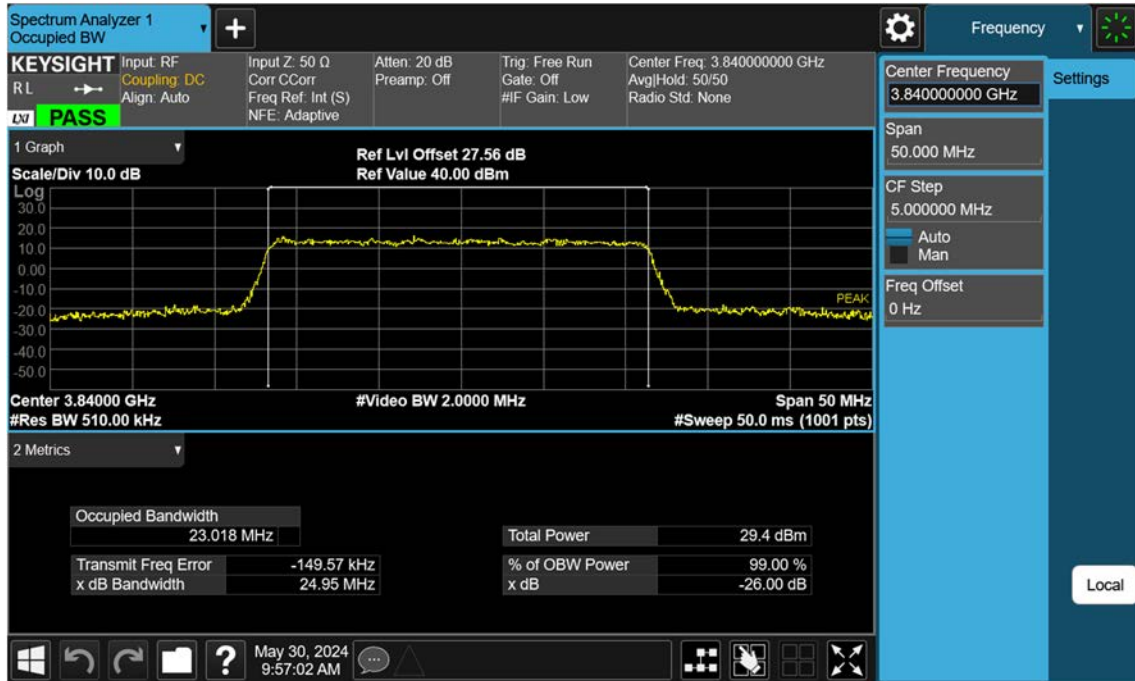
n77(3700~3980 MHz)_25 M_OBW_Mid_16QAM_FullRB



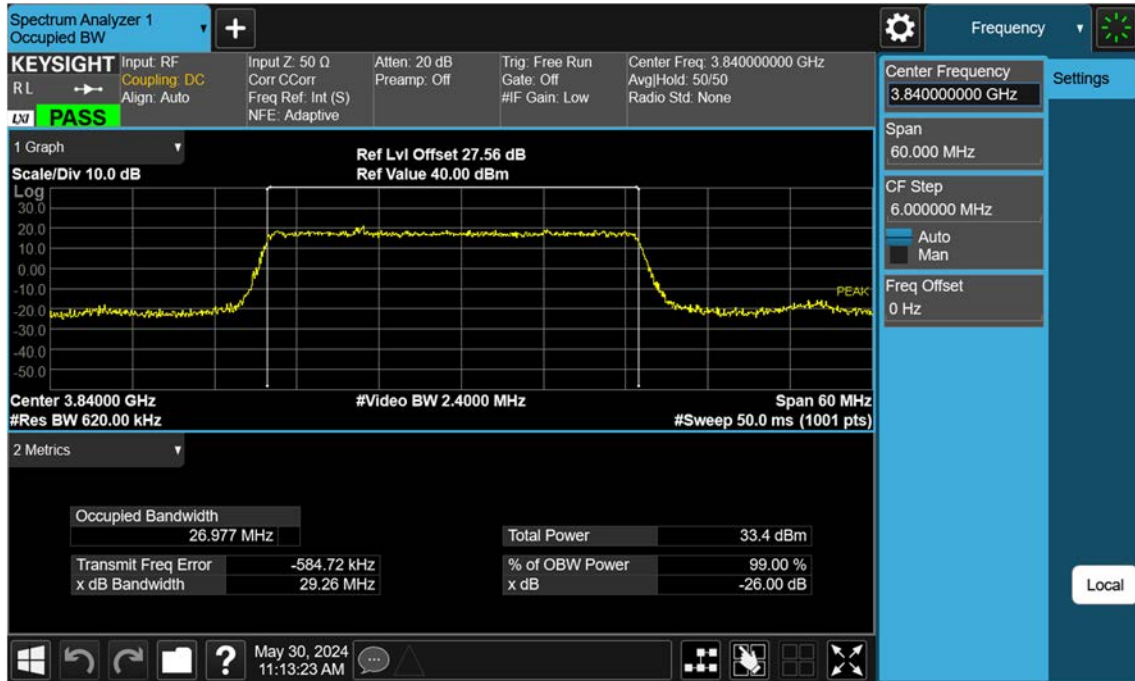
n77(3700~3980 MHz)_25 M_OBW_Mid_64QAM_FullRB



n77(3700~3980 MHz)_25 M_OBW_Mid_256QAM_FullRB



n77(3700~3980 MHz)_30 M_OBW_Mid_BPSK_FullRB



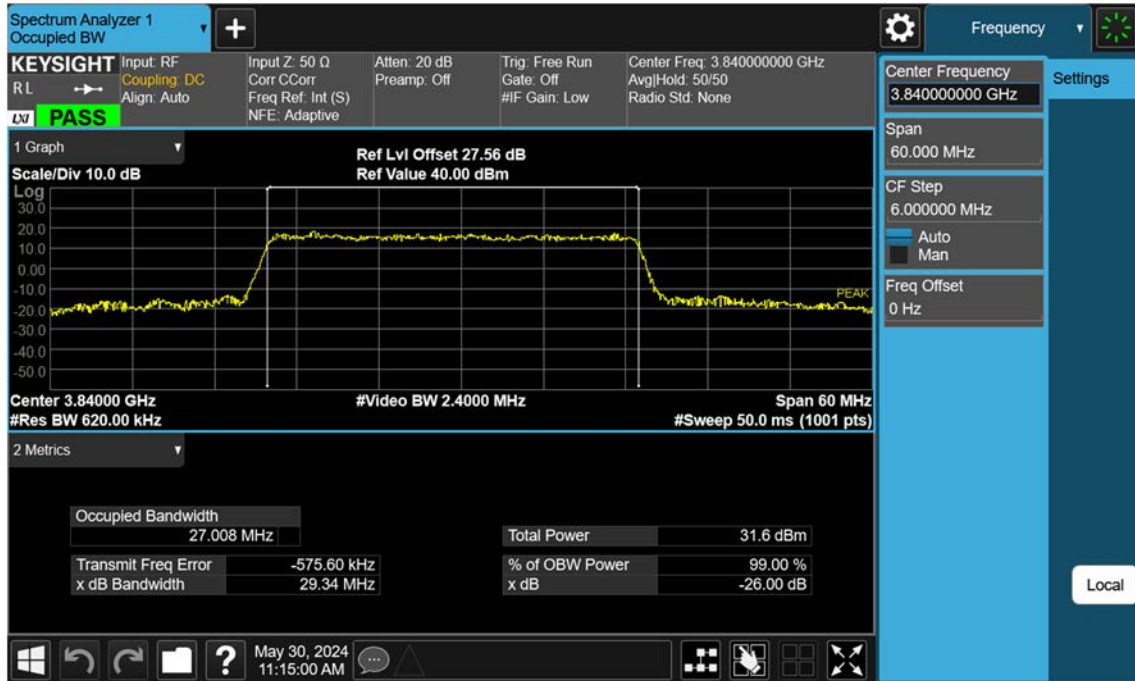
n77(3700~3980 MHz)_30 M_OBW_Mid_QPSK_FullRB



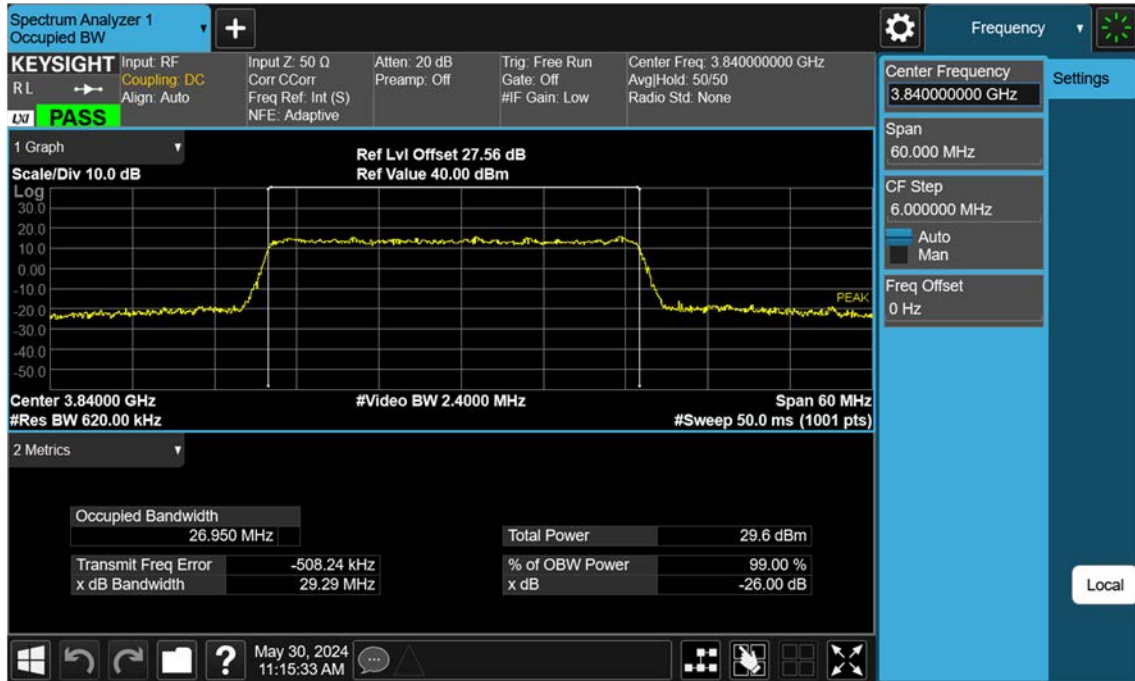
n77(3700~3980 MHz)_30 M_OBW_Mid_16QAM_FullRB



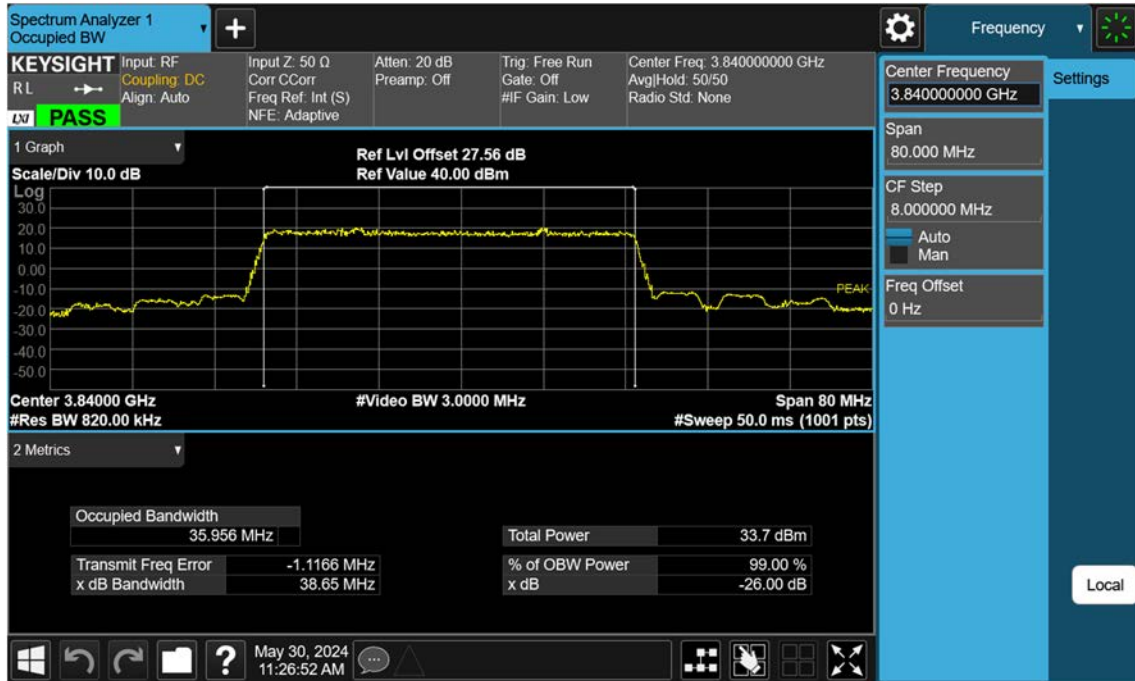
n77(3700~3980 MHz)_30 M_OBW_Mid_64QAM_FullRB



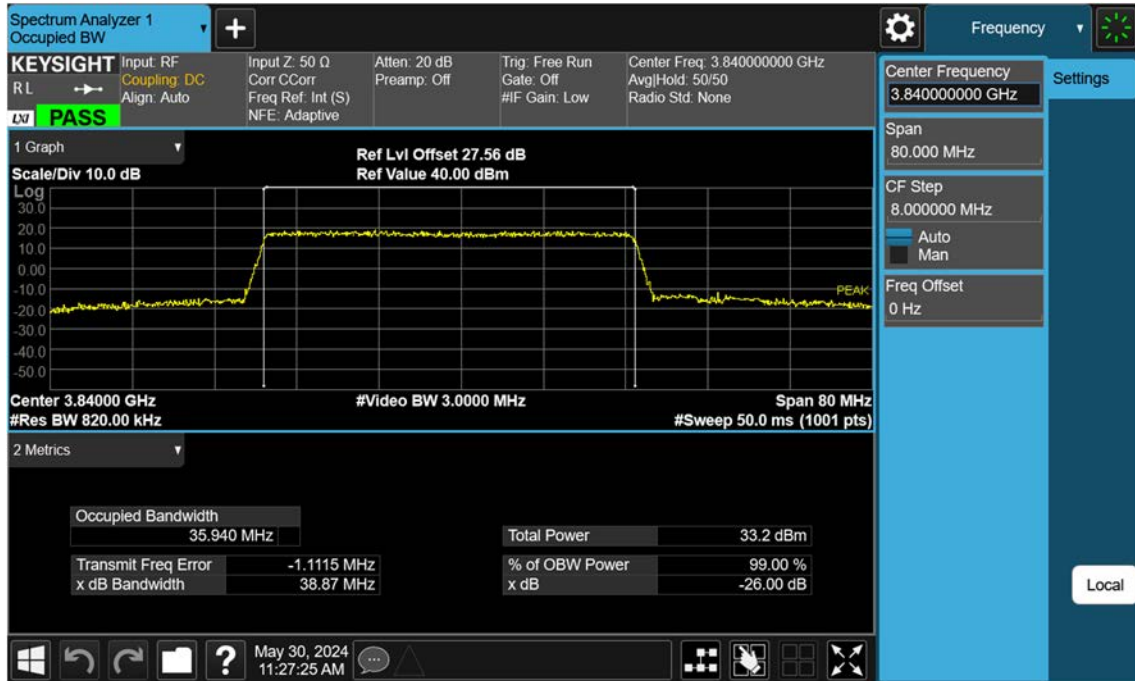
n77(3700~3980 MHz)_30 M_OBW_Mid_256QAM_FullRB



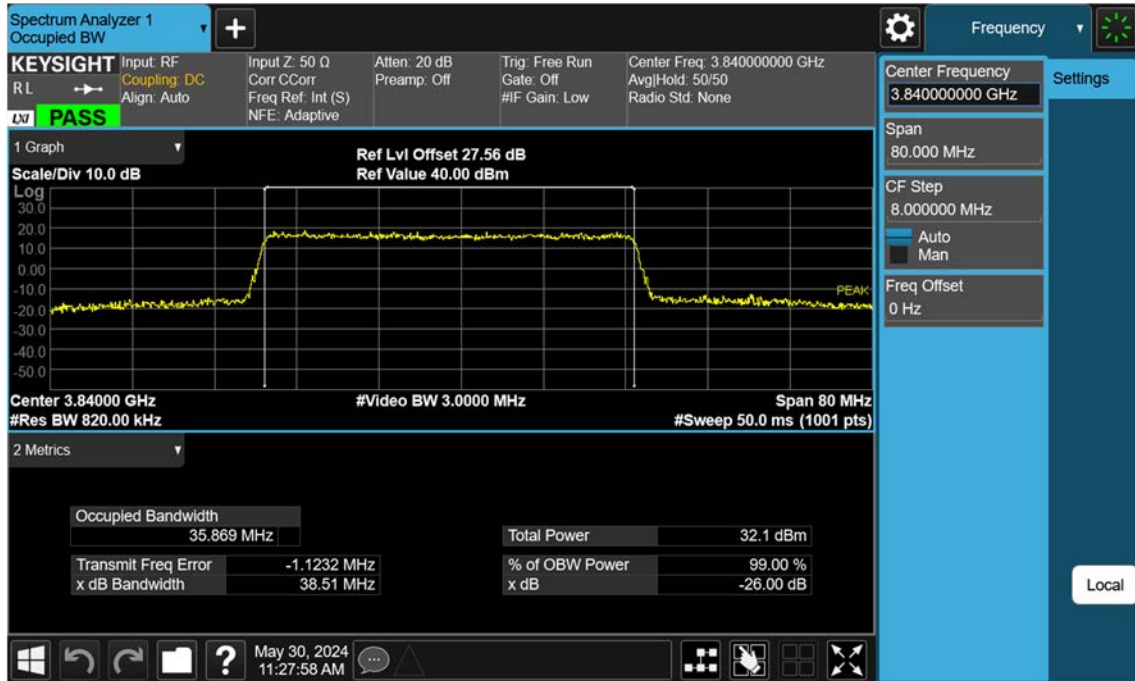
n77(3700~3980 MHz)_40 M_OBW_Mid_BPSK_FullRB



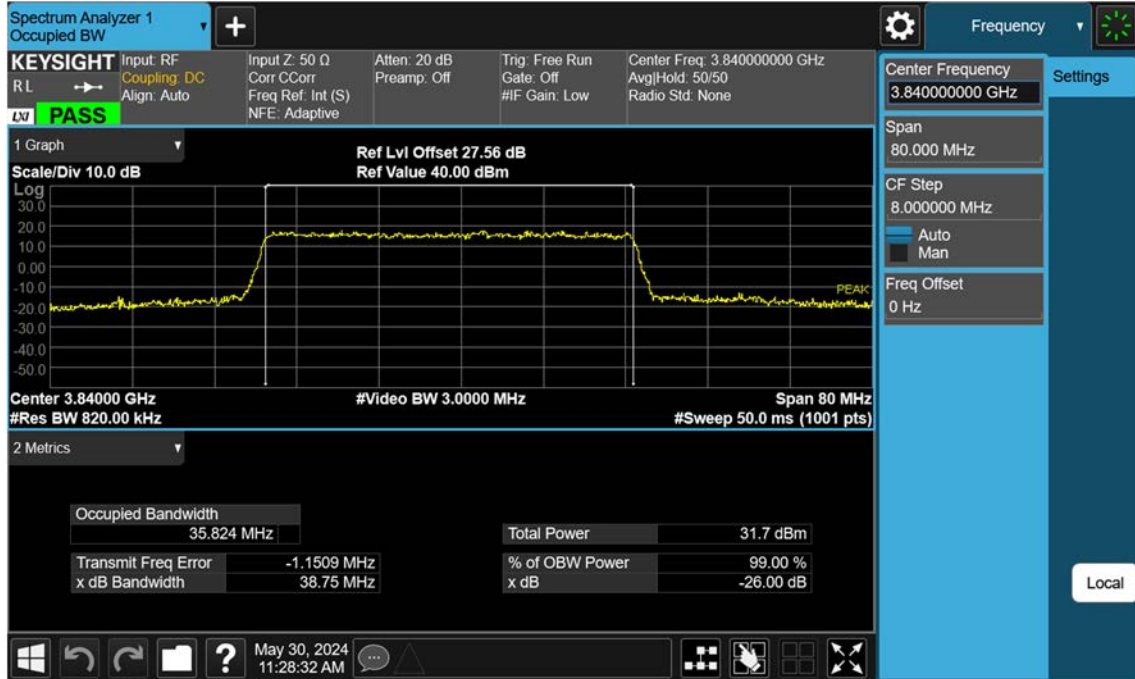
n77(3700~3980 MHz)_40 M_OBW_Mid_QPSK_FullRB



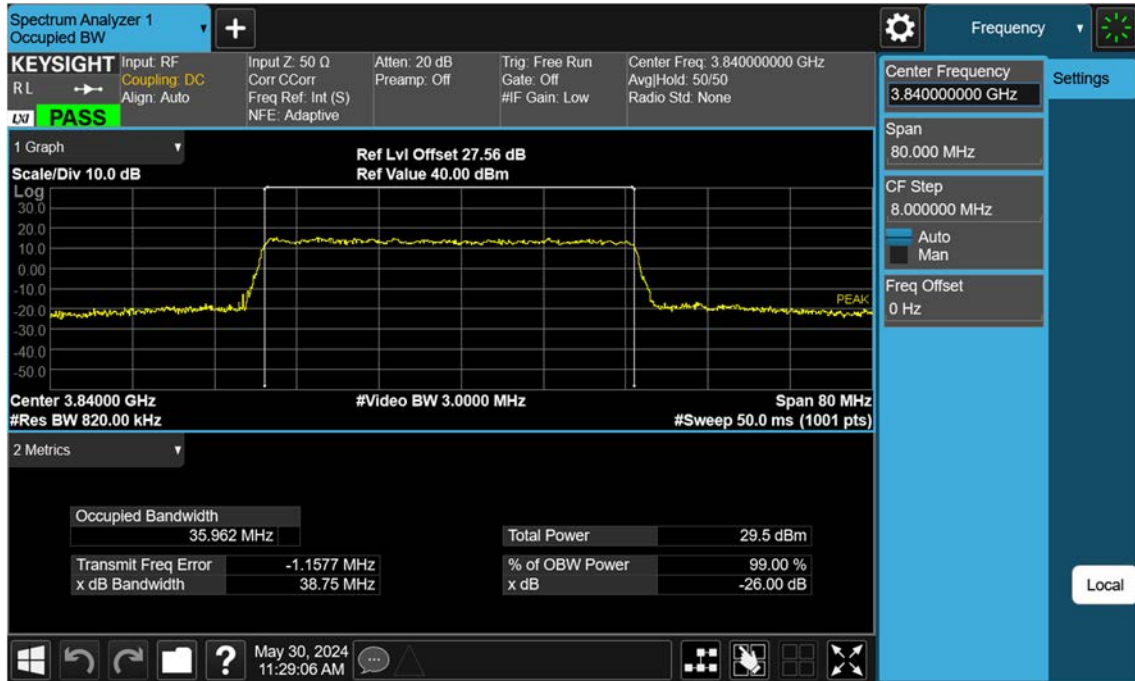
n77(3700~3980 MHz)_40 M_OBW_Mid_16QAM_FullRB



n77(3700~3980 MHz)_40 M_OBW_Mid_64QAM_FullRB



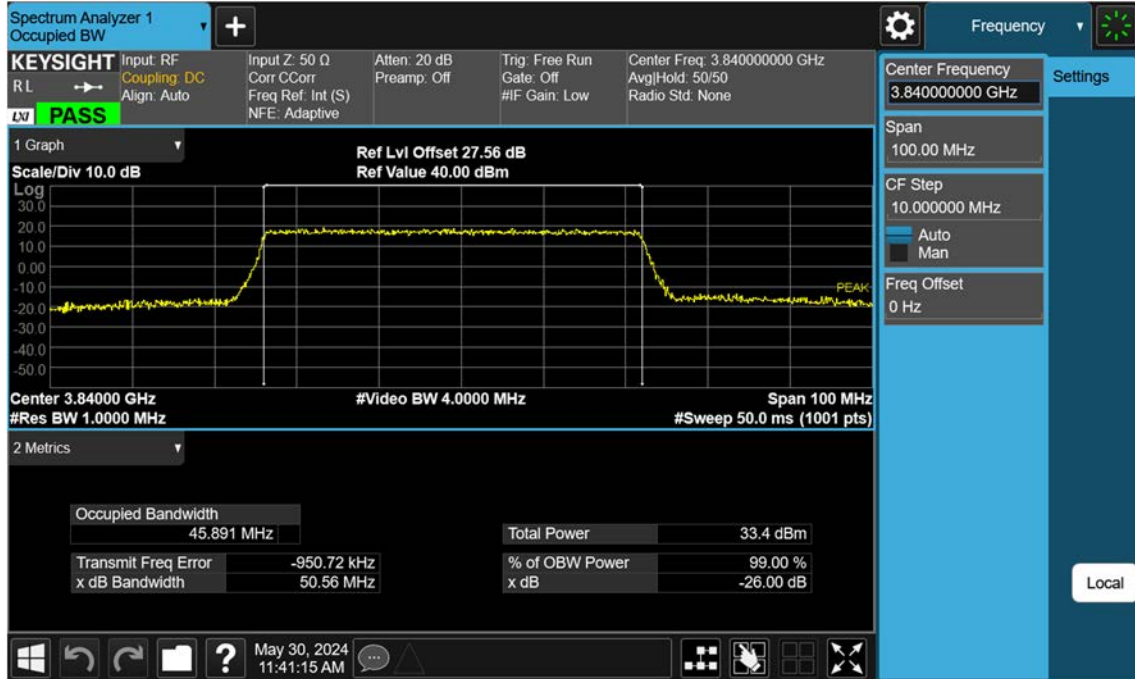
n77(3700~3980 MHz)_40 M_OBW_Mid_256QAM_FullRB



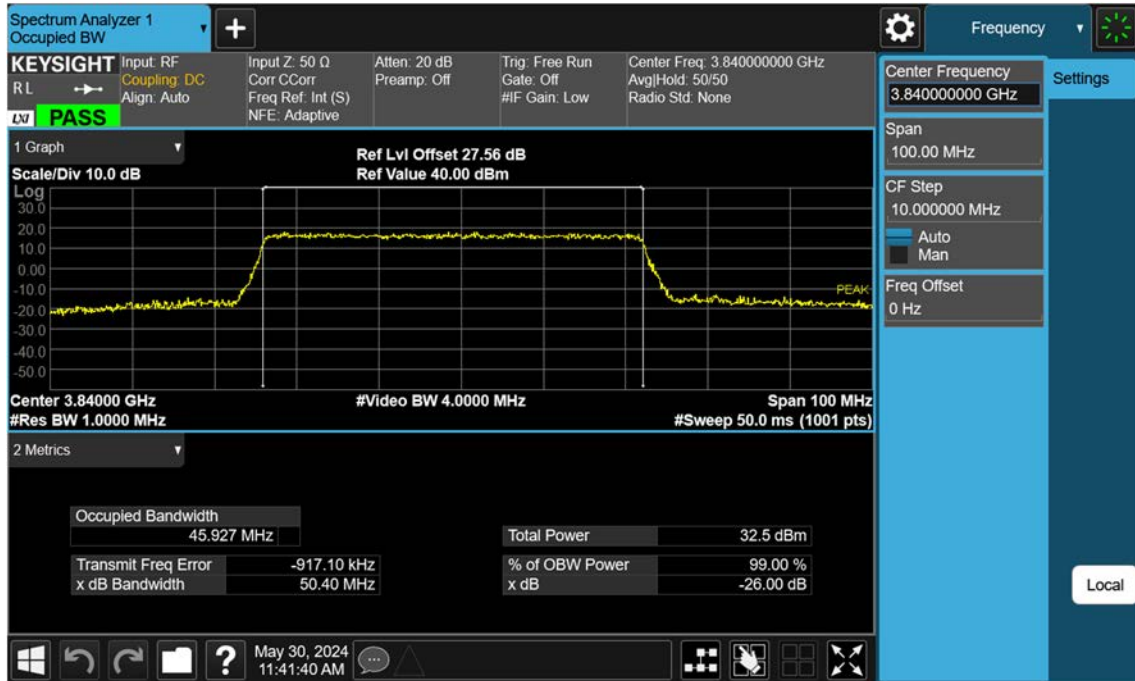
n77(3700~3980 MHz)_50 M_OBW_Mid_BPSK_FullRB



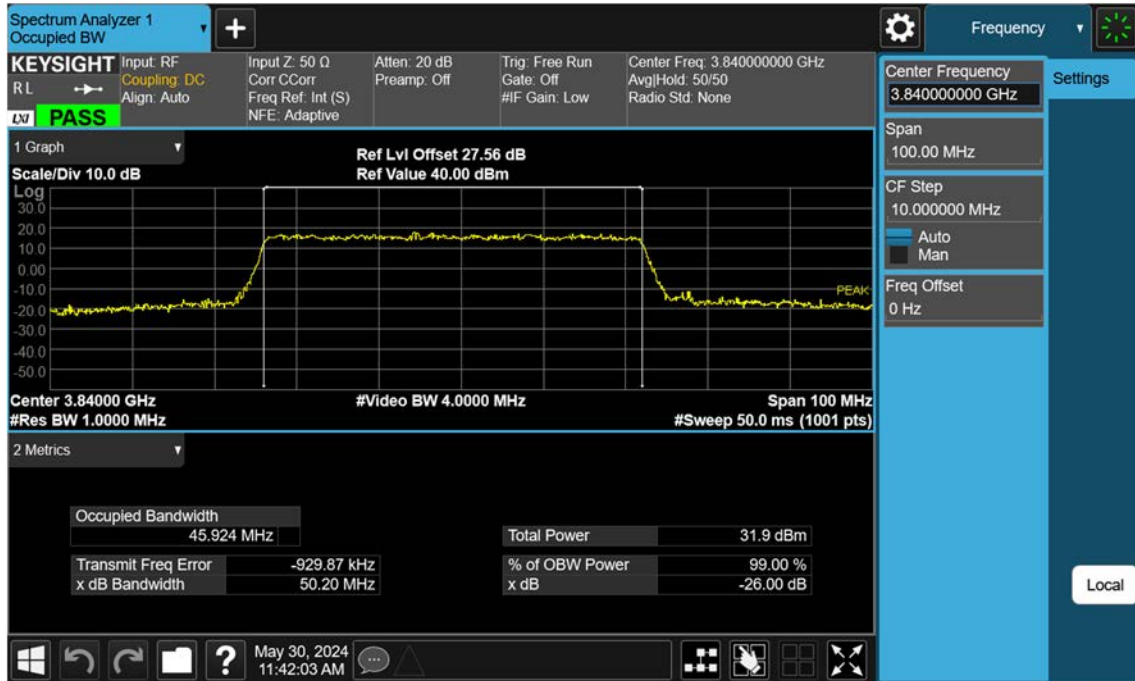
n77(3700~3980 MHz)_50 M_OBW_Mid_QPSK_FullRB



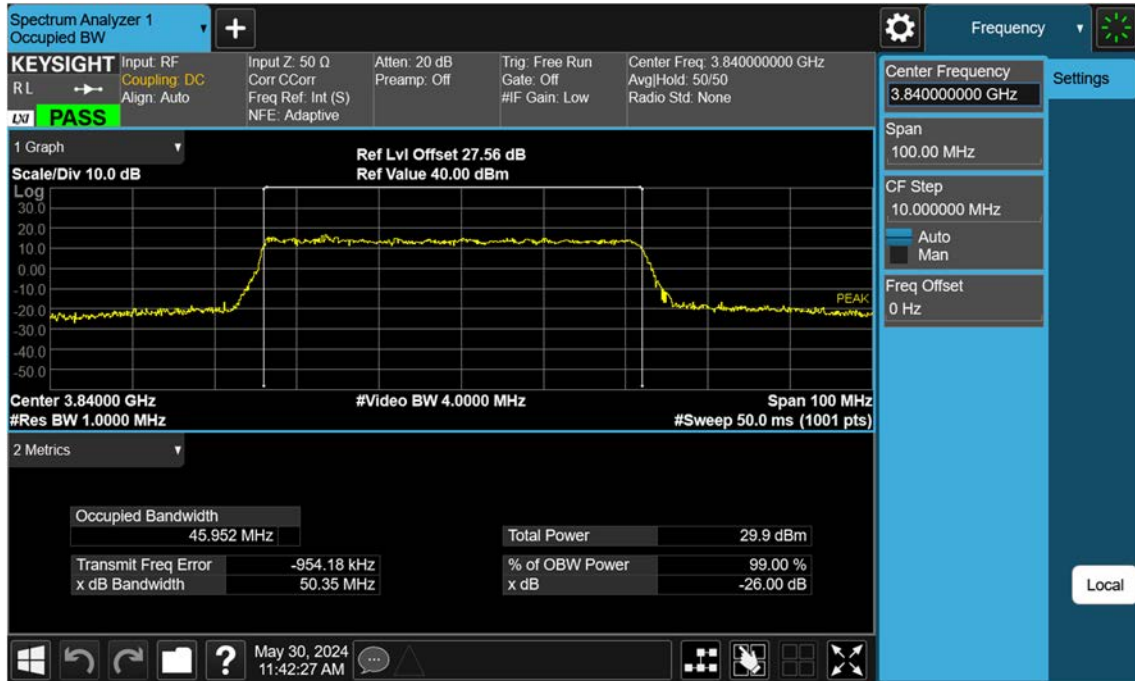
n77(3700~3980 MHz)_50 M_OBW_Mid_16QAM_FullRB



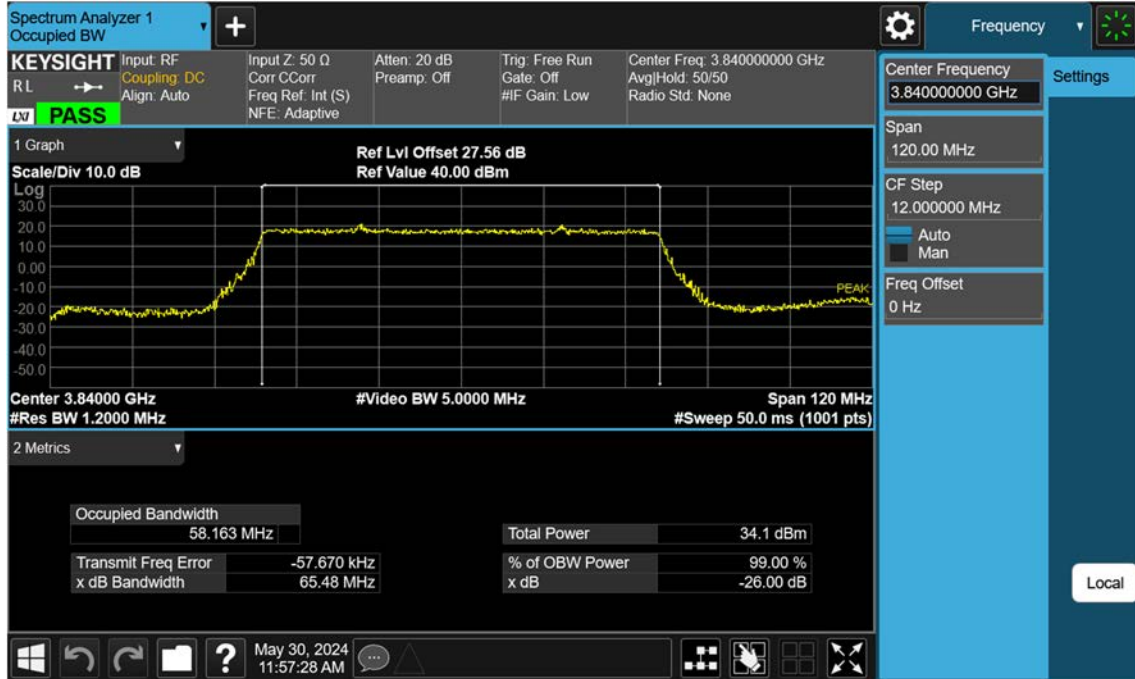
n77(3700~3980 MHz)_50 M_OBW_Mid_64QAM_FullRB



n77(3700~3980 MHz)_50 M_OBW_Mid_256QAM_FullRB



n77(3700~3980 MHz)_60 M_OBW_Mid_BPSK_FullRB



n77(3700~3980 MHz)_60 M_OBW_Mid_QPSK_FullRB



n77(3700~3980 MHz)_60 M_OBW_Mid_16QAM_FullRB



n77(3700~3980 MHz)_60 M_OBW_Mid_64QAM_FullRB



n77(3700~3980 MHz)_60 M_OBW_Mid_256QAM_FullRB



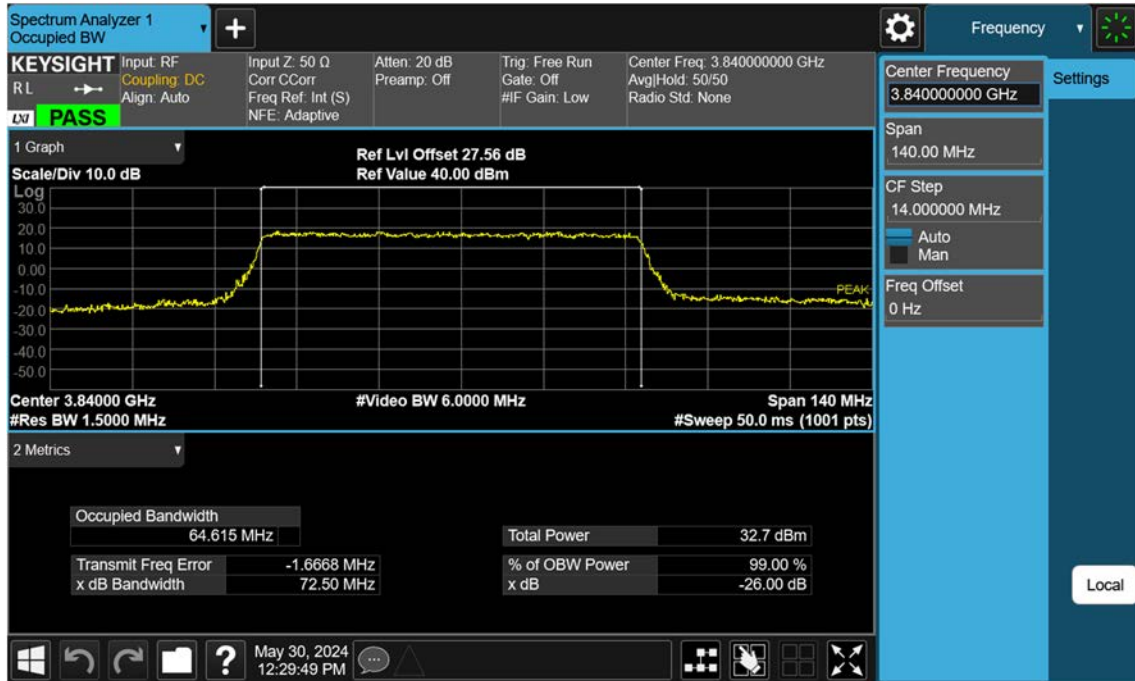
n77(3700~3980 MHz)_70 M_OBW_Mid_BPSK_FullRB



n77(3700~3980 MHz)_70 M_OBW_Mid_QPSK_FullRB



n77(3700~3980 MHz)_70 M_OBW_Mid_16QAM_FullRB



n77(3700~3980 MHz)_70 M_OBW_Mid_64QAM_FullRB

