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n77(3450~3550 MHz)_70 M_Band Edge_Low_BPSK_FullRB(3)

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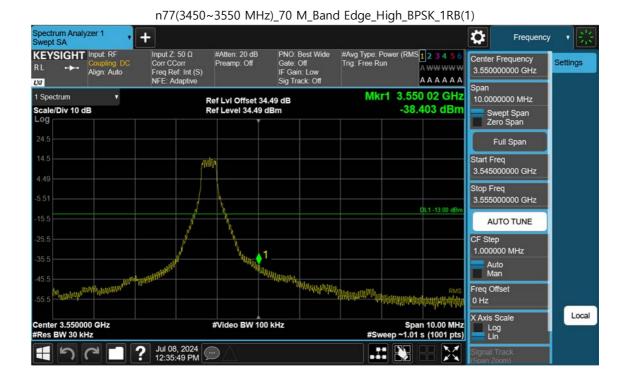






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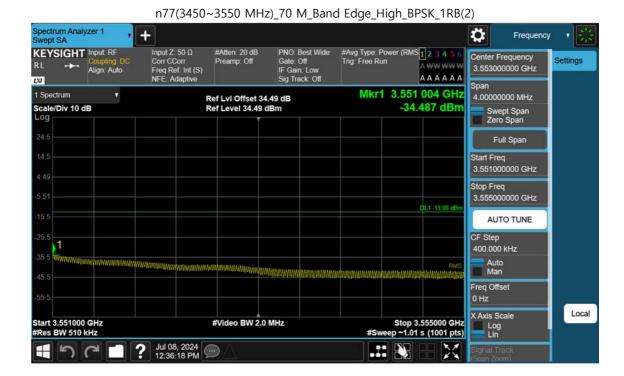
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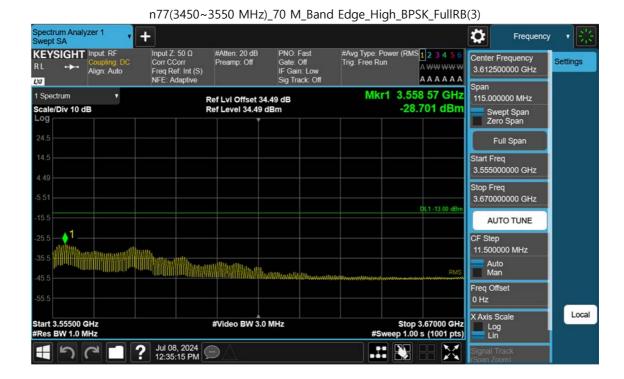


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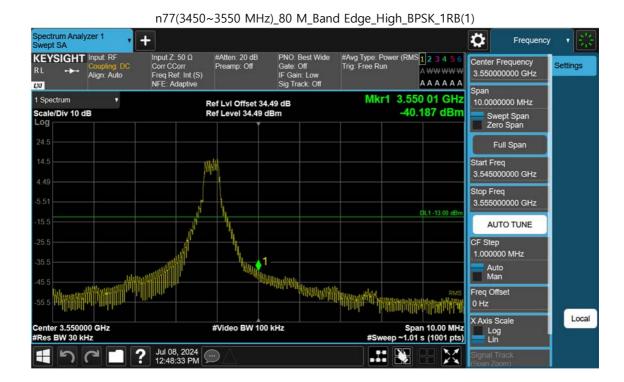


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n77(3450~3550 MHz)_80 M_Band Edge_High_BPSK_FullRB(2)

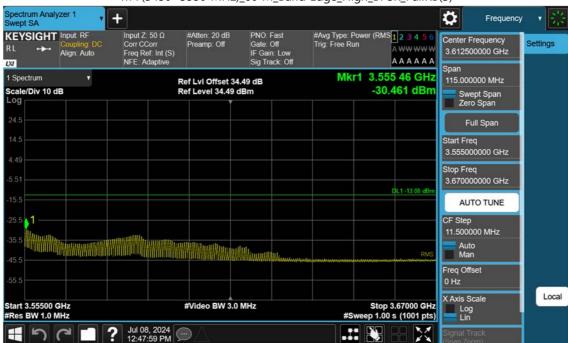
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n77(3450~3550 MHz)_80 M_Band Edge_High_BPSK_FullRB(3)

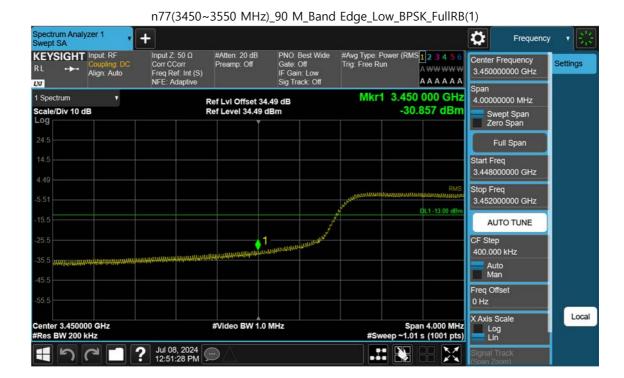
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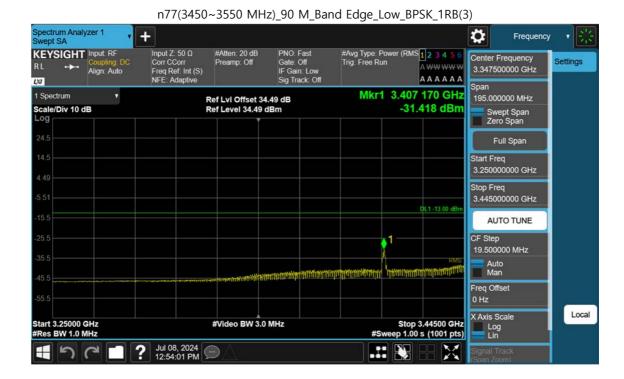


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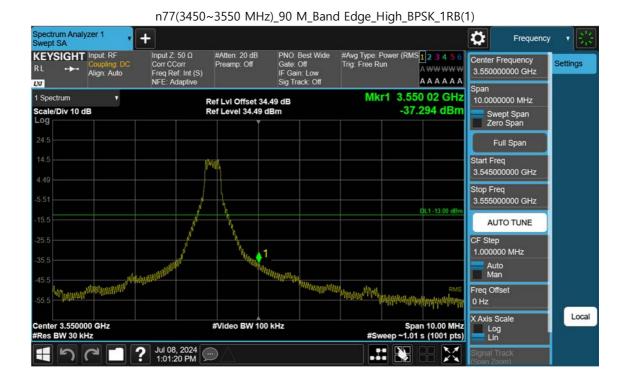






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n77(3450~3550 MHz)_100 M_Band Edge_Low_BPSK_FullRB(2)

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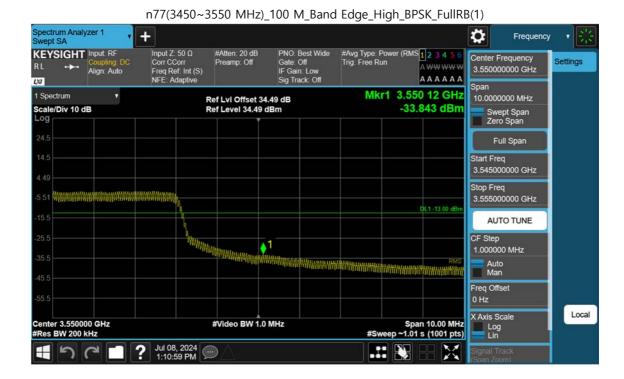
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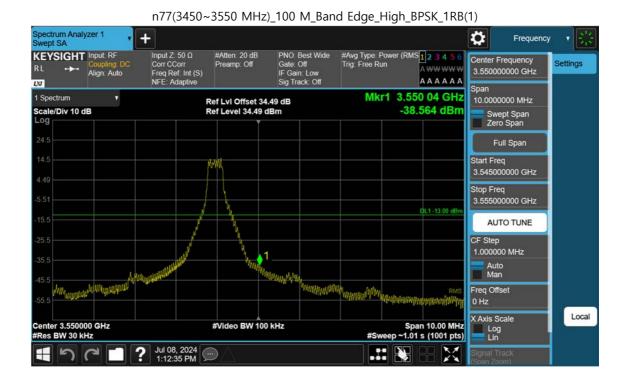






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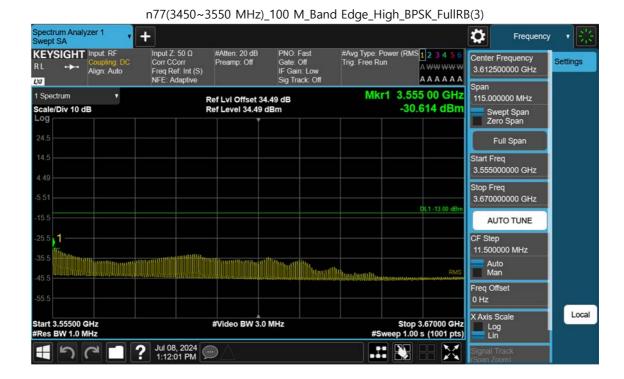




 $n77(3450{\sim}3550~MHz)_100~M_Band~Edge_High_BPSK_1RB(2)$

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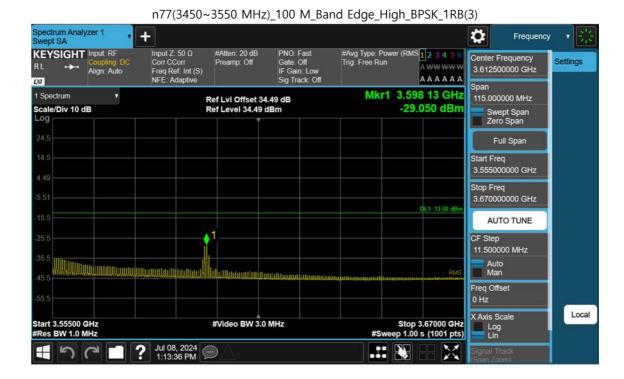


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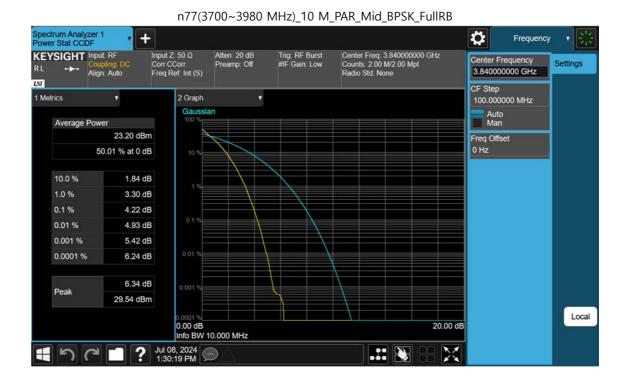




11. TEST PLOTS(3700 MHz - 3980 MHz)

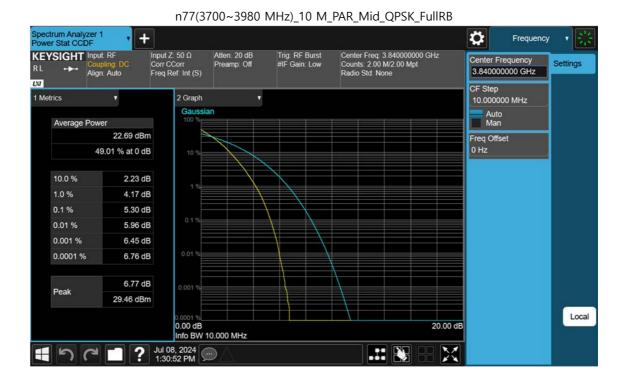
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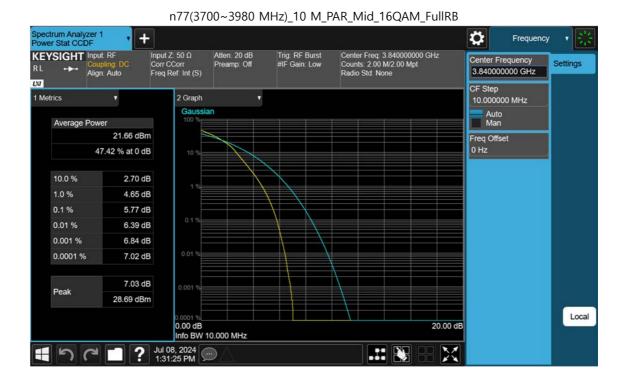
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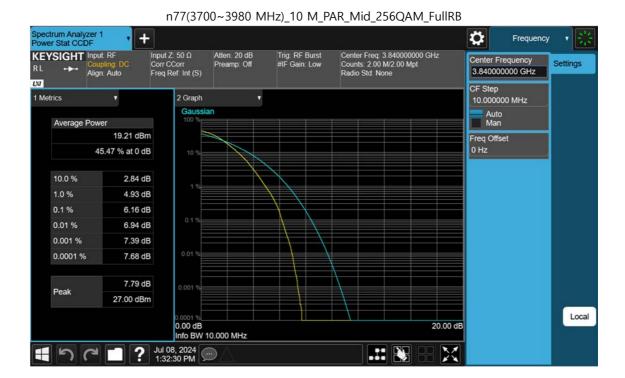
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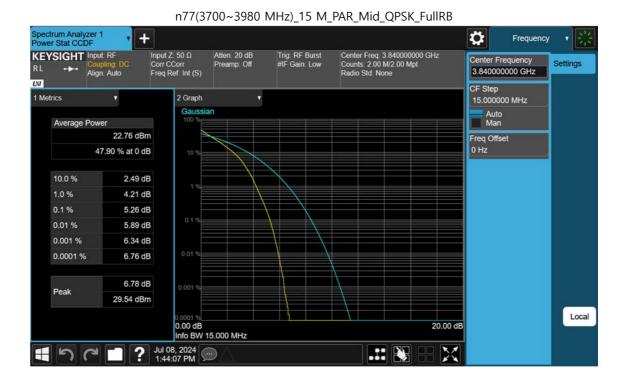
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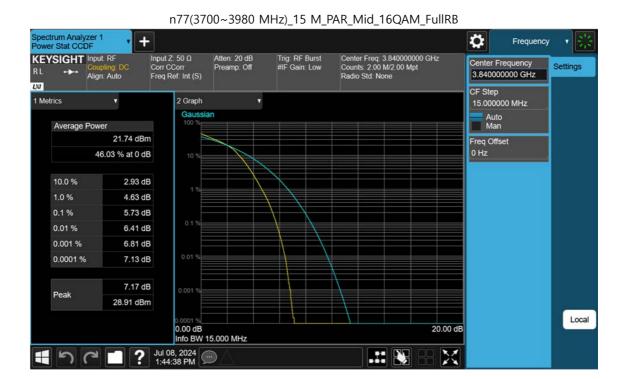
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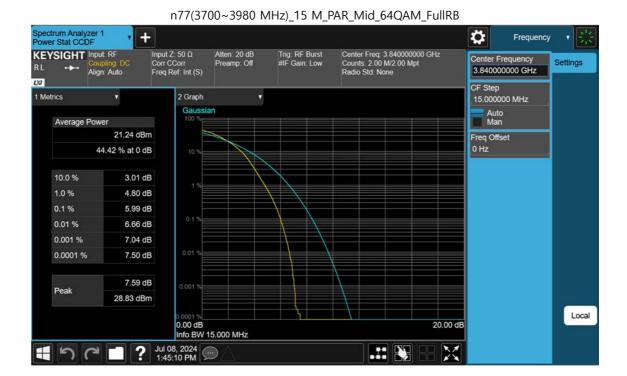
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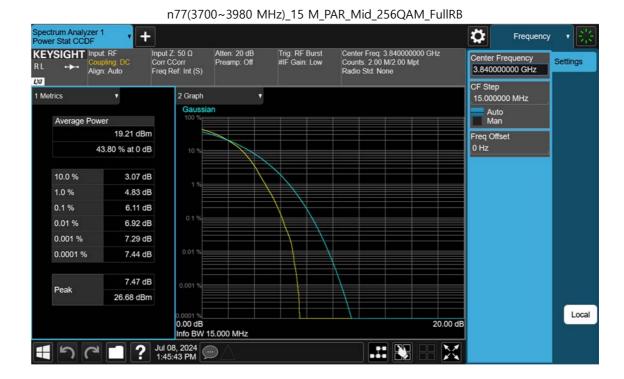
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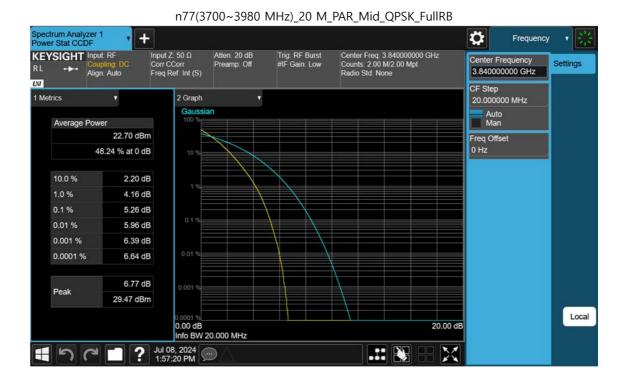
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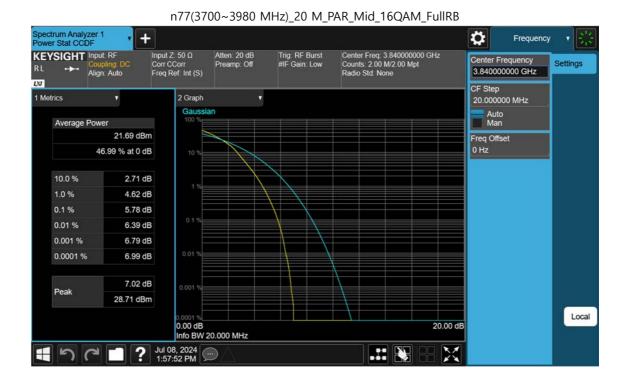
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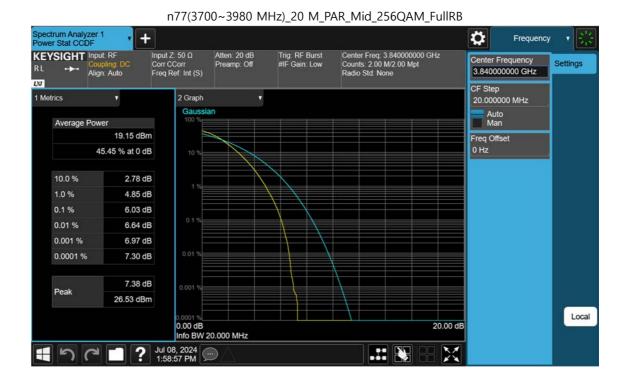
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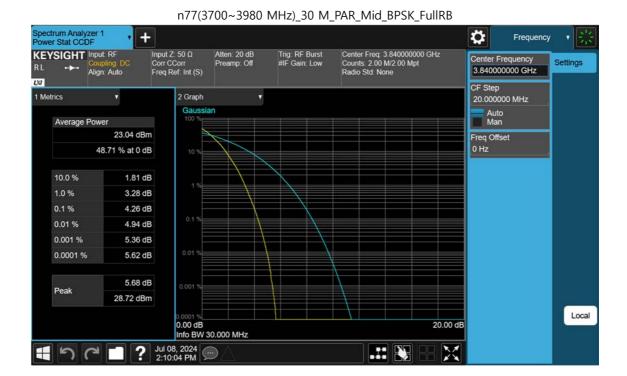
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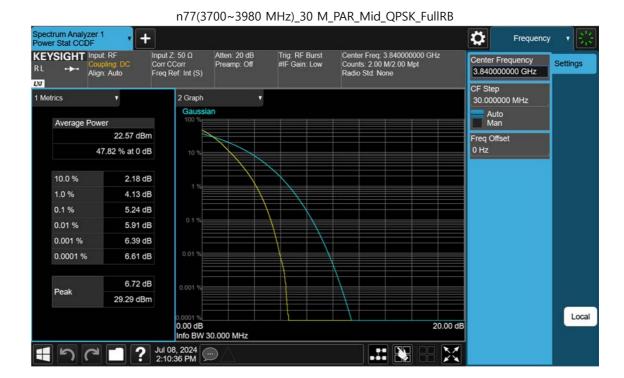
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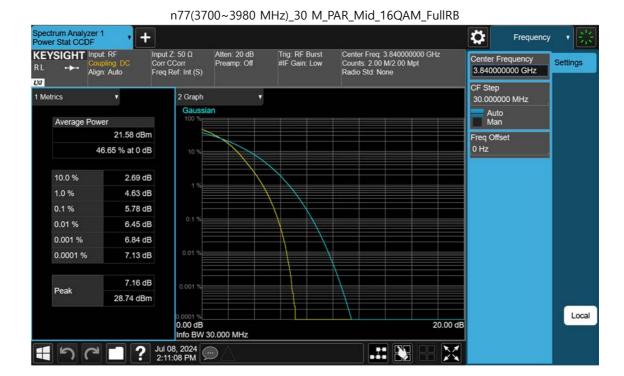
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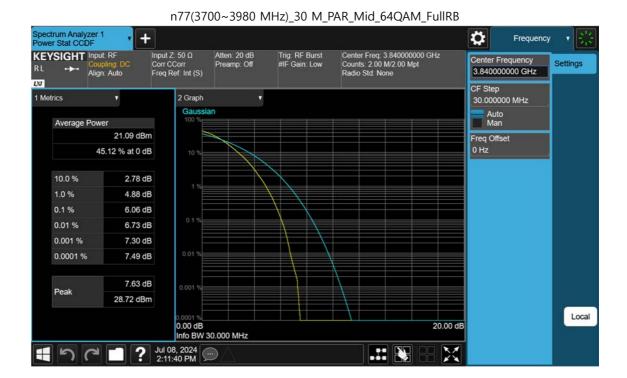
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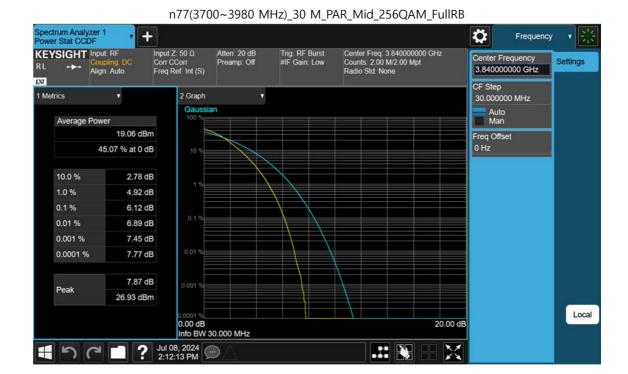
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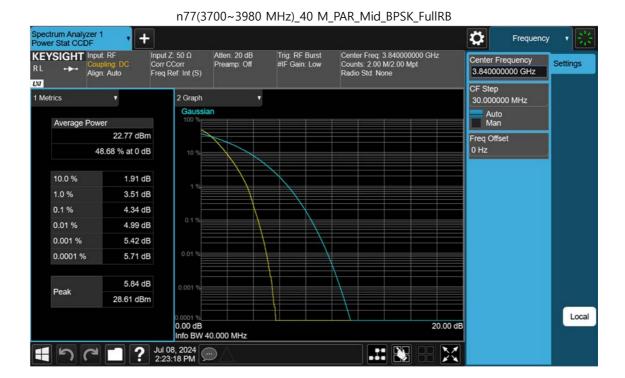
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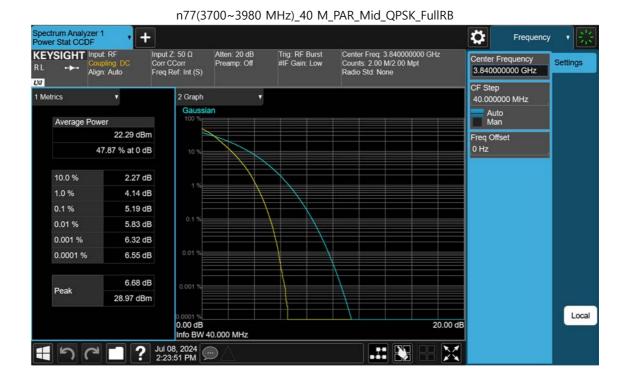
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