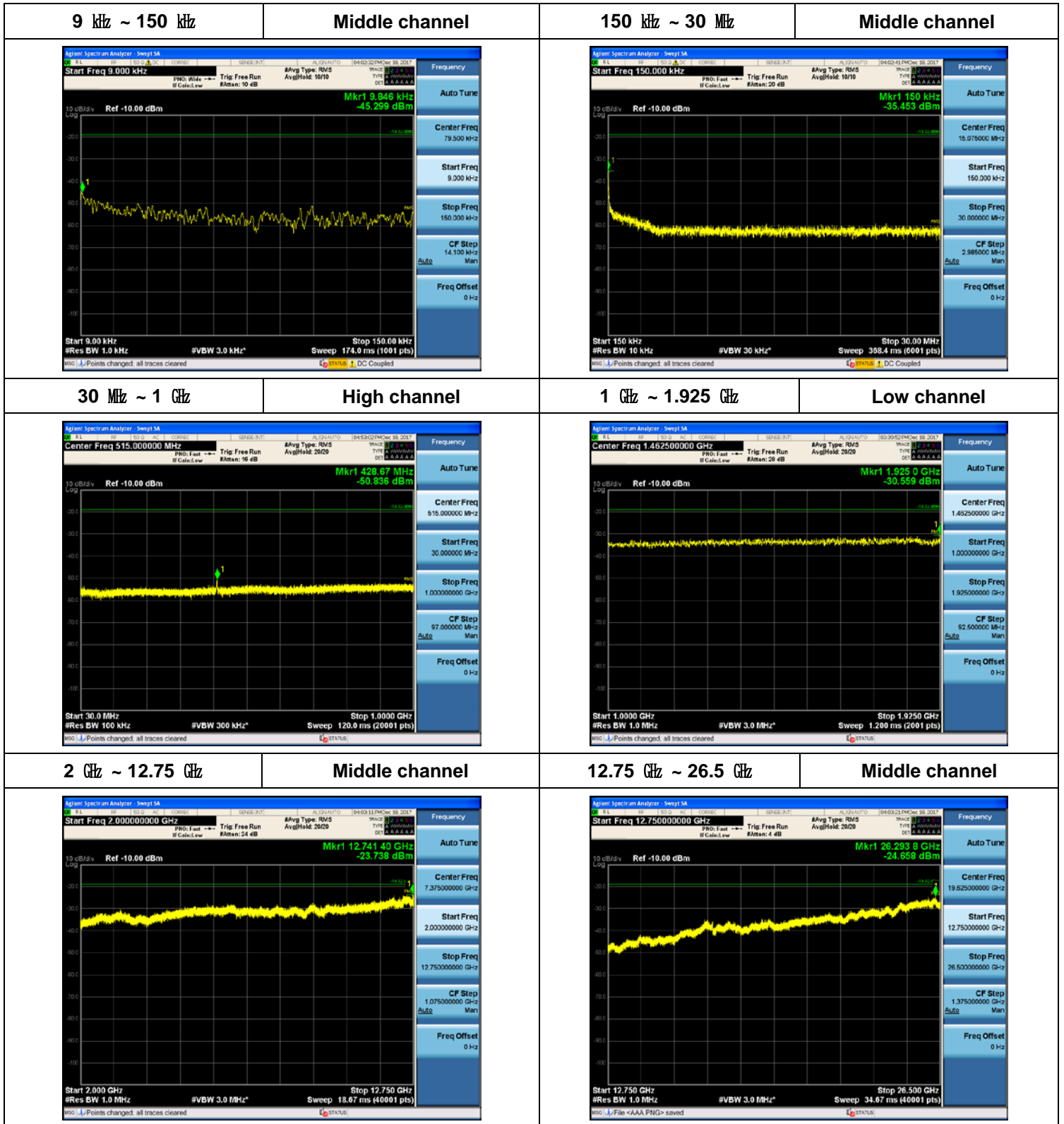
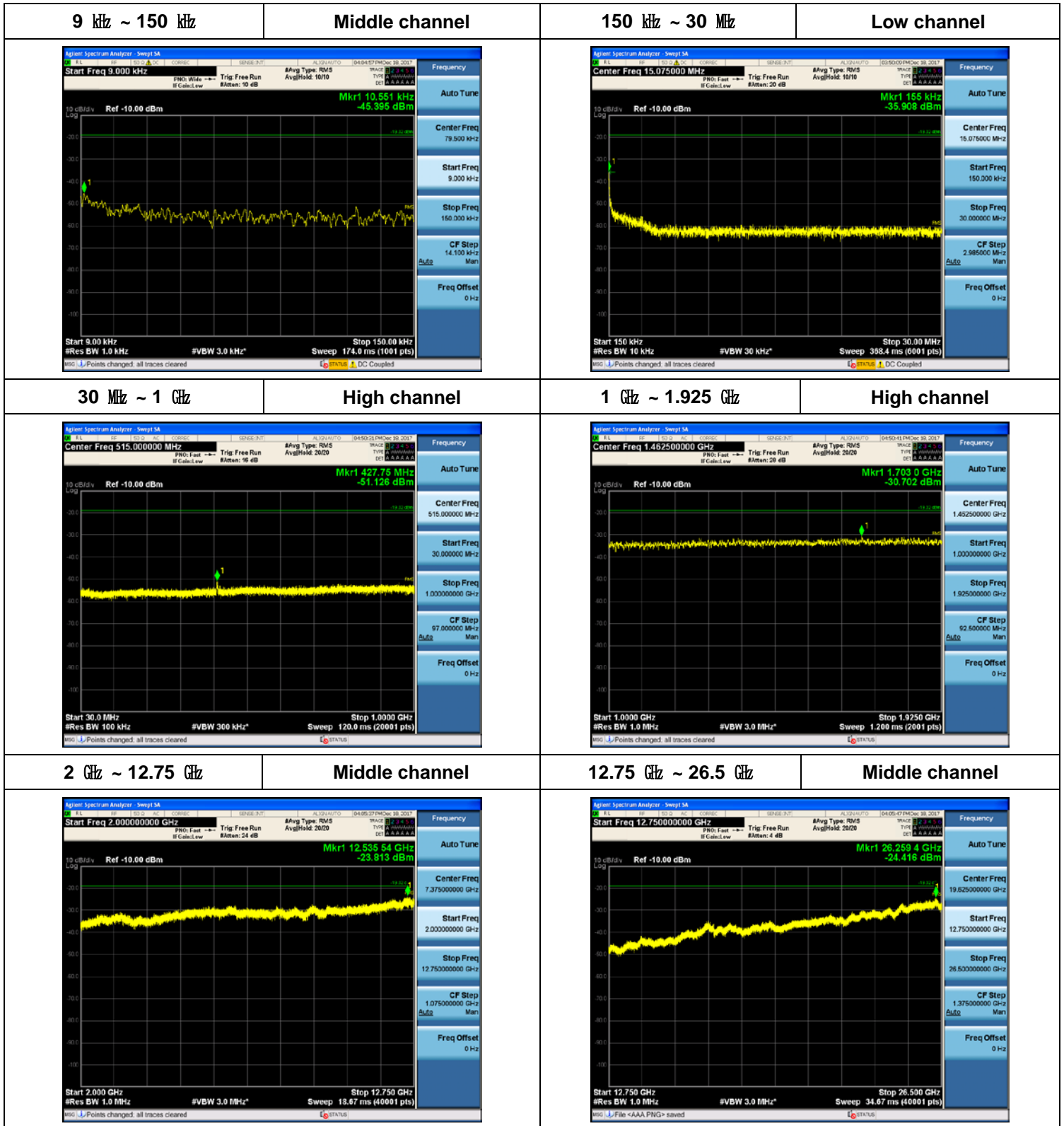


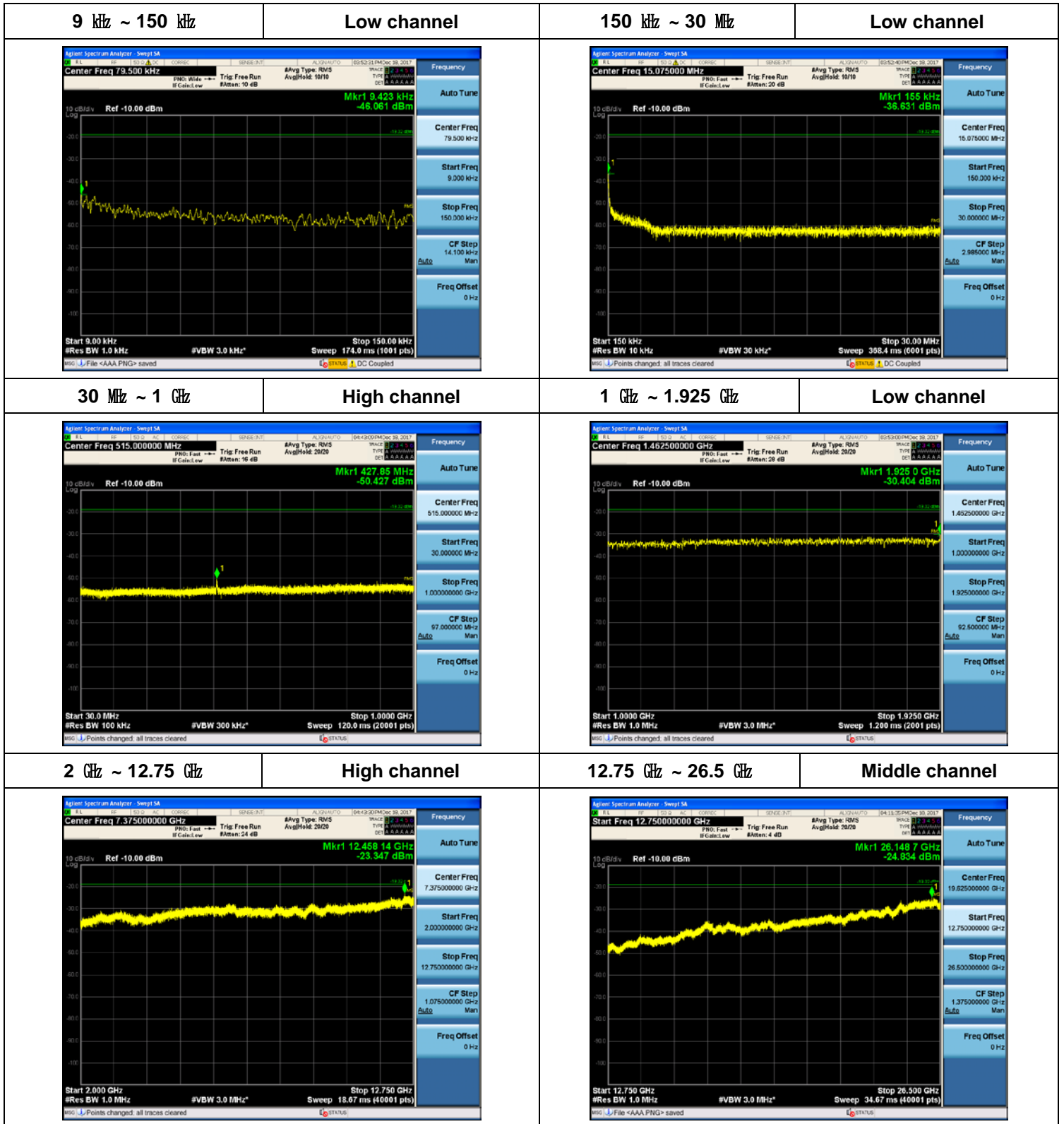
Plot Data for Output Port 2_QPSK



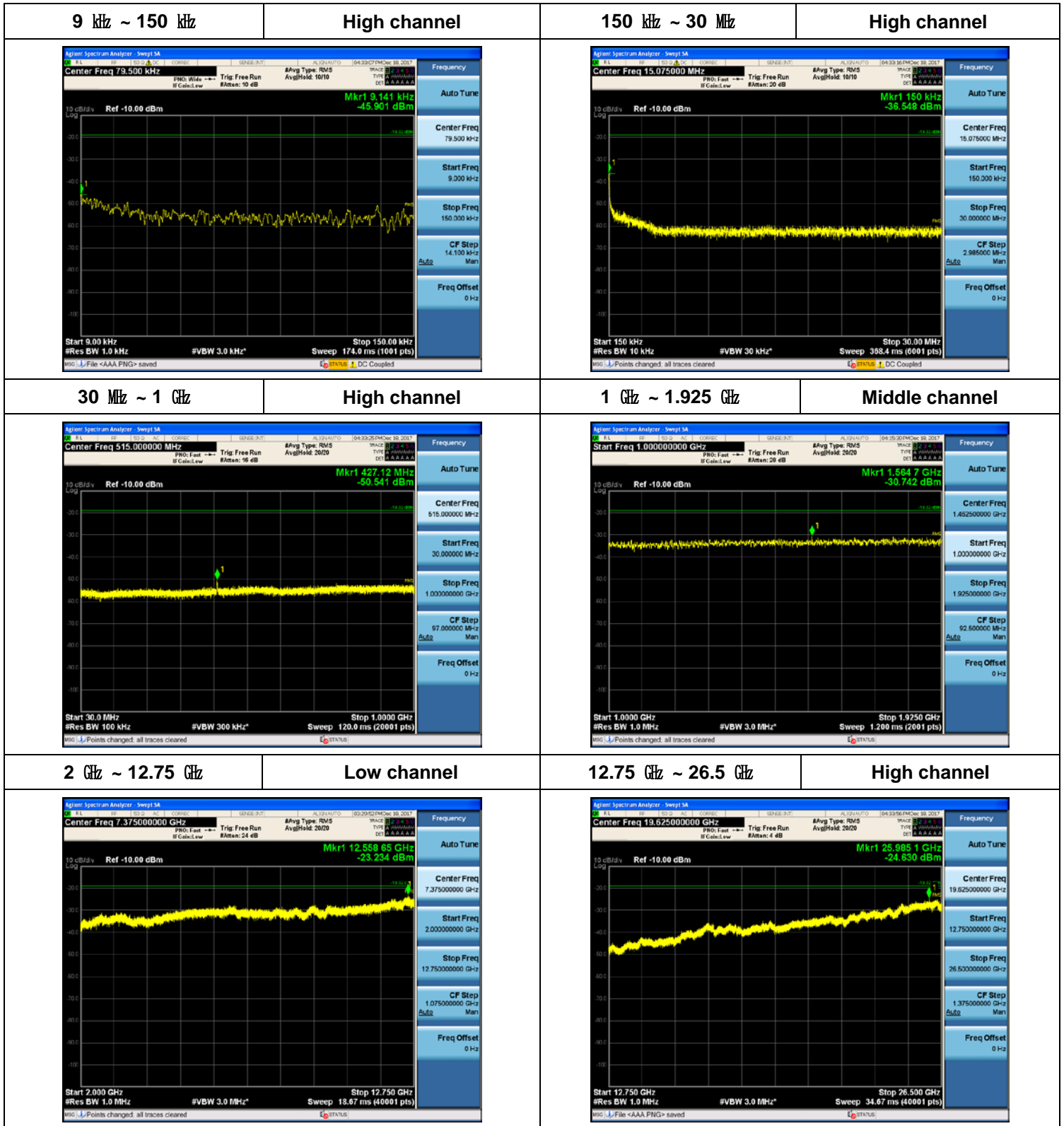
Plot Data for Output Port 2_16QAM



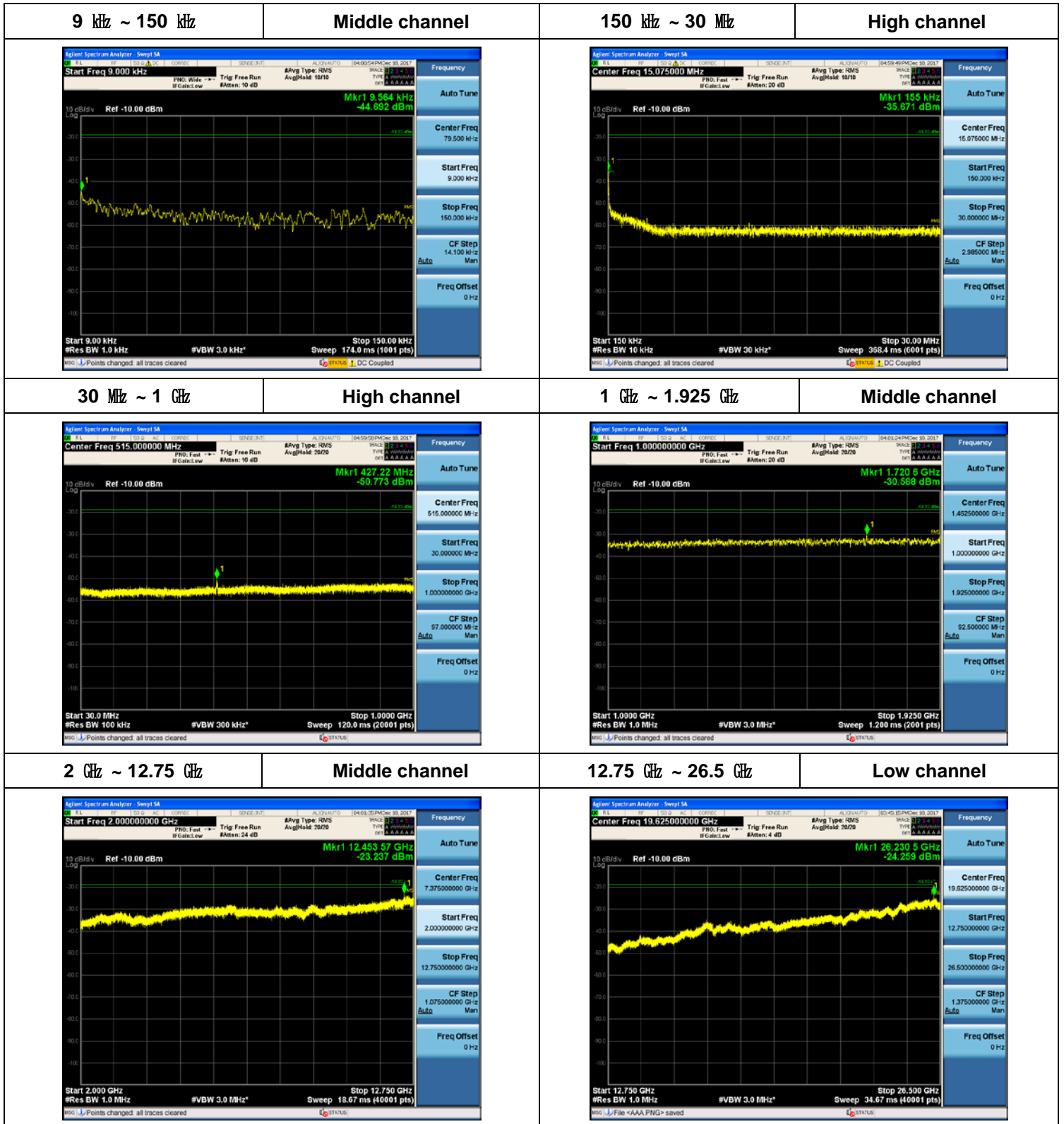
Plot Data for Output Port 2_64QAM



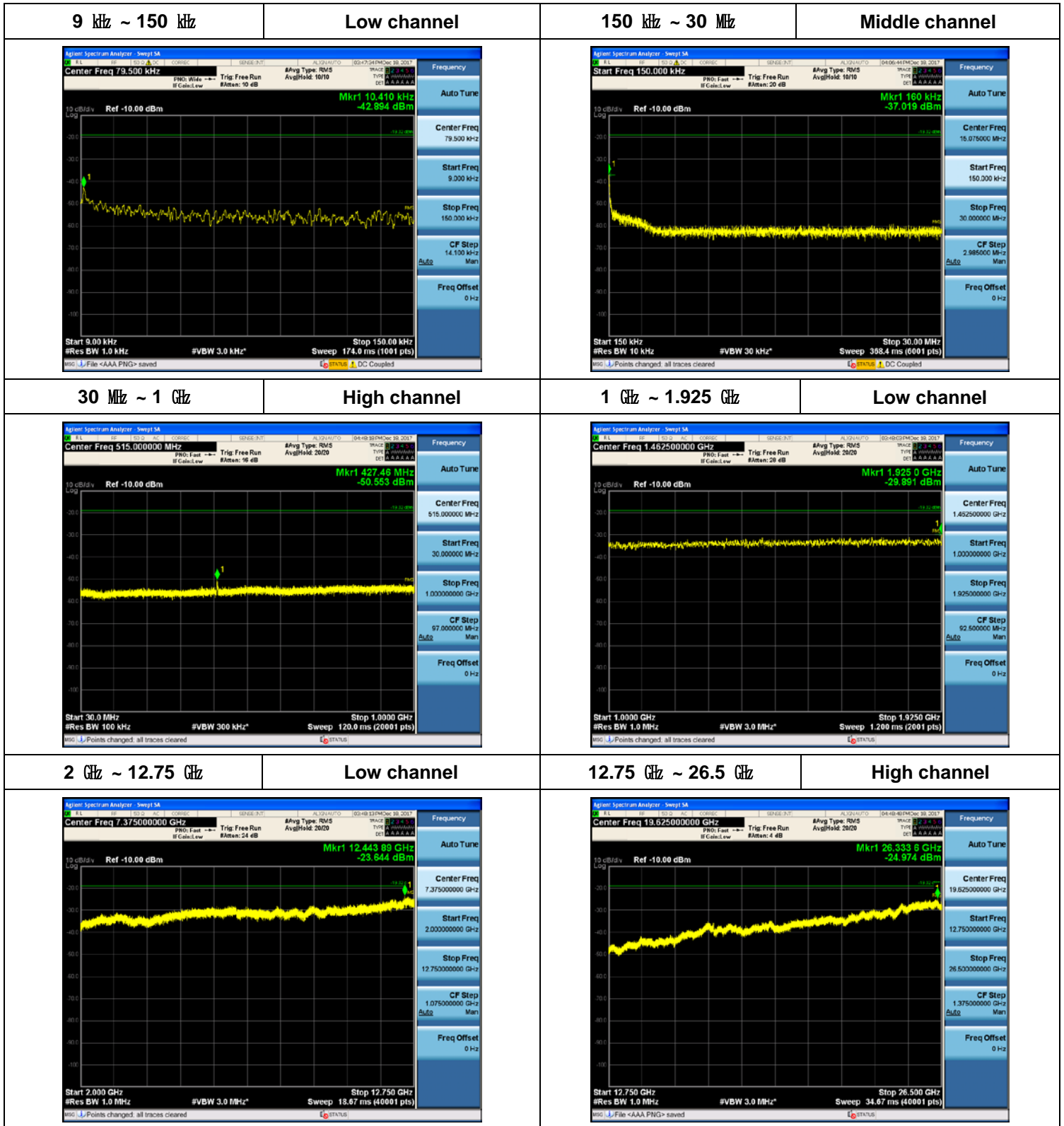
Plot Data for Output Port 2_256QAM



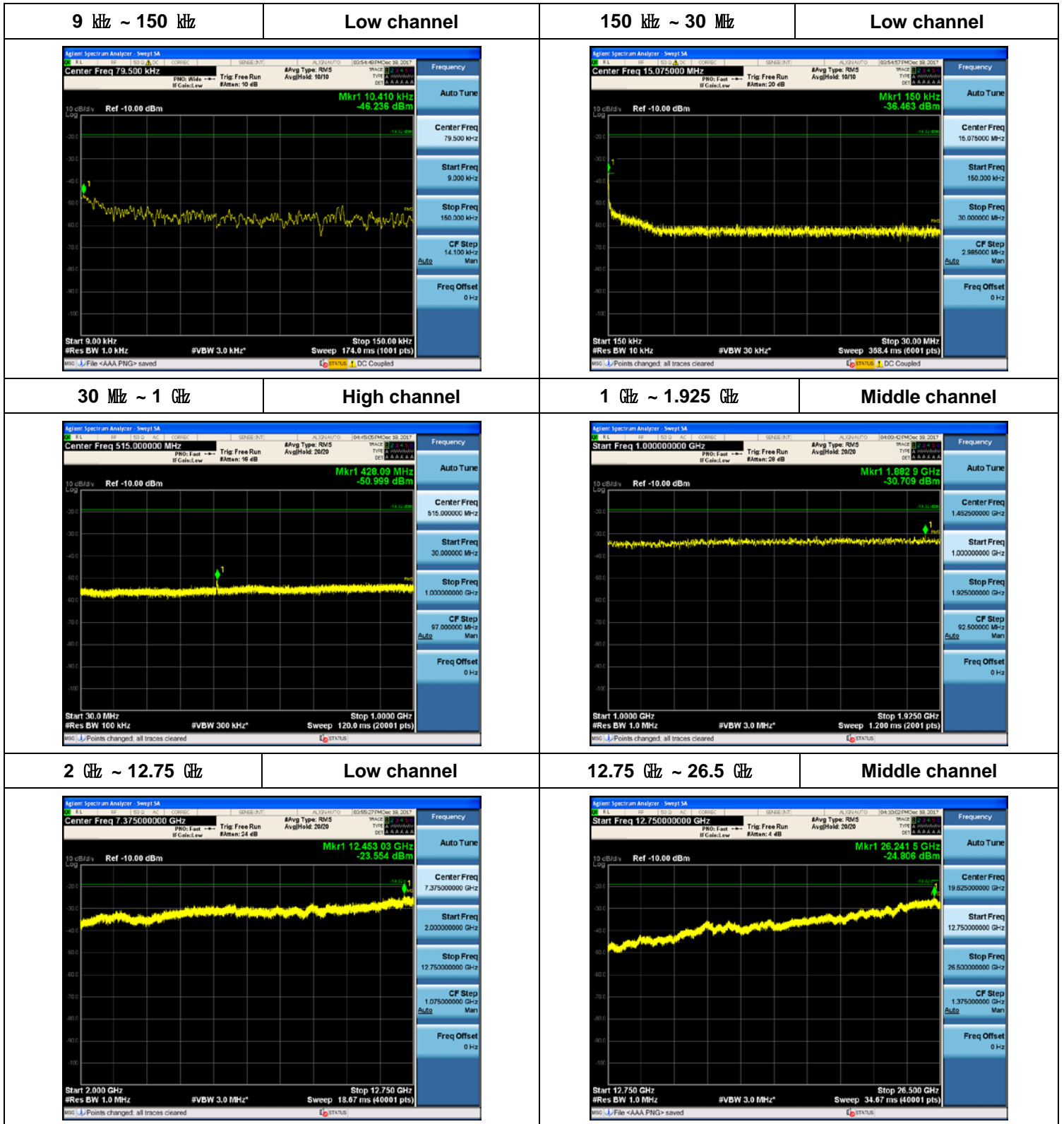
Plot Data for Output Port 3_QPSK



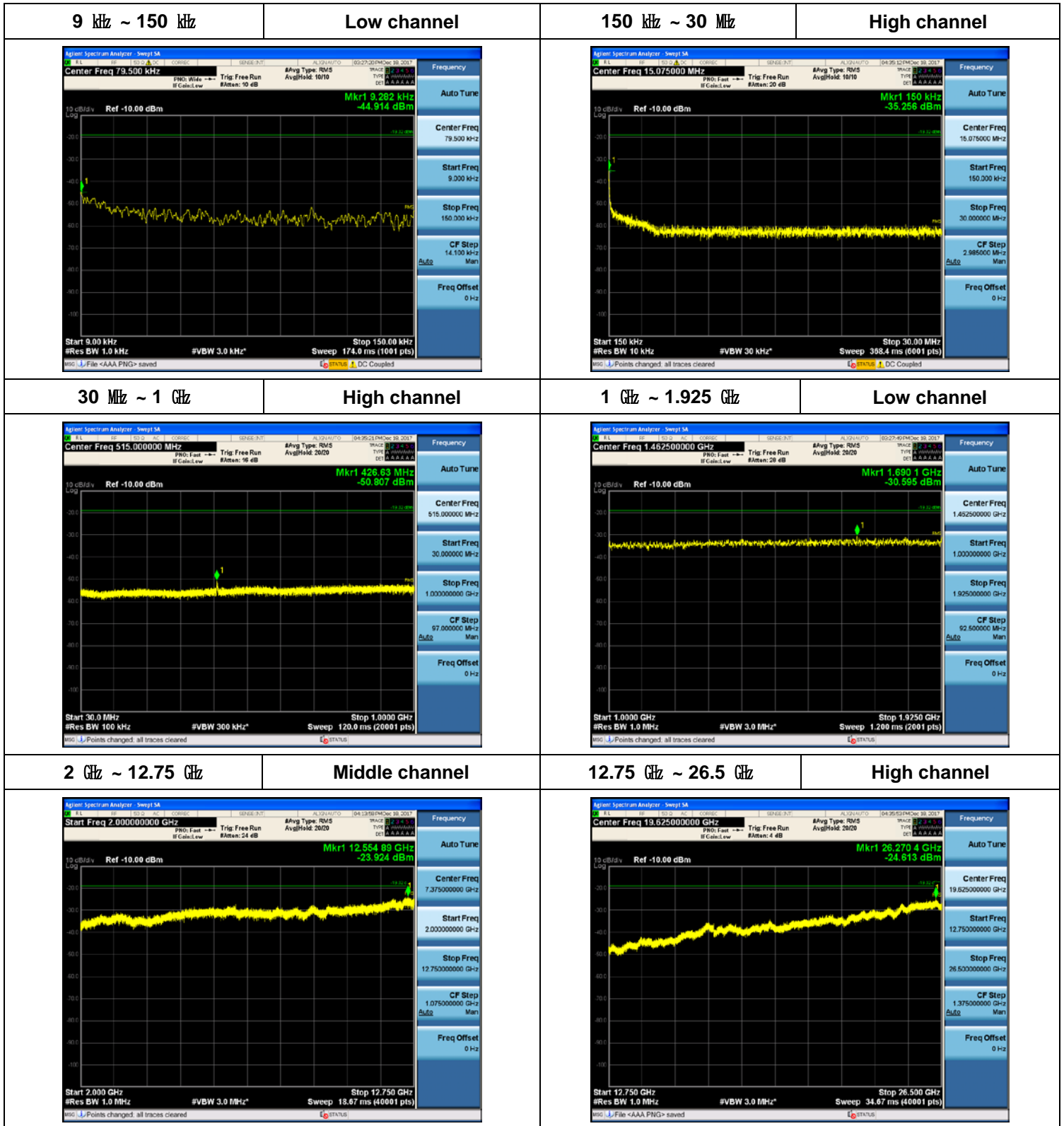
Plot Data for Output Port 3_16QAM



Plot Data for Output Port 3_64QAM

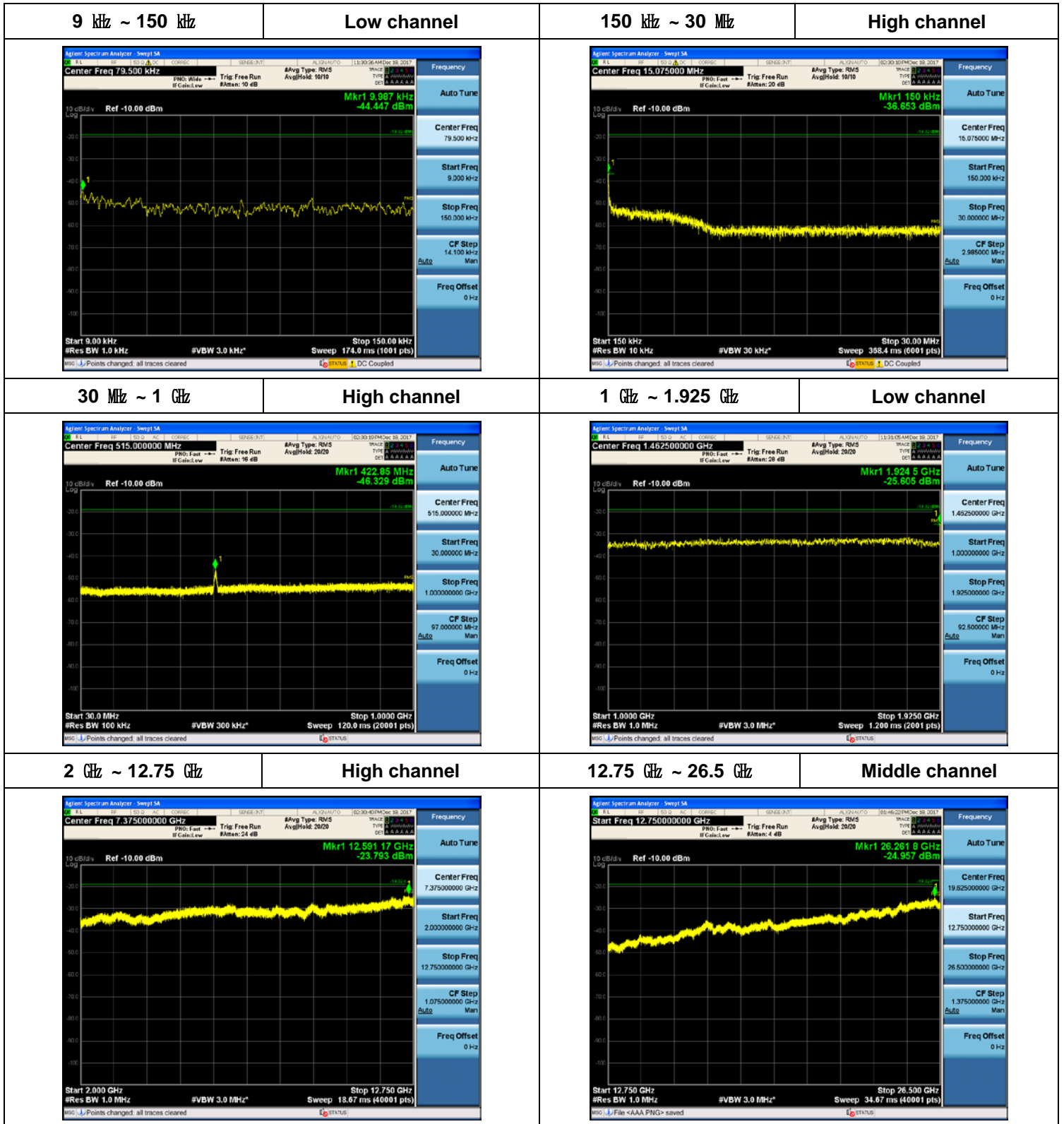


Plot Data for Output Port 3_256QAM

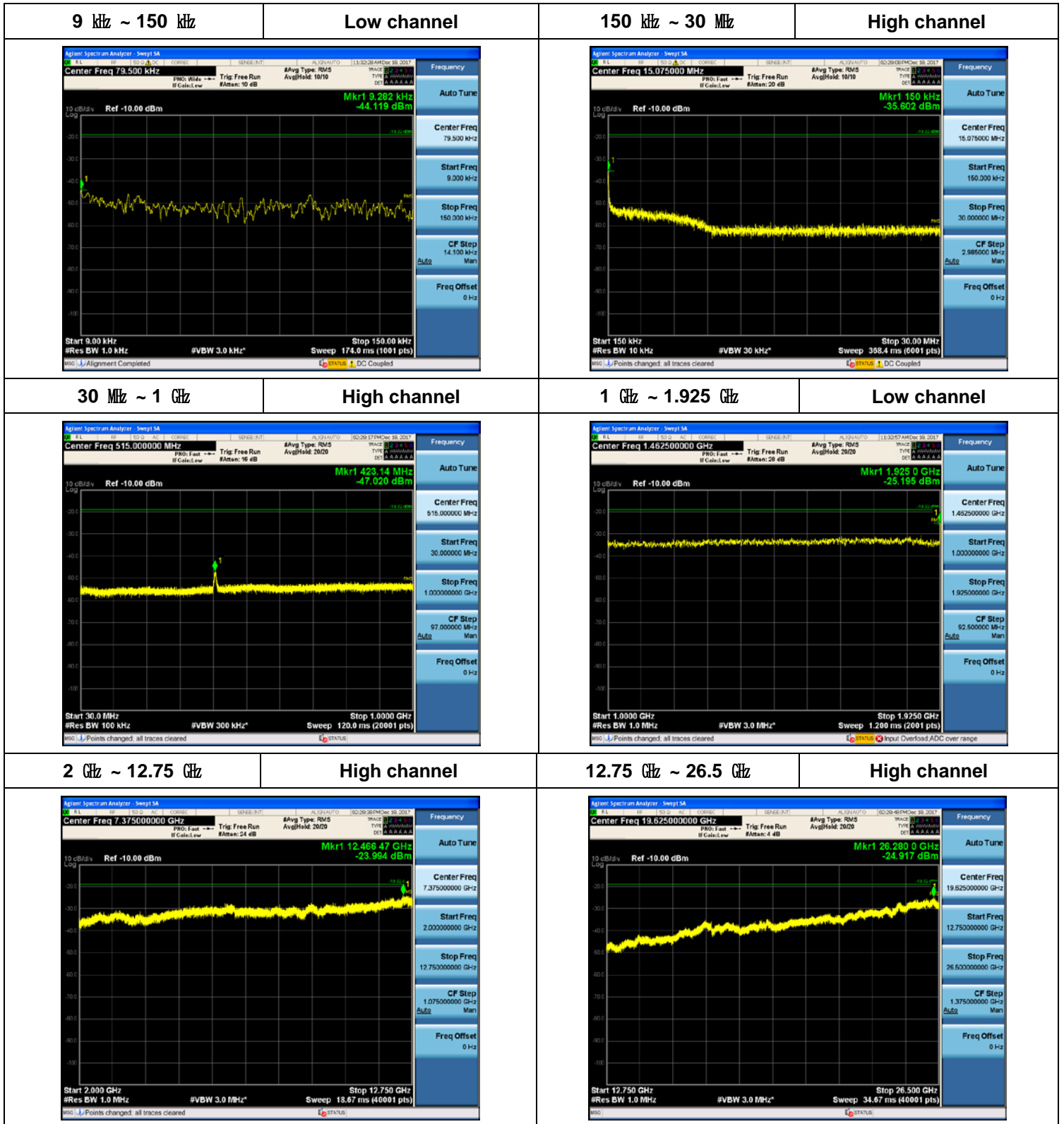


5 MHz + 5 MHz / 2 Carriers (20 W + 20 W)

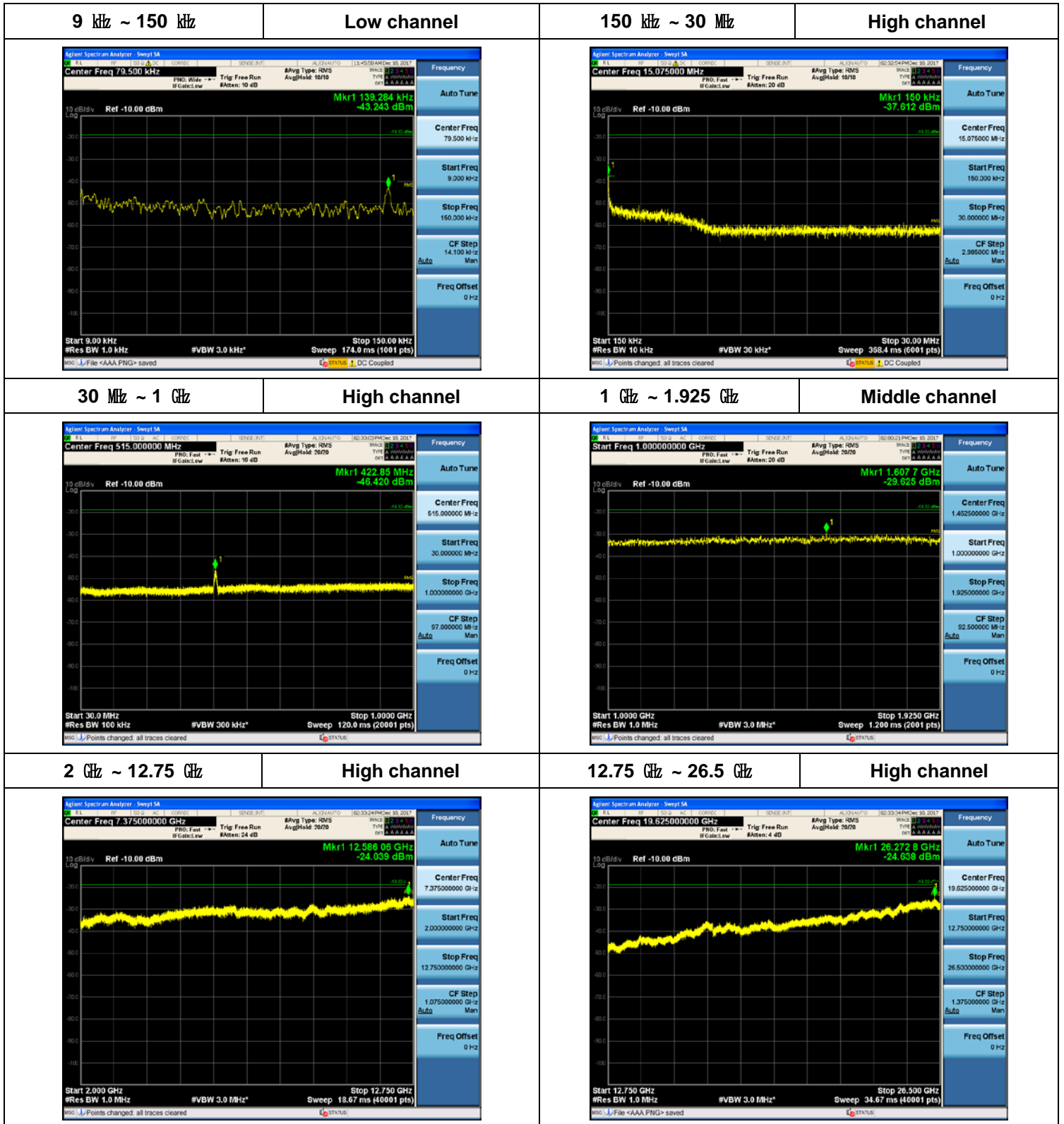
Plot Data for Output Port 0_256QAM



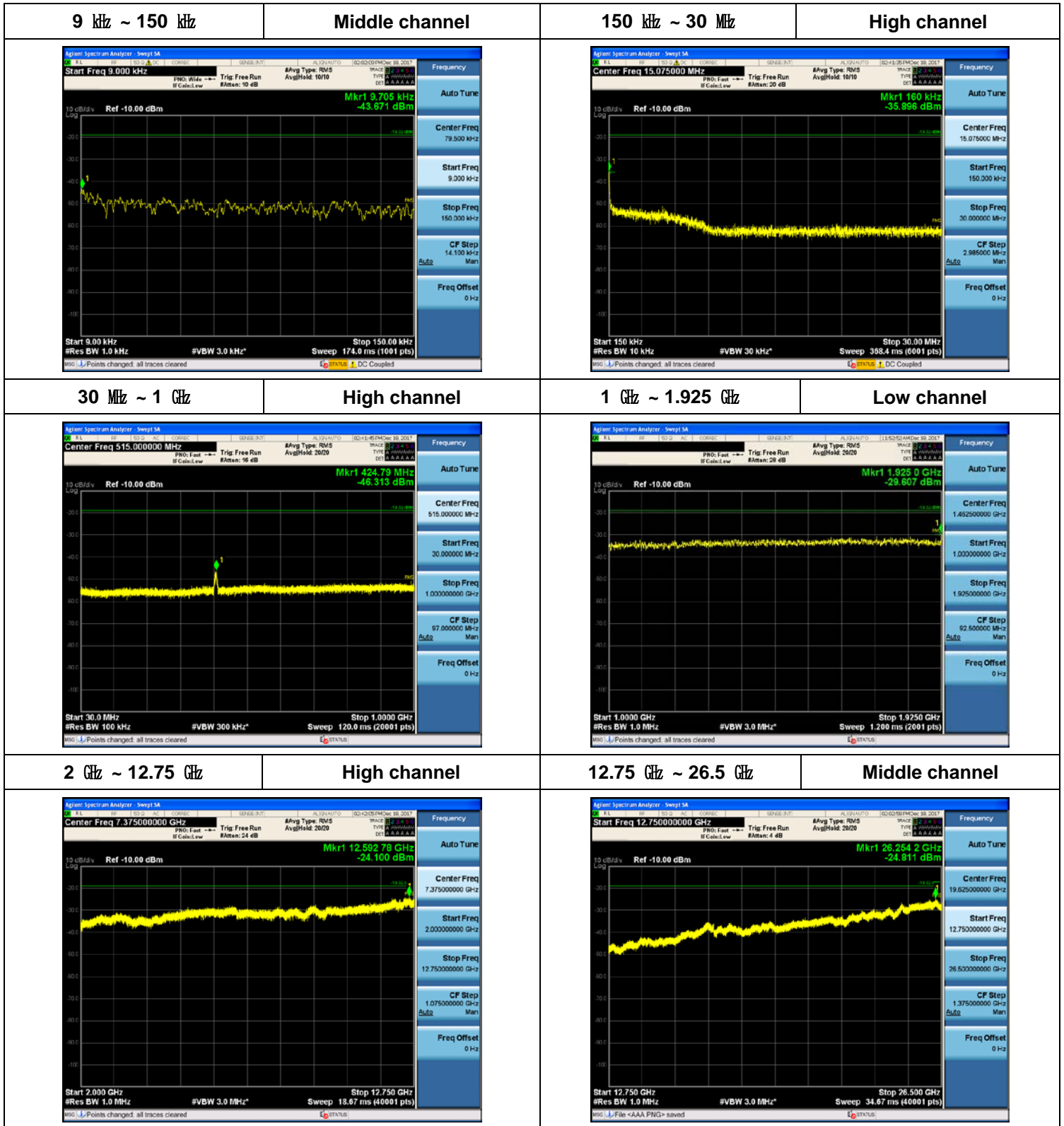
Plot Data for Output Port 1_256QAM



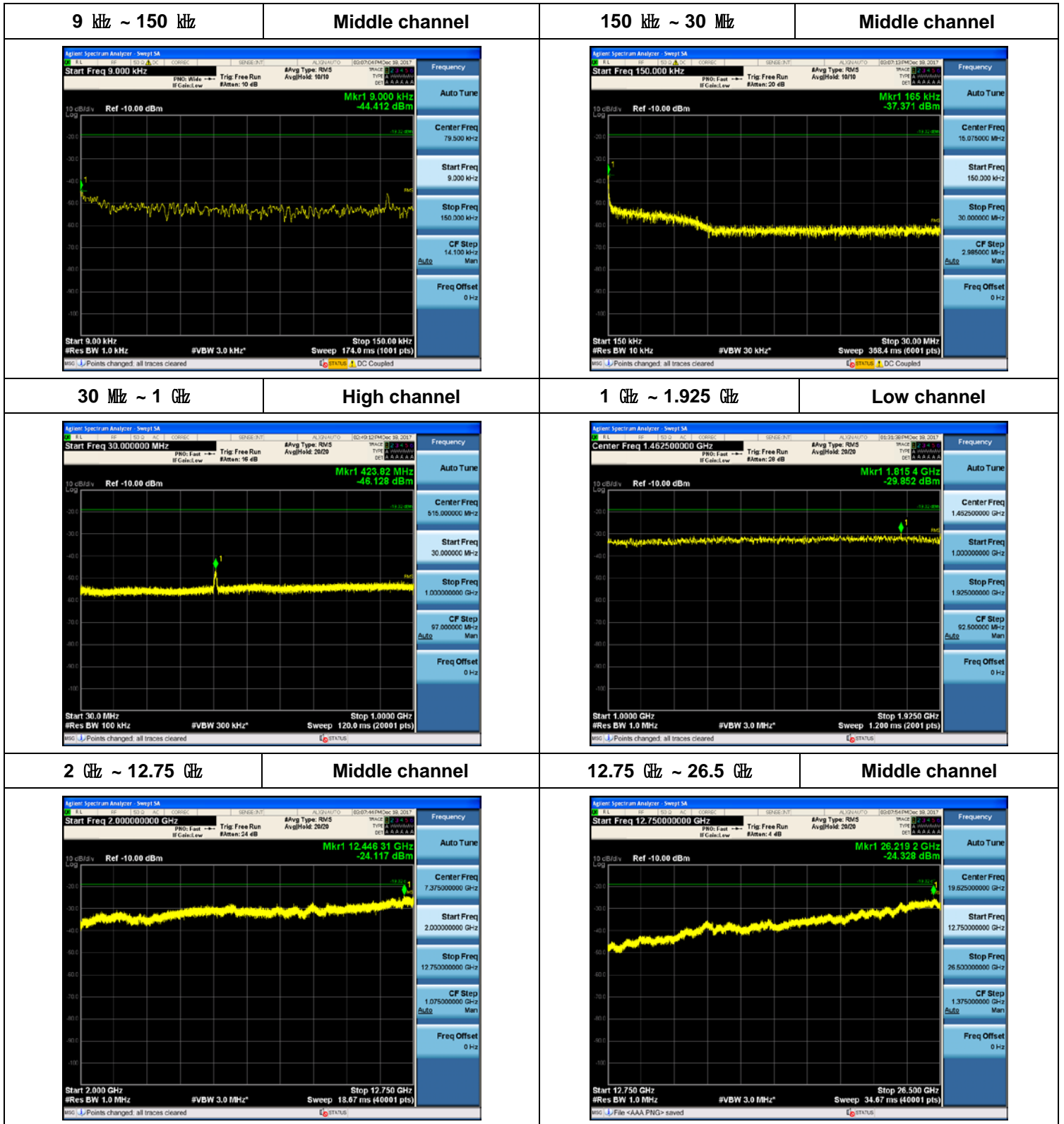
Plot Data for Output Port 2_QPSK



Plot Data for Output Port 2_16QAM



Plot Data for Output Port 2_64QAM



Plot Data for Output Port 2_256QAM

