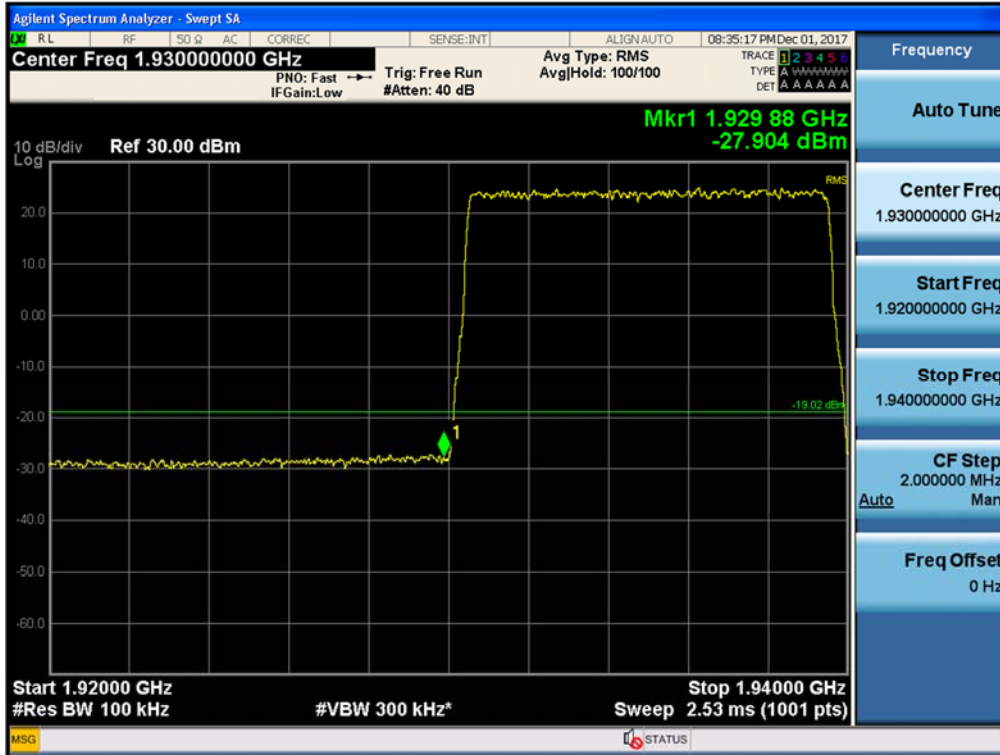
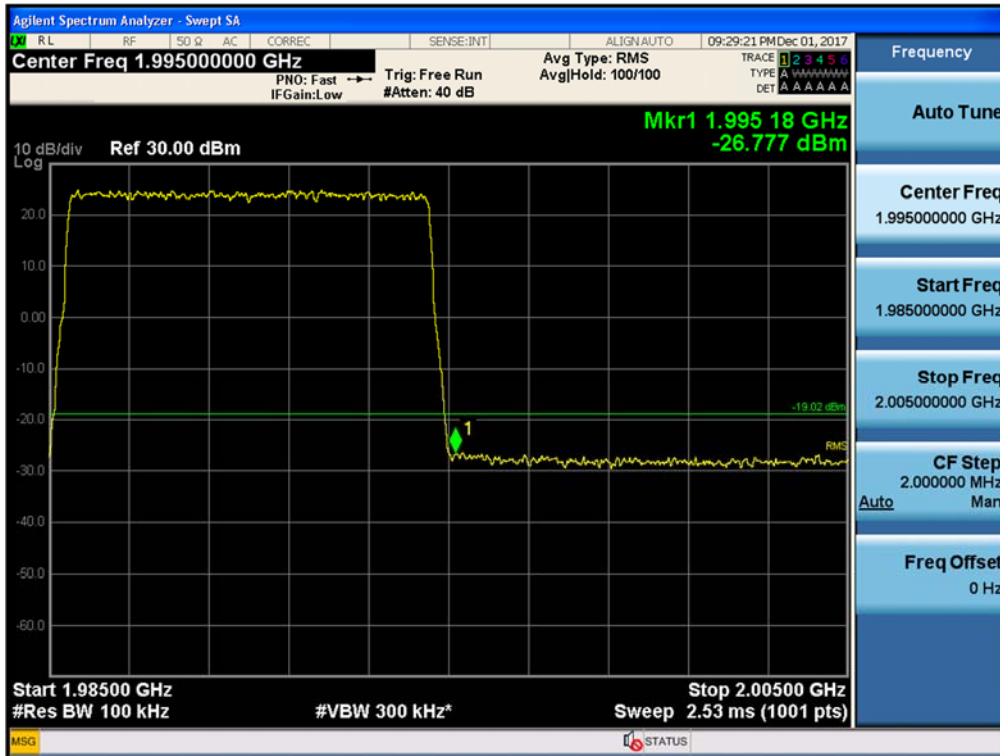


Test Data at Output Port 2_256QAM

[Downlink Low]

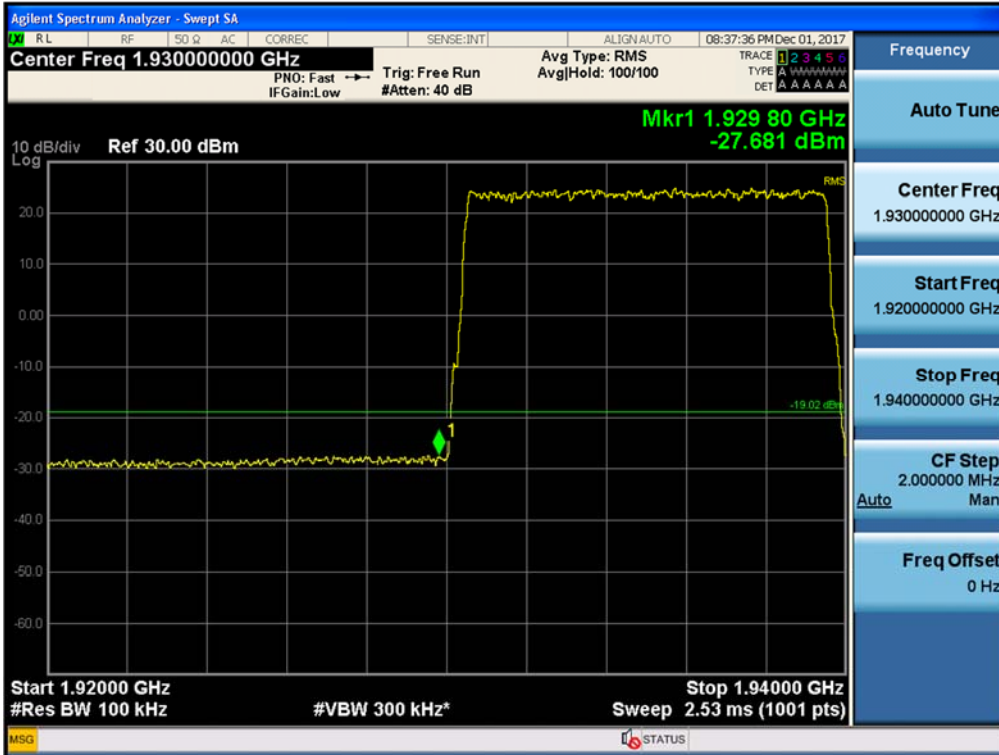


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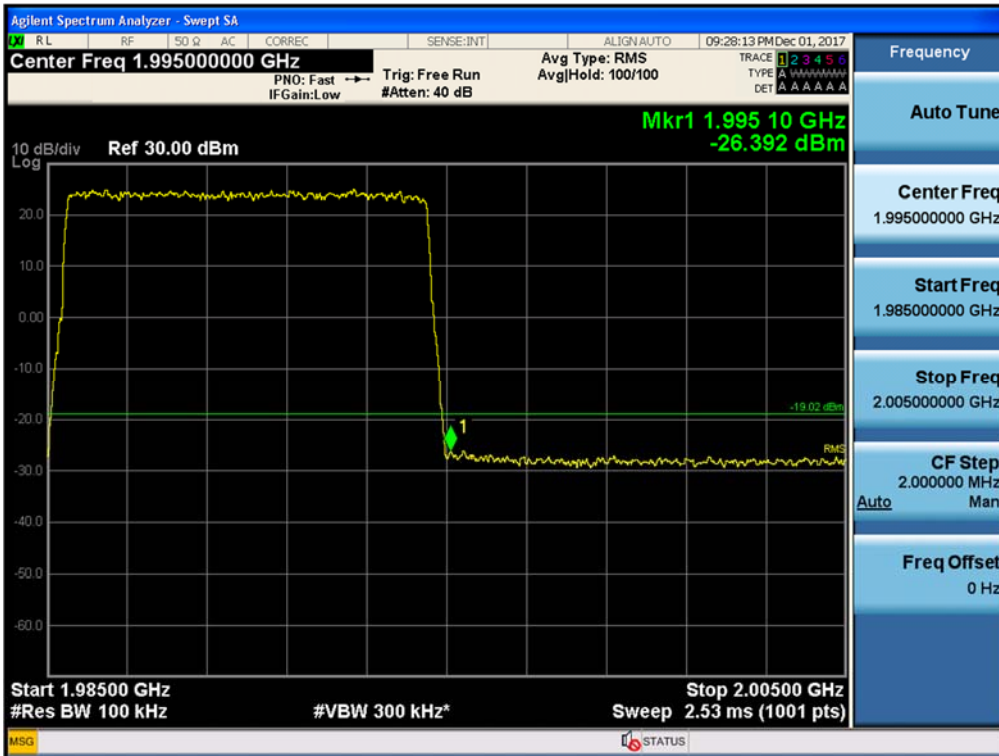


Test Data at Output Port 3_256QAM

[Downlink Low]



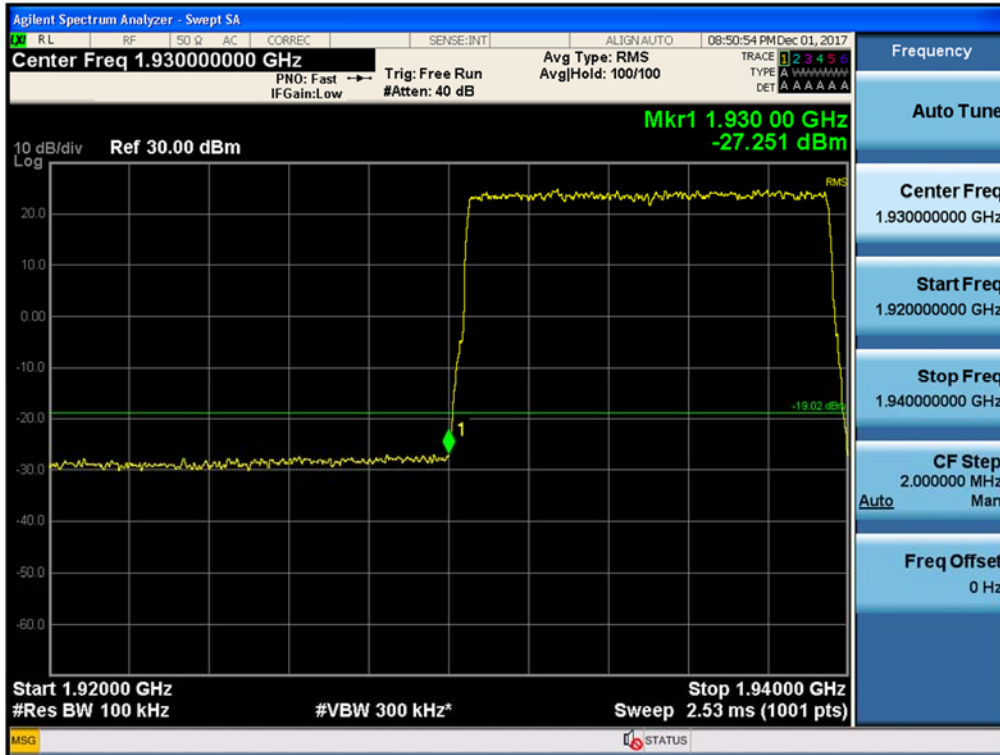
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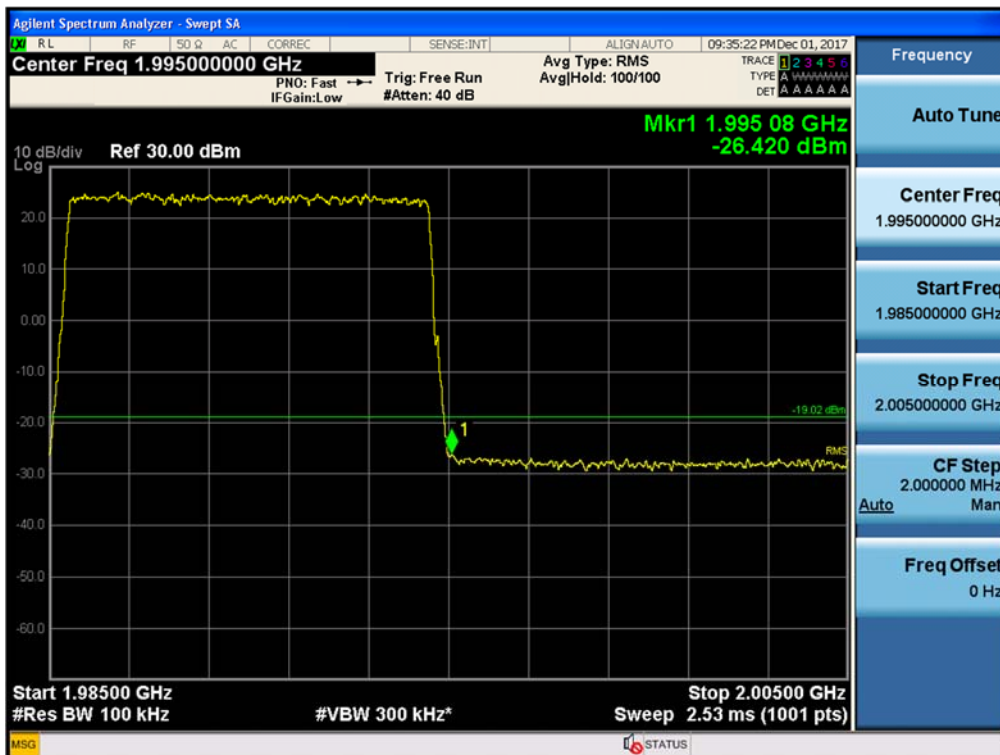
10 MHz + 10 MHz / 2 Carriers (20 W + 20 W)

Test Data at Output Port 0_256QAM

[Downlink Low]

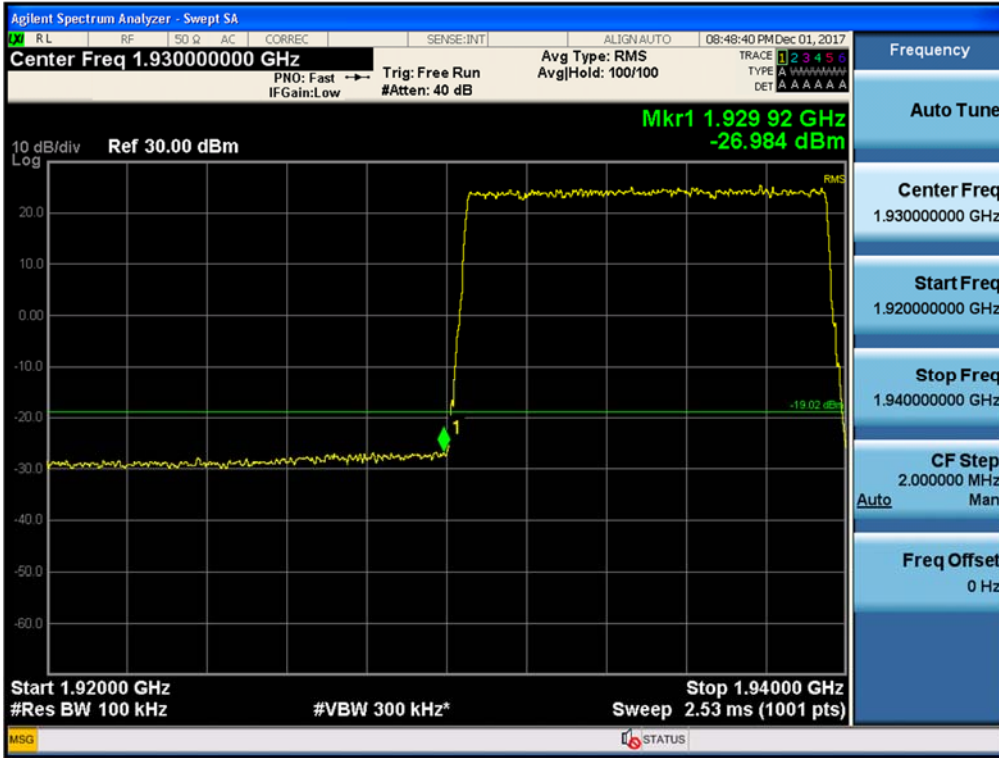


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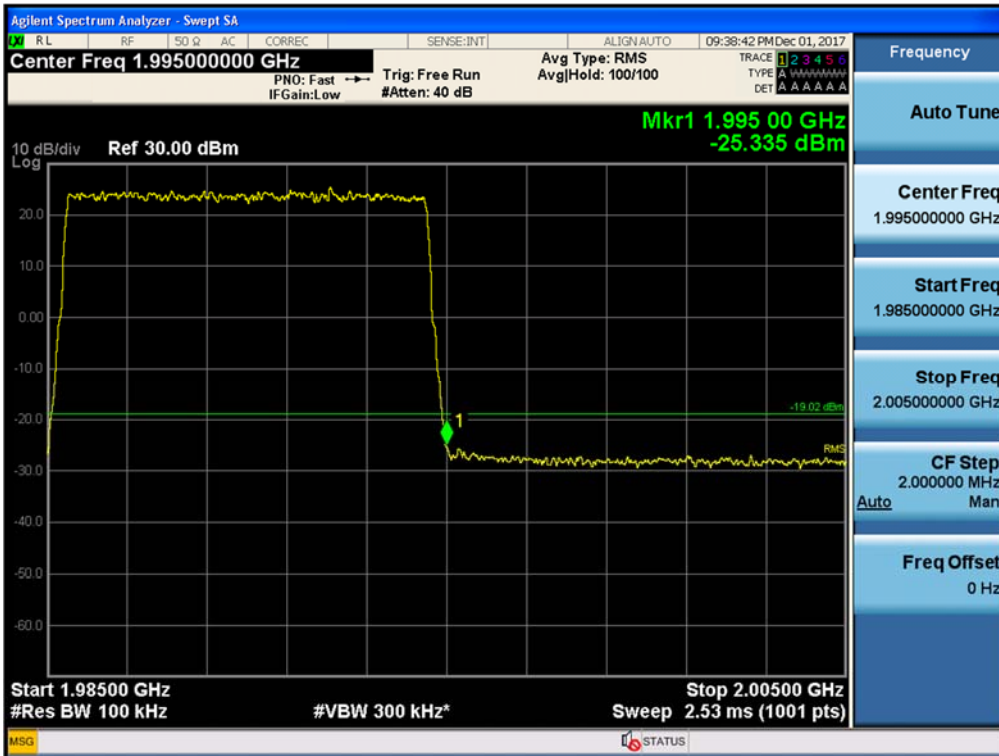


Test Data at Output Port 1_256QAM

[Downlink Low]

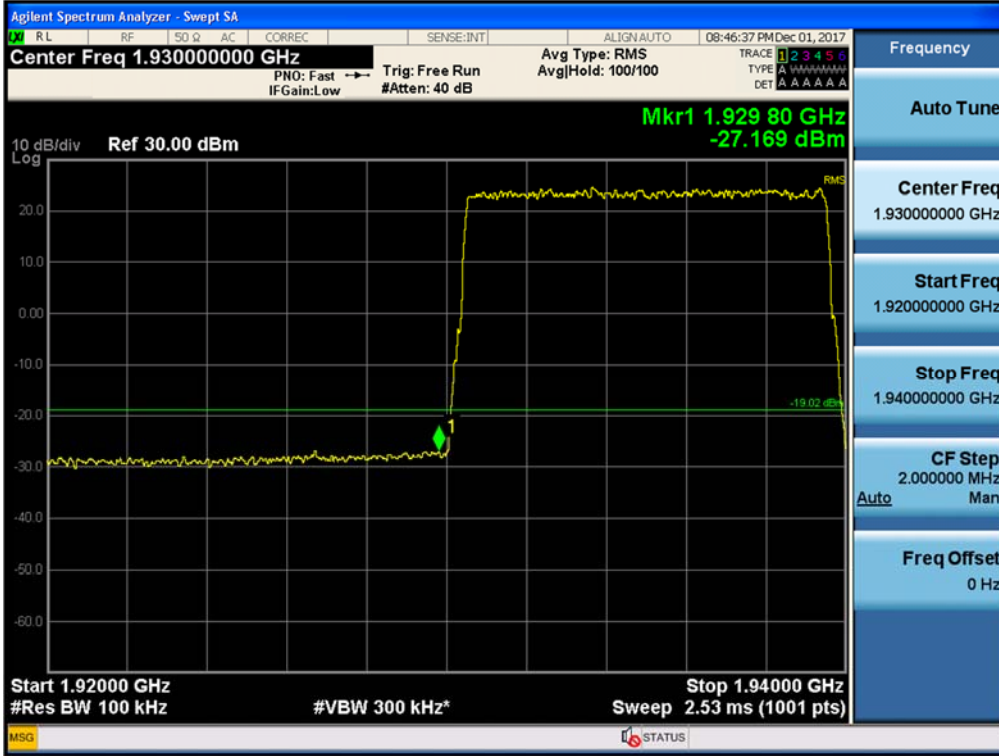


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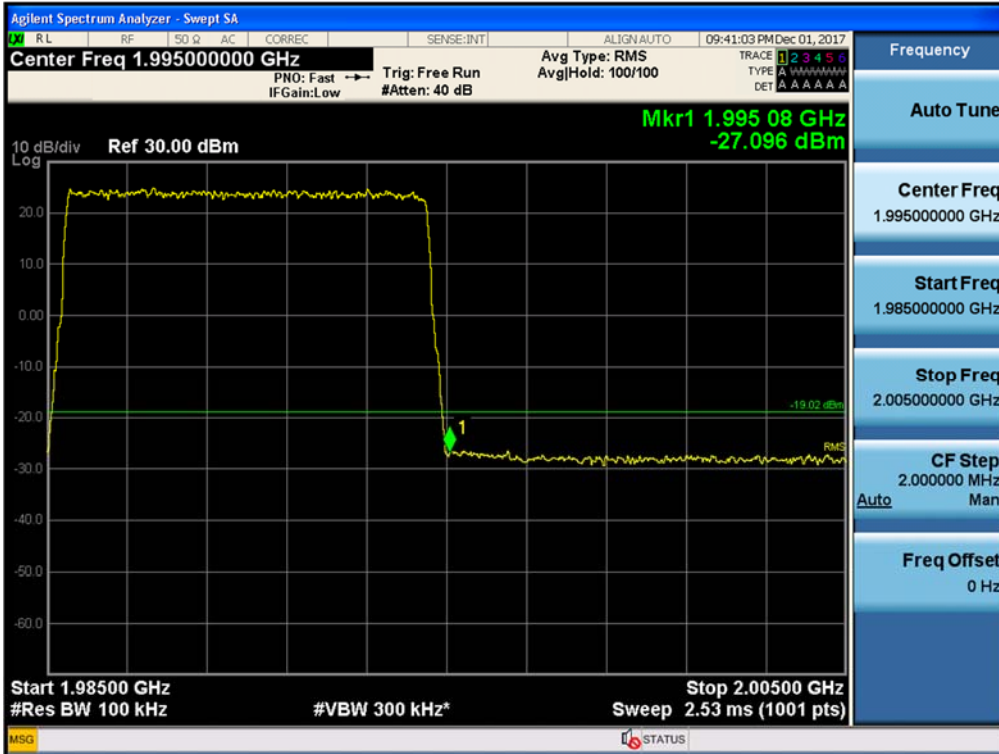


Test Data at Output Port 2_256QAM

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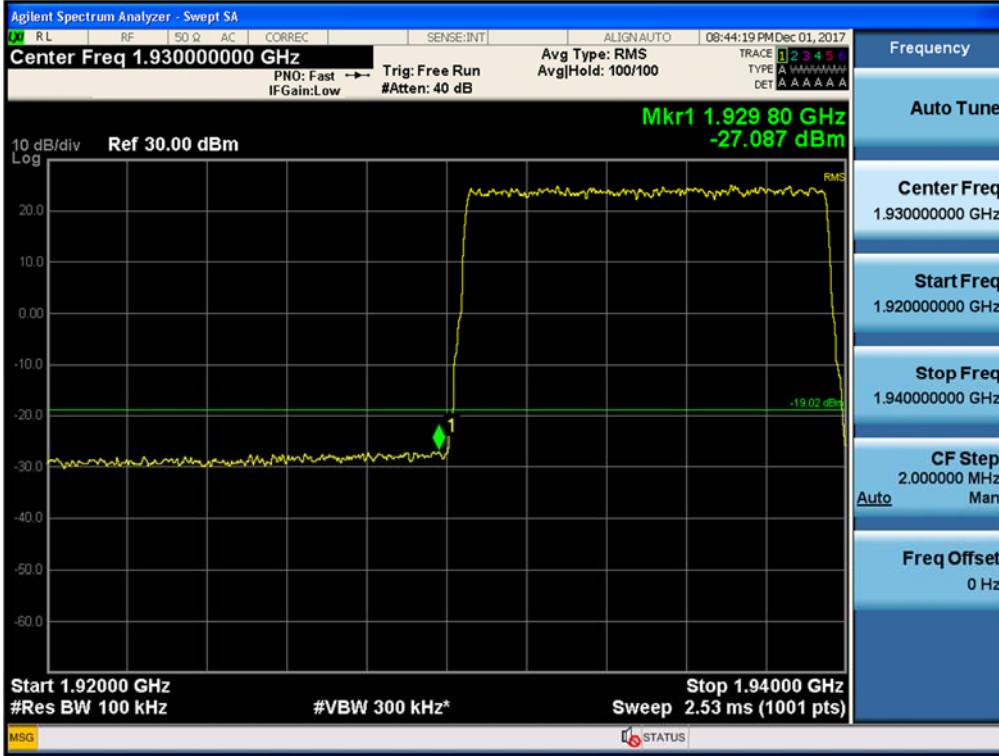


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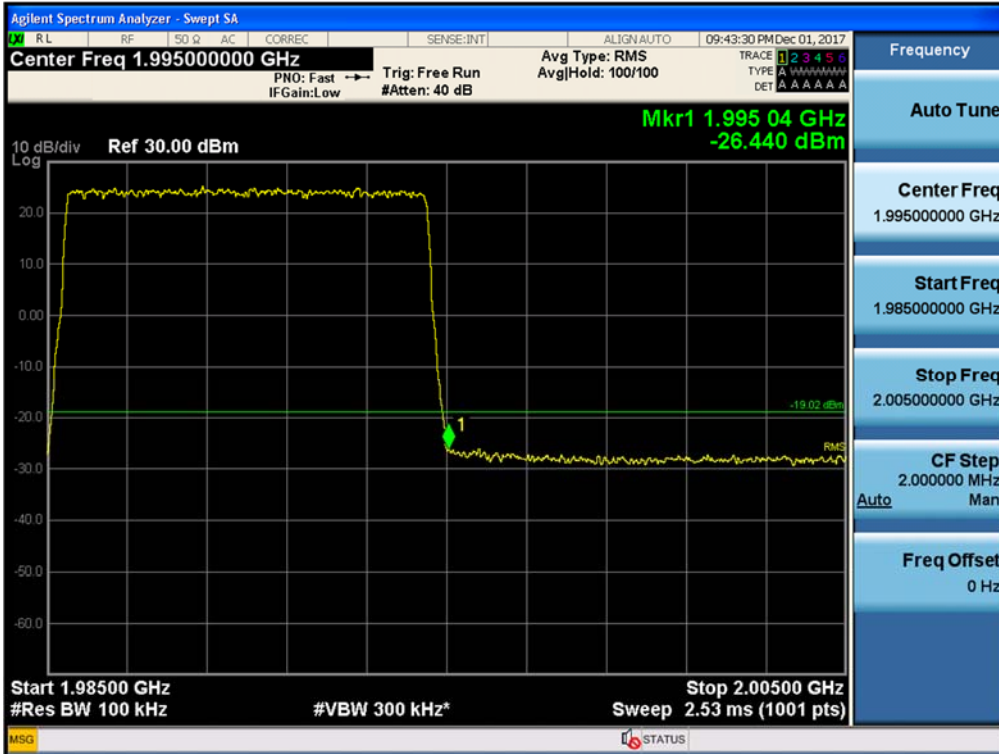


Test Data at Output Port 3_256QAM

[Downlink Low]



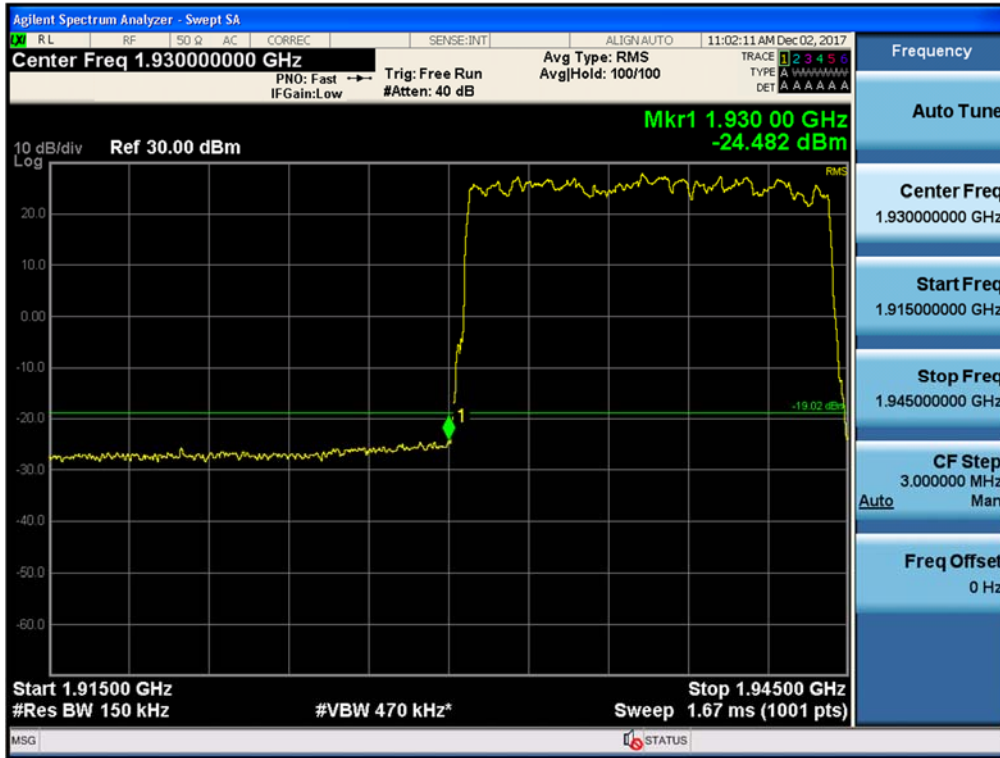
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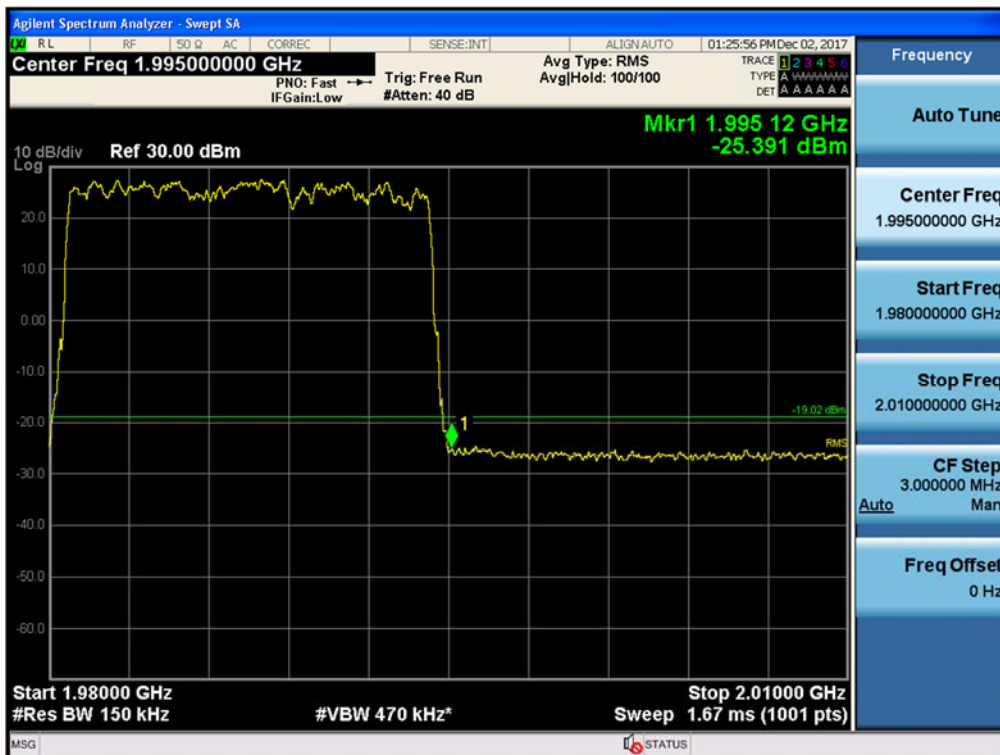
15 MHz / 1 Carrier (30 W)

Test Data at Output Port 0_QPSK

[Downlink Low]

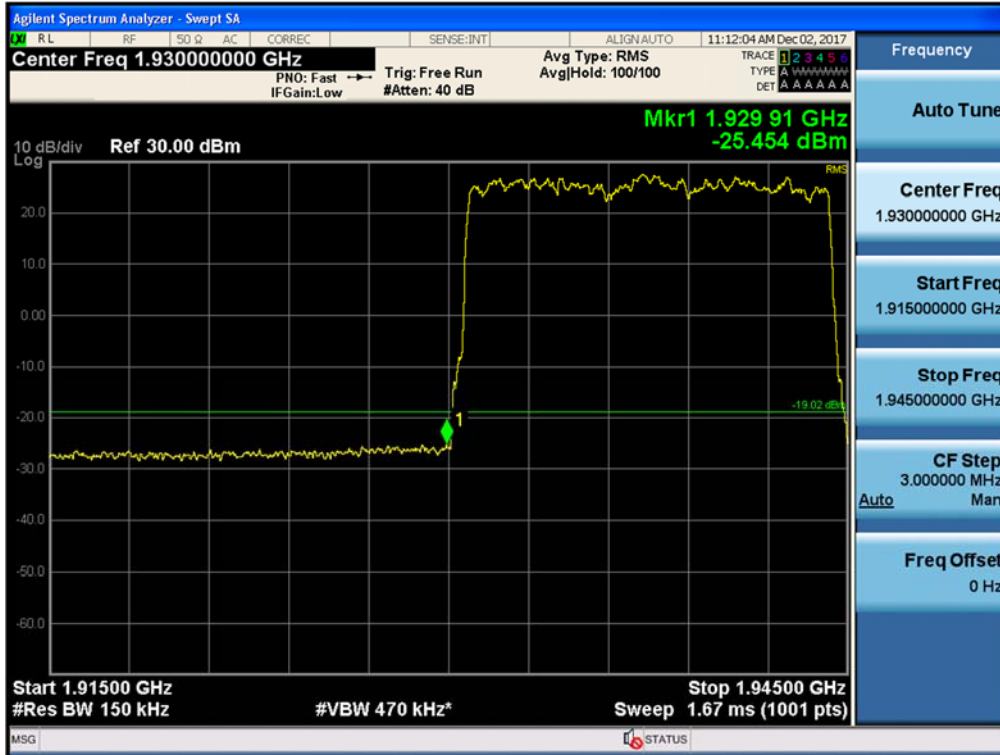


[Downlink High]

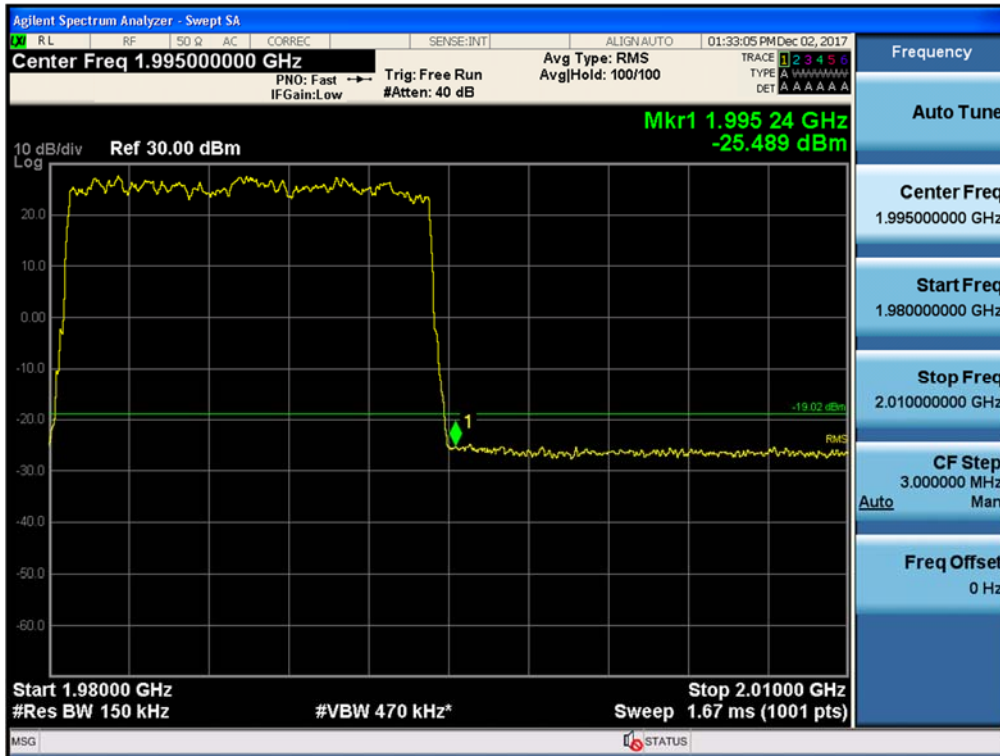


Test Data at Output Port 0_16QAM

[Downlink Low]

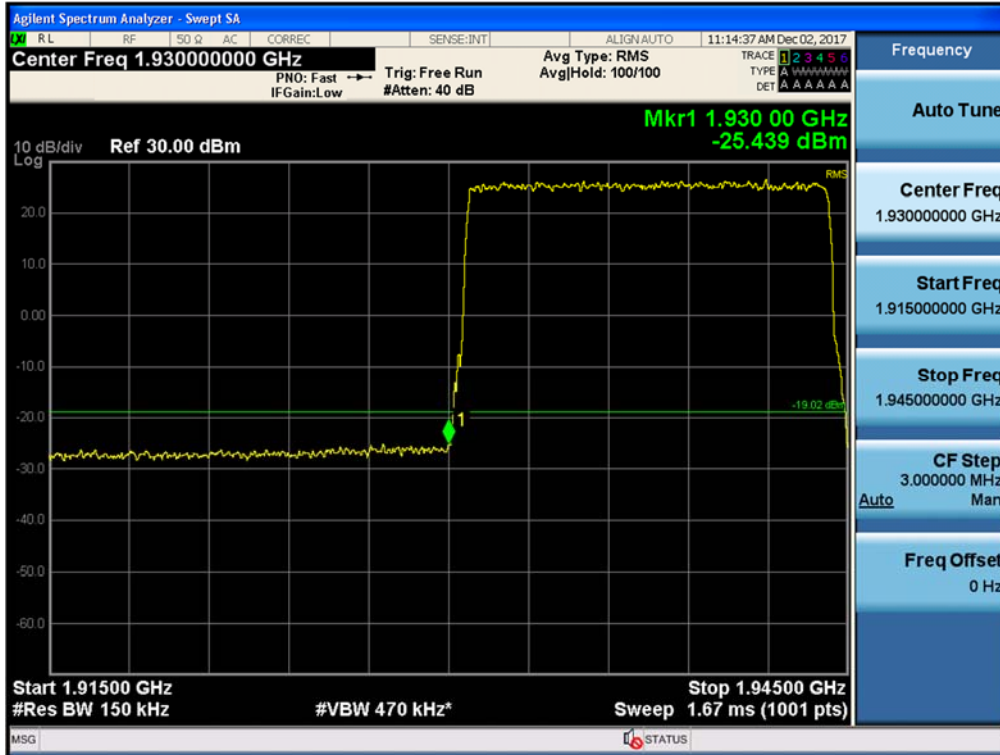


[Downlink High]

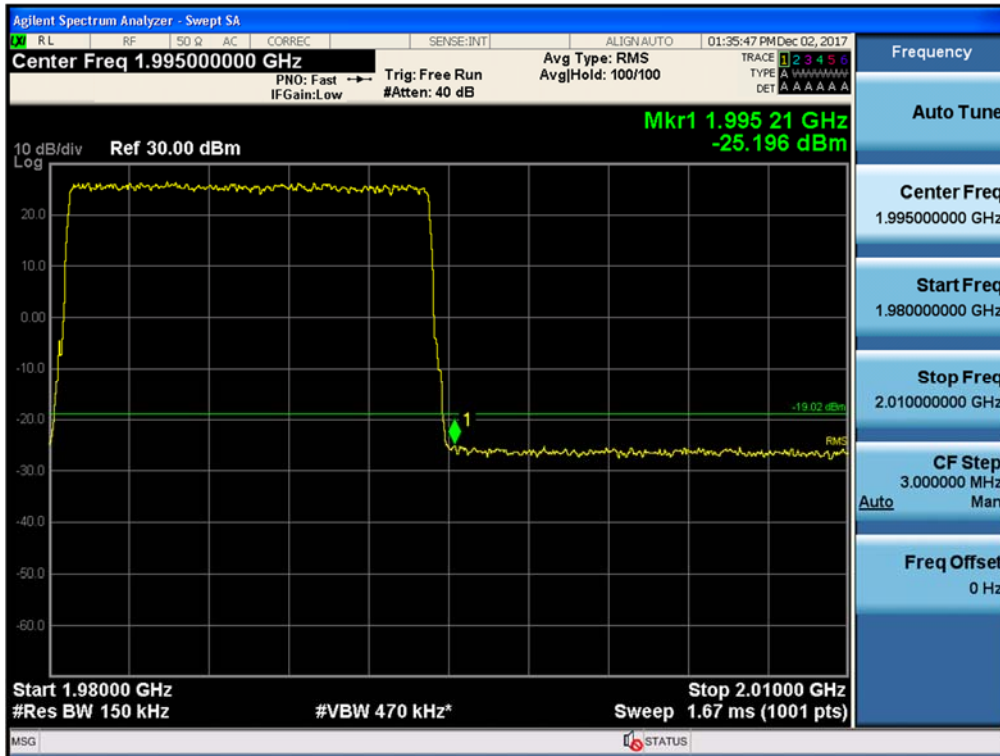


Test Data at Output Port 0_64QAM

[Downlink Low]

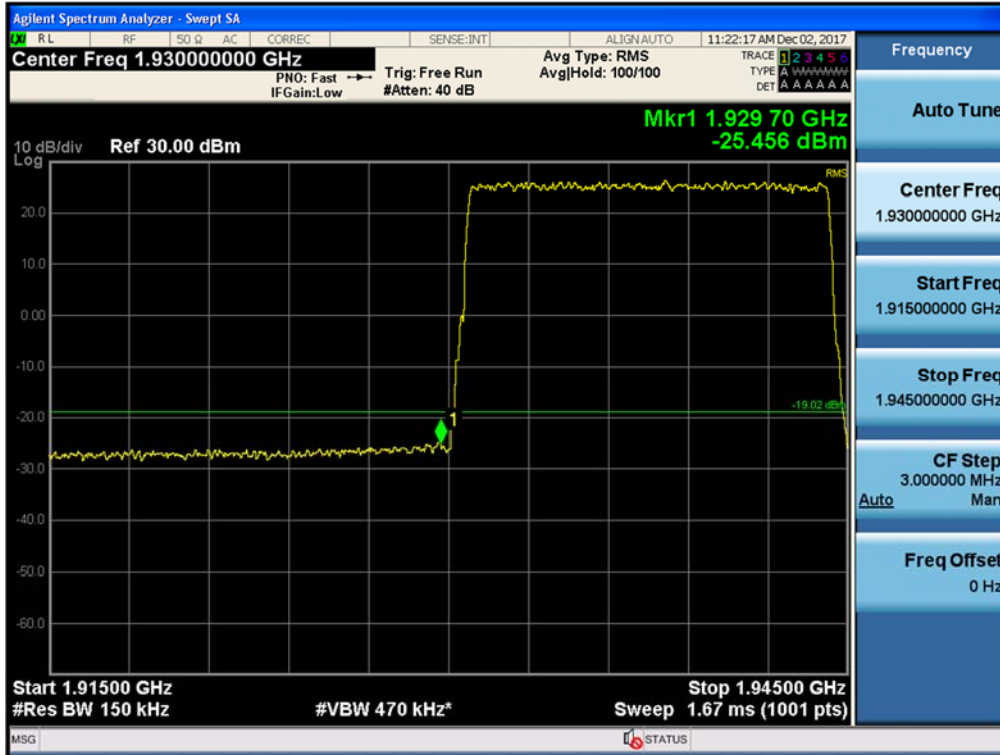


[Downlink High]

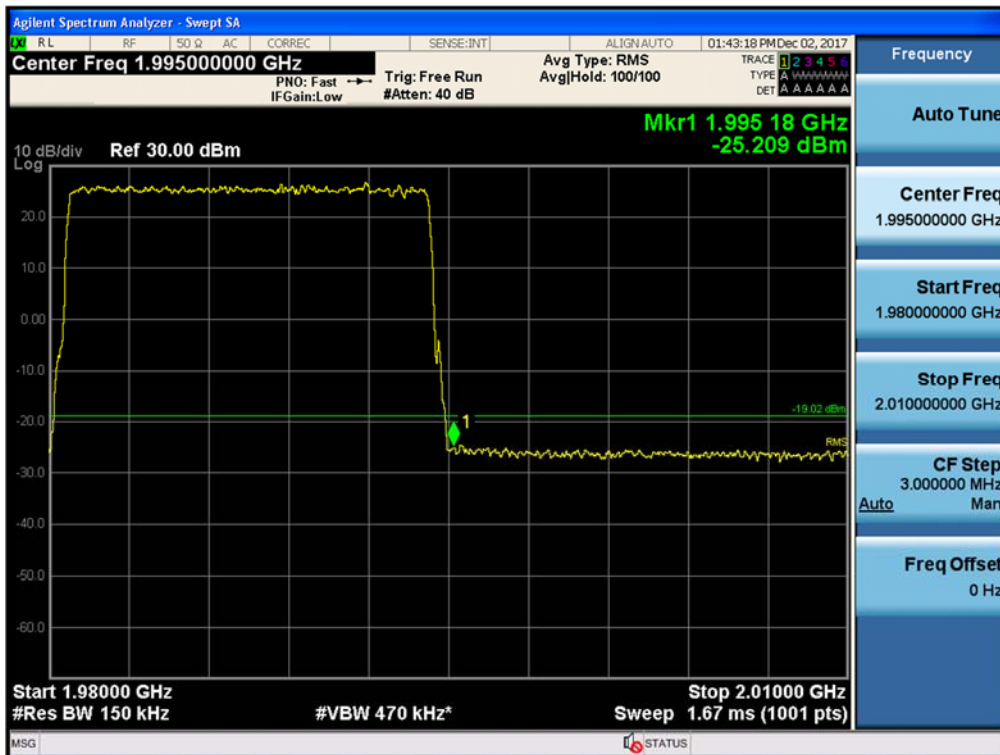


Test Data at Output Port 0_256QAM

[Downlink Low]

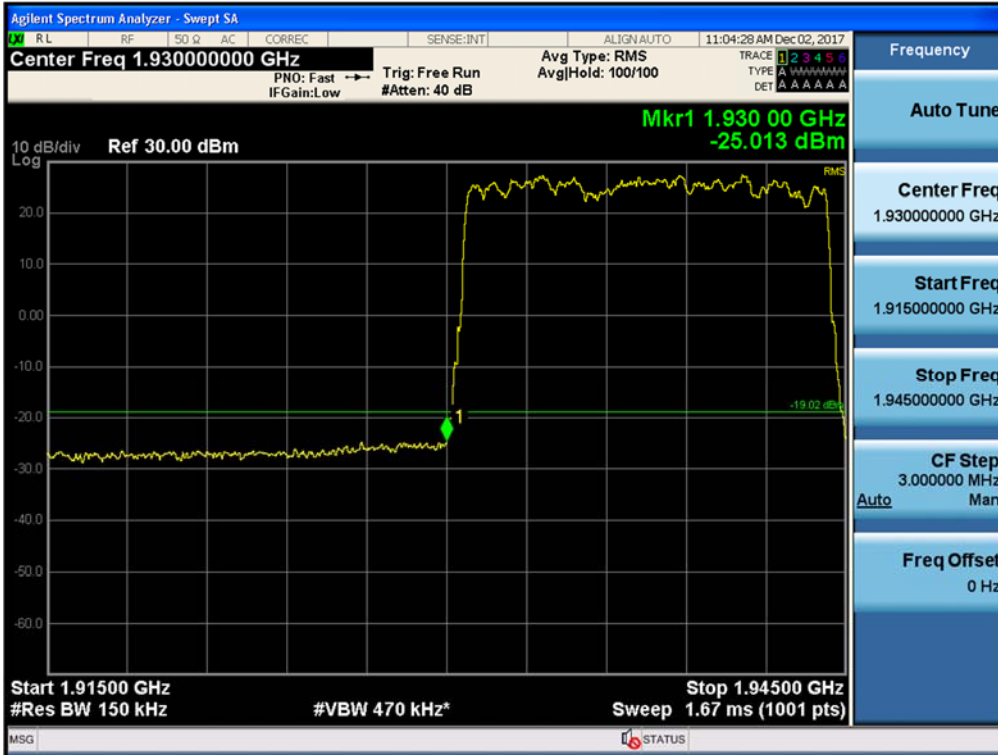


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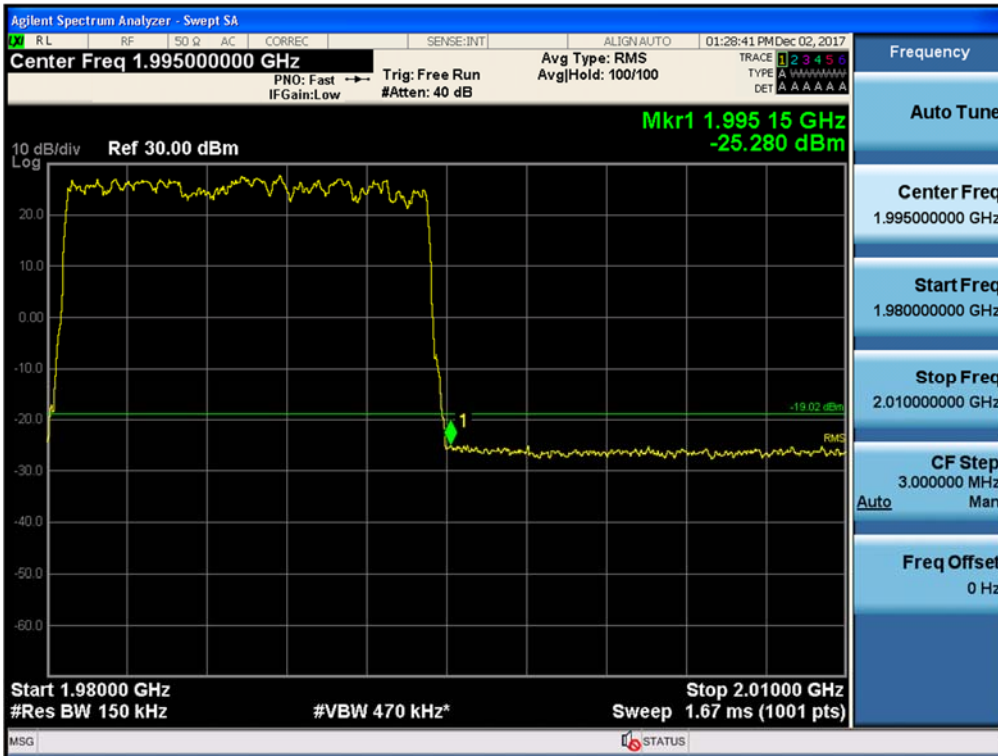


Test Data at Output Port 1_QPSK

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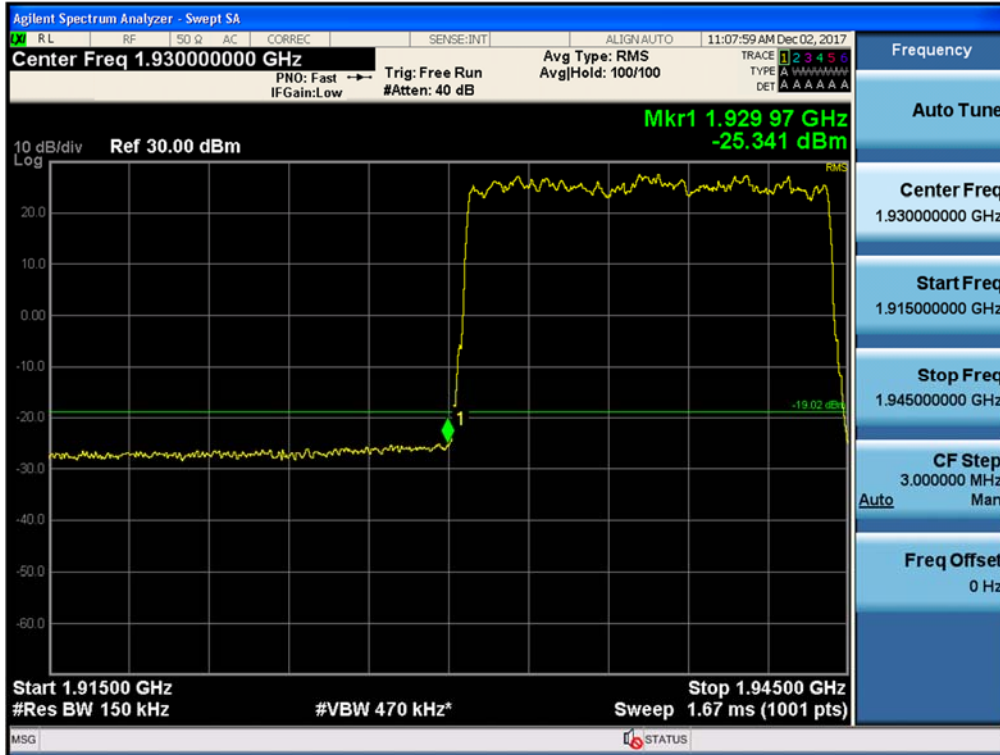


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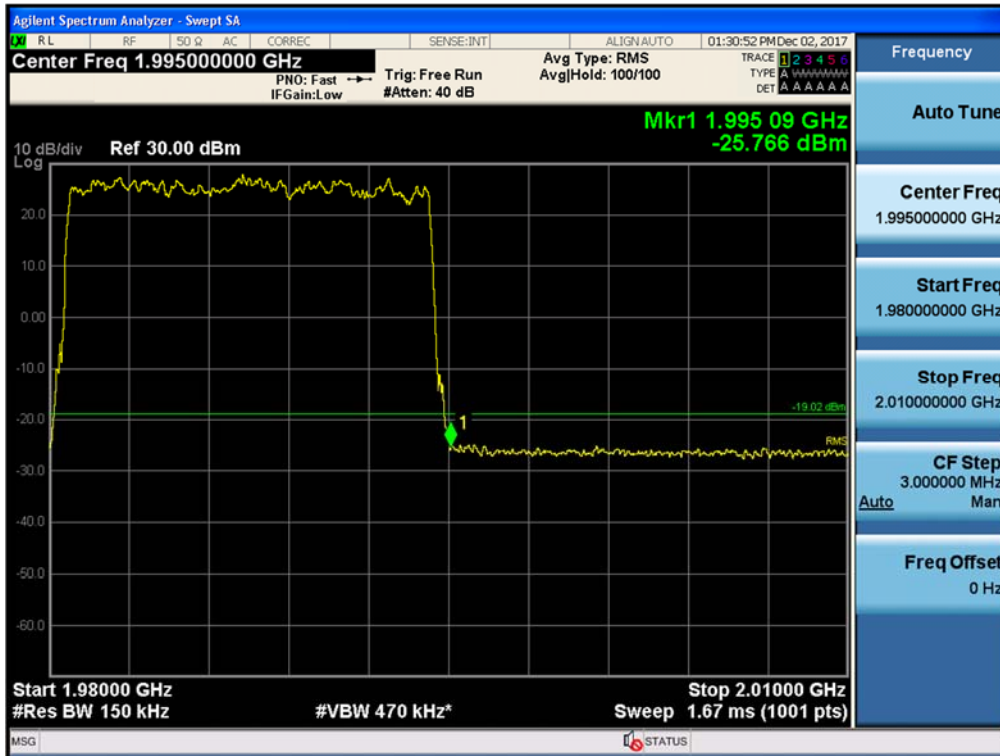


Test Data at Output Port 1_16QAM

[Downlink Low]

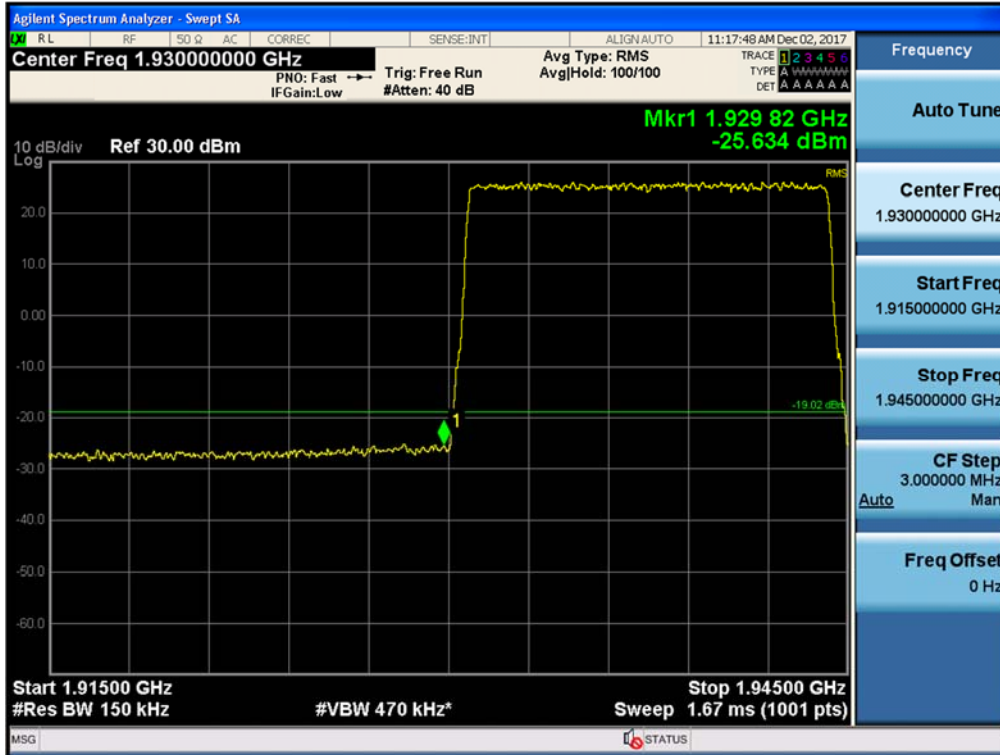


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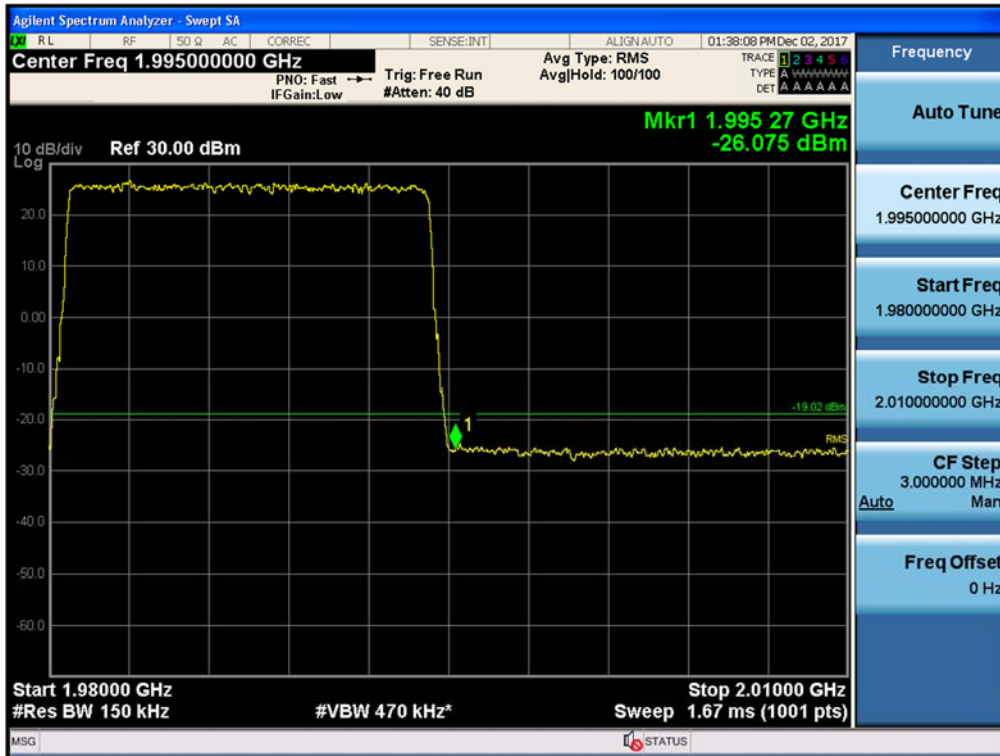


Test Data at Output Port 1_64QAM

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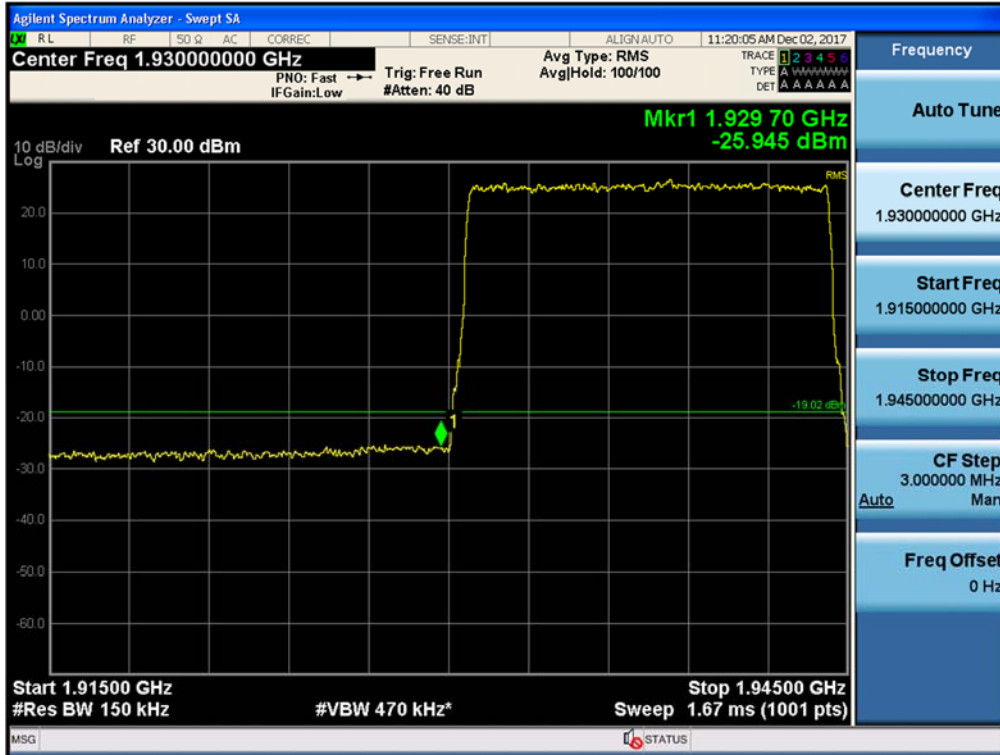


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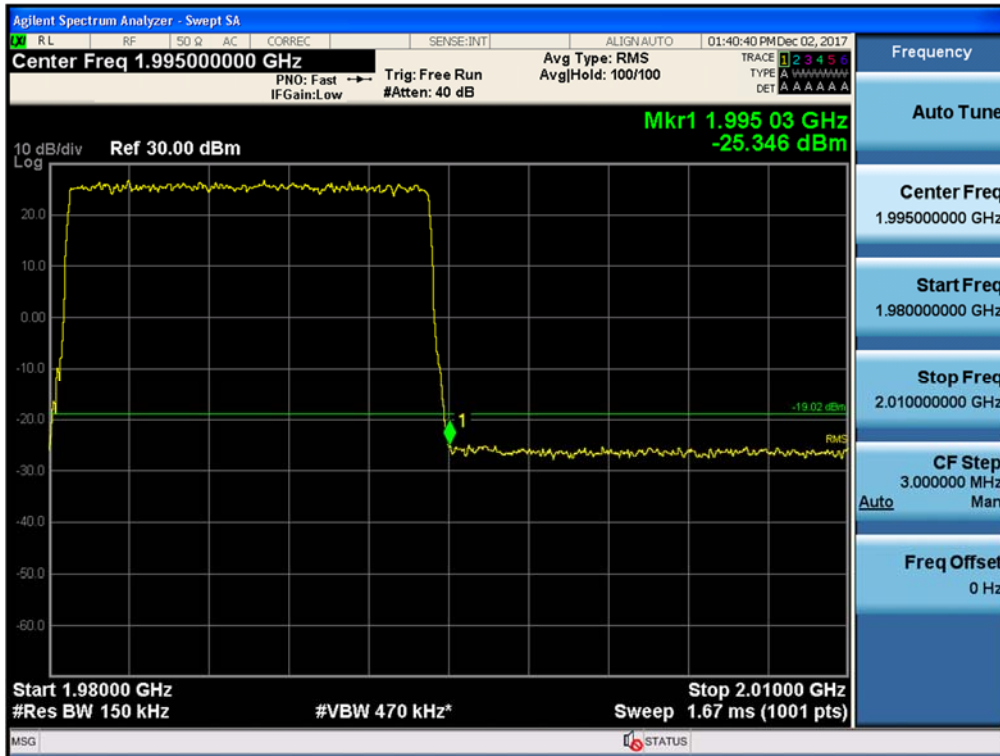


Test Data at Output Port 1_256QAM

[Downlink Low]

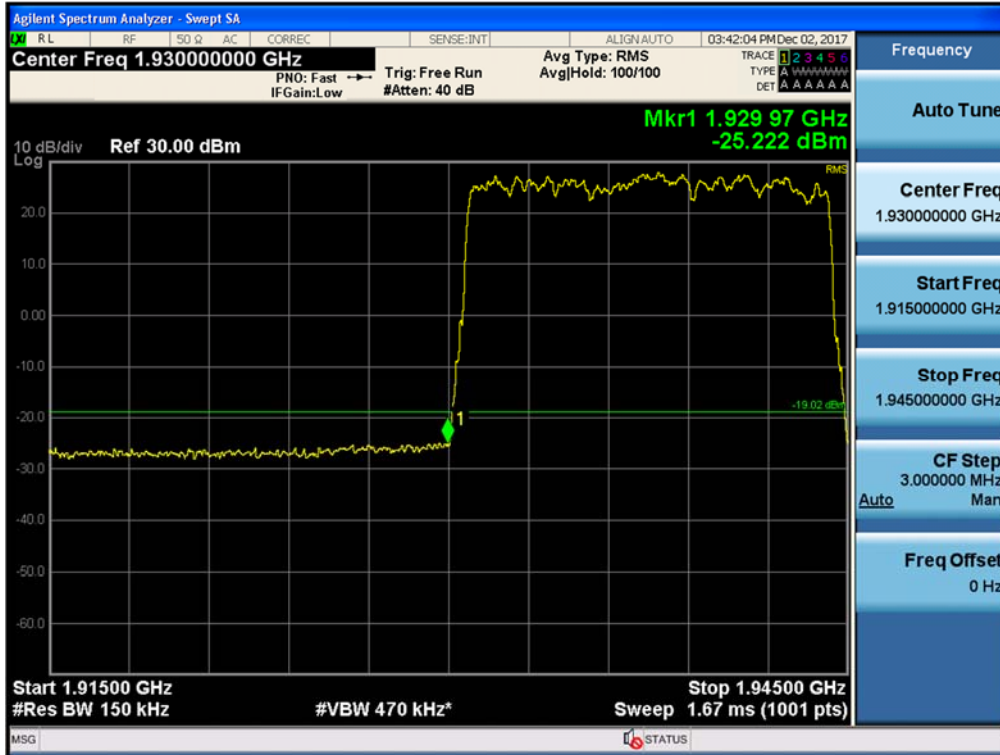


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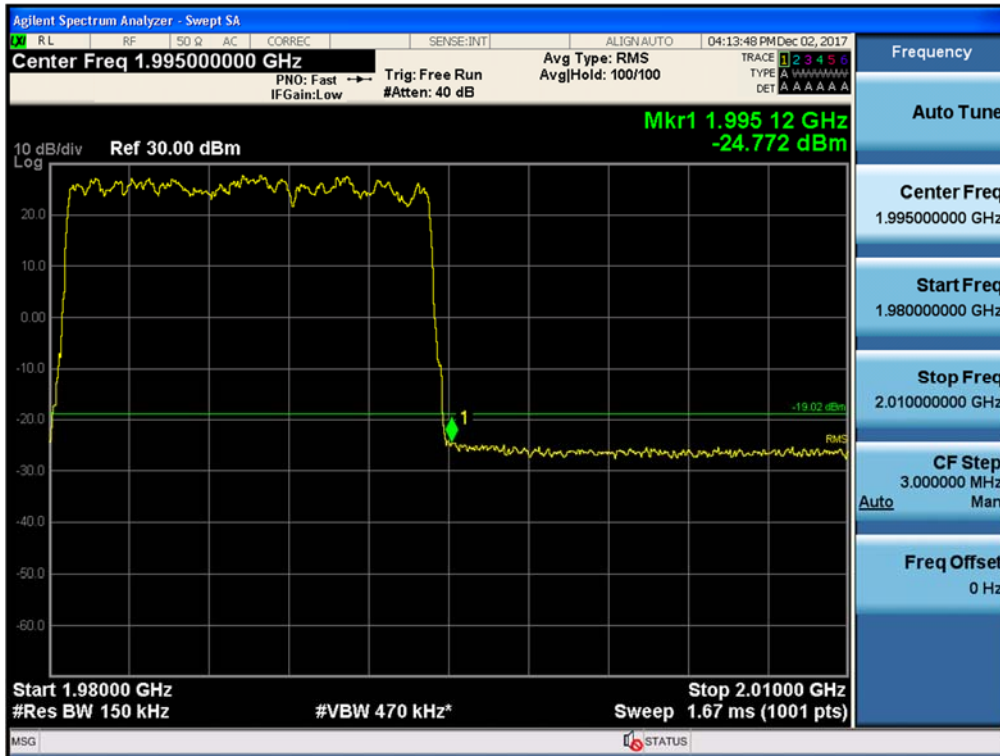


Test Data at Output Port 2_QPSK

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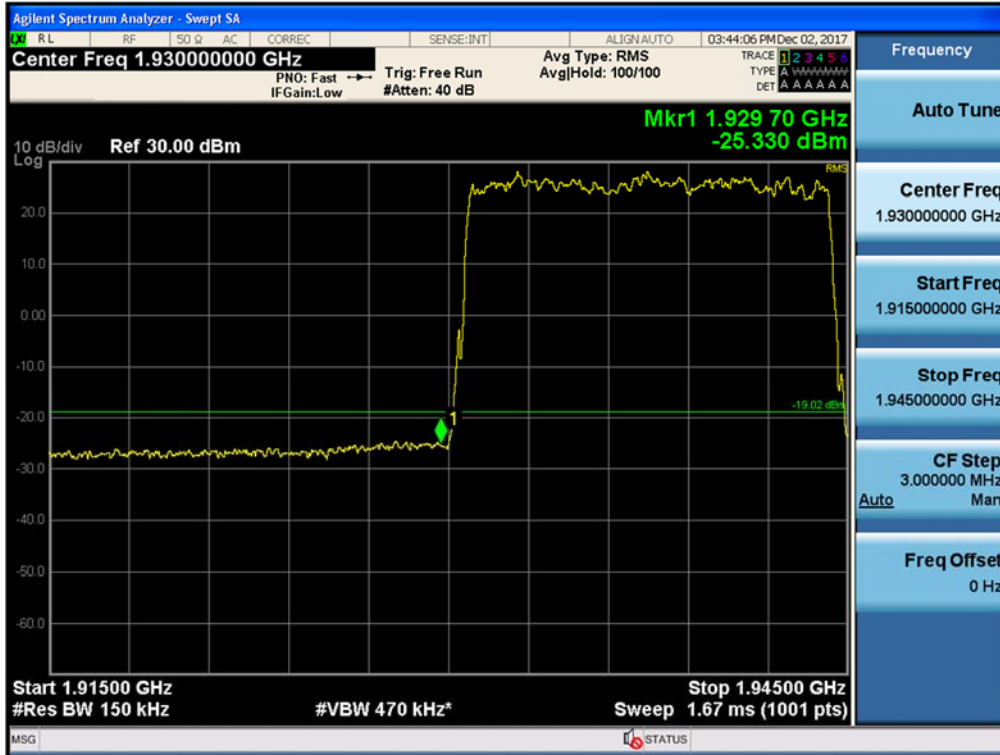


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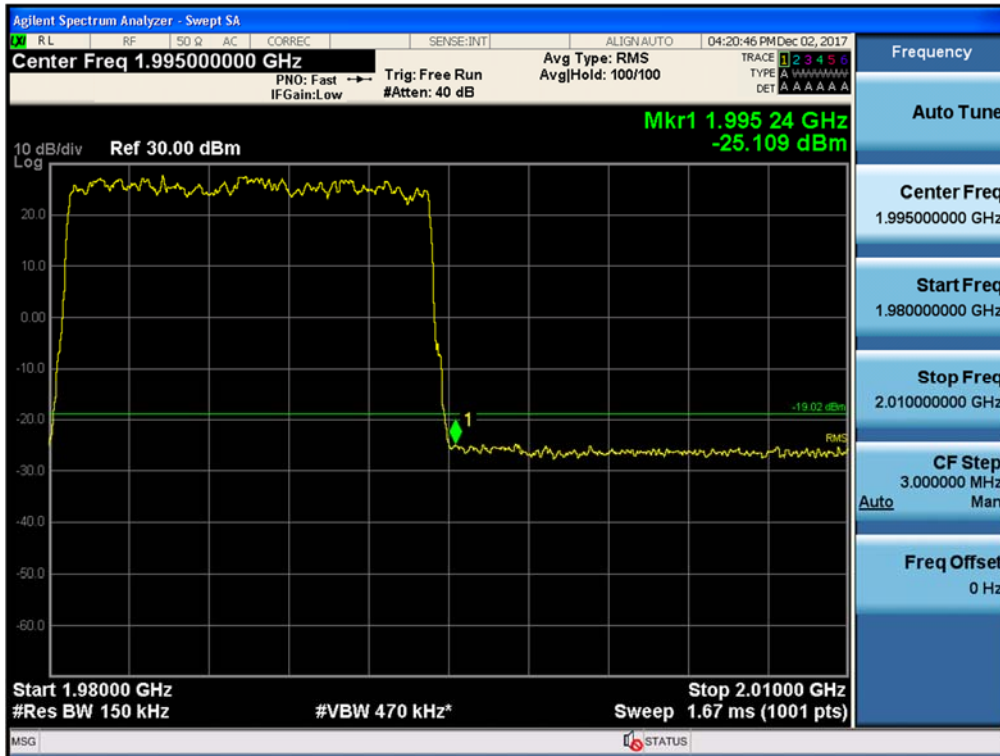


Test Data at Output Port 2_16QAM

[Downlink Low]

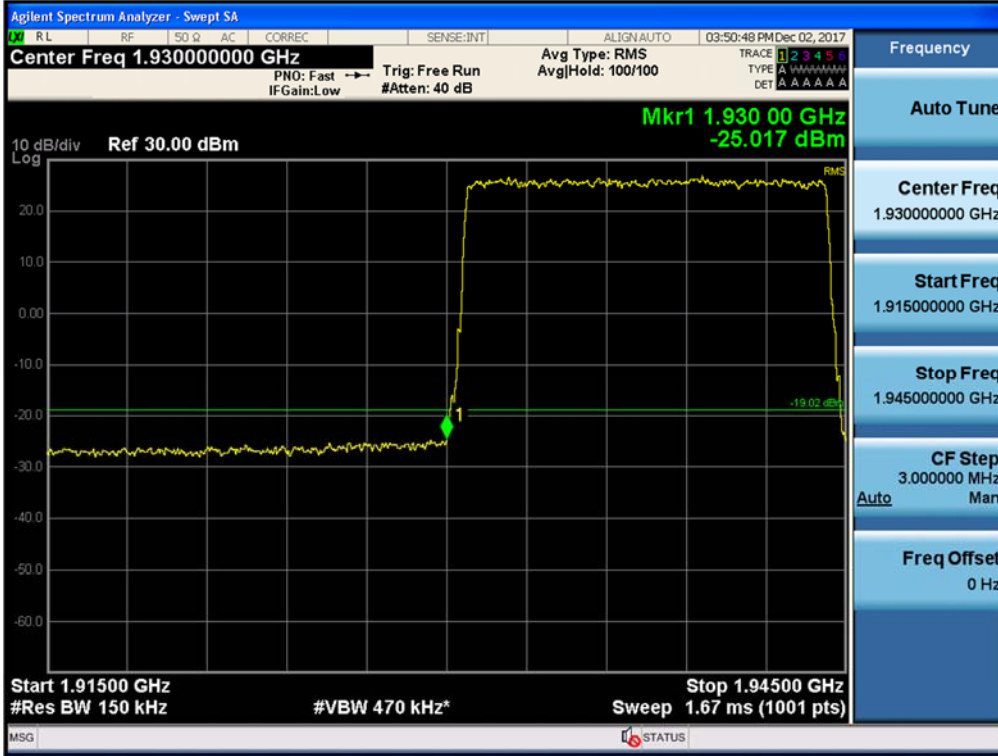


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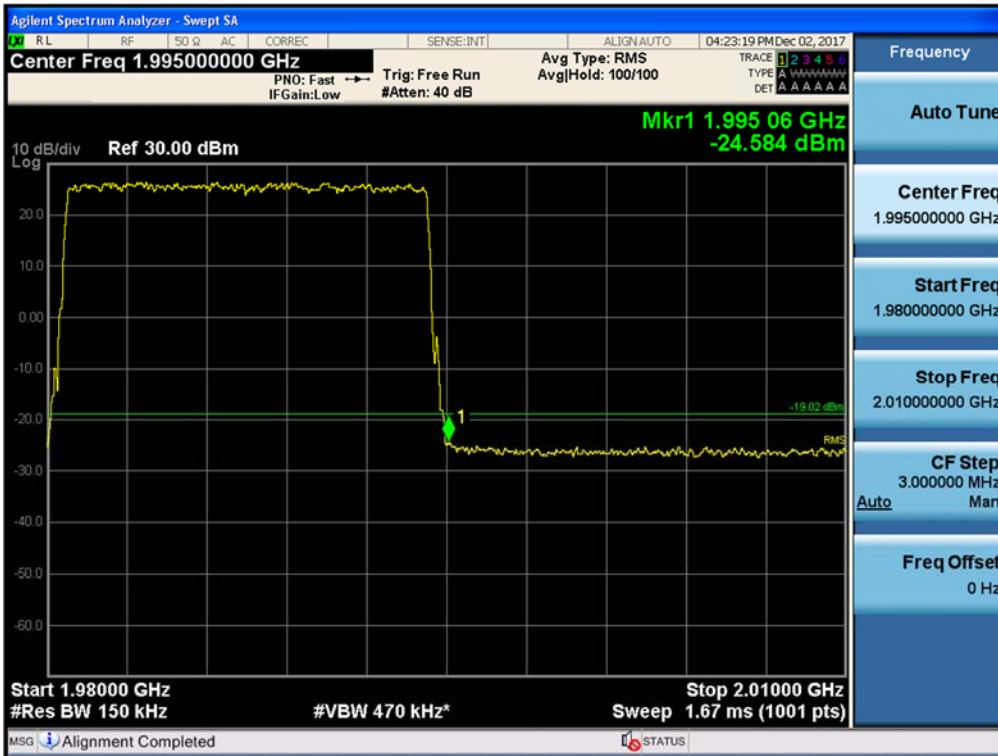


Test Data at Output Port 2_64QAM

[Downlink Low]

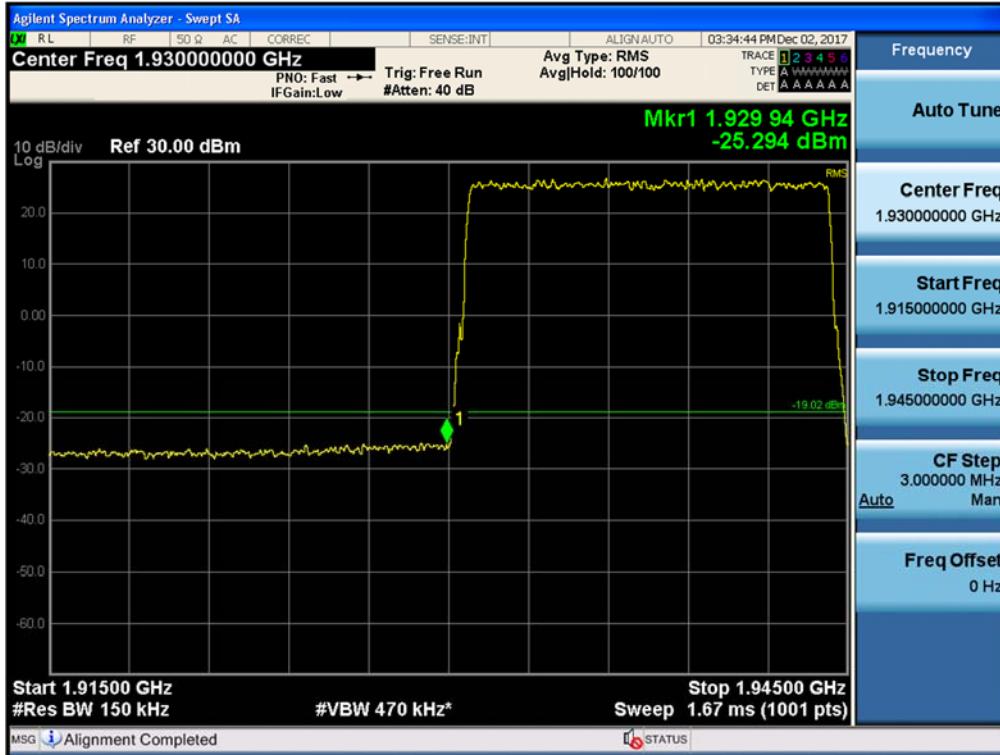


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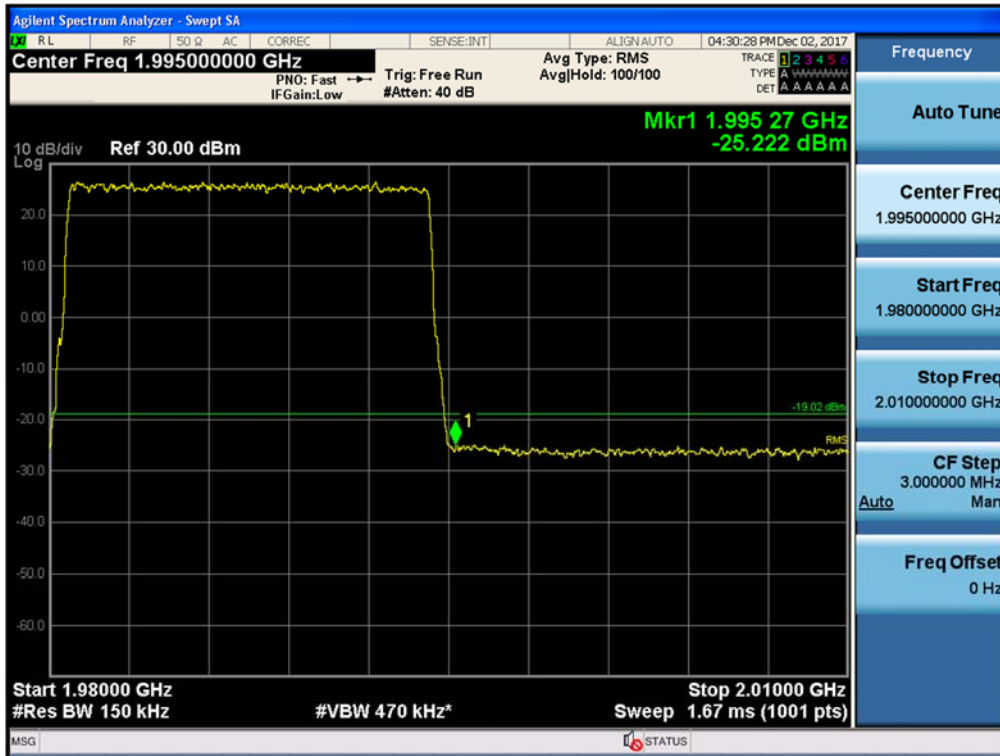


Test Data at Output Port 2_256QAM

[Downlink Low]

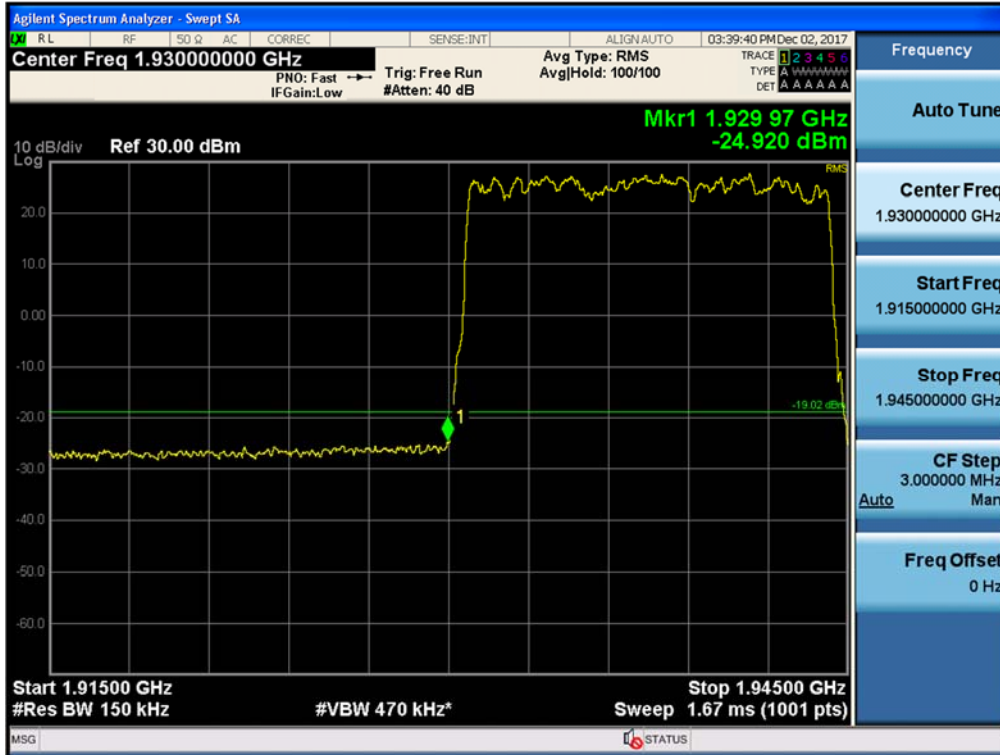


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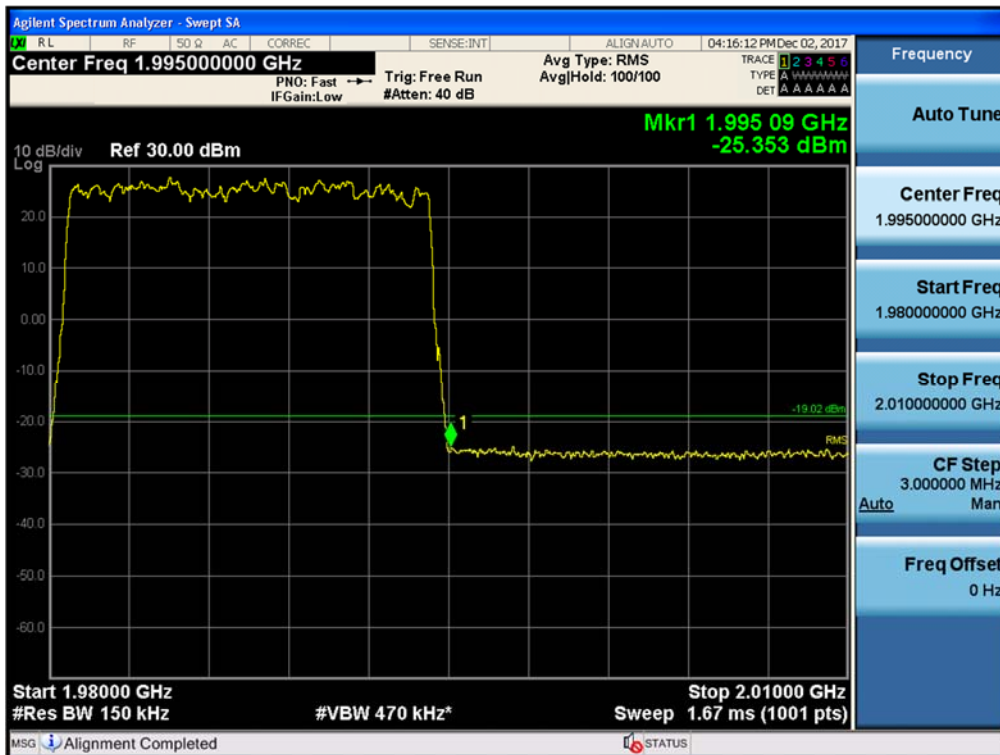


Test Data at Output Port 3_QPSK

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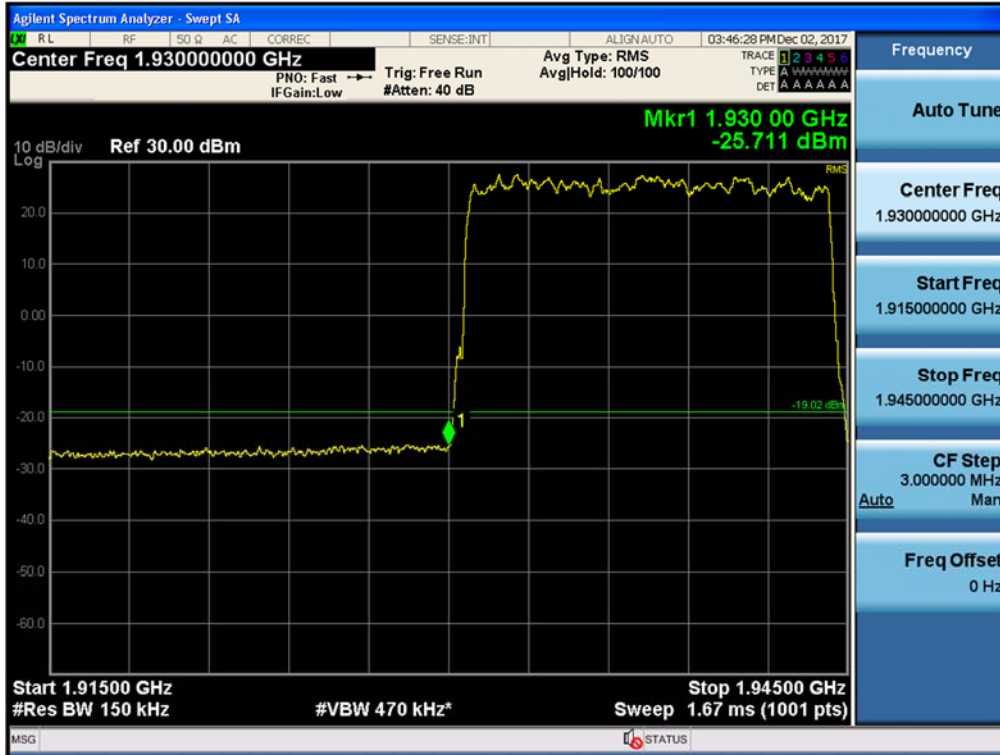


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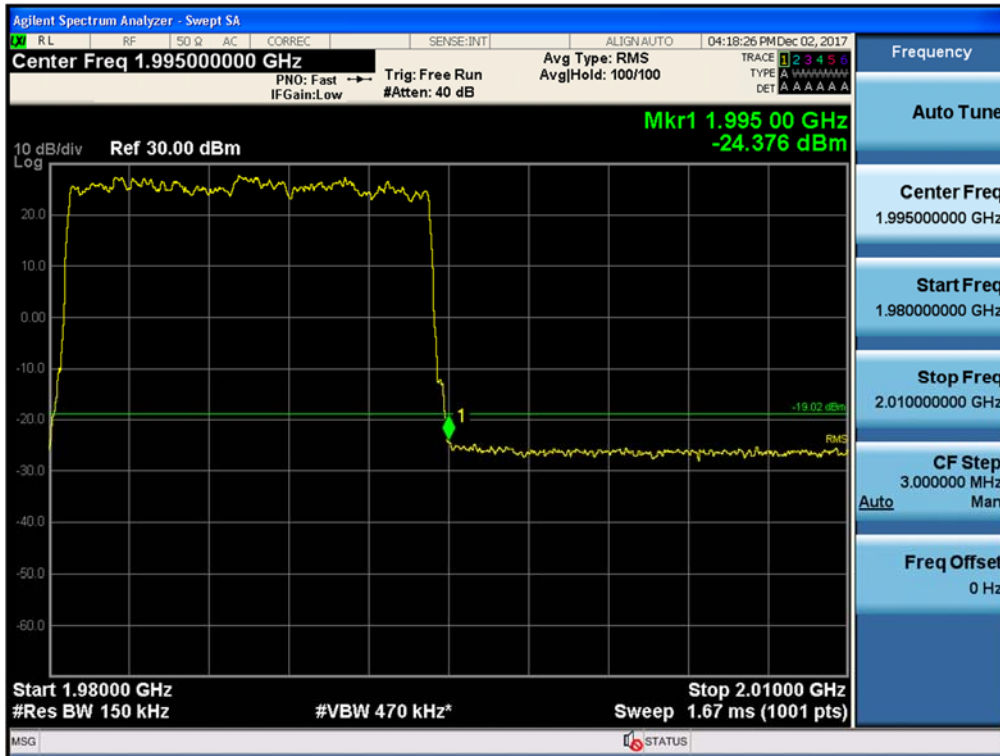


Test Data at Output Port 3_16QAM

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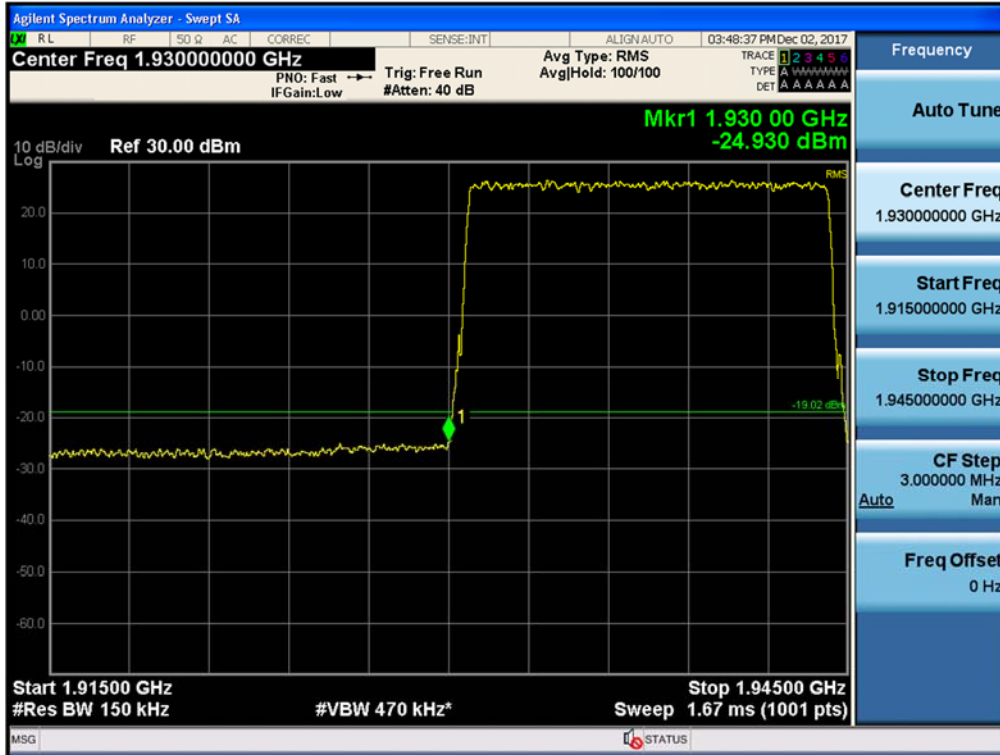


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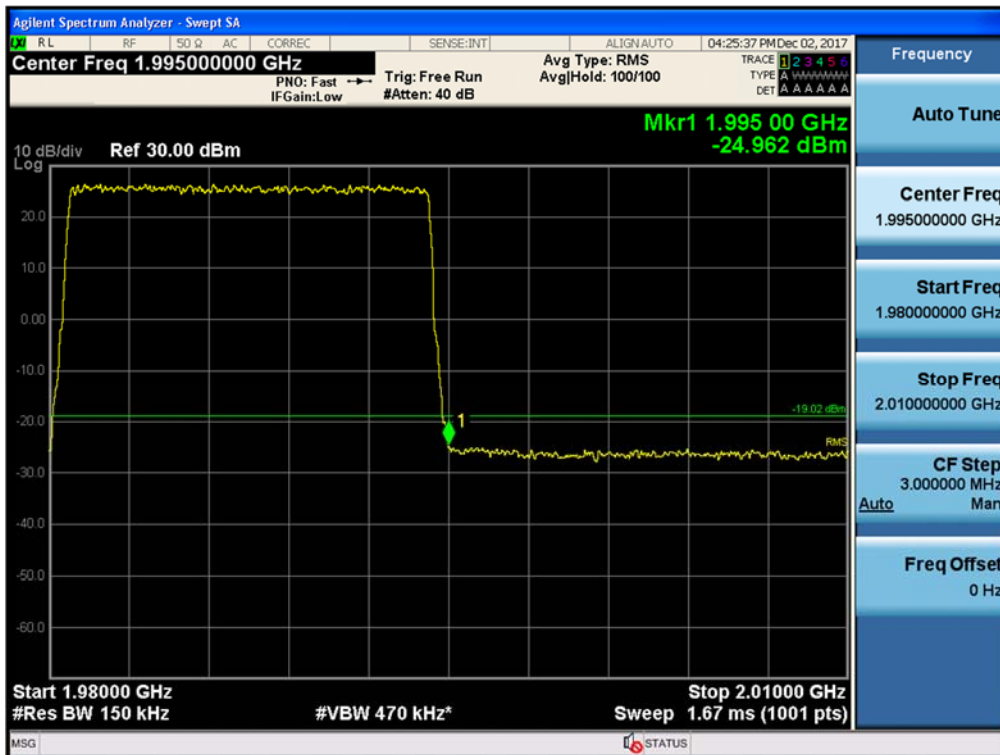


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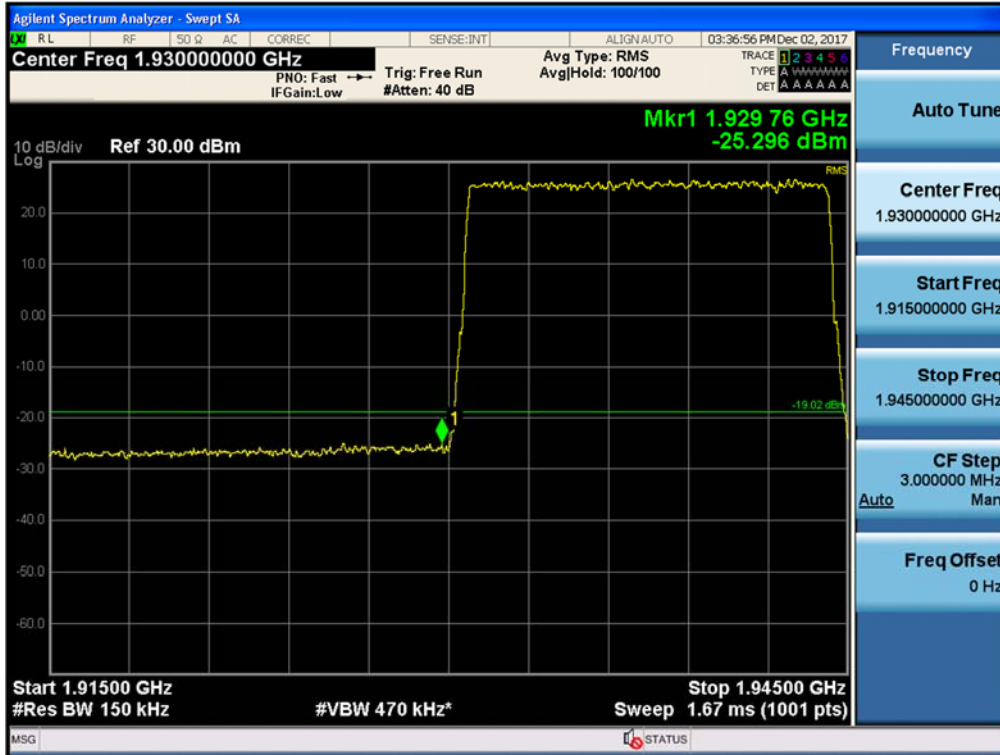


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Test Data at Output Port 3_256QAM

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