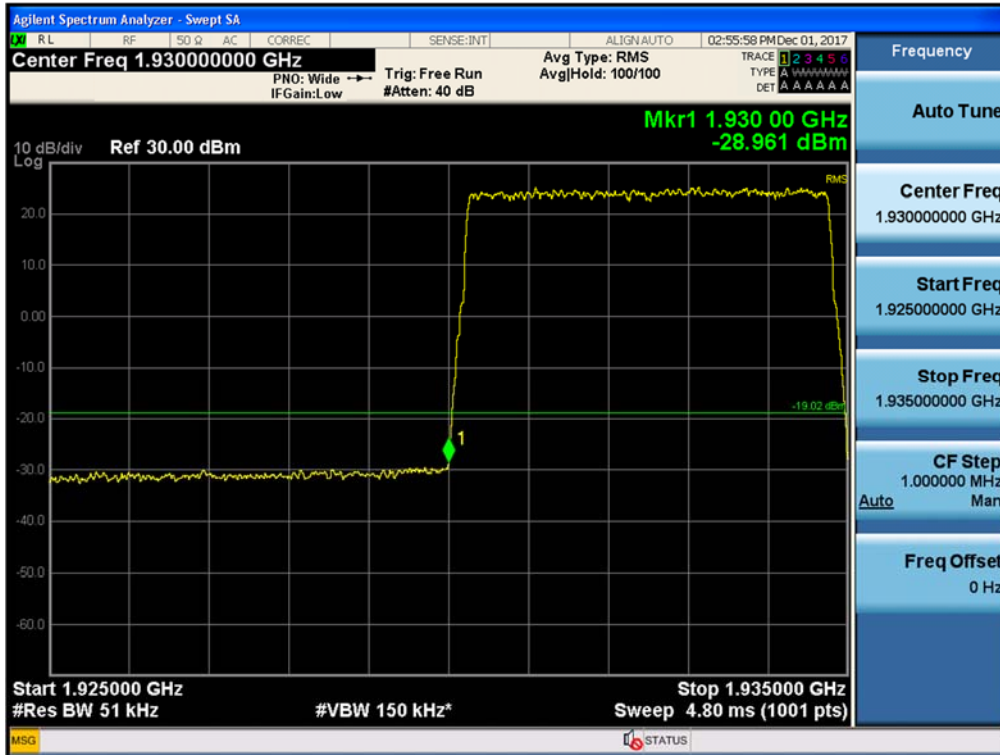


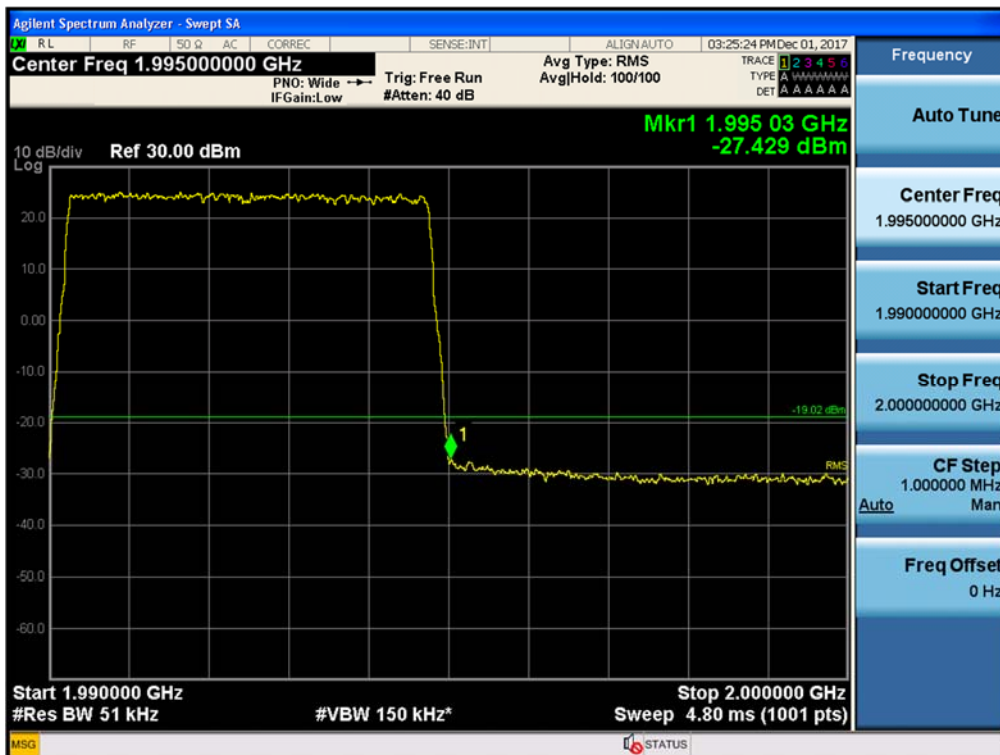
5 MHz / 1 Carrier (20 W)

Test Data at Output Port 0\_256QAM

[Downlink Low]

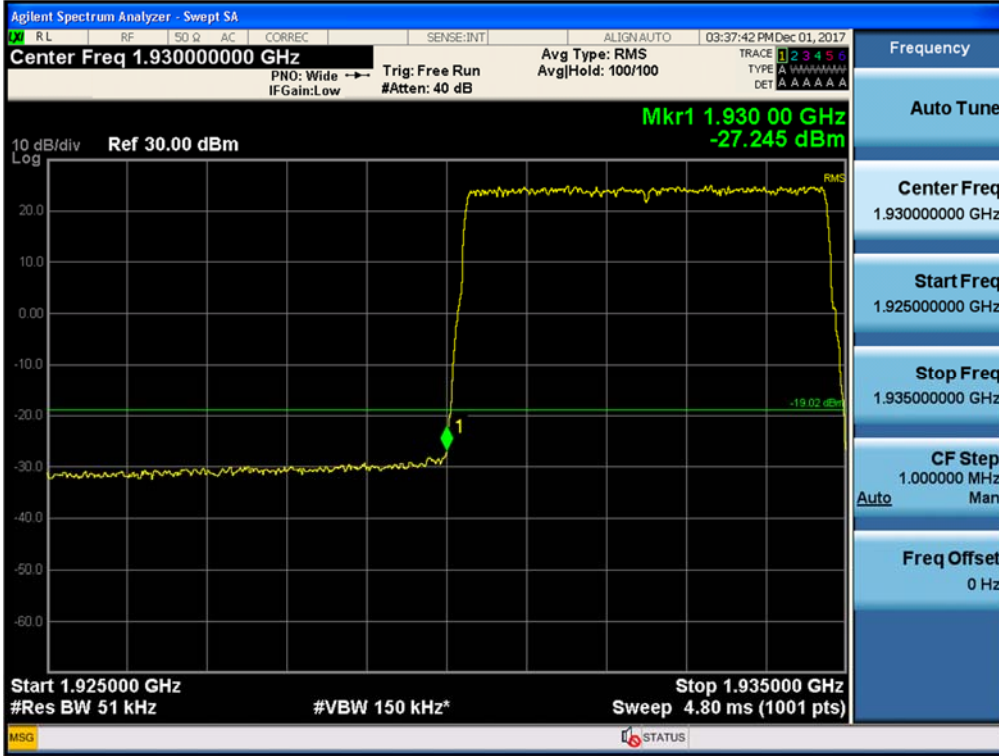


[Downlink High]

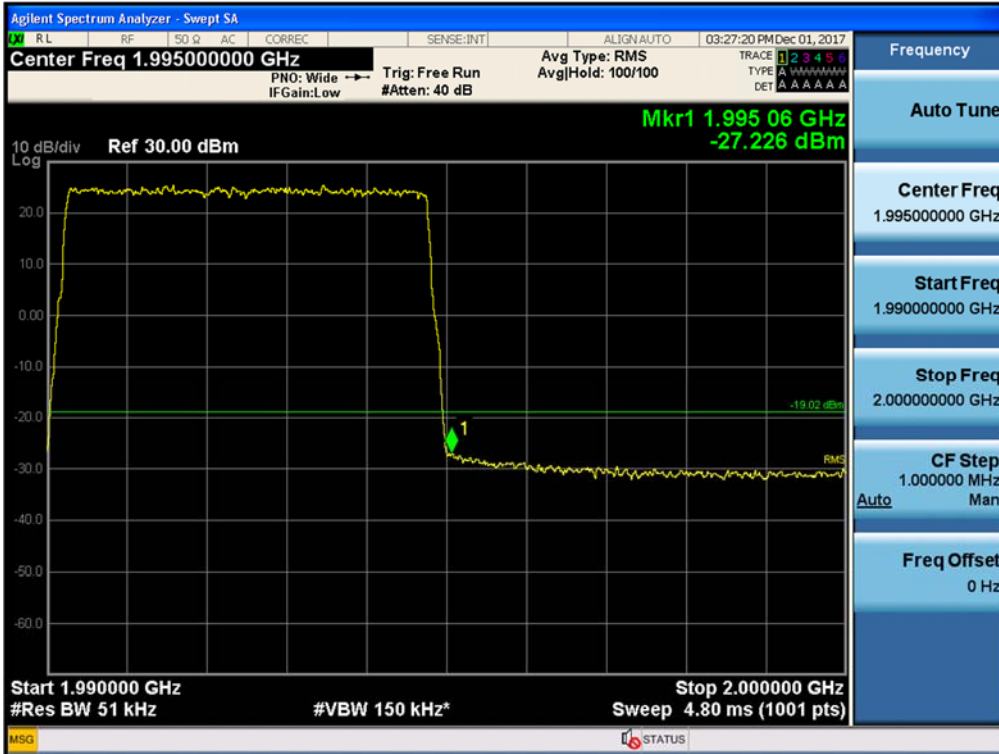


Test Data at Output Port 1\_256QAM

[Downlink Low]

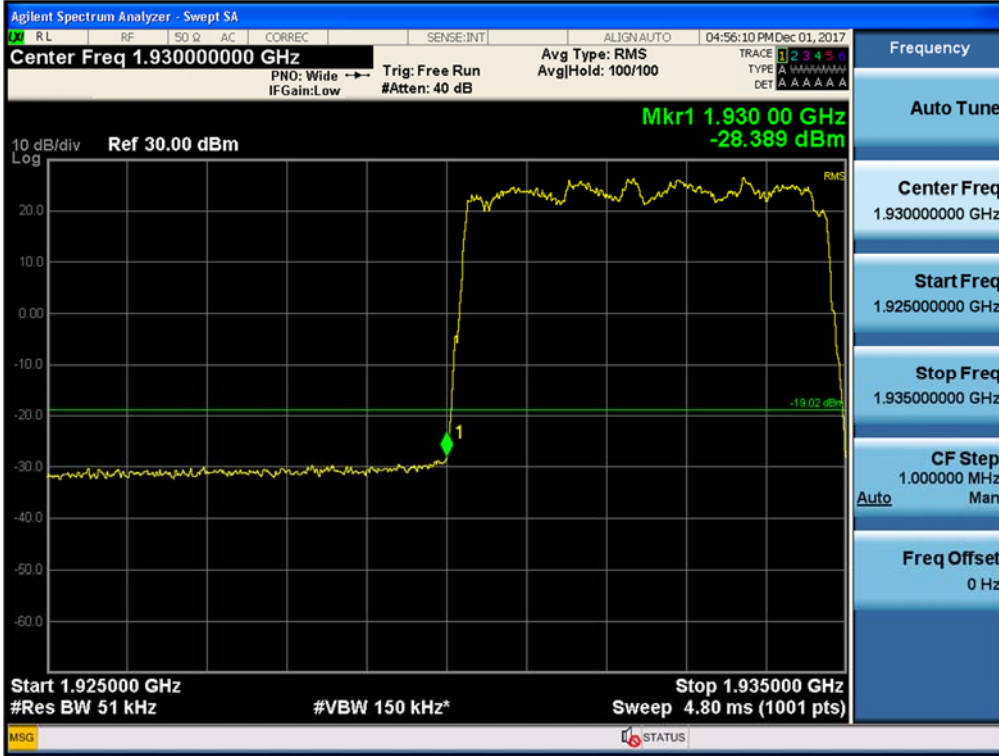


[Downlink High]

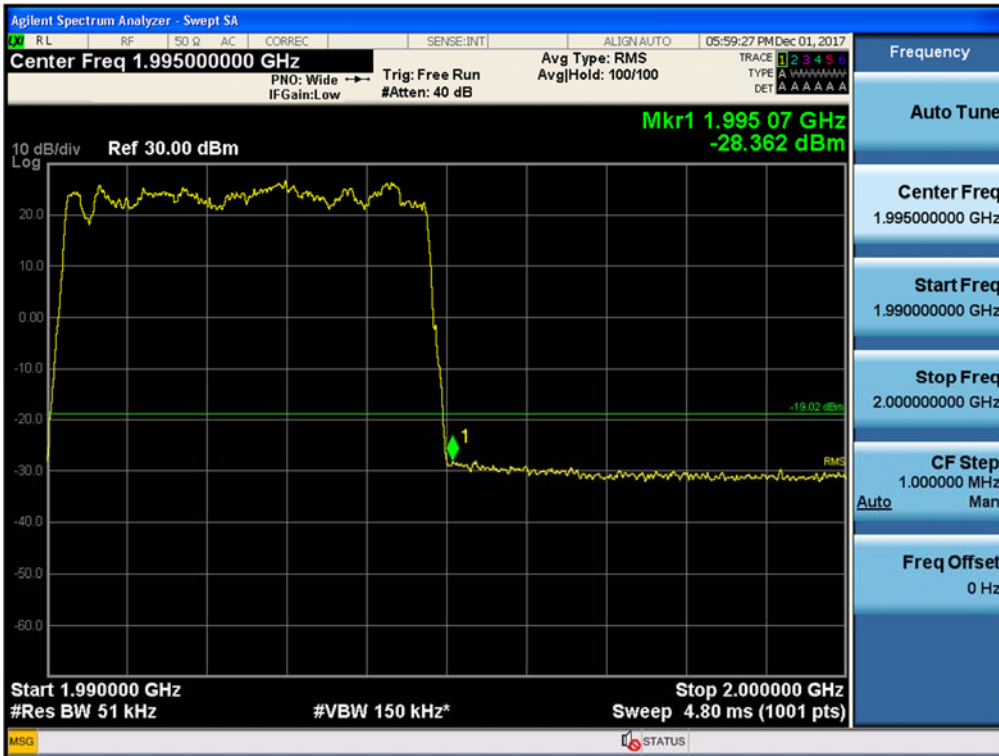


Test Data at Output Port 2\_QPSK

[Downlink Low]

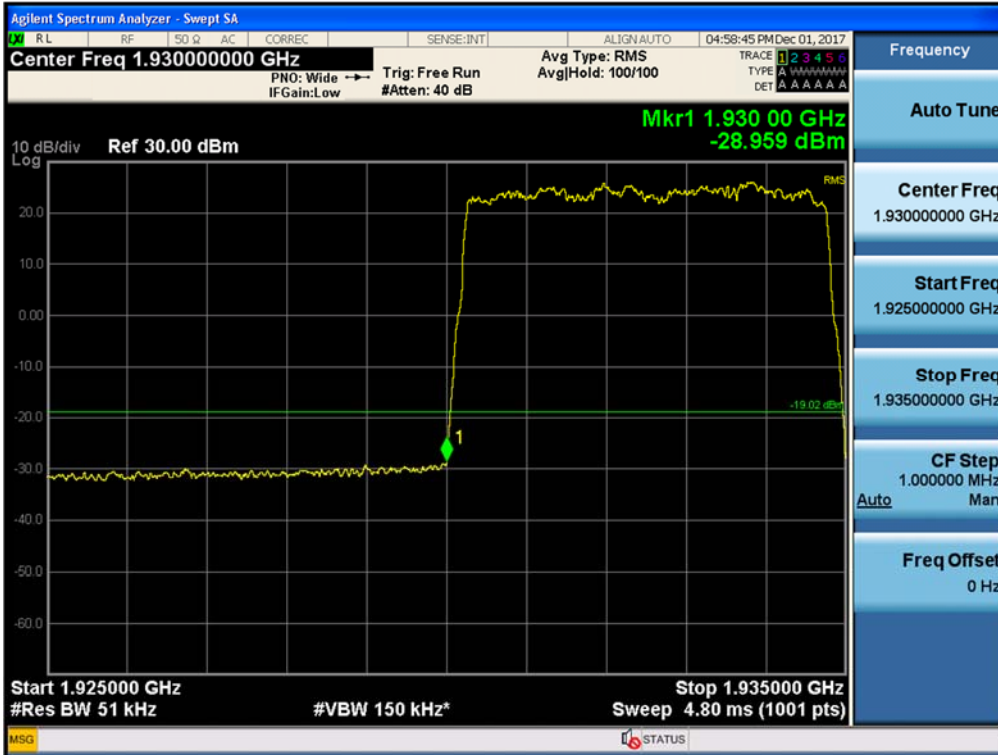


[Downlink High]

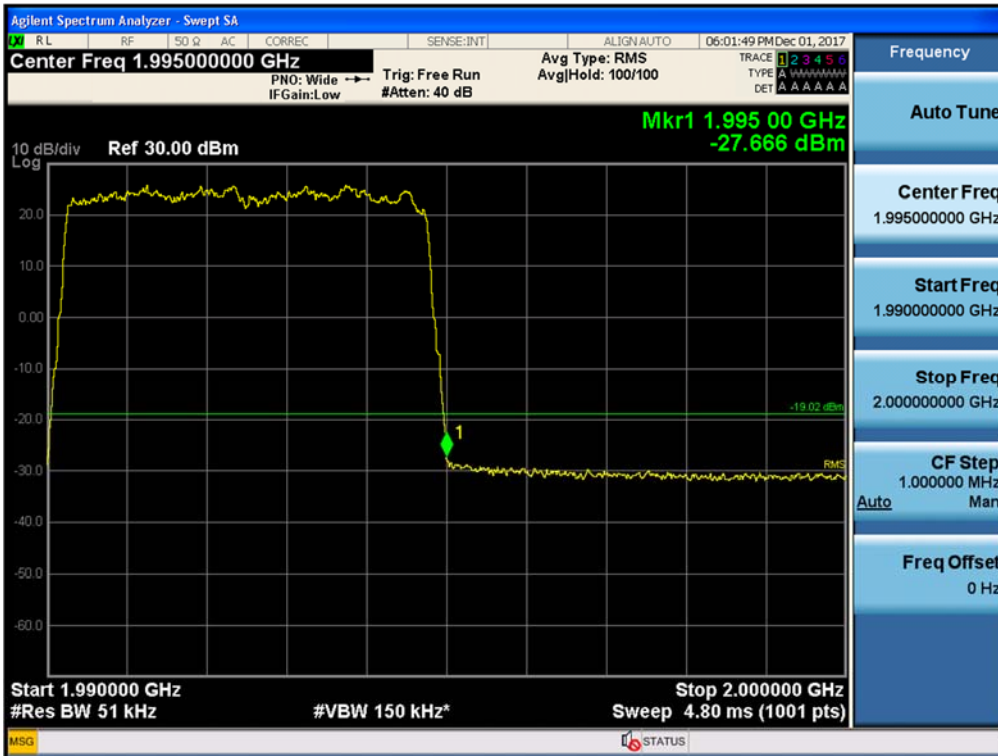


Test Data at Output Port 2\_16QAM

[Downlink Low]

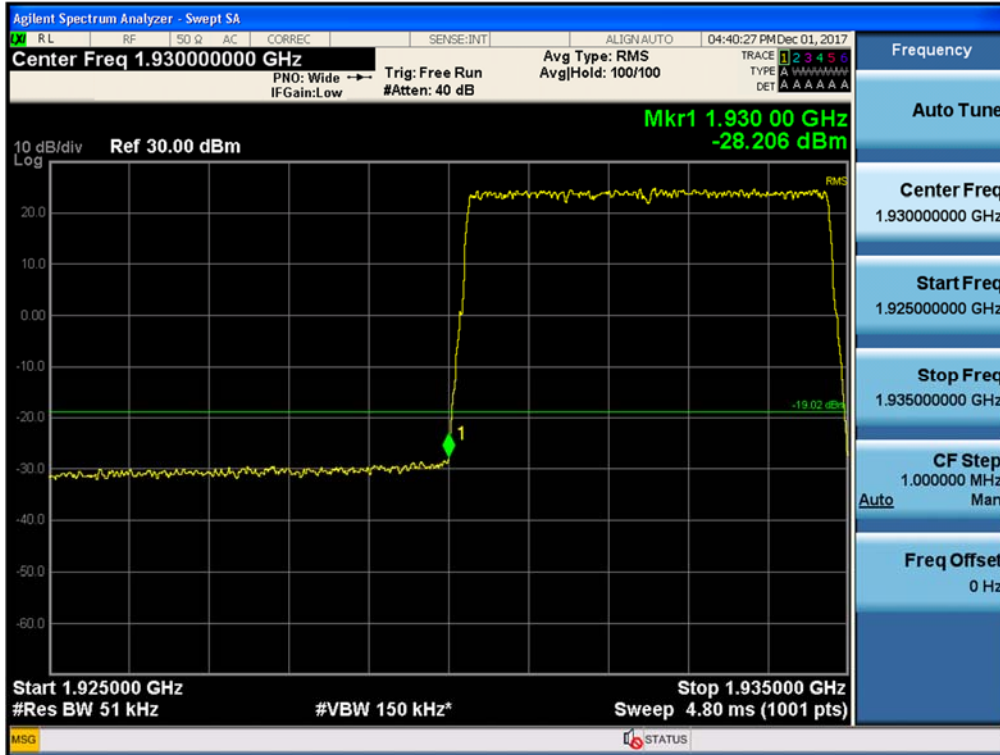


[Downlink High]

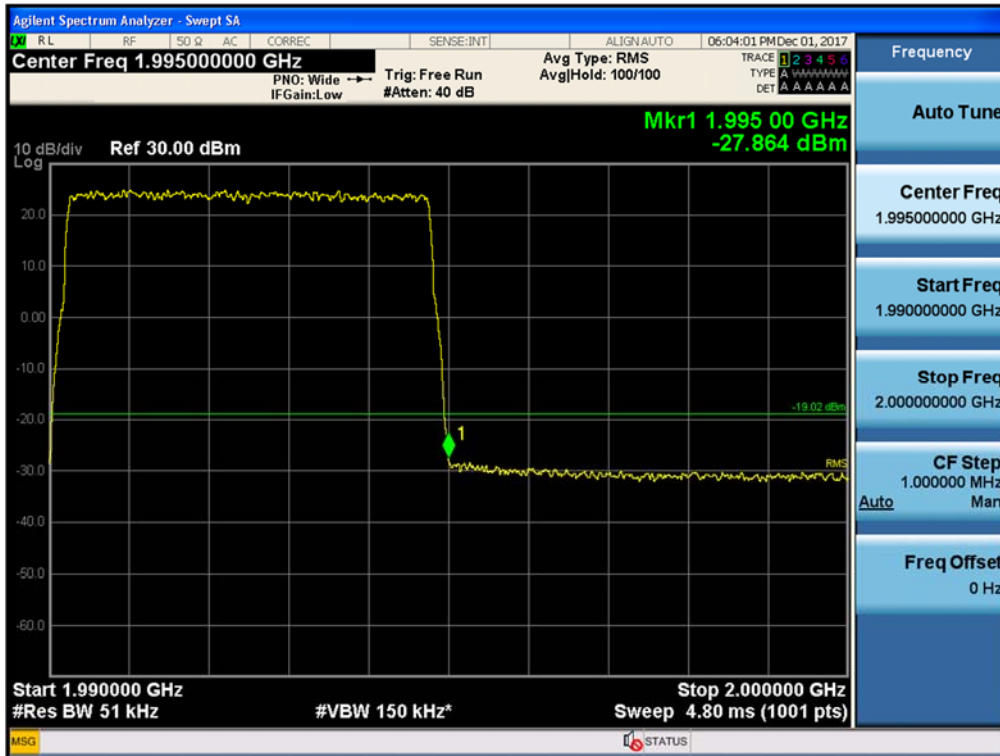


Test Data at Output Port 2\_64QAM

[Downlink Low]

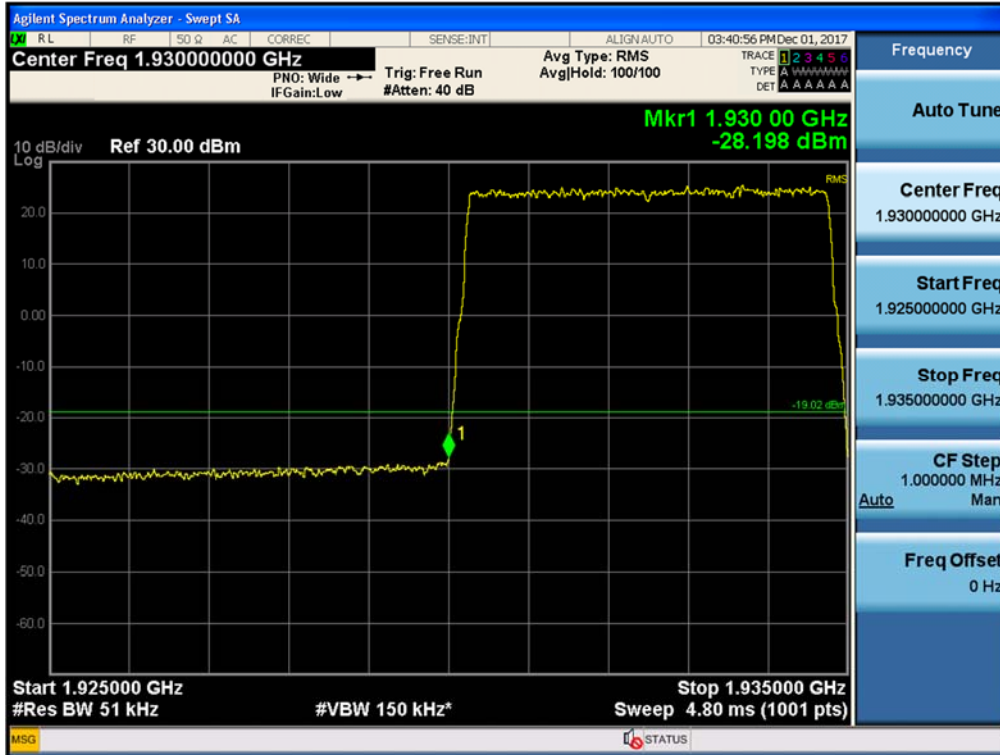


[Downlink High]

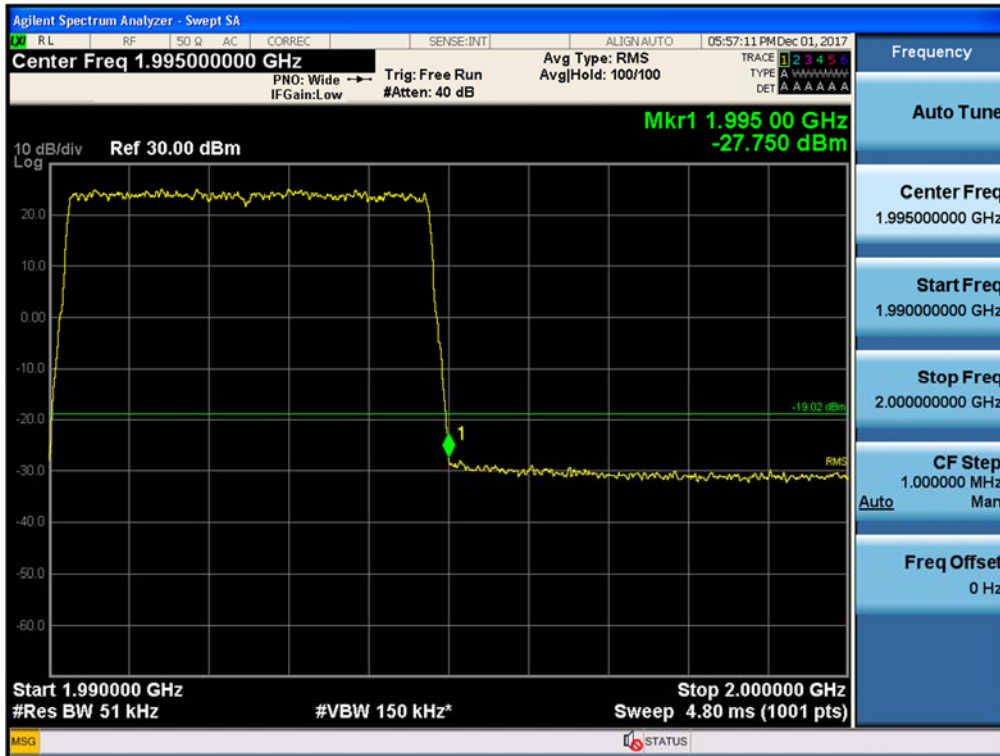


Test Data at Output Port 2\_256QAM

[Downlink Low]

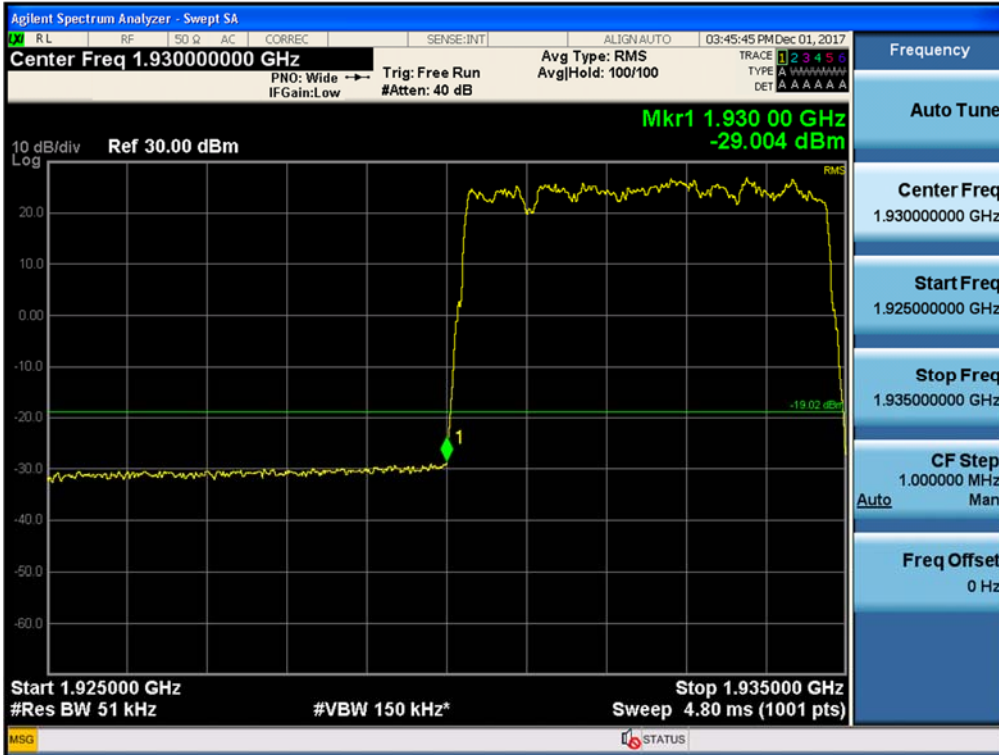


[Downlink High]

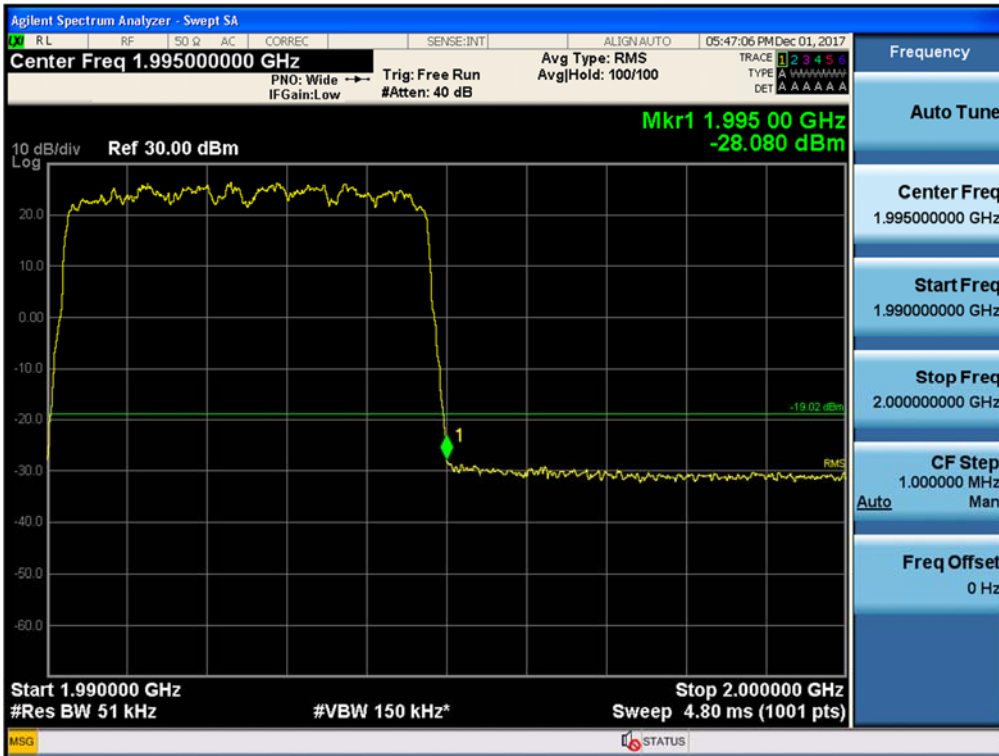


Test Data at Output Port 3\_QPSK

[Downlink Low]

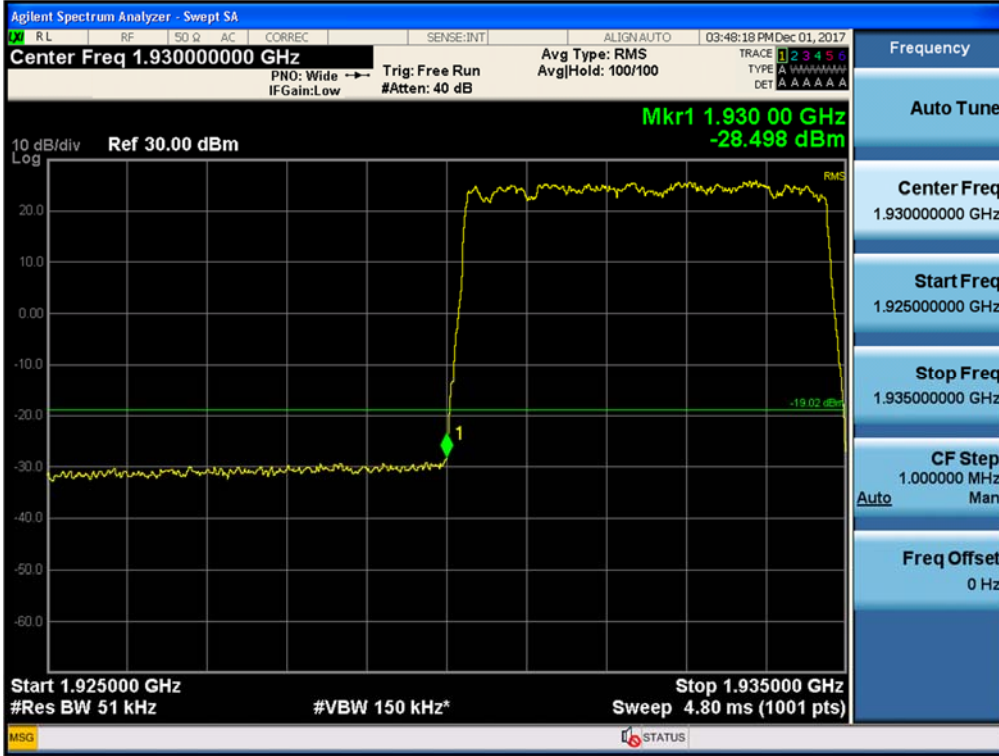


[Downlink High]

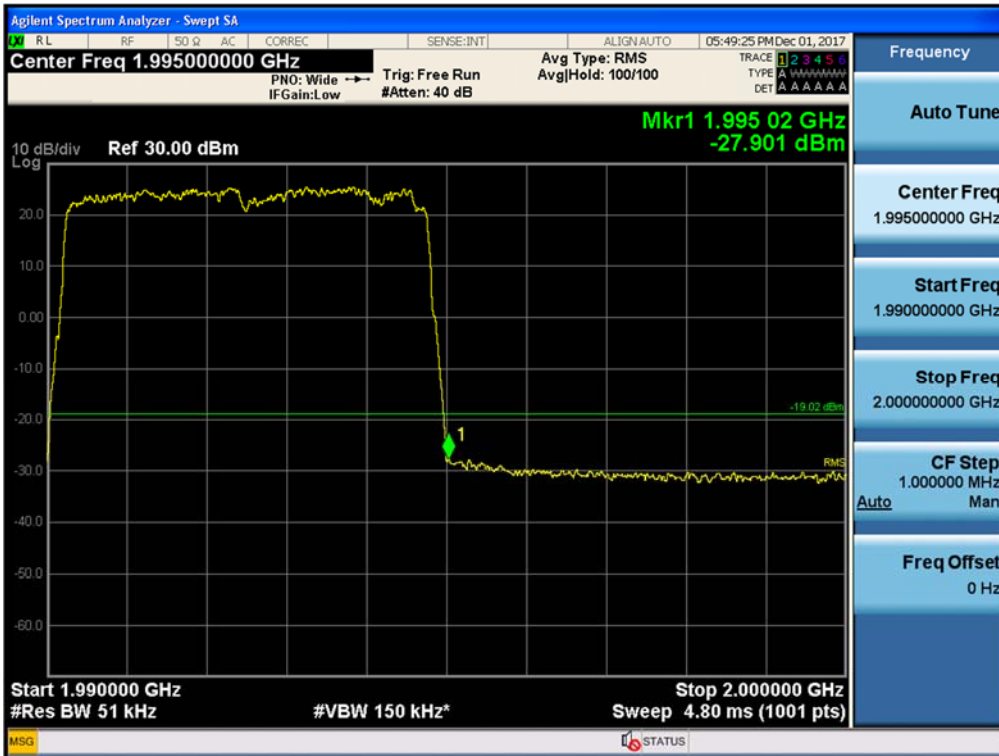


Test Data at Output Port 3\_16QAM

[Downlink Low]

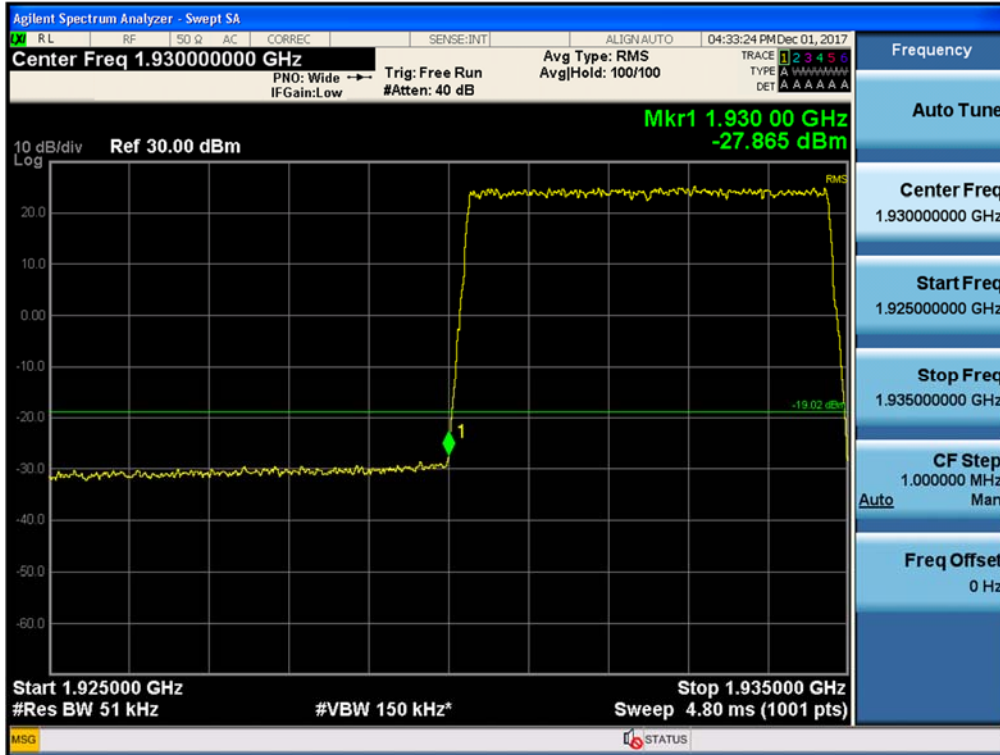


[Downlink High]

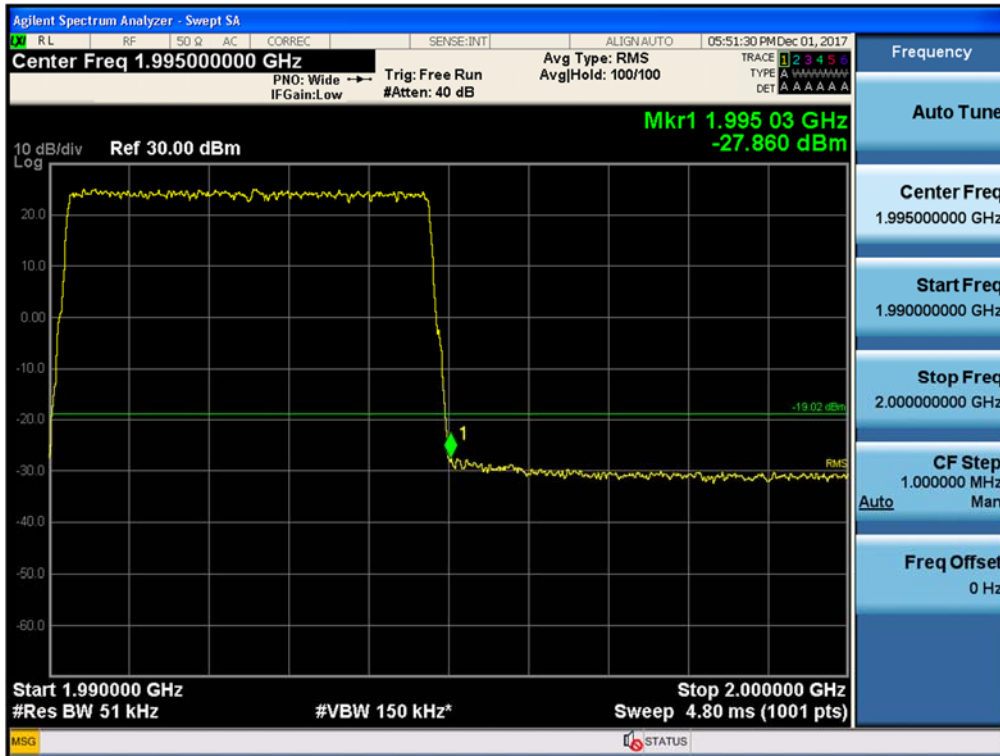


Test Data at Output Port 3\_64QAM

[Downlink Low]

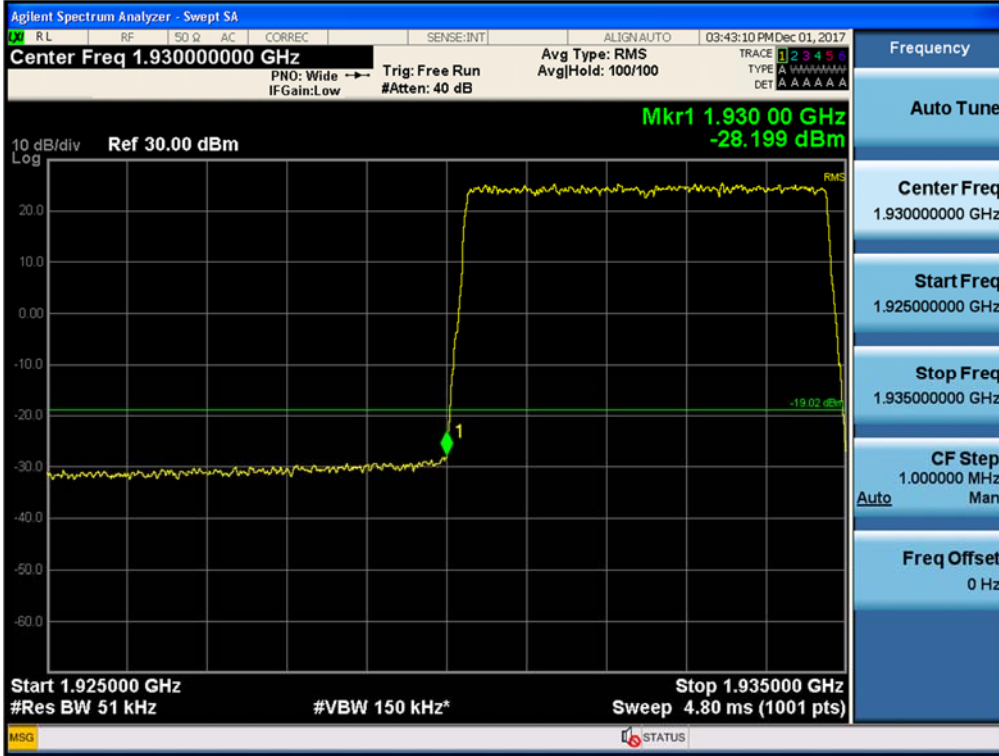


[Downlink High]

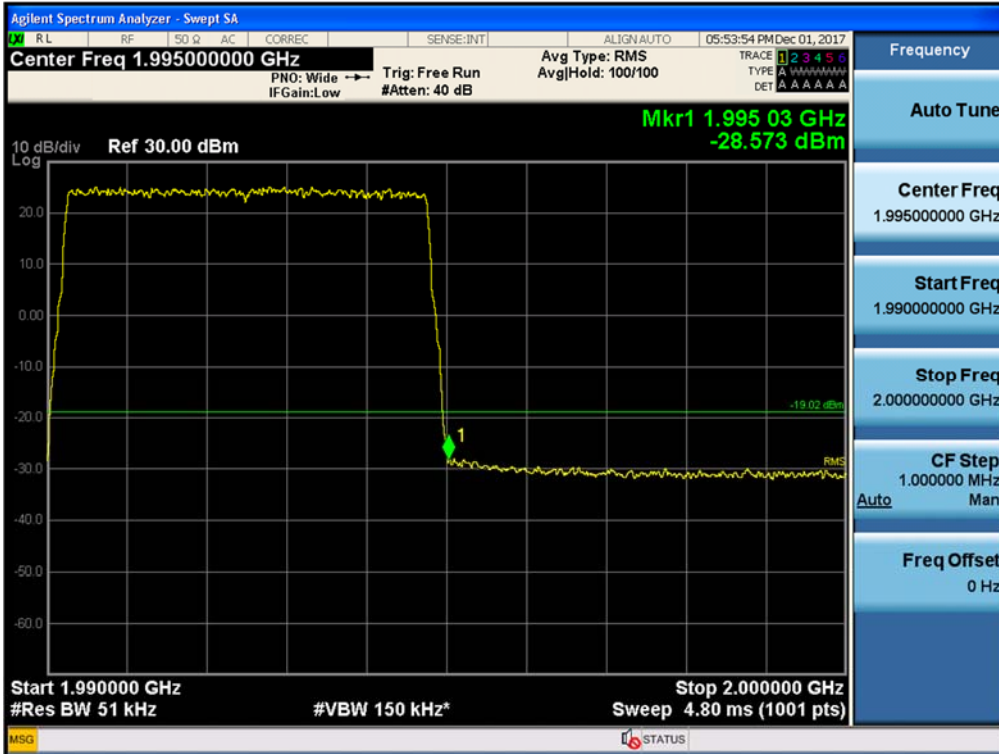


Test Data at Output Port 3\_256QAM

[Downlink Low]



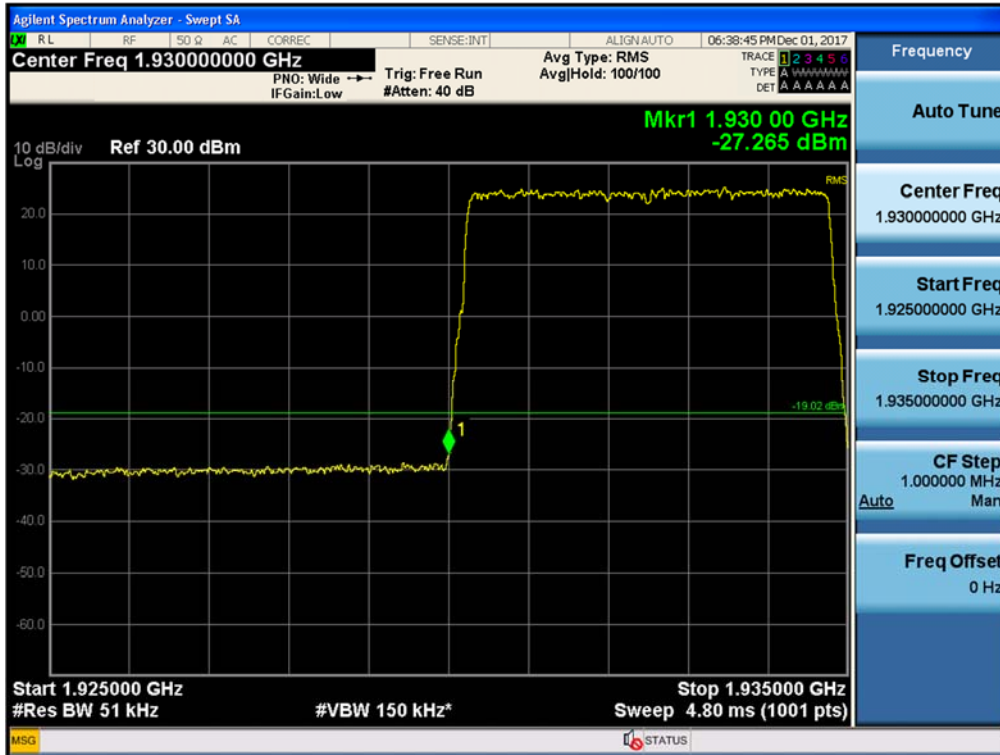
[Downlink High]



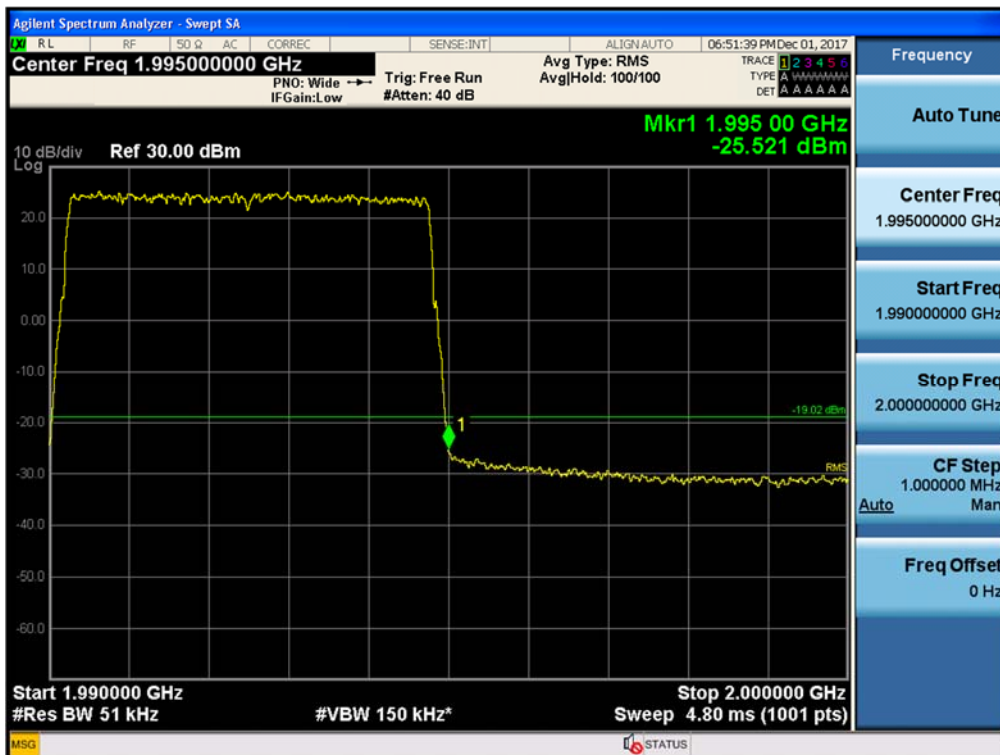
5 MHz + 5 MHz / 2 Carriers (20 W + 20 W)

Test Data at Output Port 0\_256QAM

[Downlink Low]

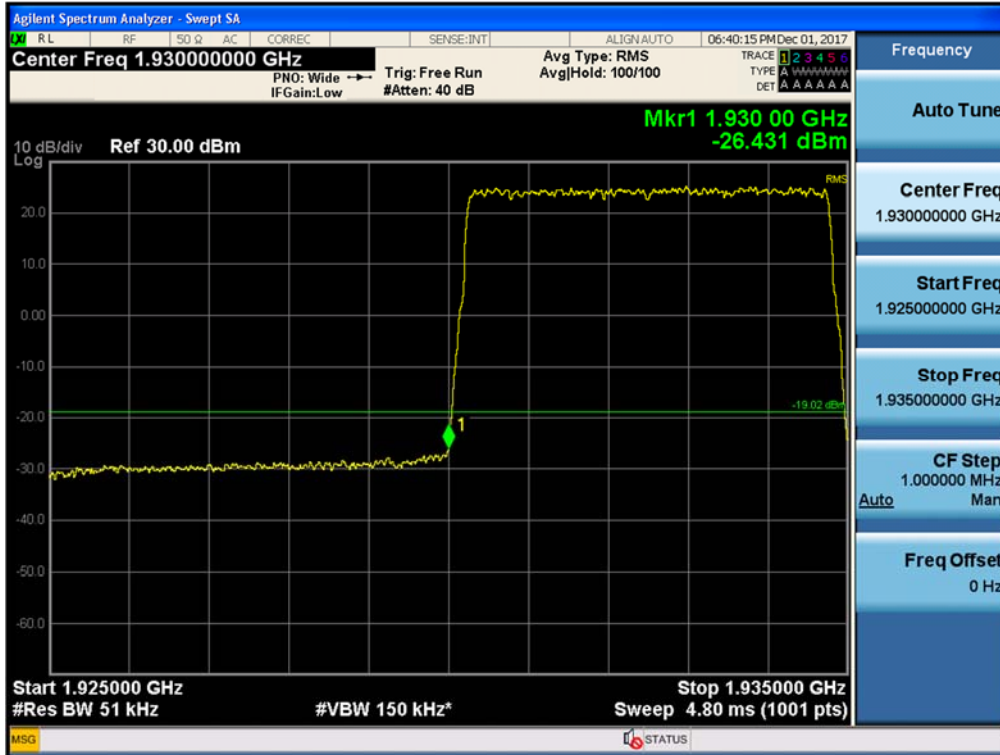


[Downlink High]

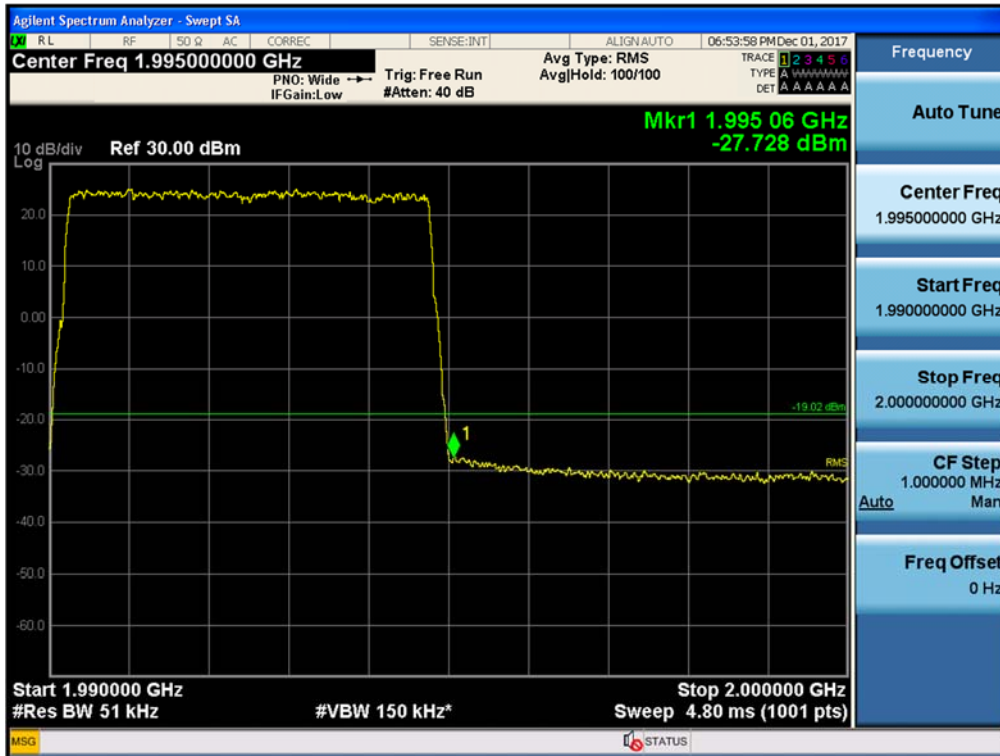


Test Data at Output Port 1\_256QAM

[Downlink Low]



[Downlink High]

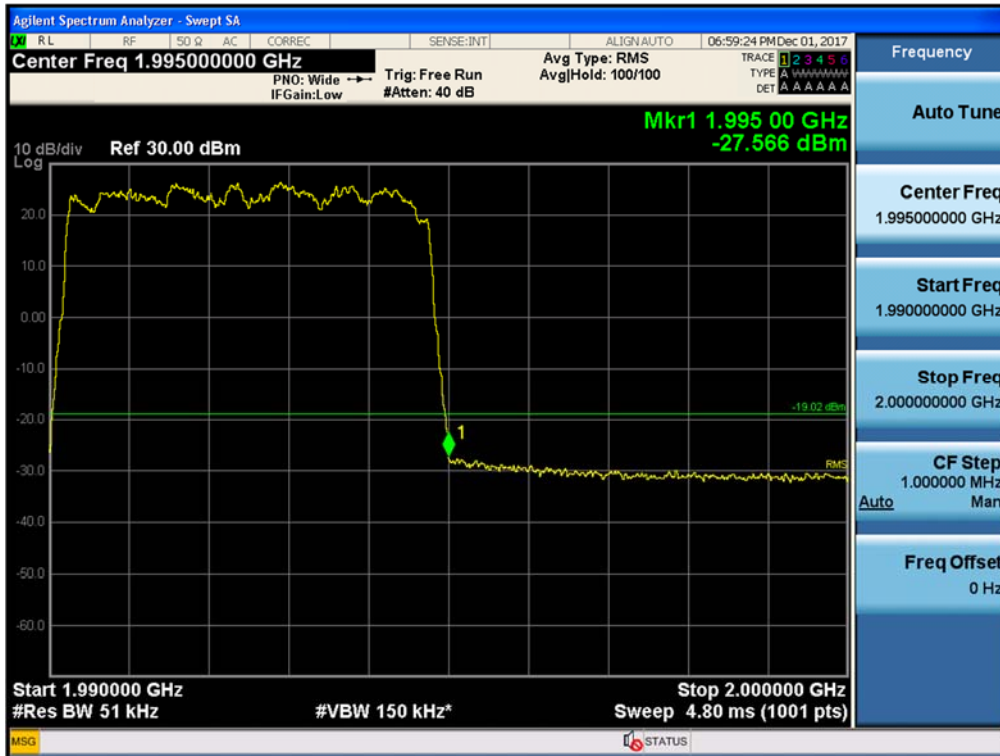


Test Data at Output Port 2\_QPSK

[Downlink Low]

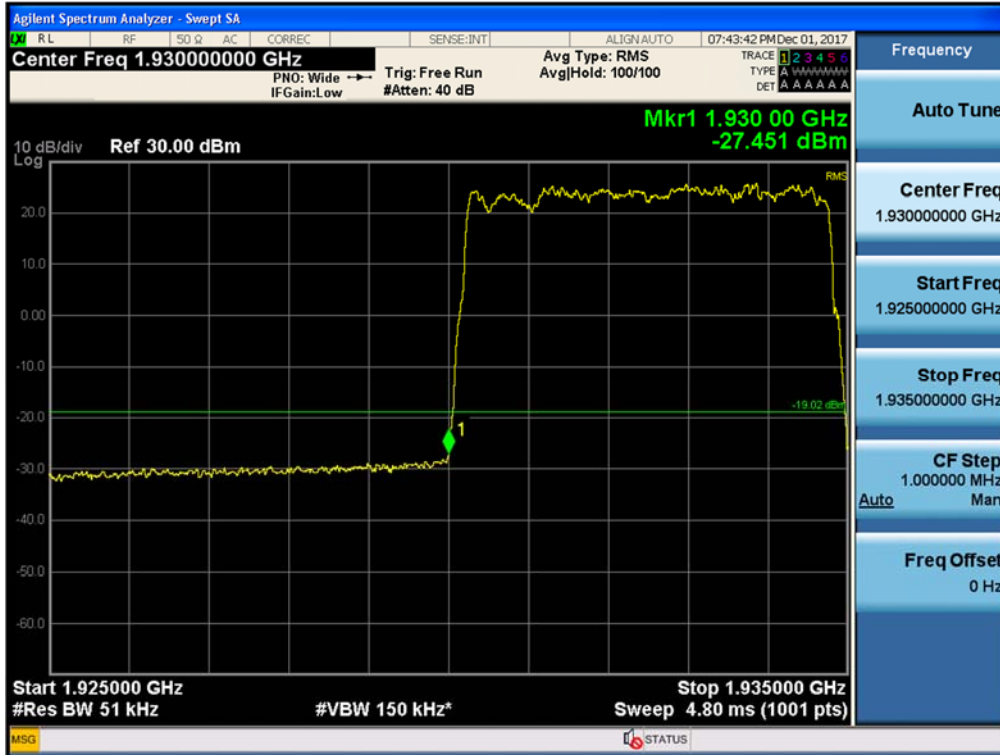


[Downlink High]

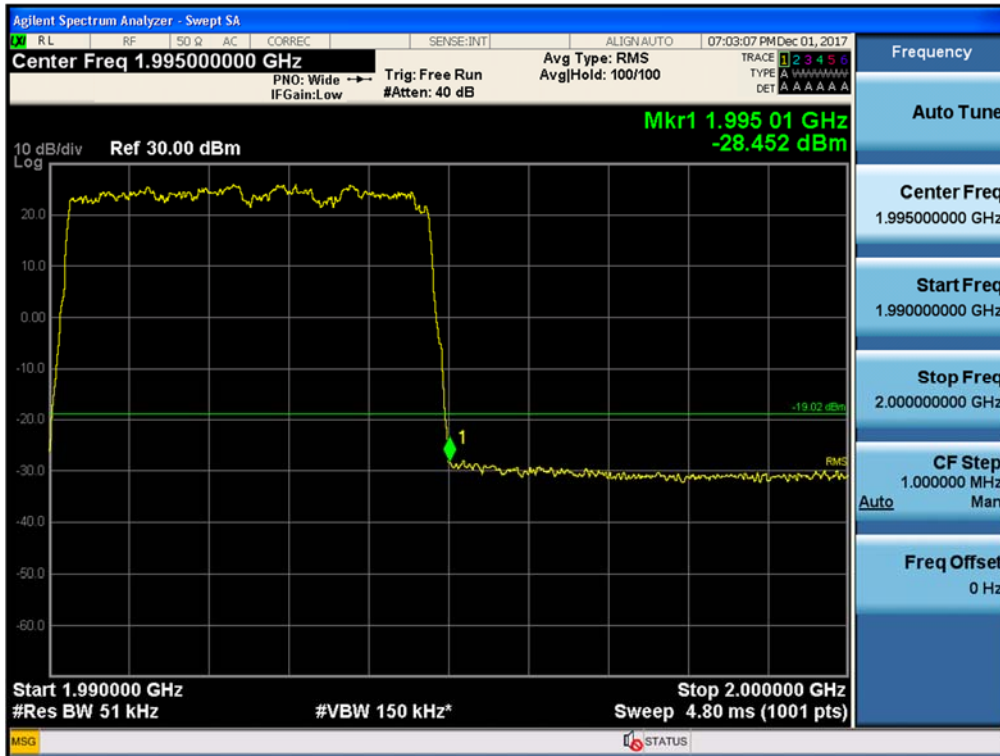


Test Data at Output Port 2\_16QAM

[Downlink Low]

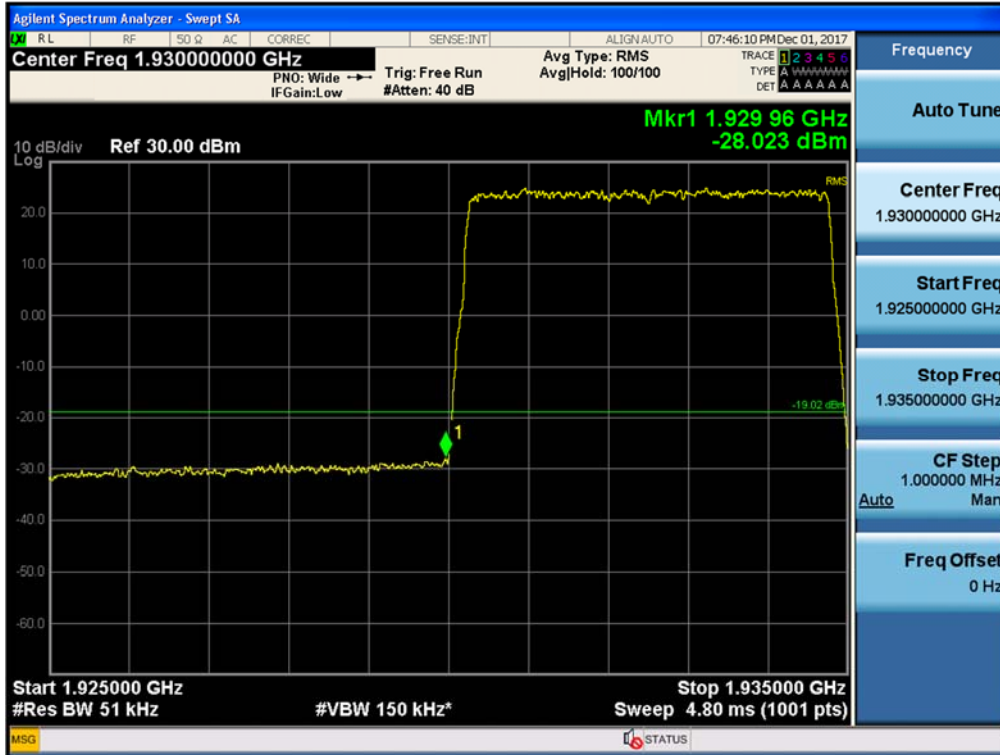


[Downlink High]

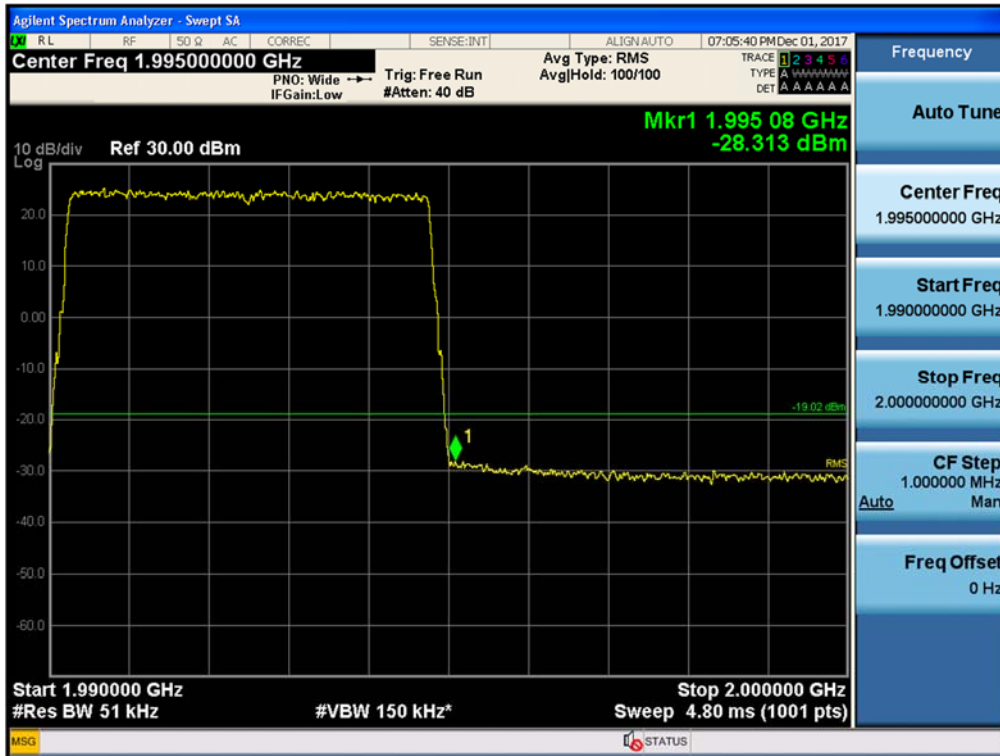


Test Data at Output Port 2\_64QAM

[Downlink Low]

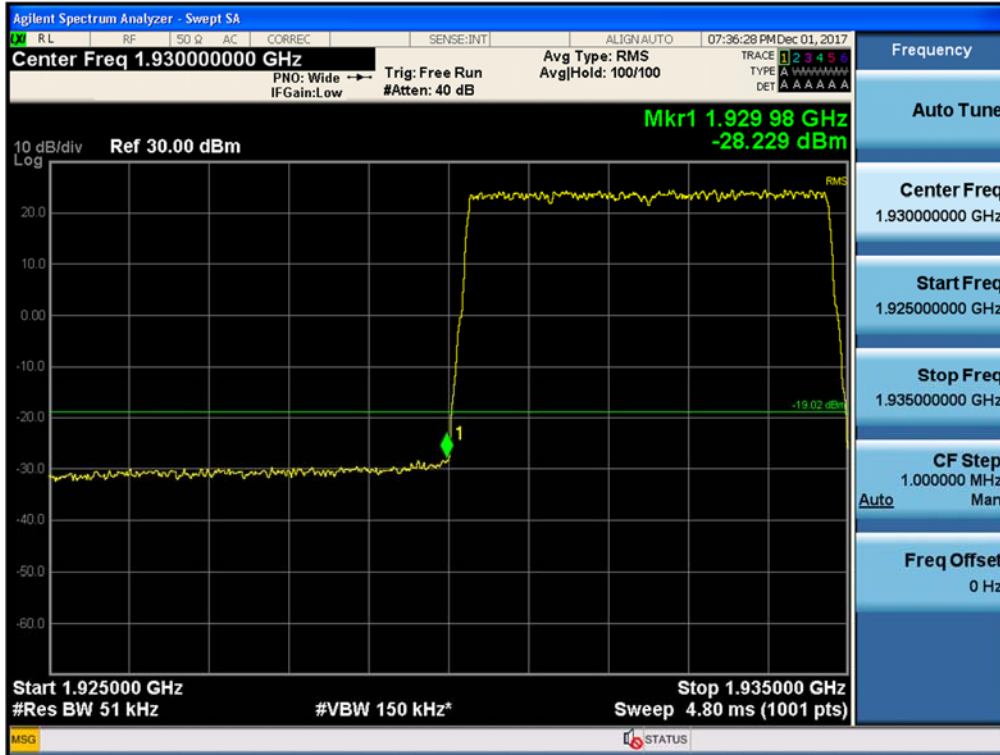


[Downlink High]

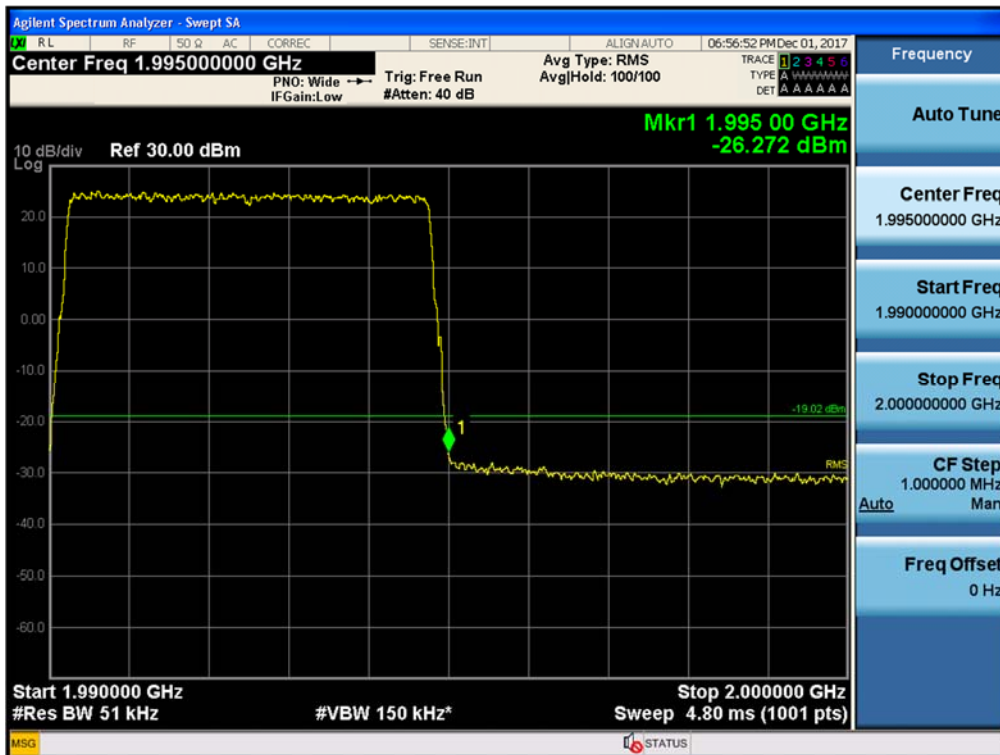


Test Data at Output Port 2\_256QAM

[Downlink Low]

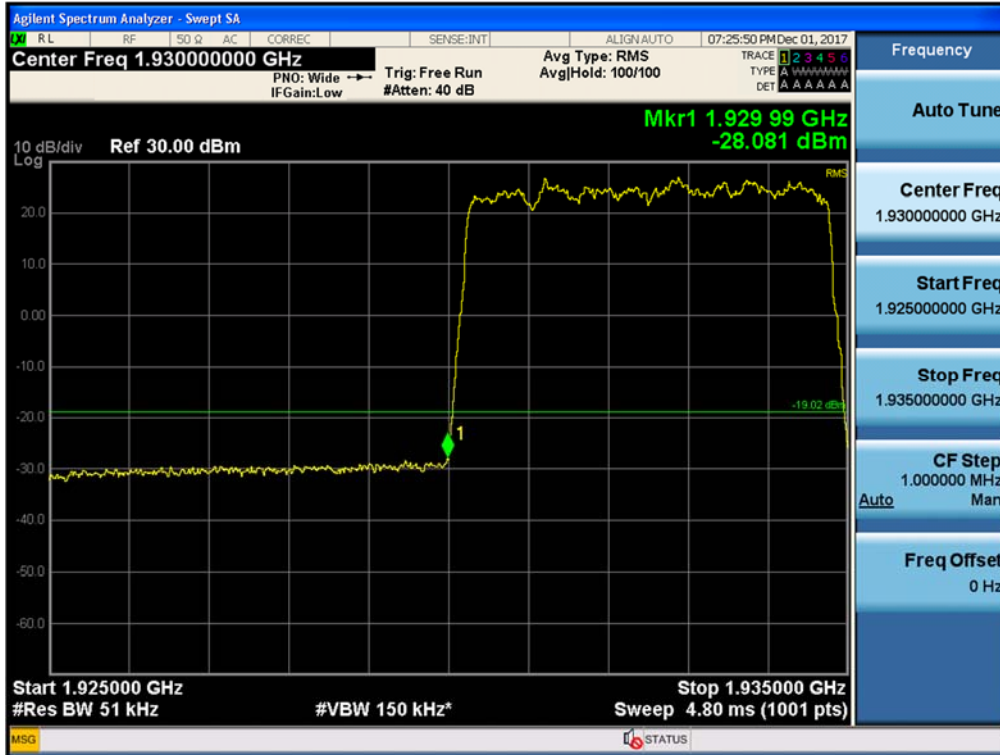


[Downlink High]

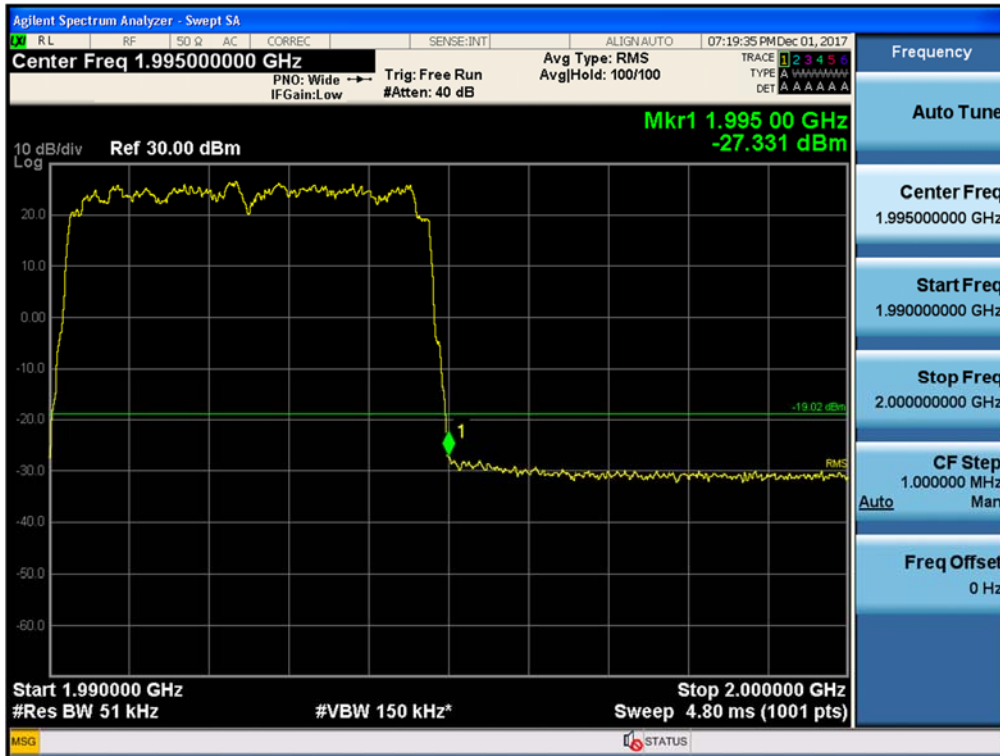


Test Data at Output Port 3\_QPSK

[Downlink Low]

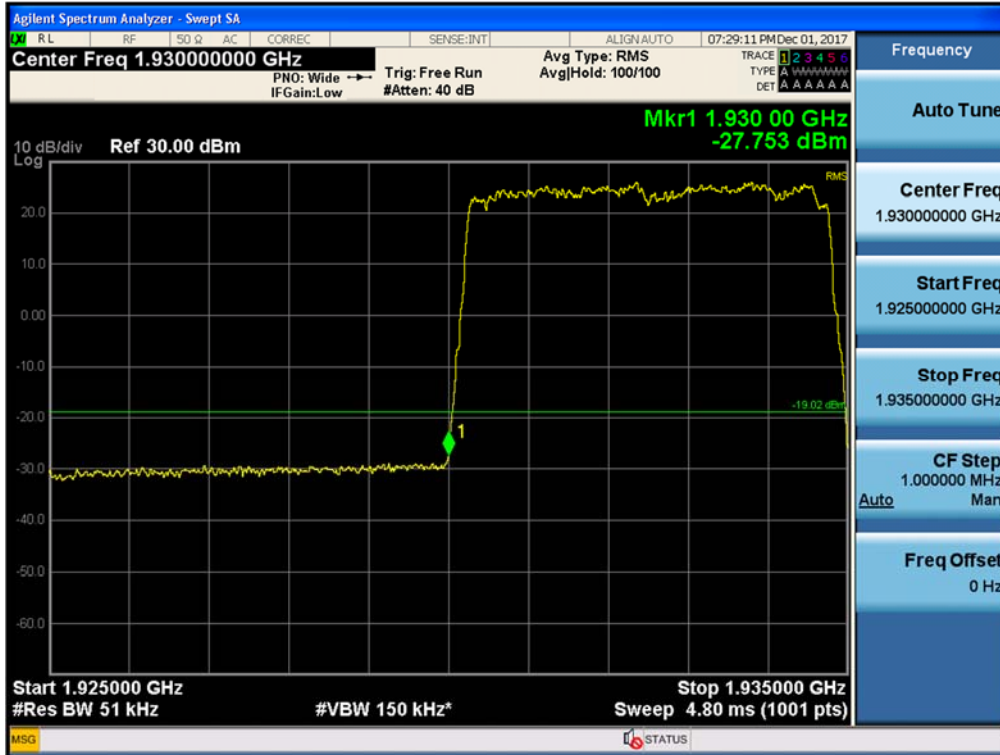


[Downlink High]

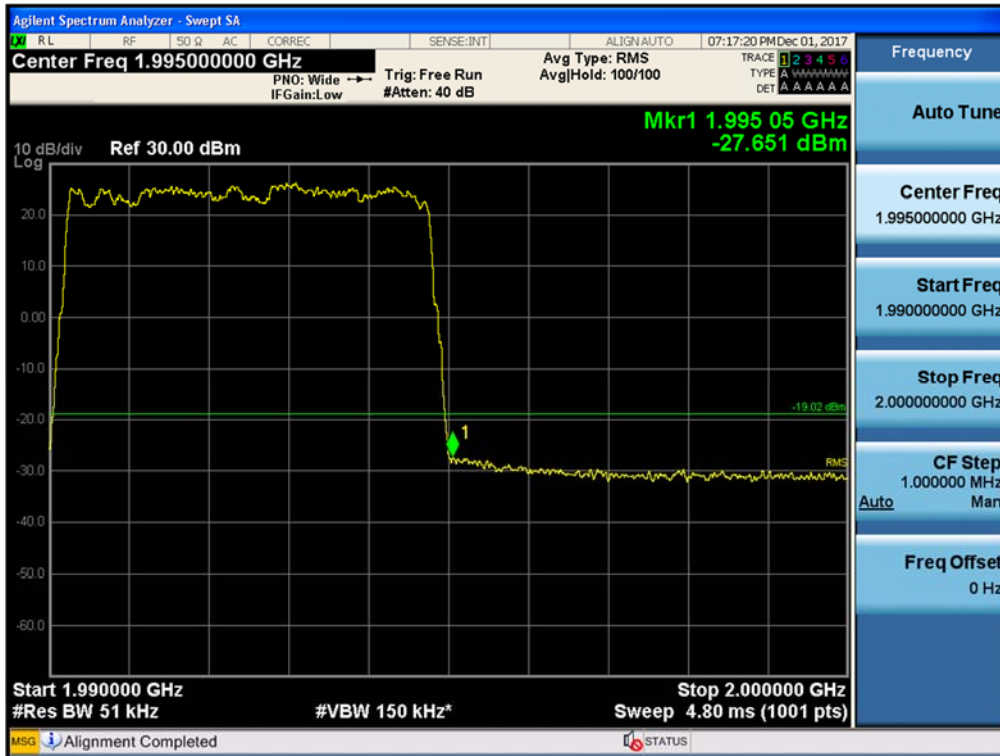


Test Data at Output Port 3\_16QAM

[Downlink Low]

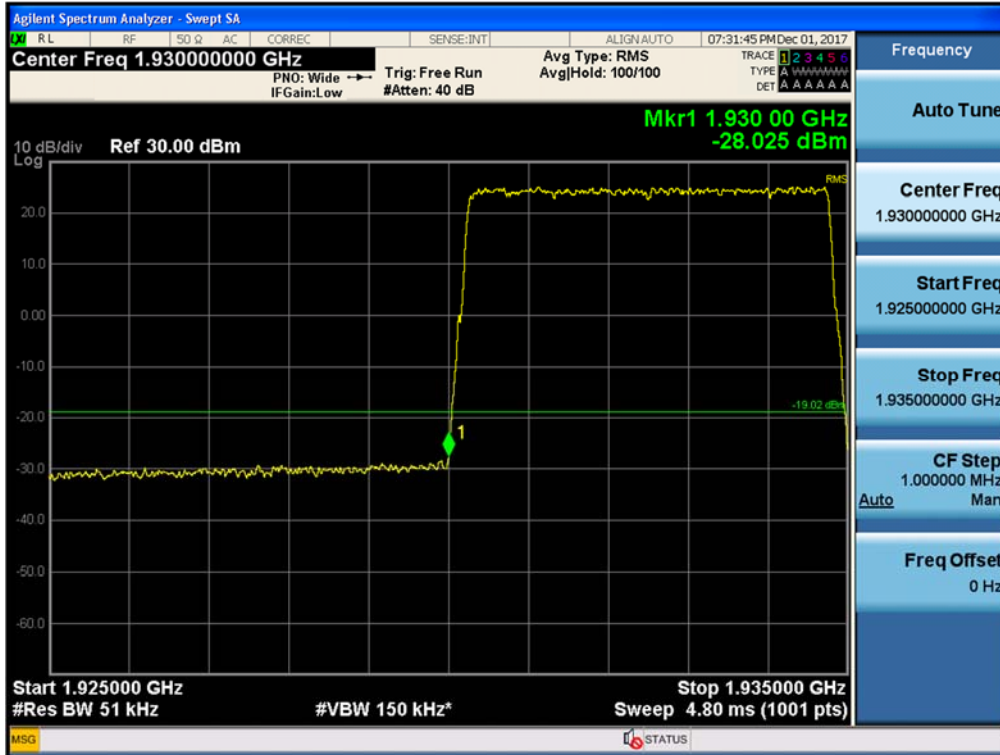


[Downlink High]

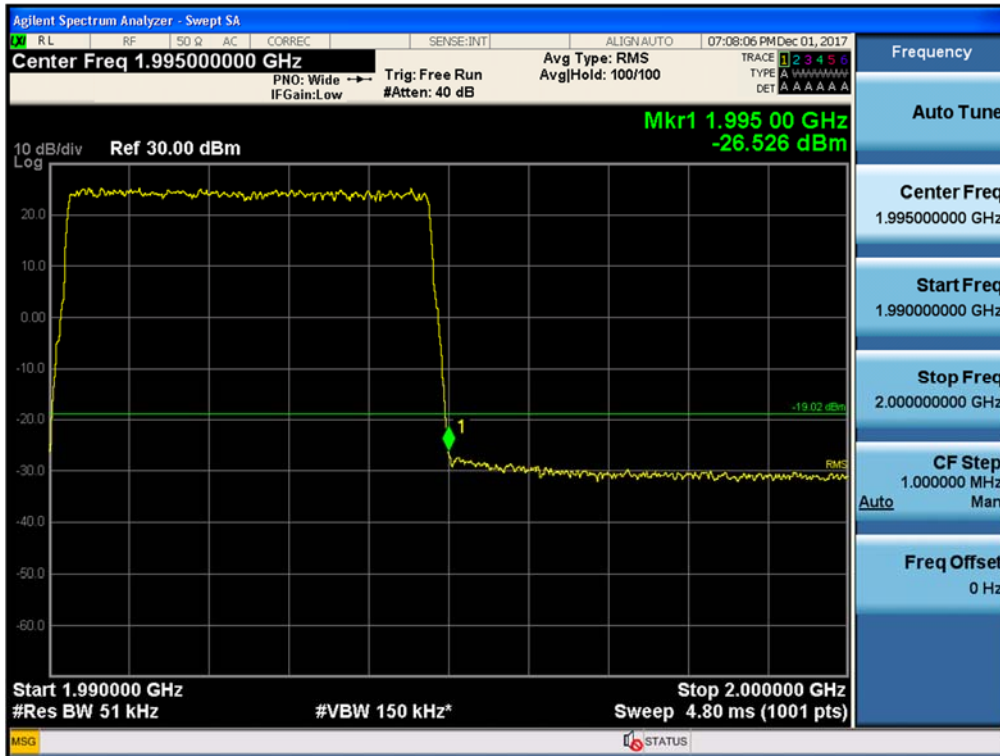


Test Data at Output Port 3\_64QAM

[Downlink Low]

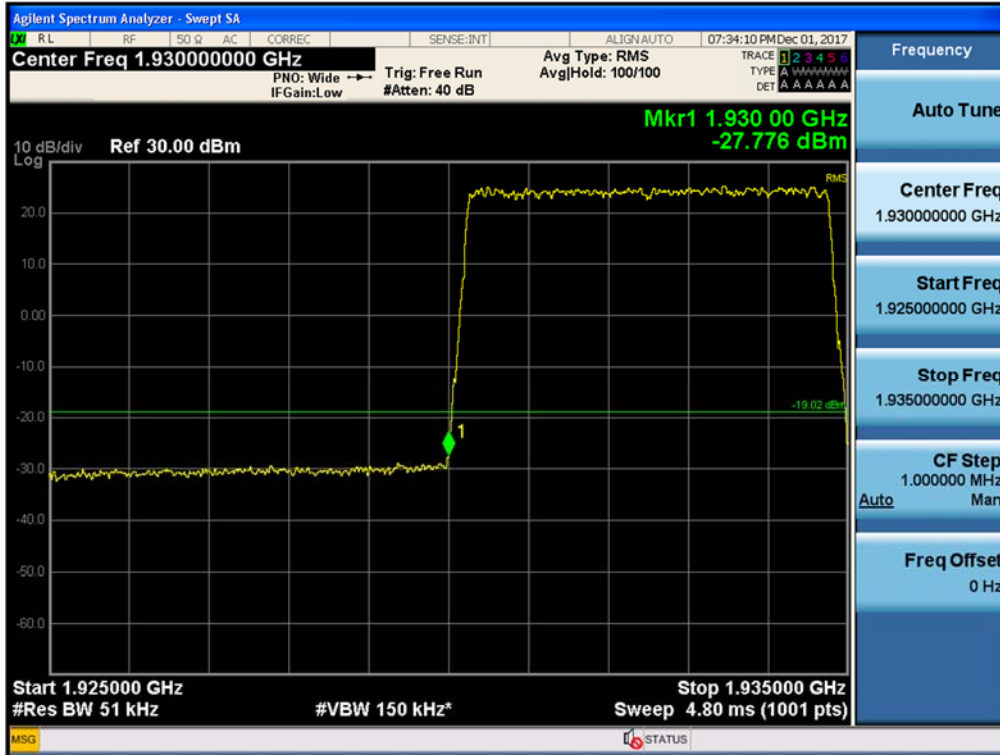


[Downlink High]

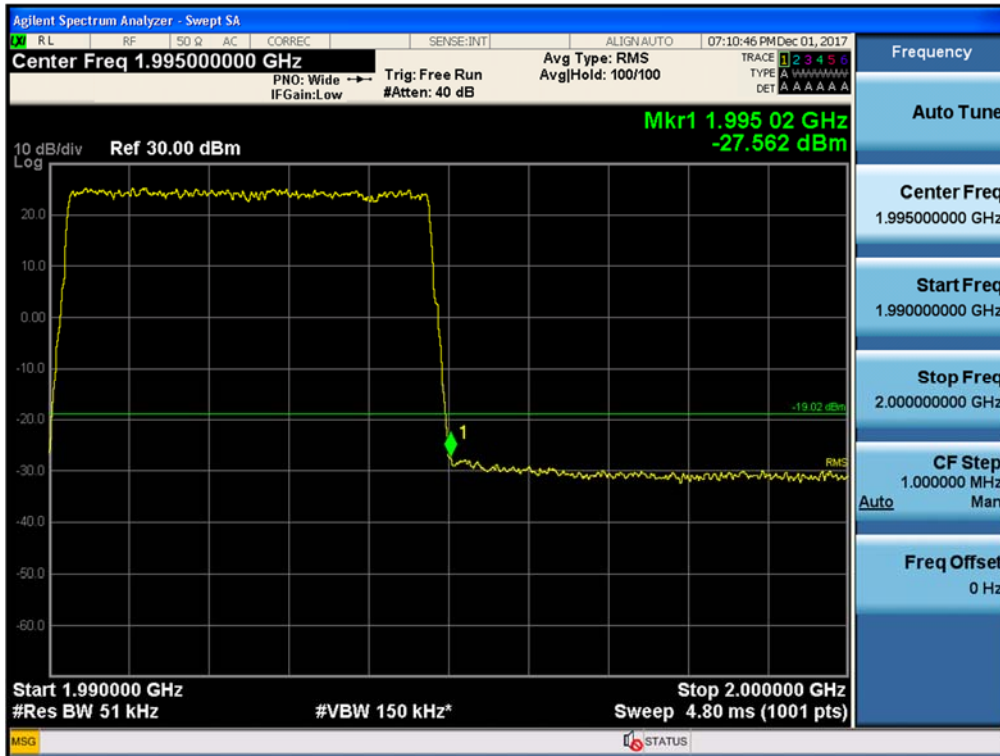


Test Data at Output Port 3\_256QAM

[Downlink Low]



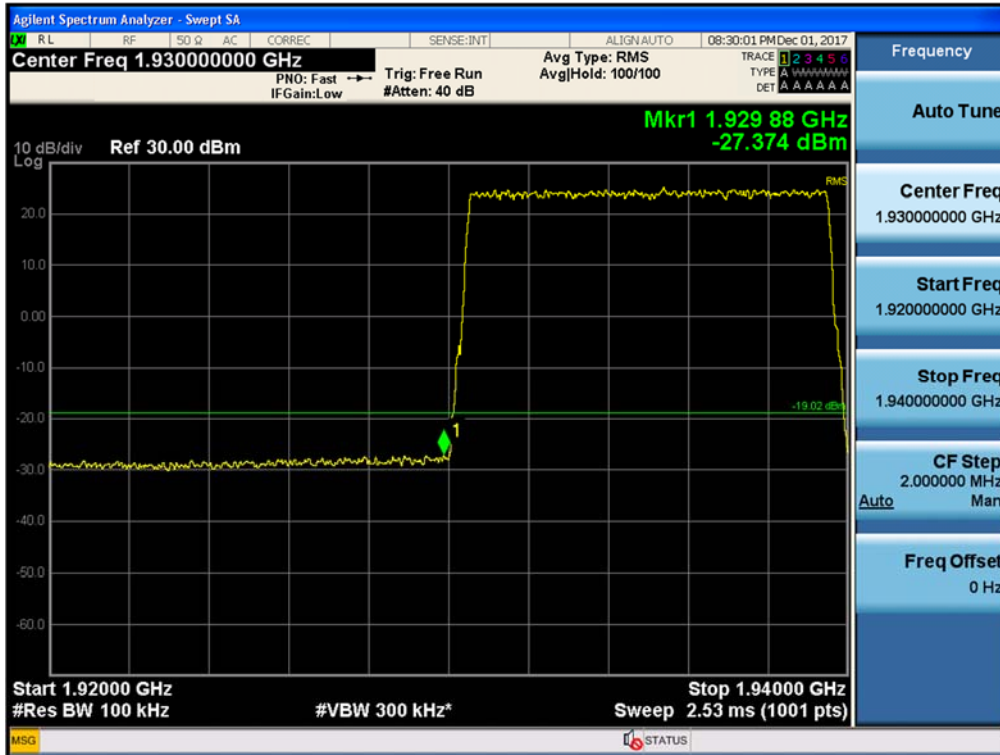
[Downlink High]



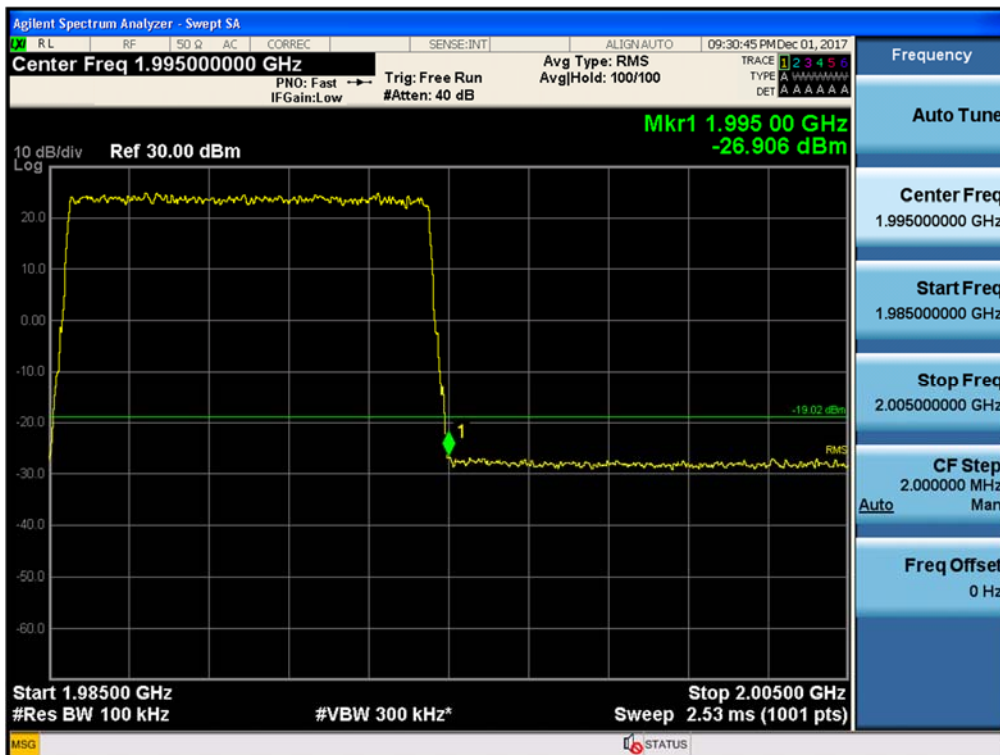
10 MHz / 1 Carrier (20 W)

Test Data at Output Port 0\_256QAM

[Downlink Low]

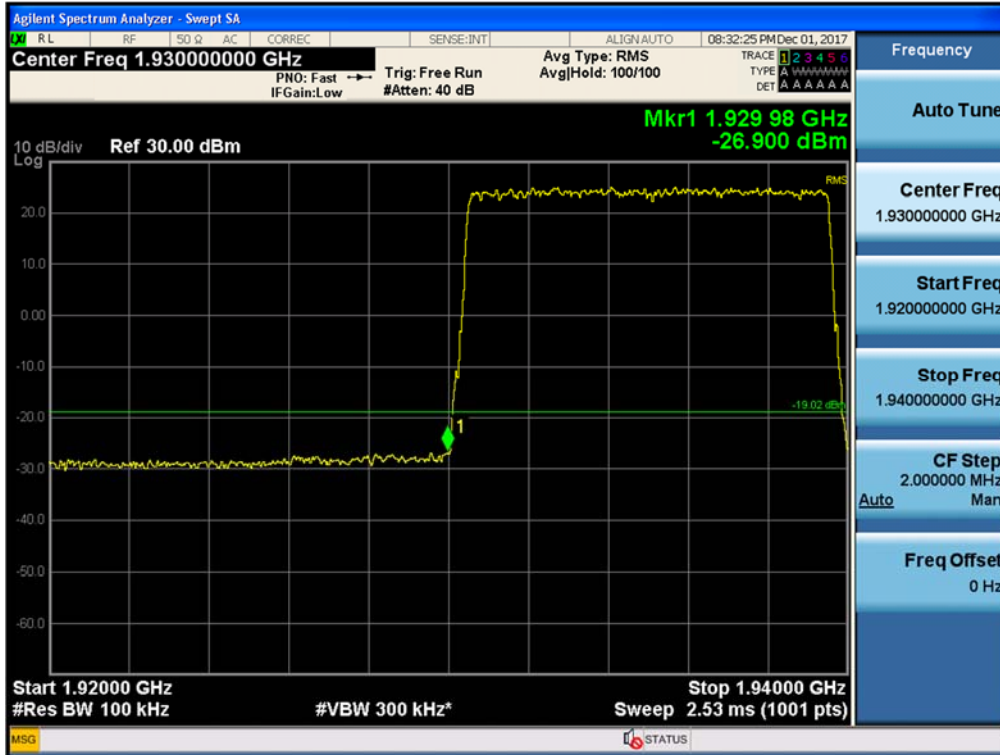


[Downlink High]



Test Data at Output Port 1\_256QAM

[Downlink Low]



[Downlink High]

