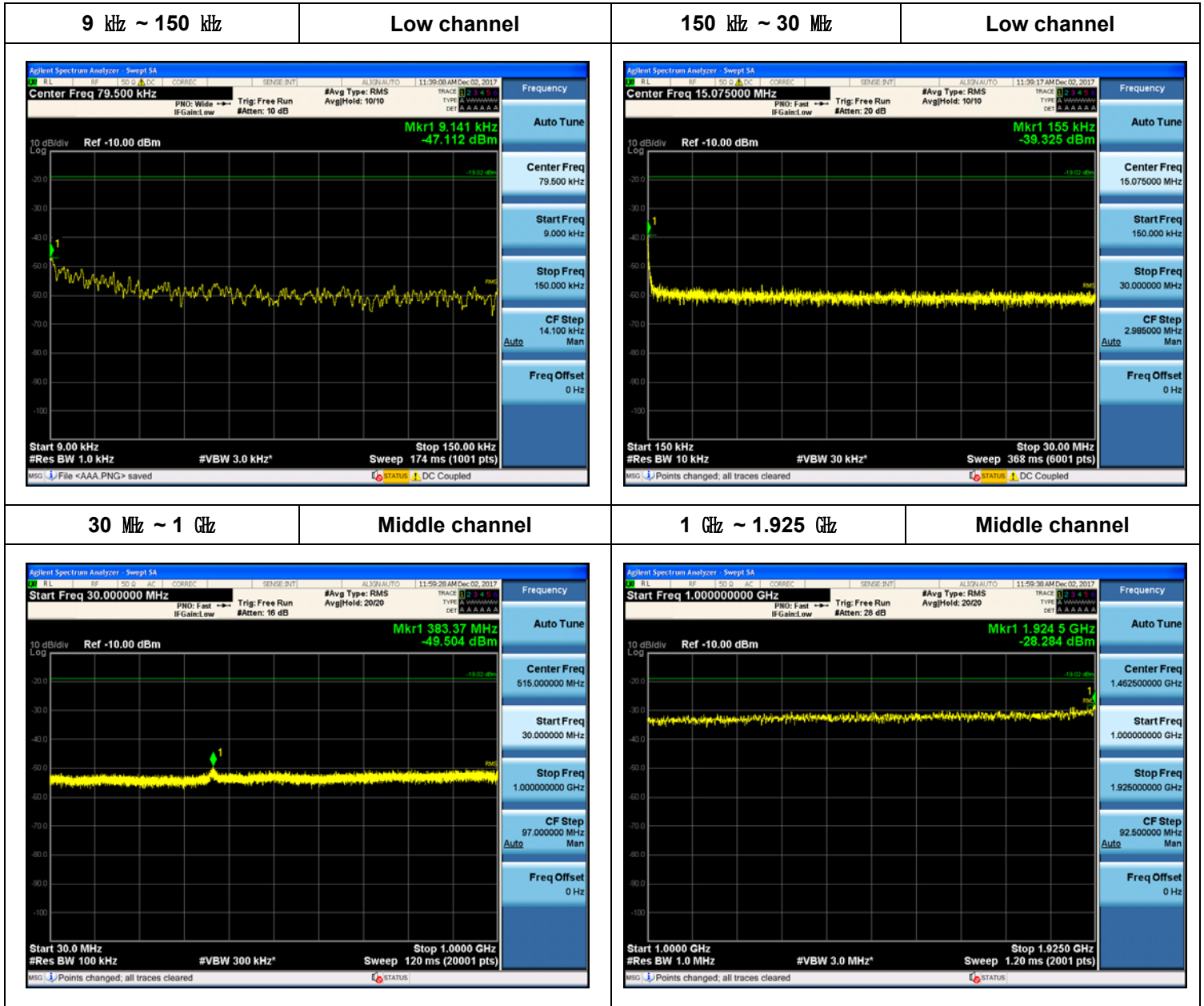
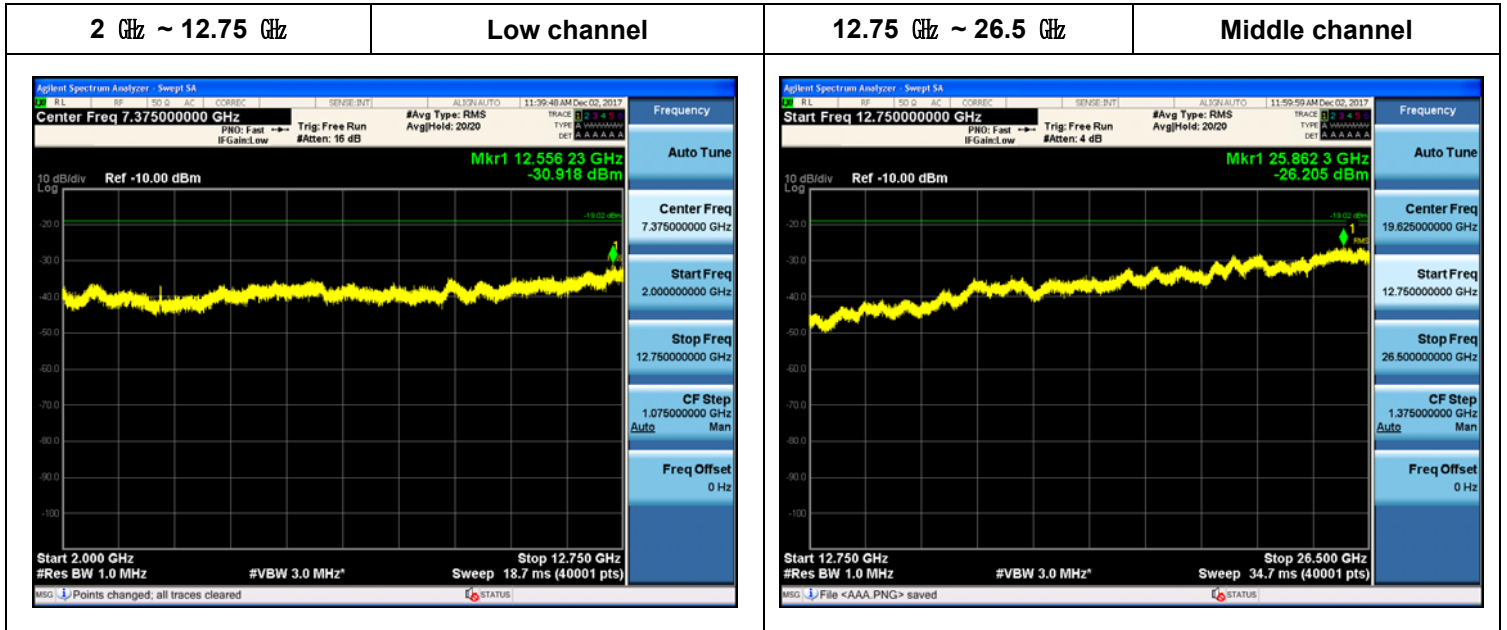
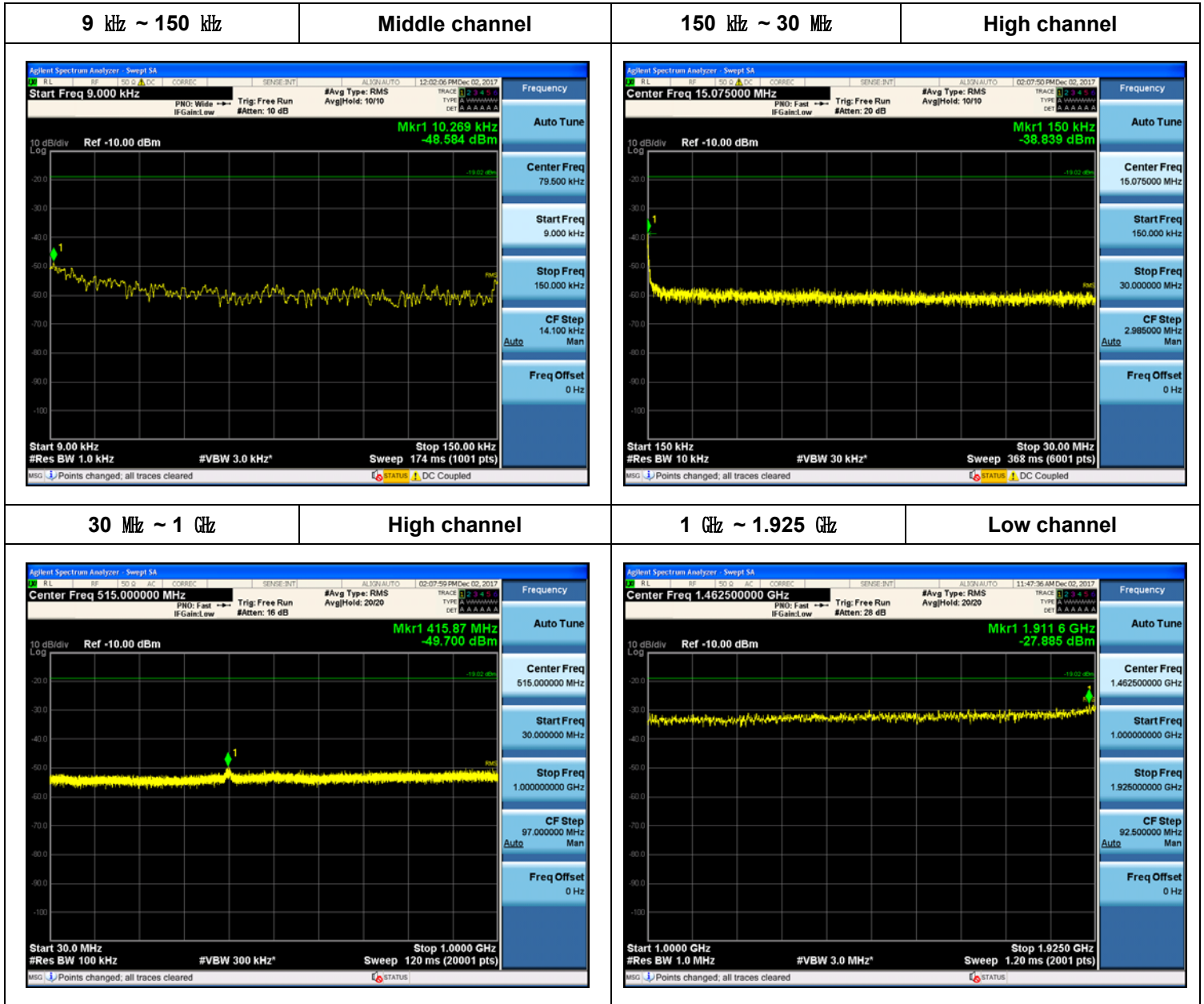


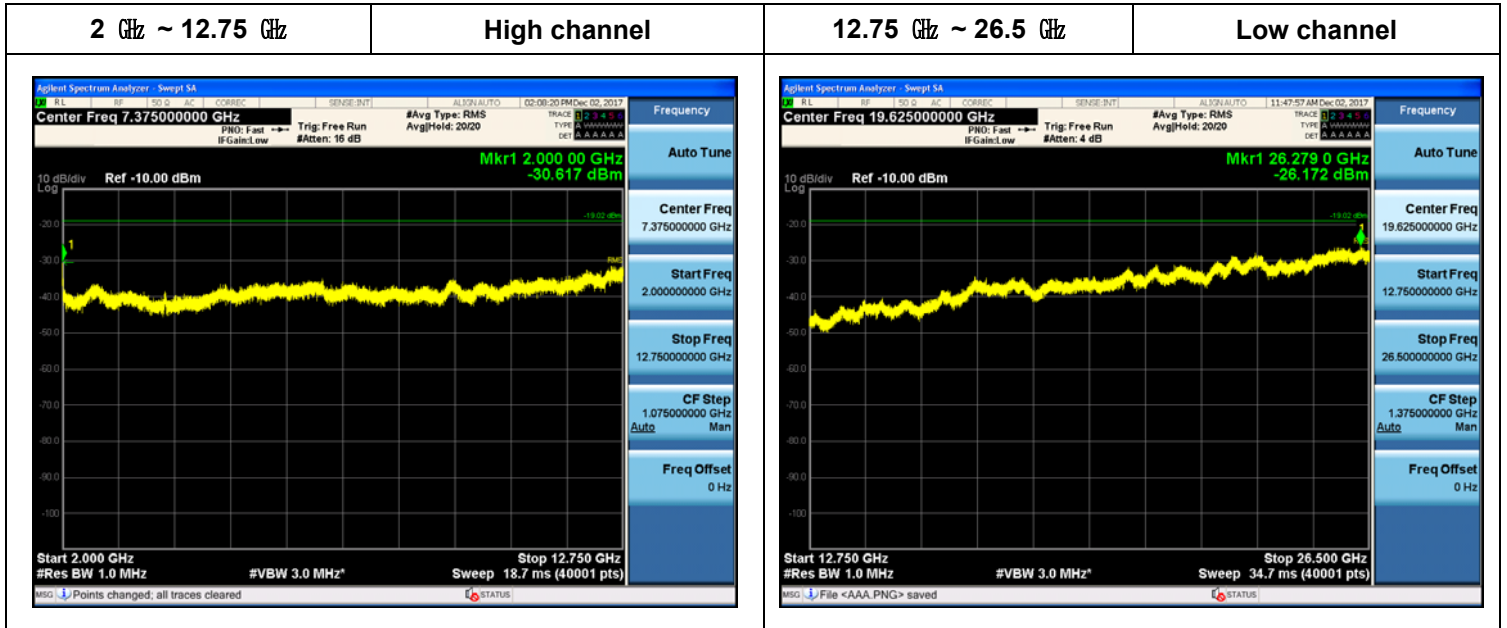
**Plot Data for Output Port 0\_16QAM**



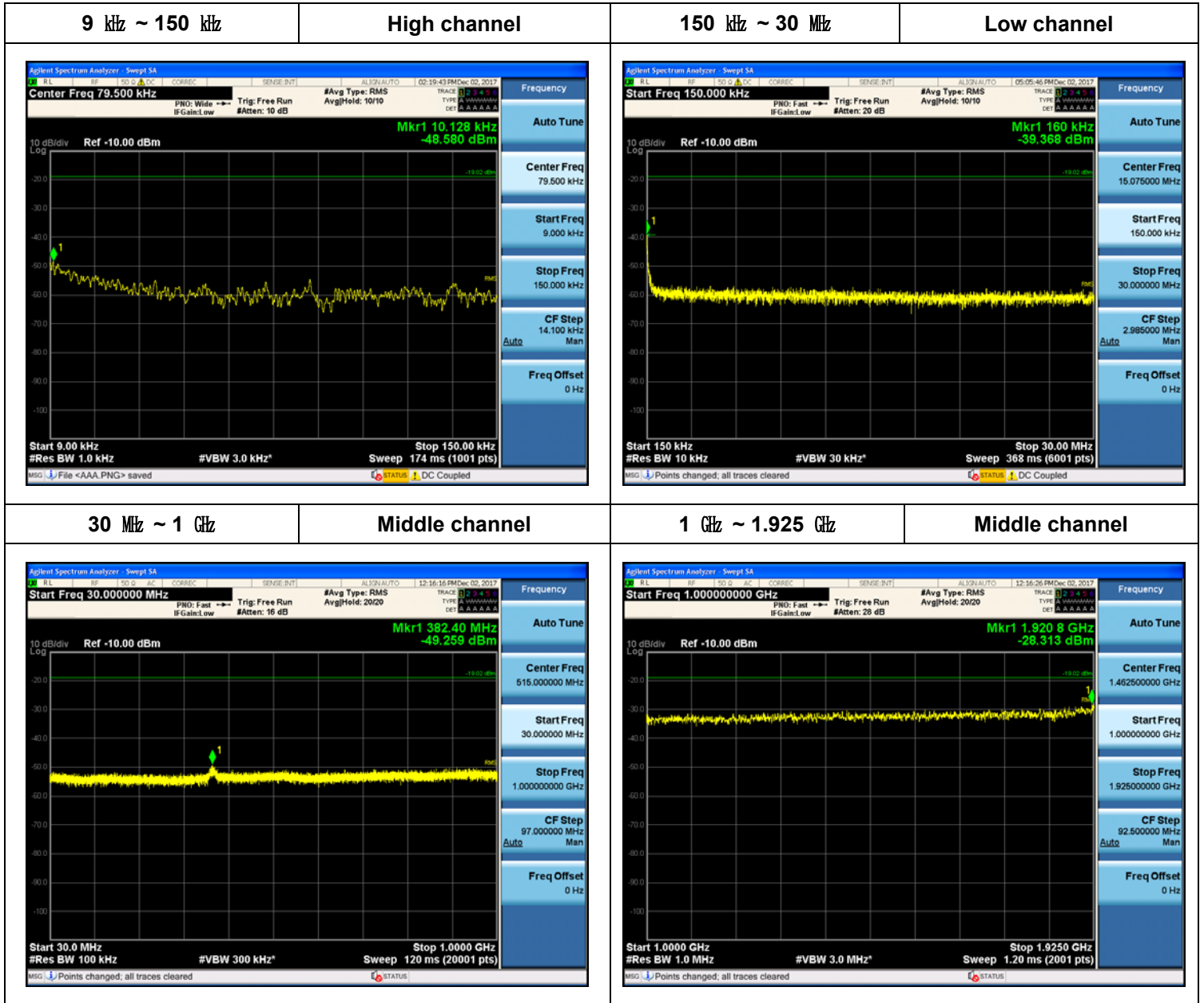


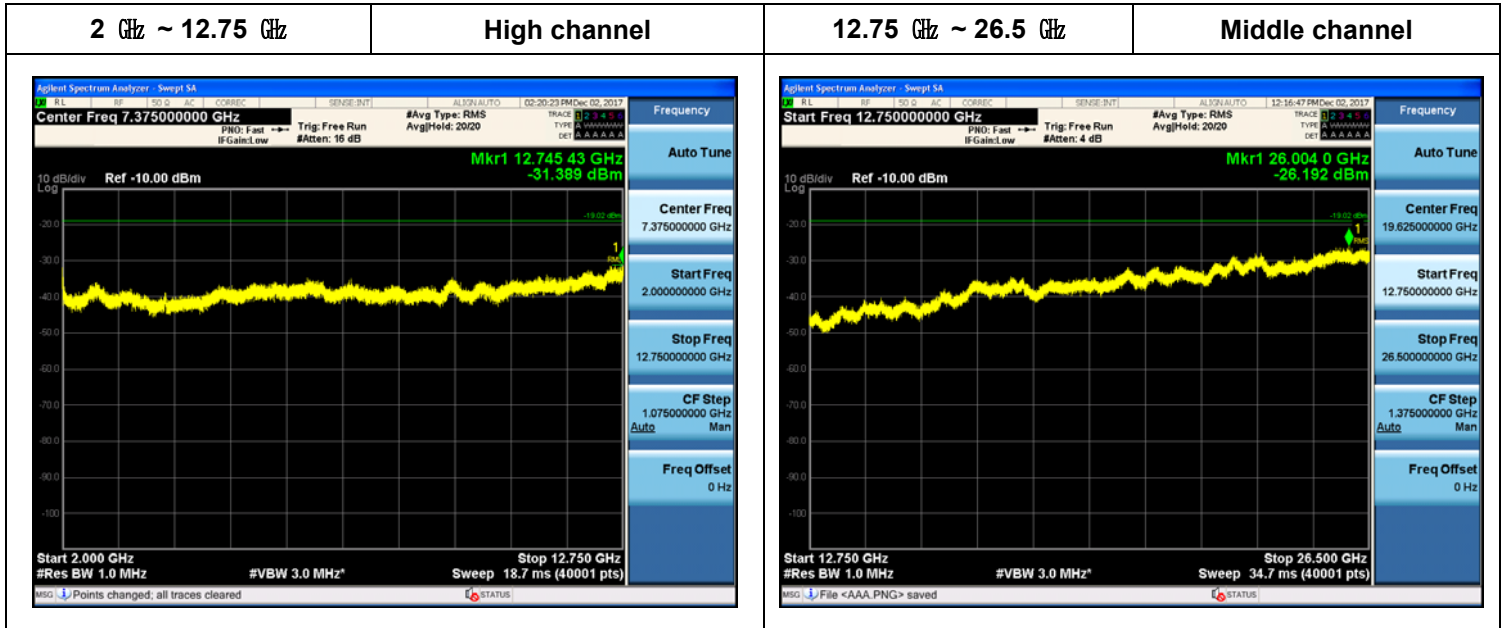
Plot Data for Output Port 0\_64QAM



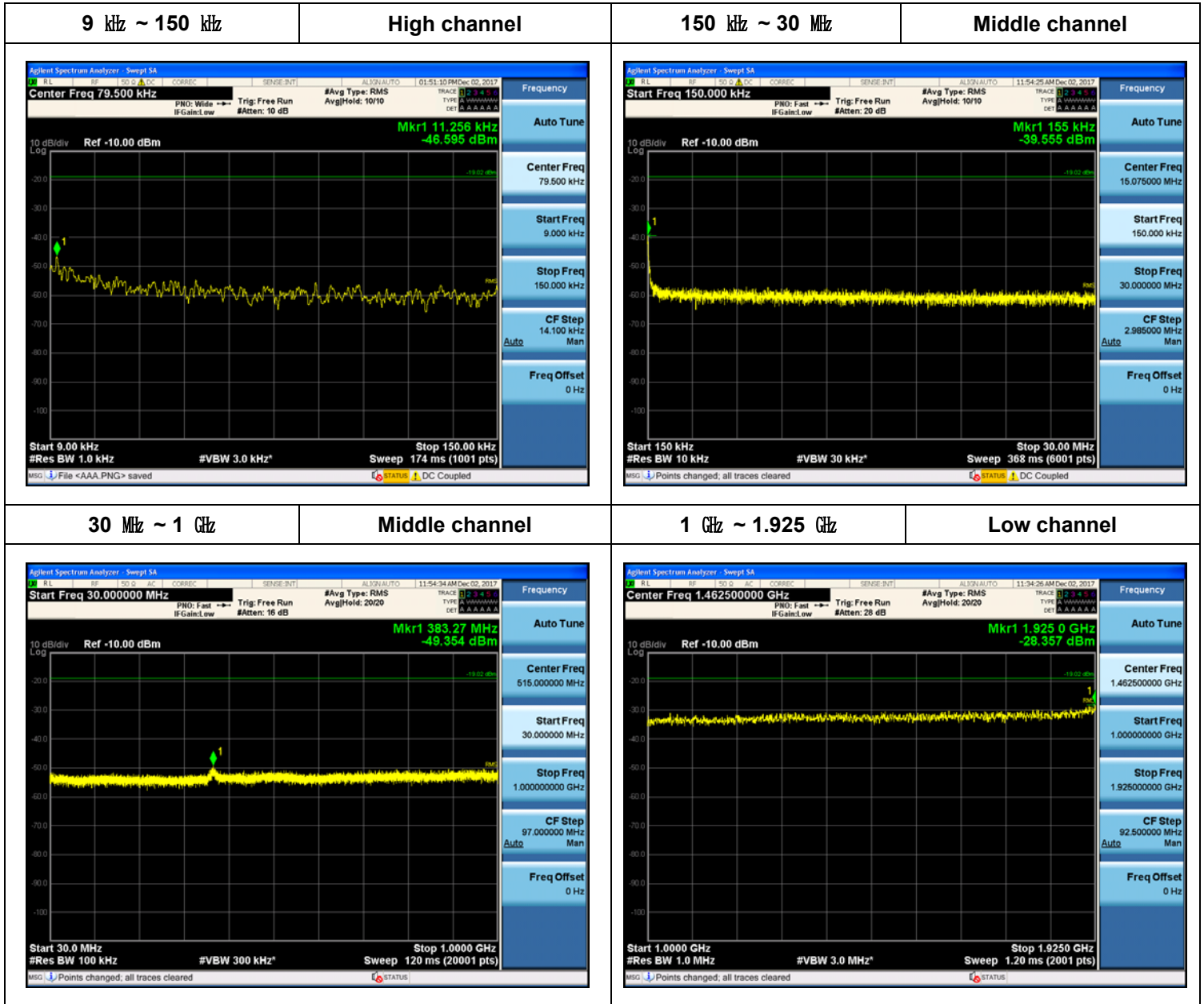


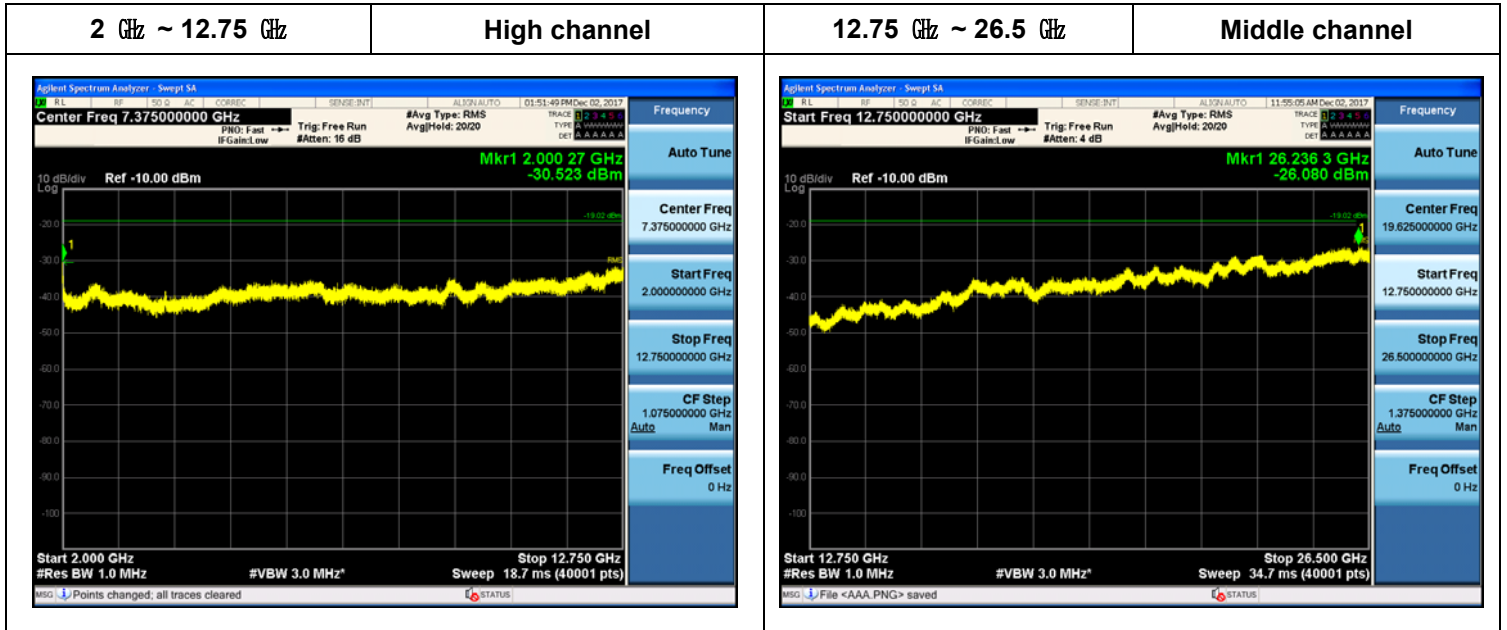
**Plot Data for Output Port 0\_256QAM**



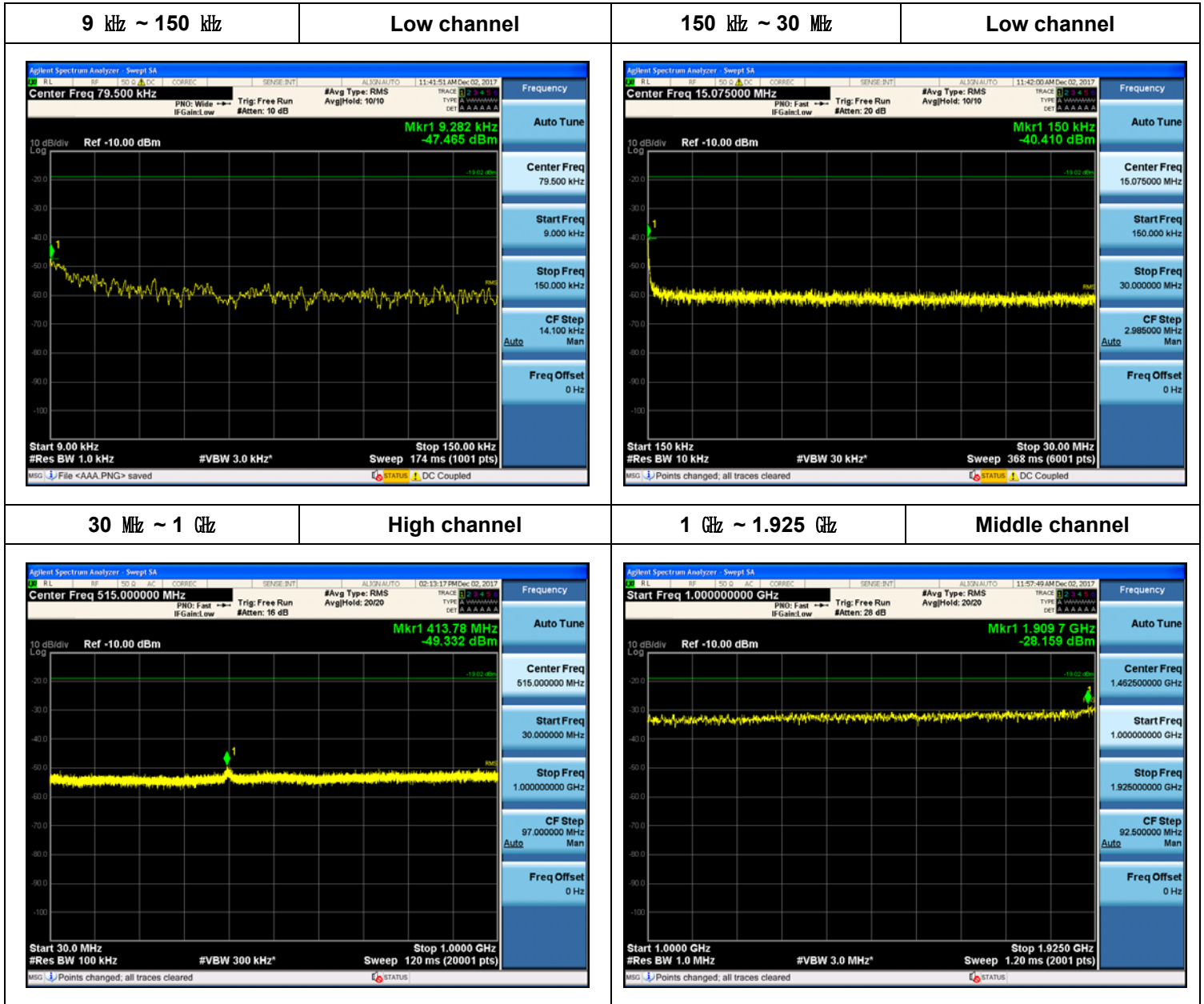


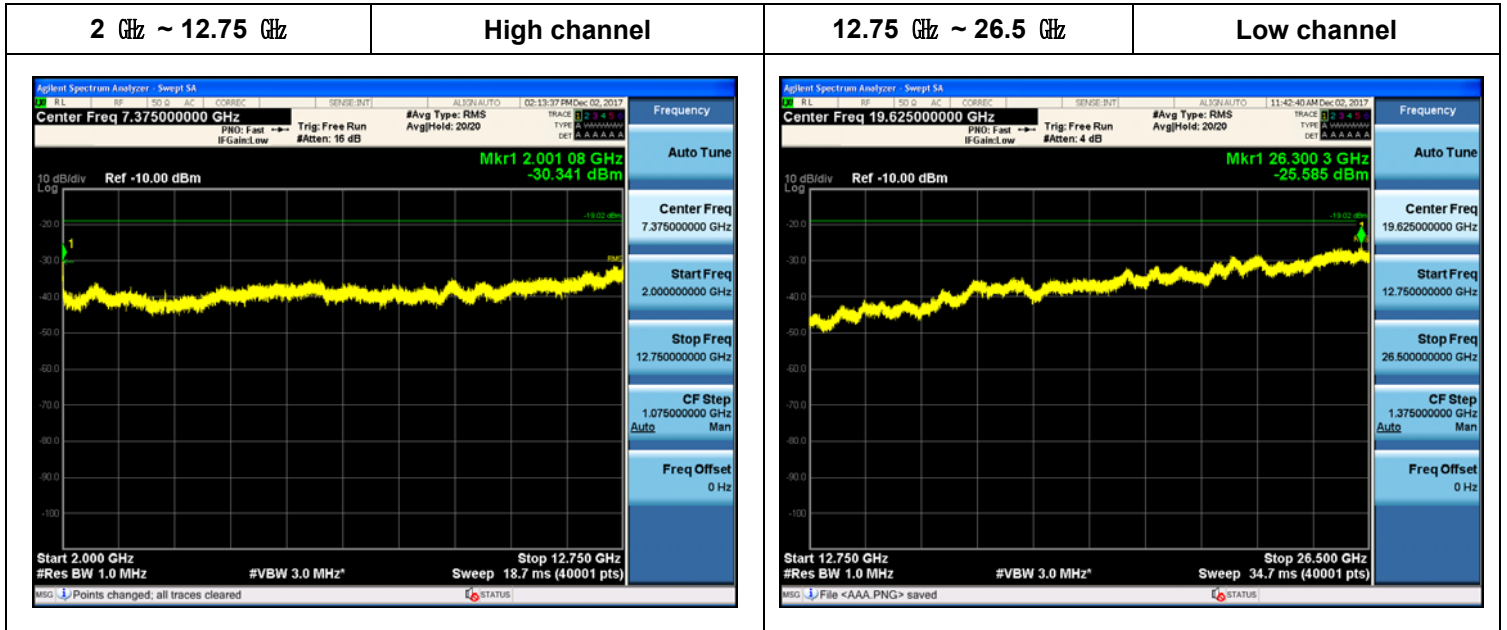
**Plot Data for Output Port 1\_QPSK**



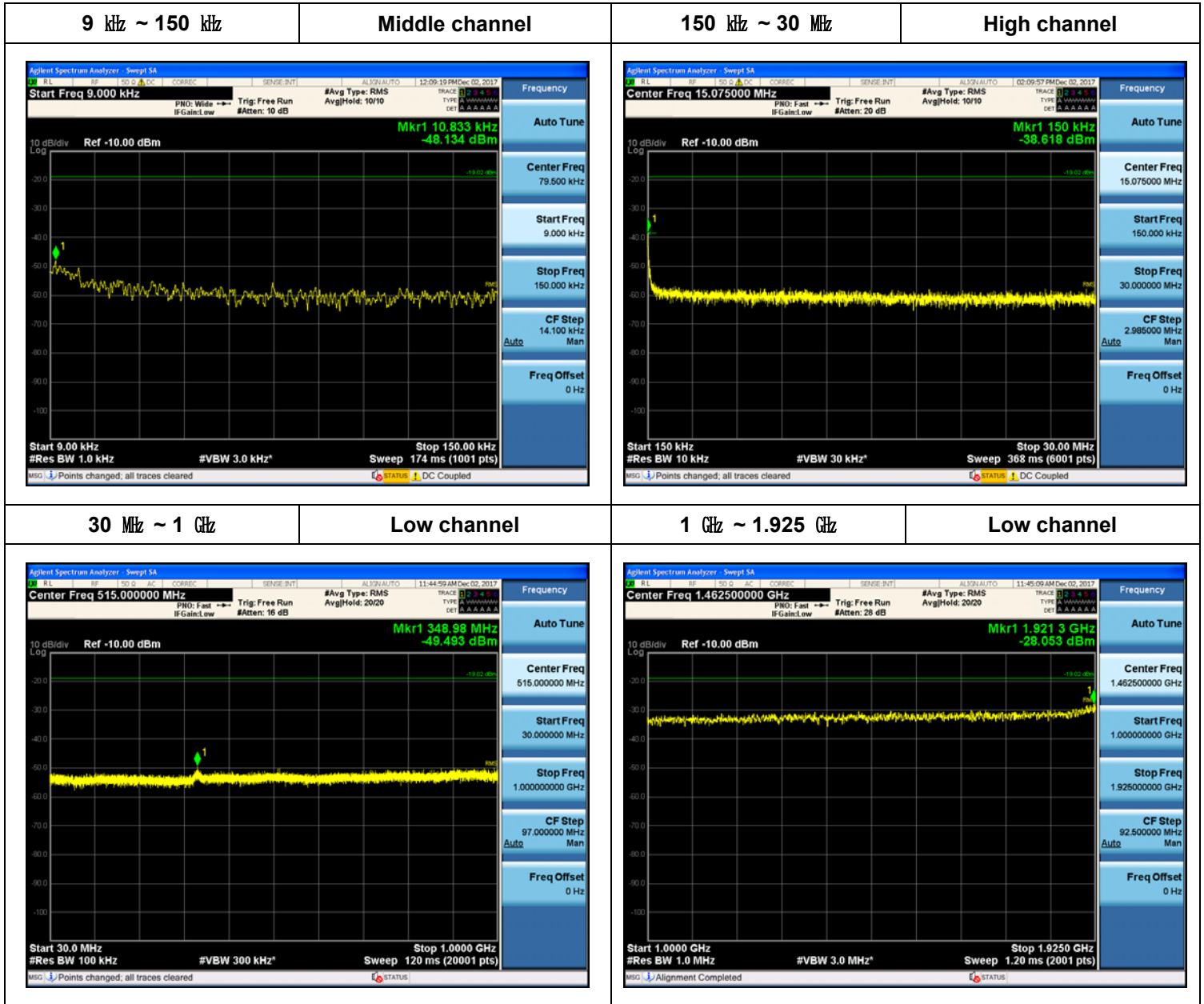


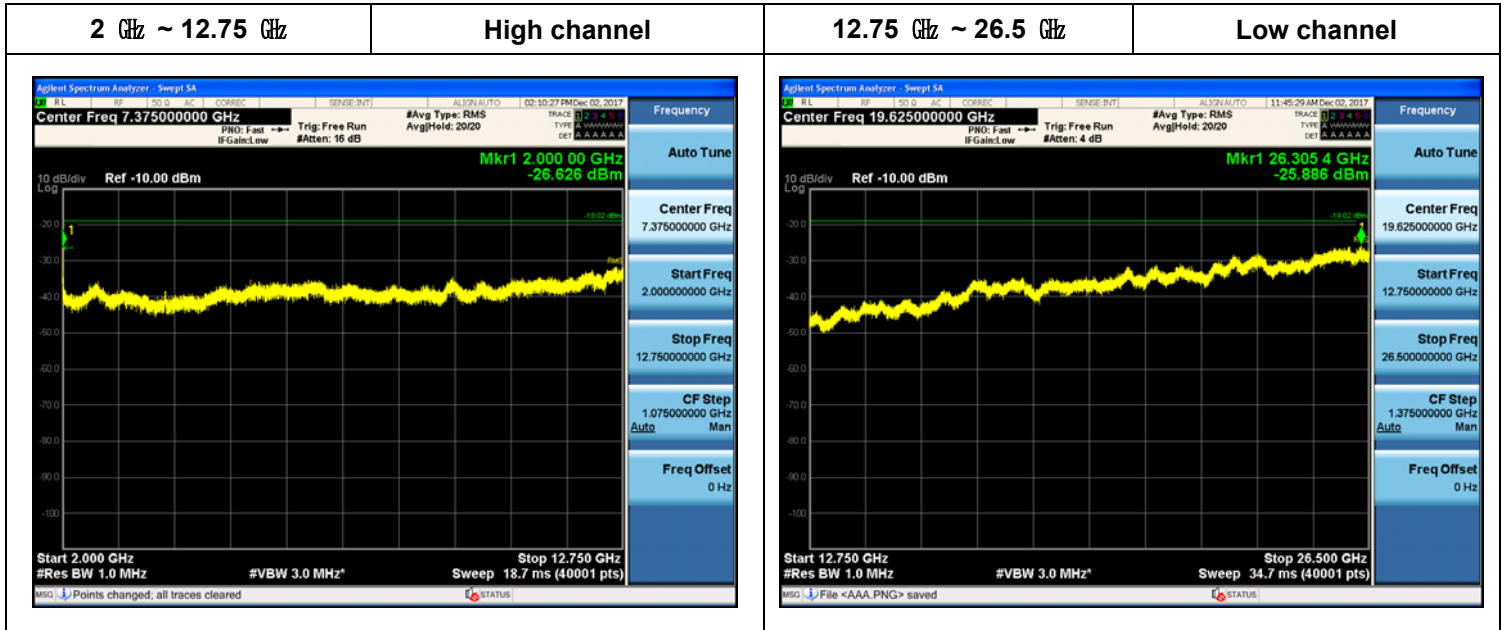
**Plot Data for Output Port 1\_16QAM**



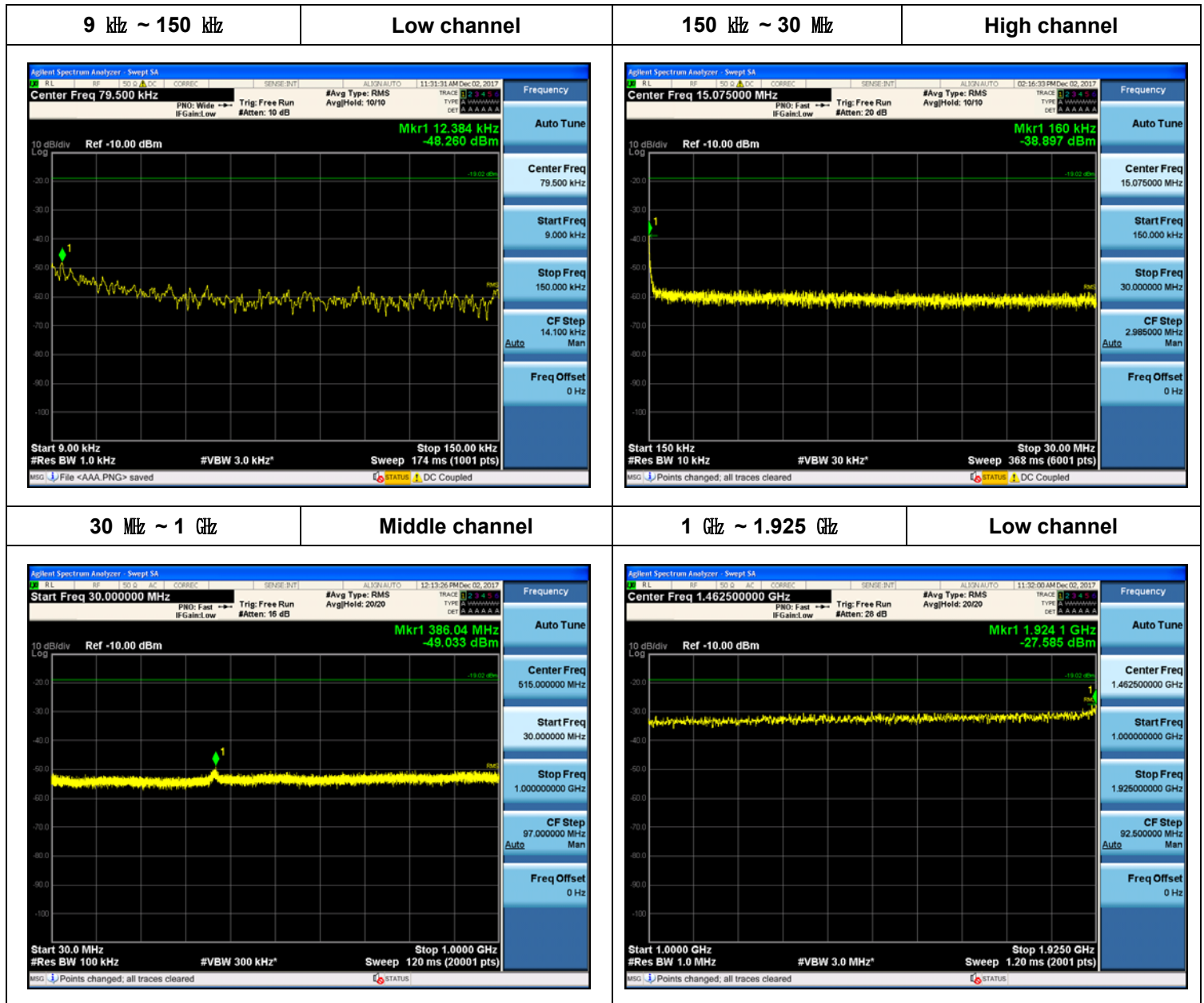


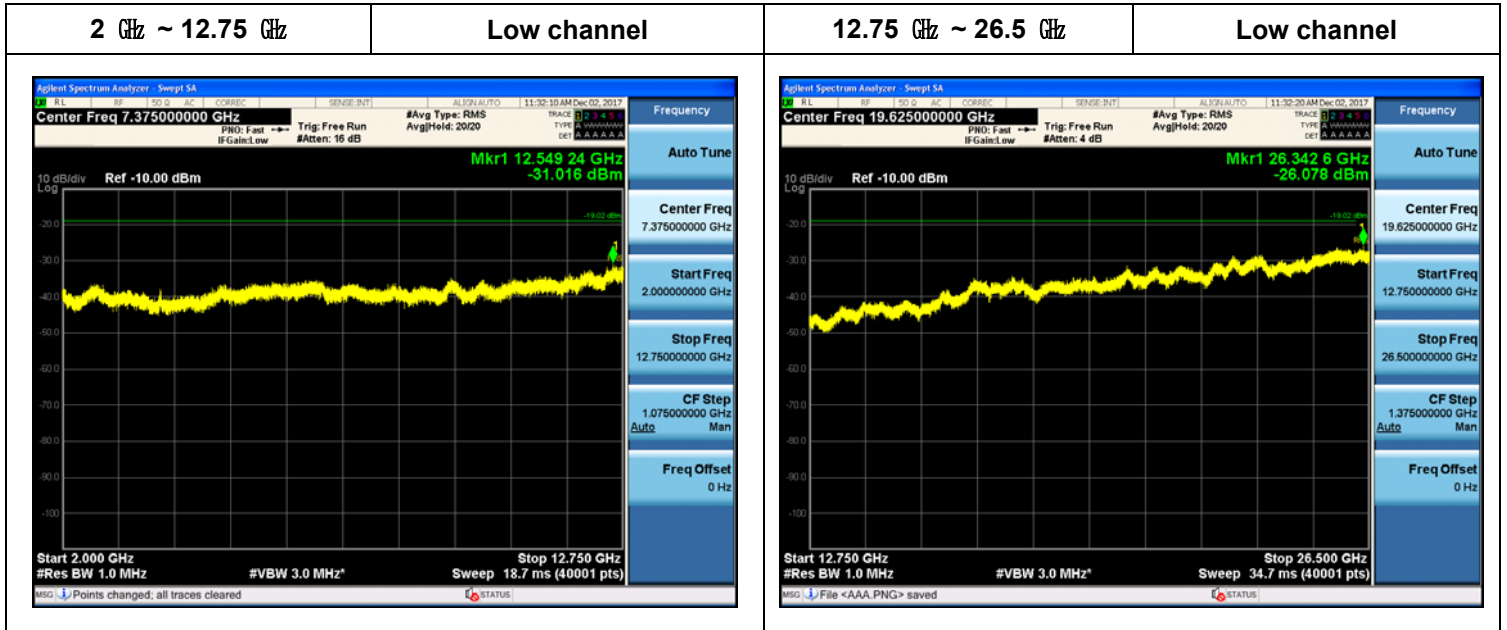
**Plot Data for Output Port 1\_64QAM**



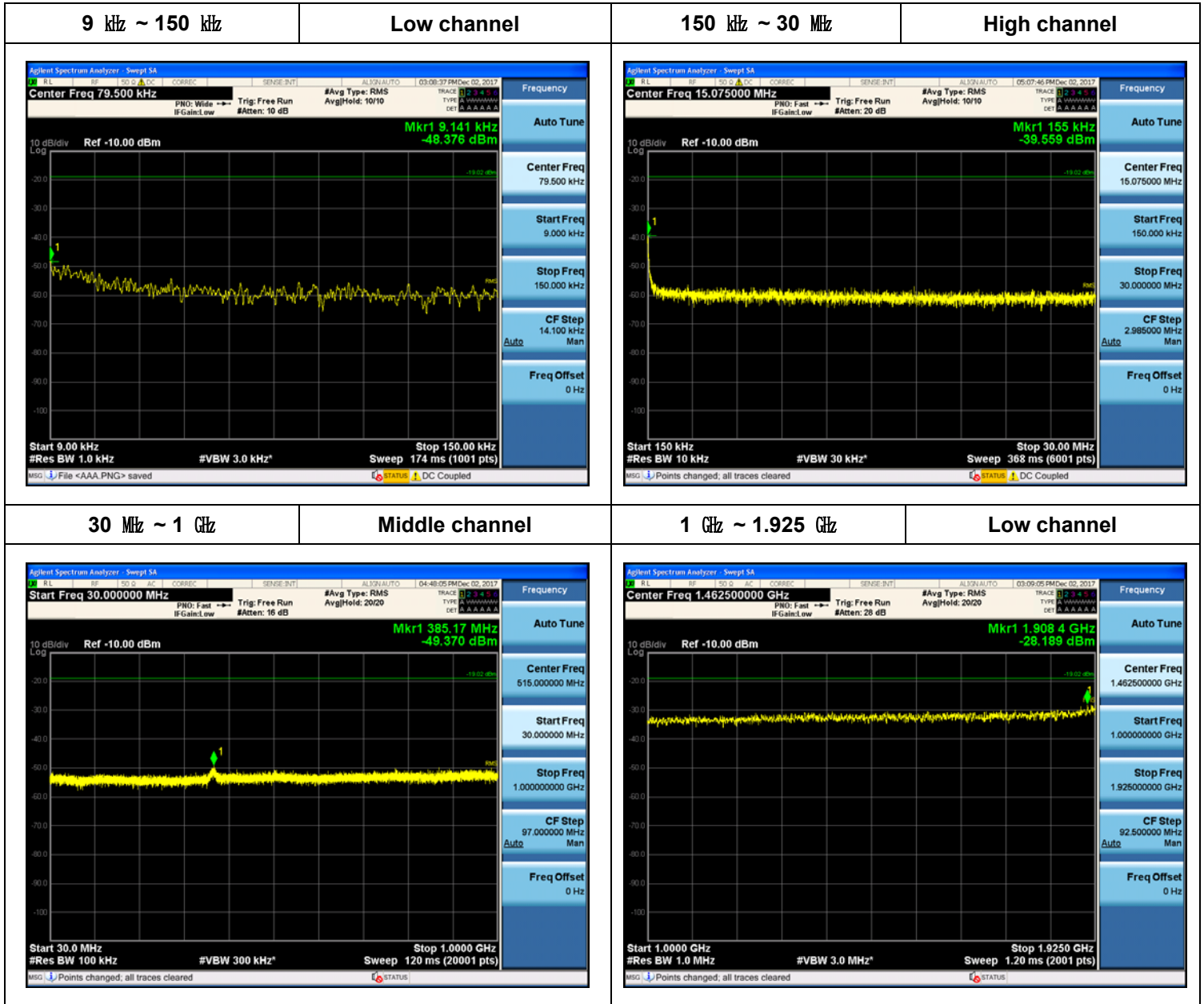


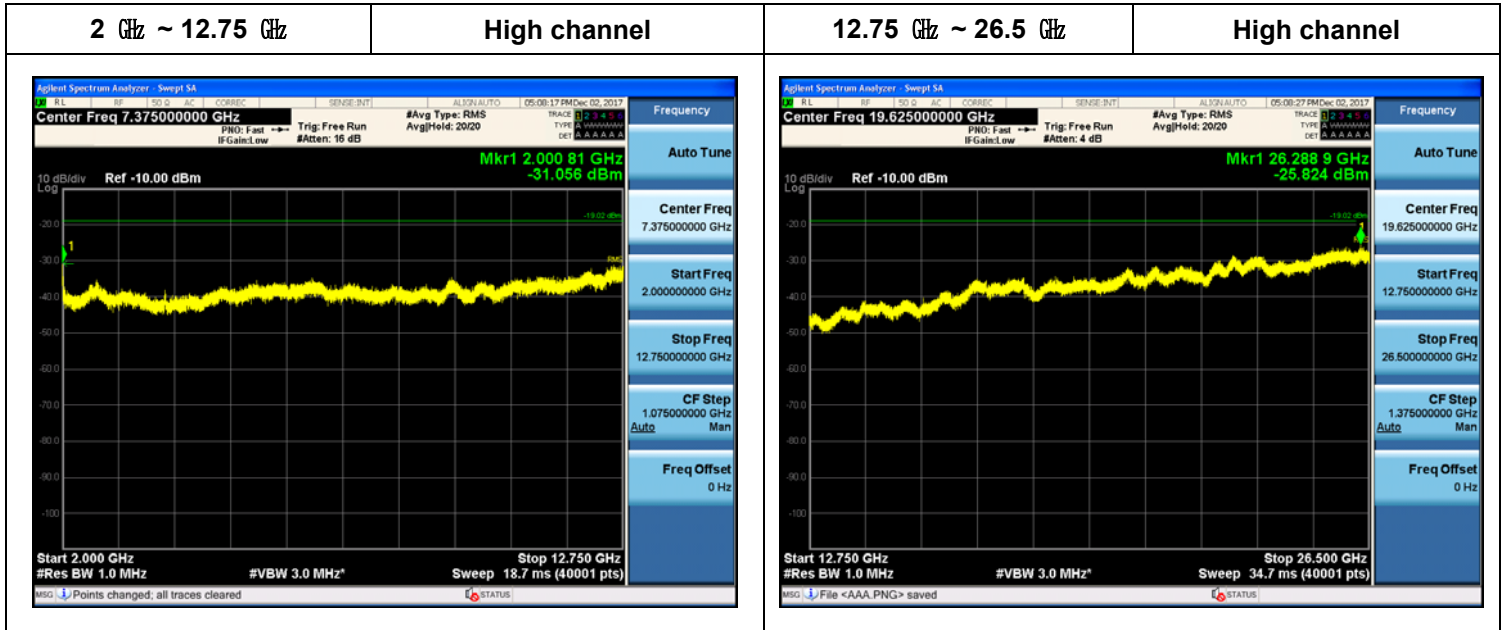
**Plot Data for Output Port 1\_256QAM**



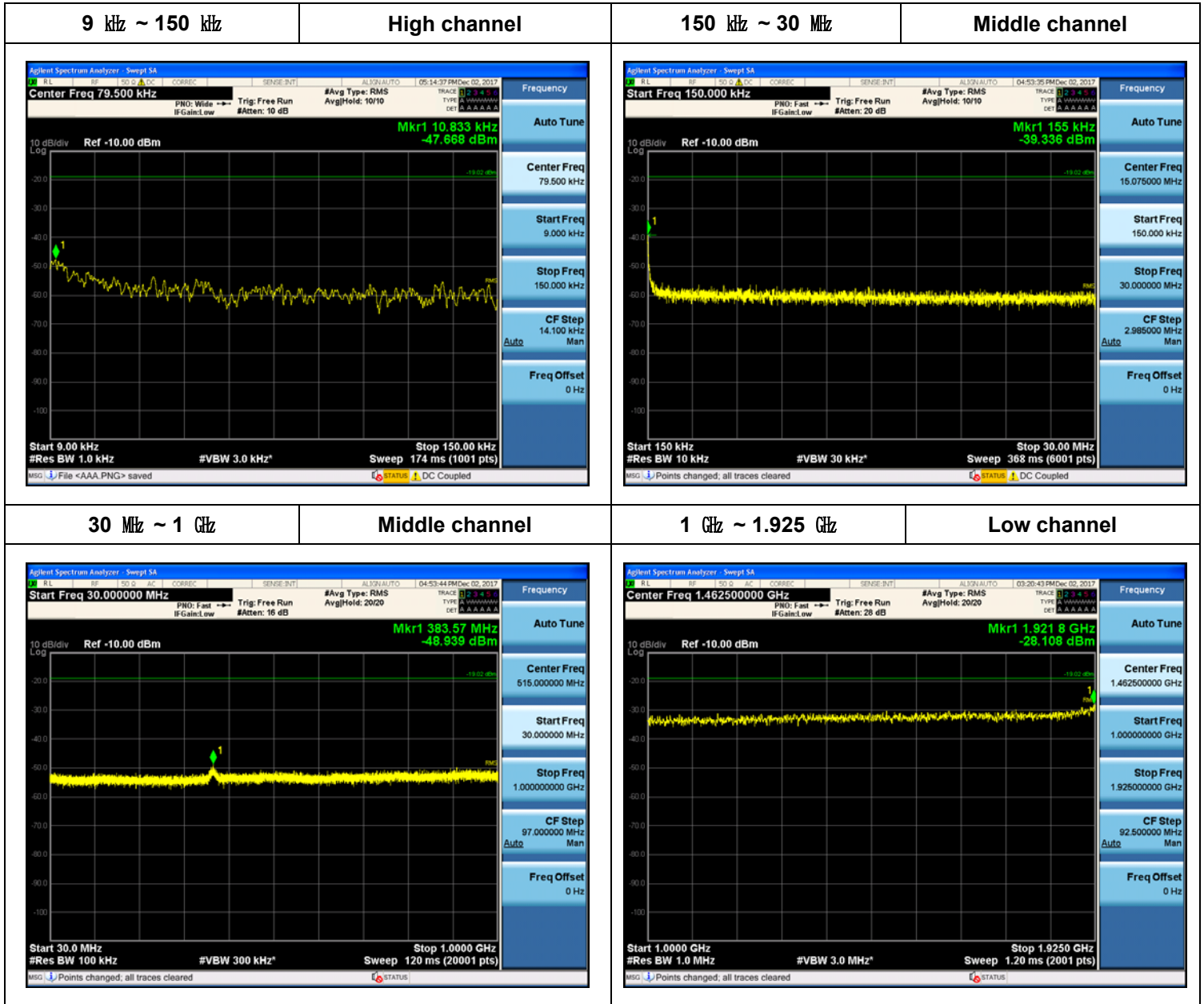


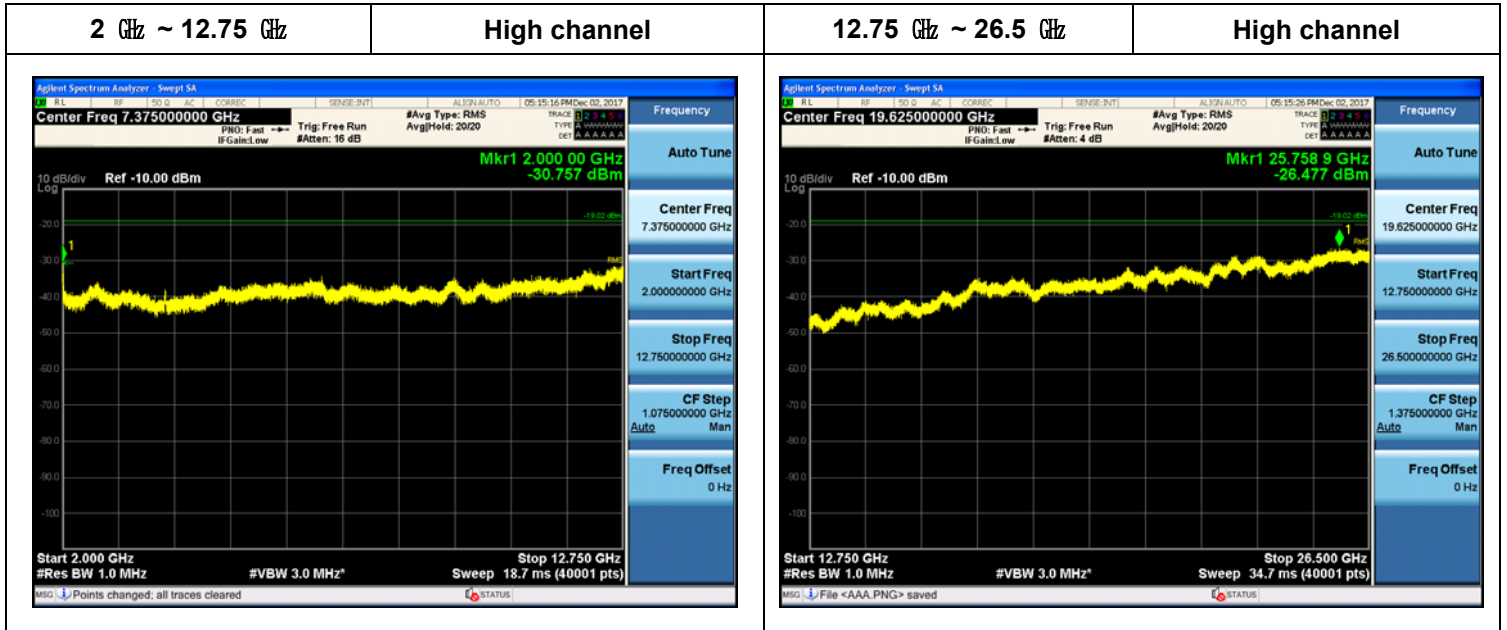
**Plot Data for Output Port 2\_QPSK**



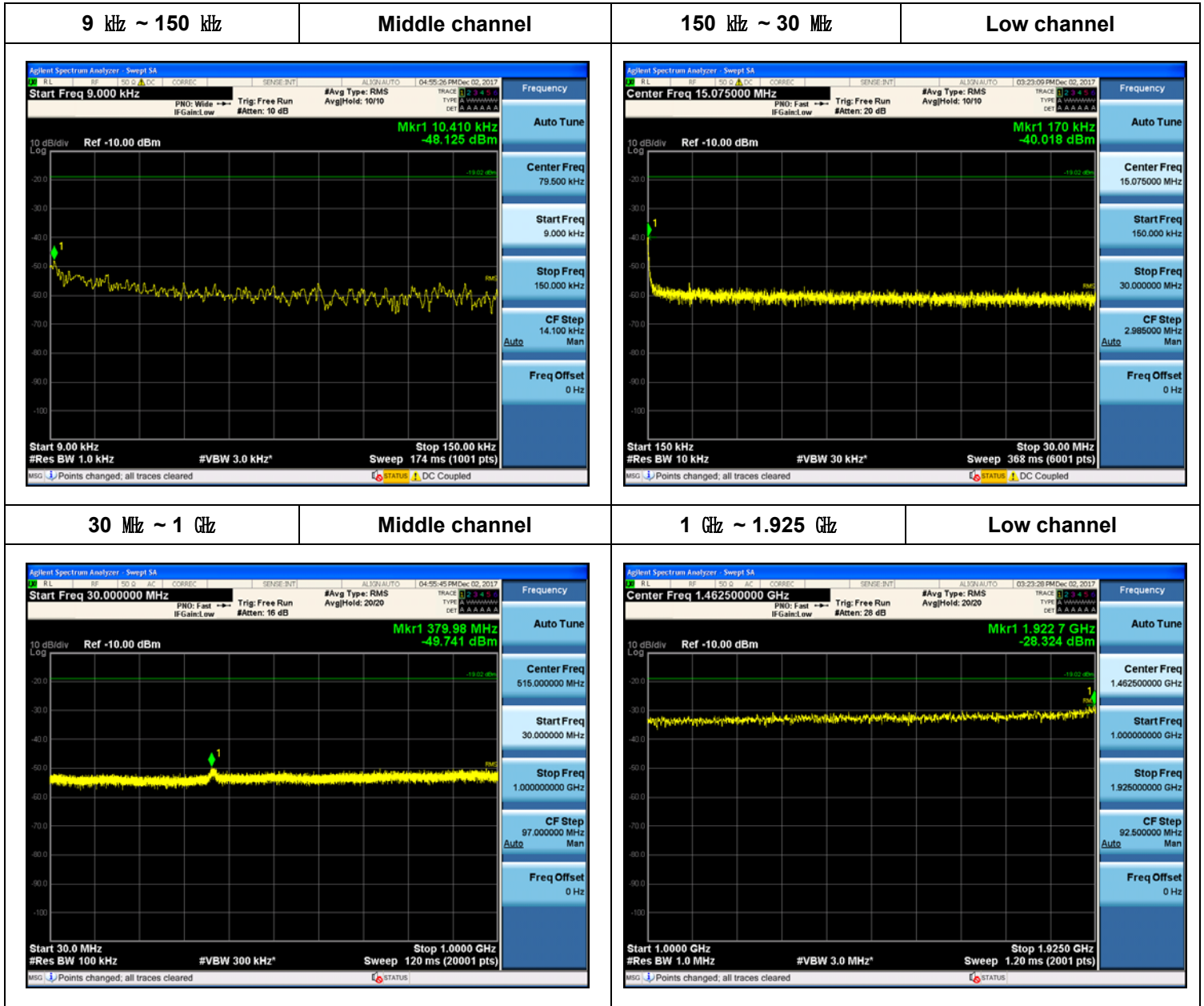


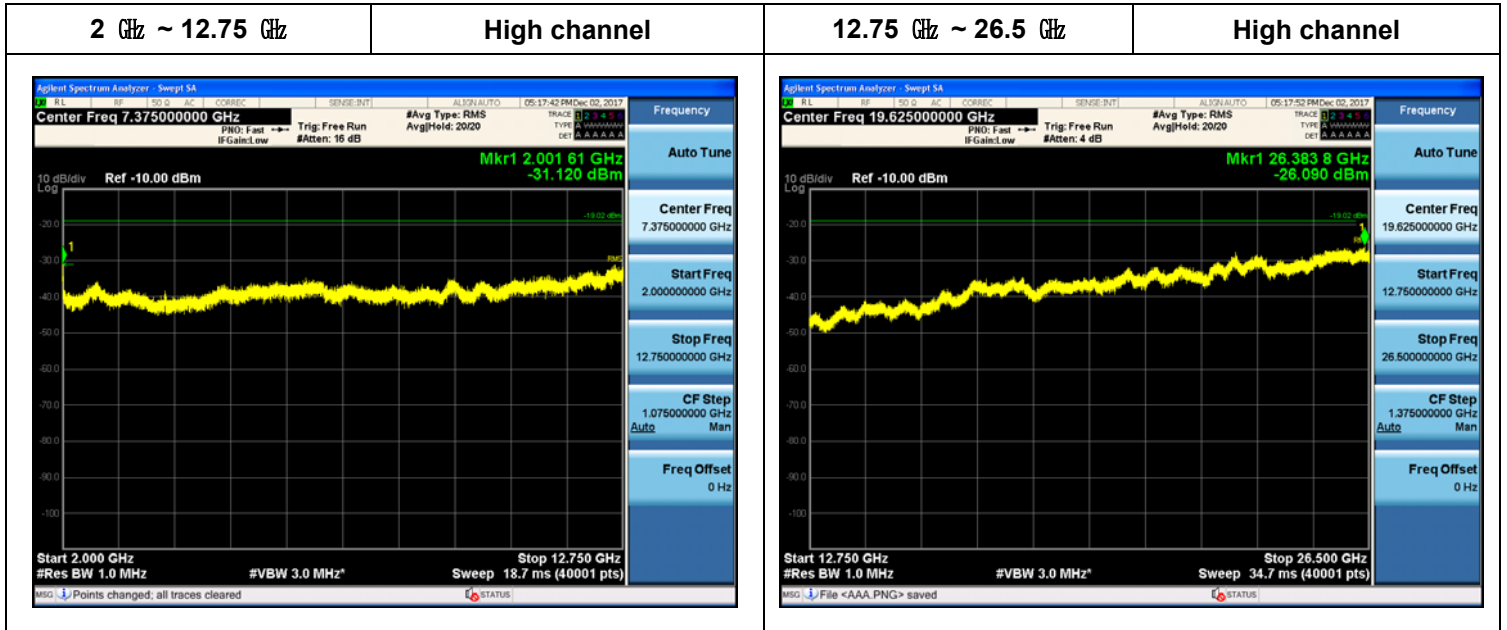
**Plot Data for Output Port 2\_16QAM**





**Plot Data for Output Port 2\_64QAM**





**Plot Data for Output Port 2\_256QAM**

