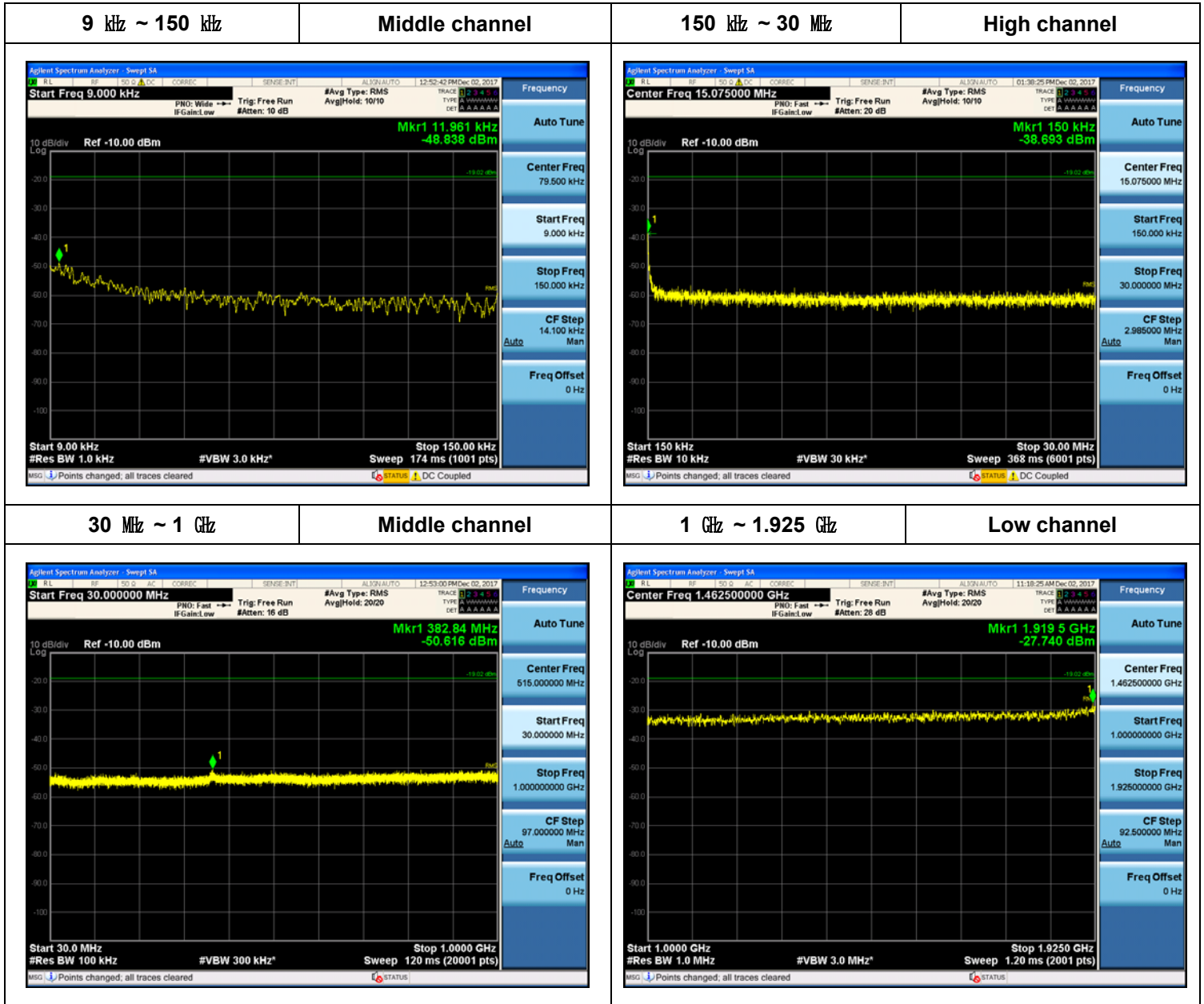
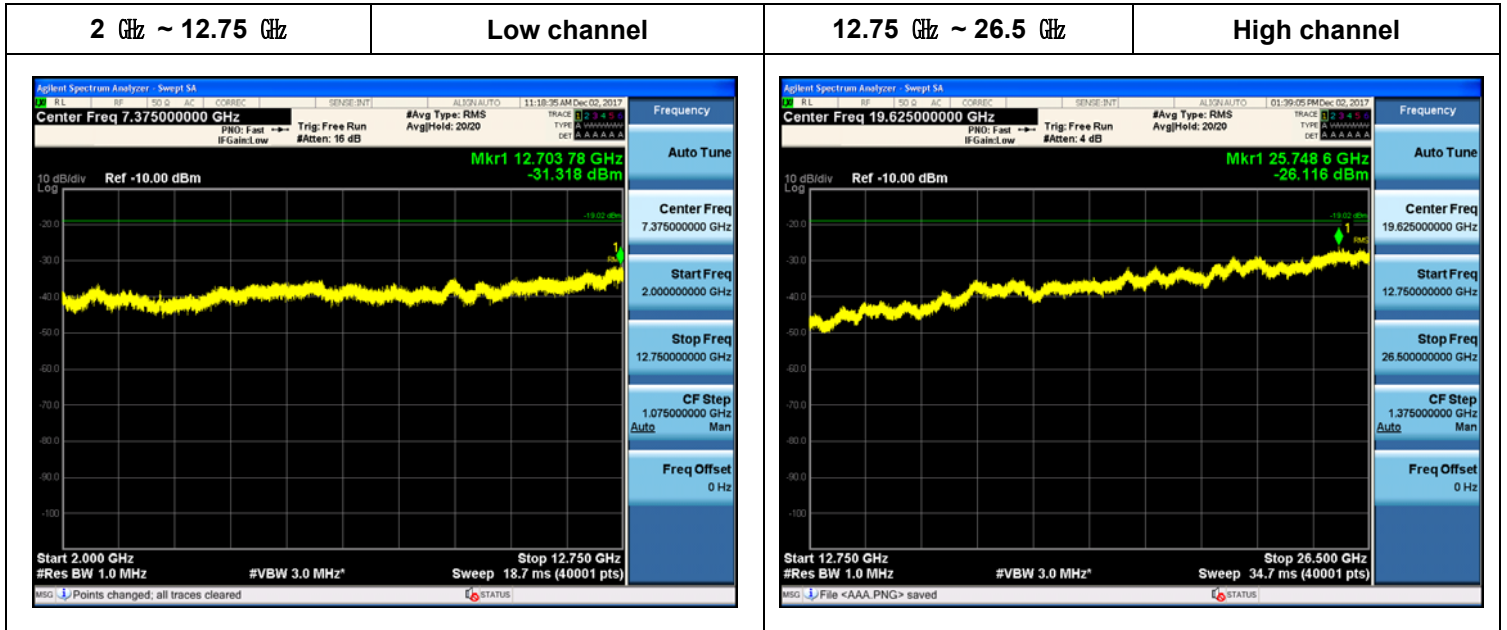
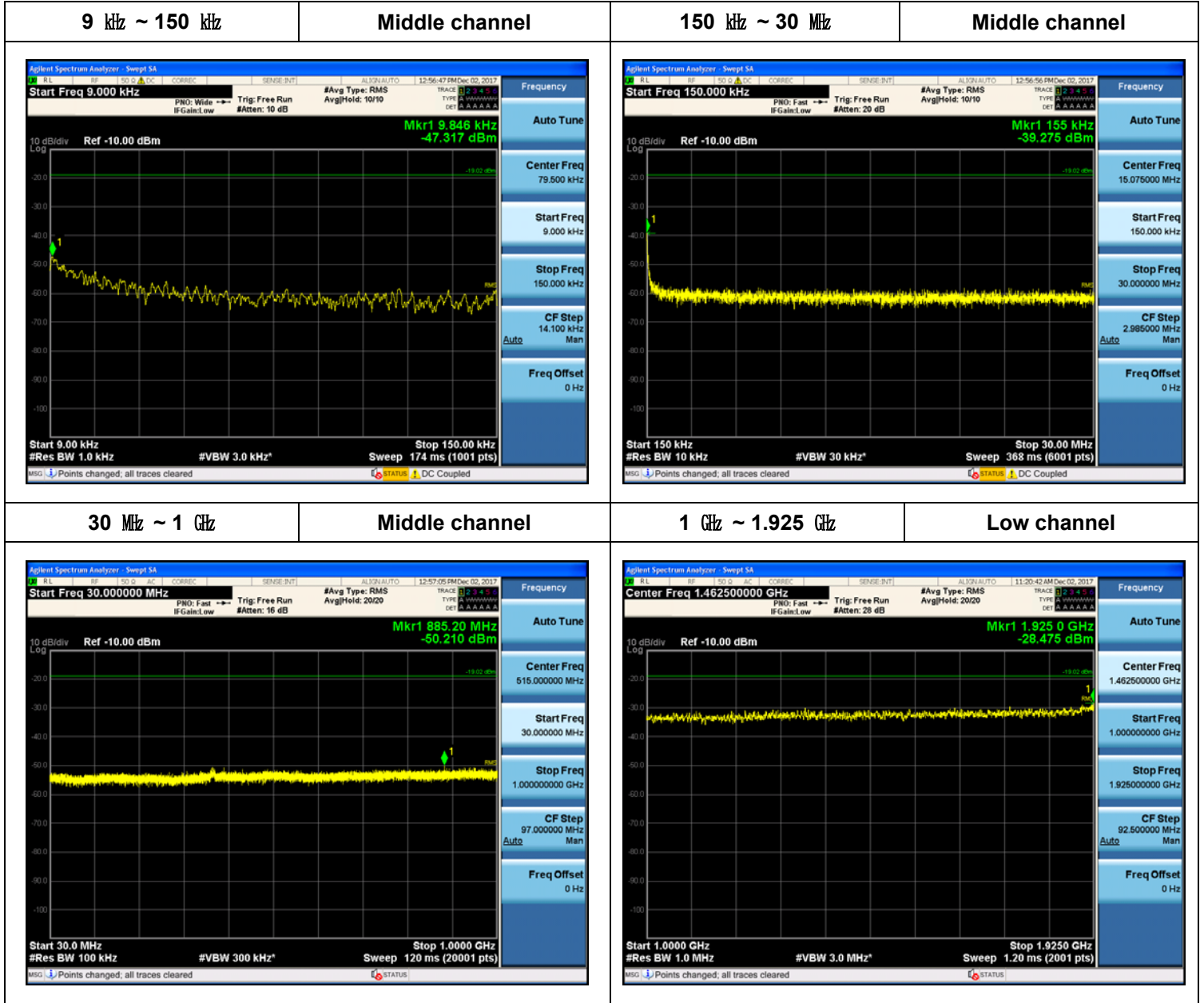


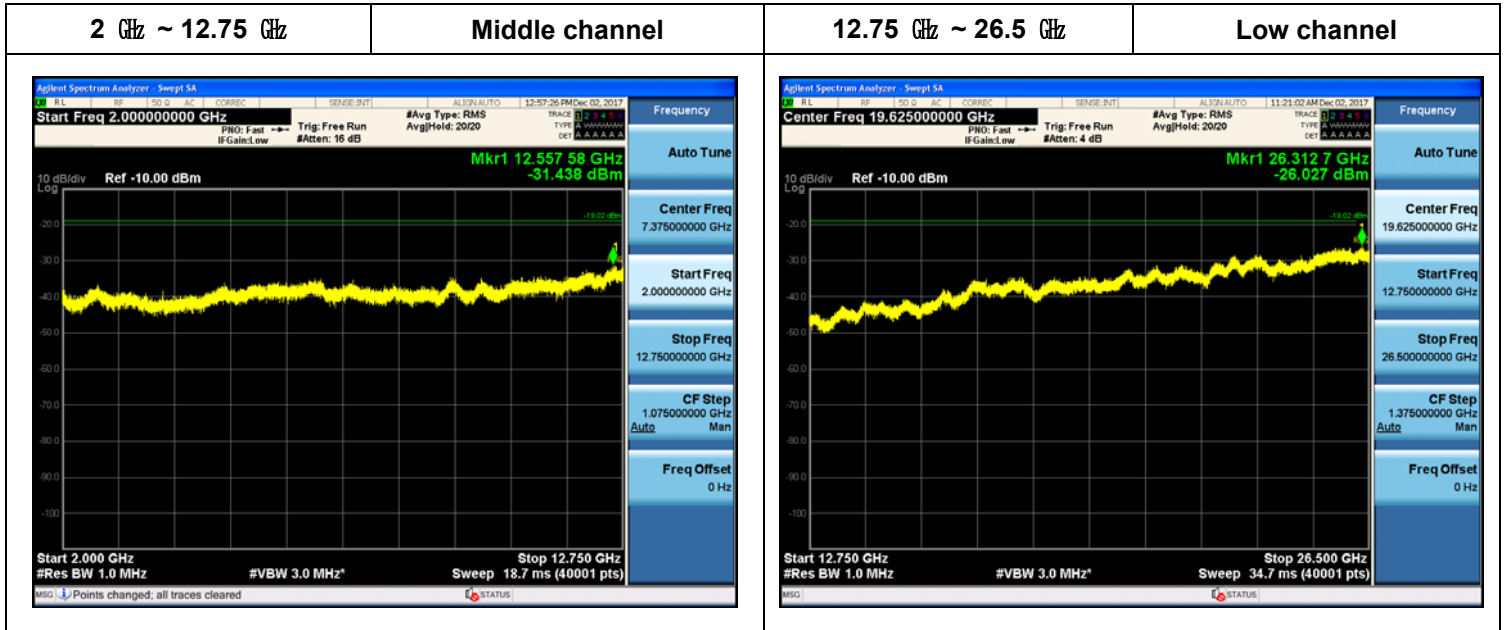
Plot Data for Output Port 1\_64QAM



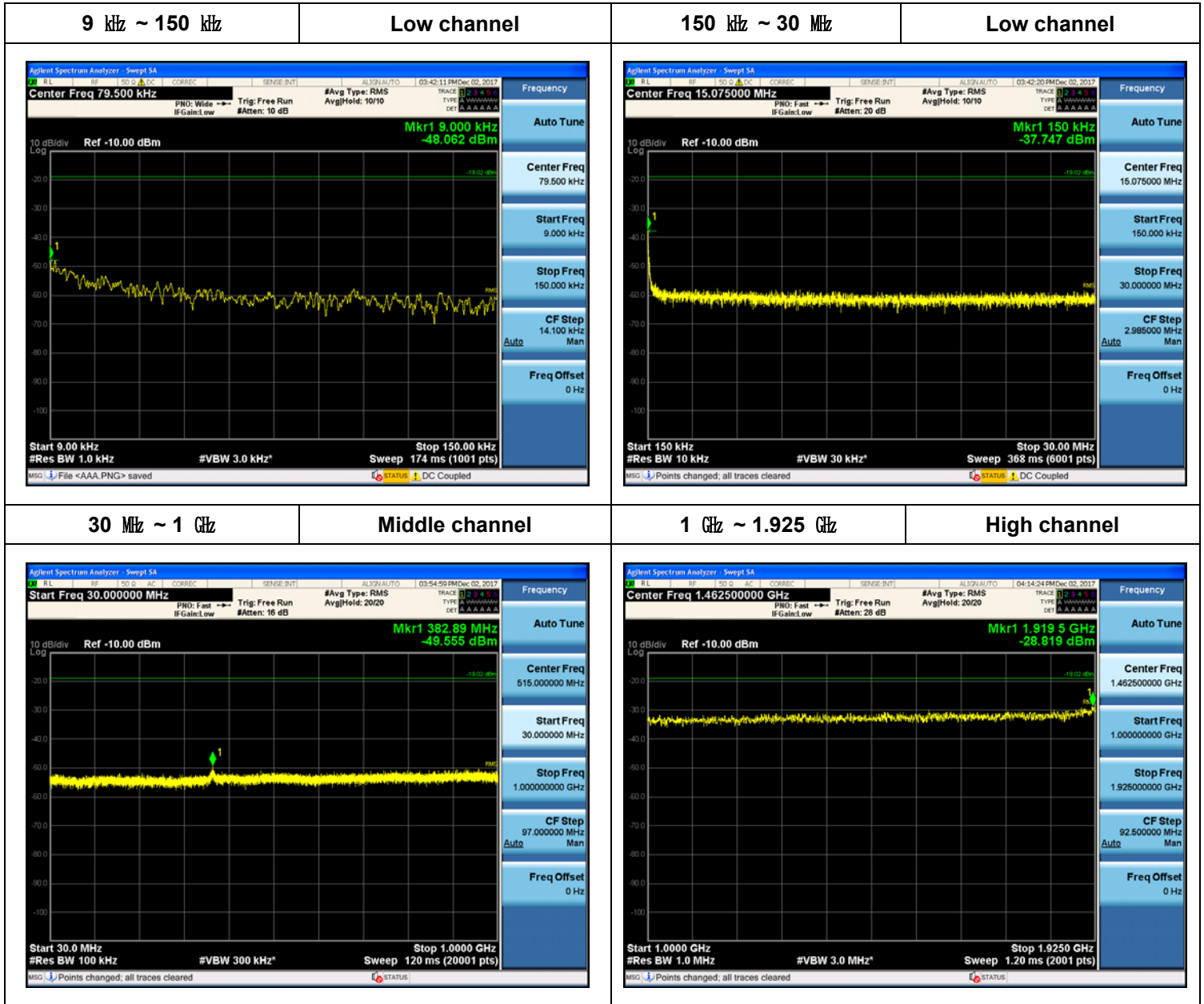


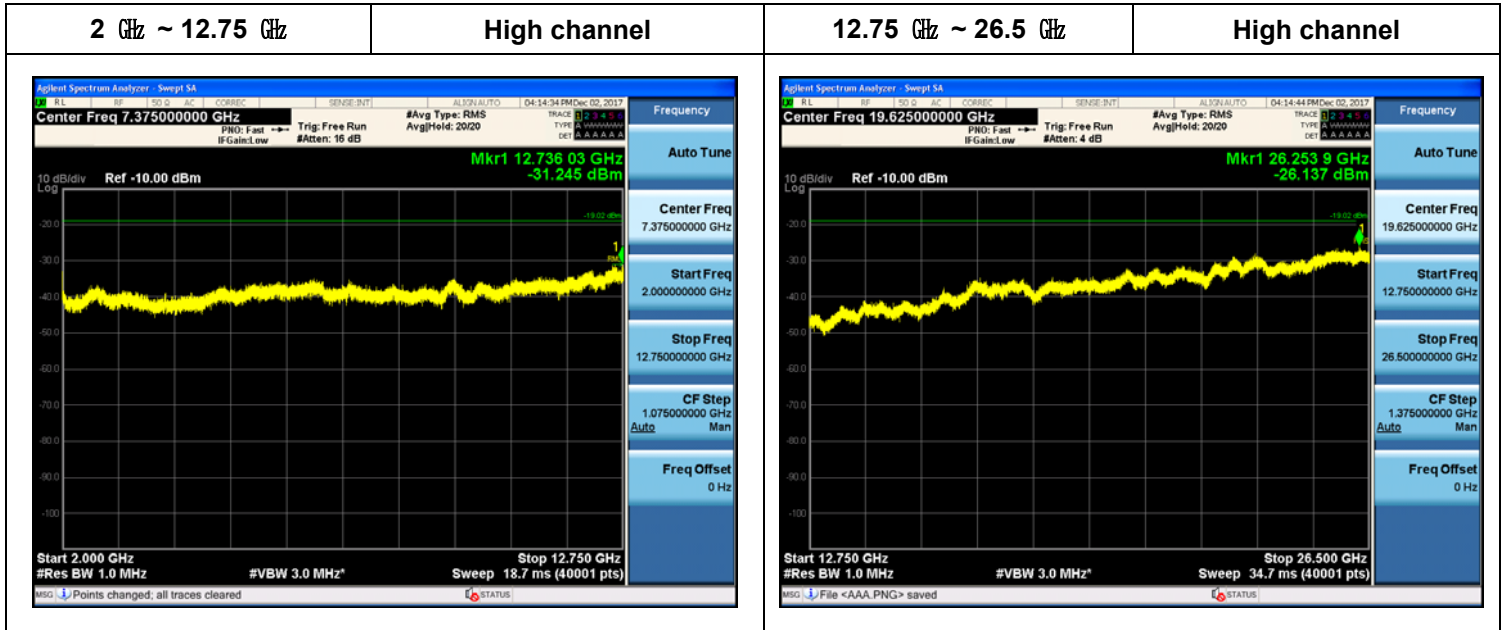
**Plot Data for Output Port 1\_256QAM**



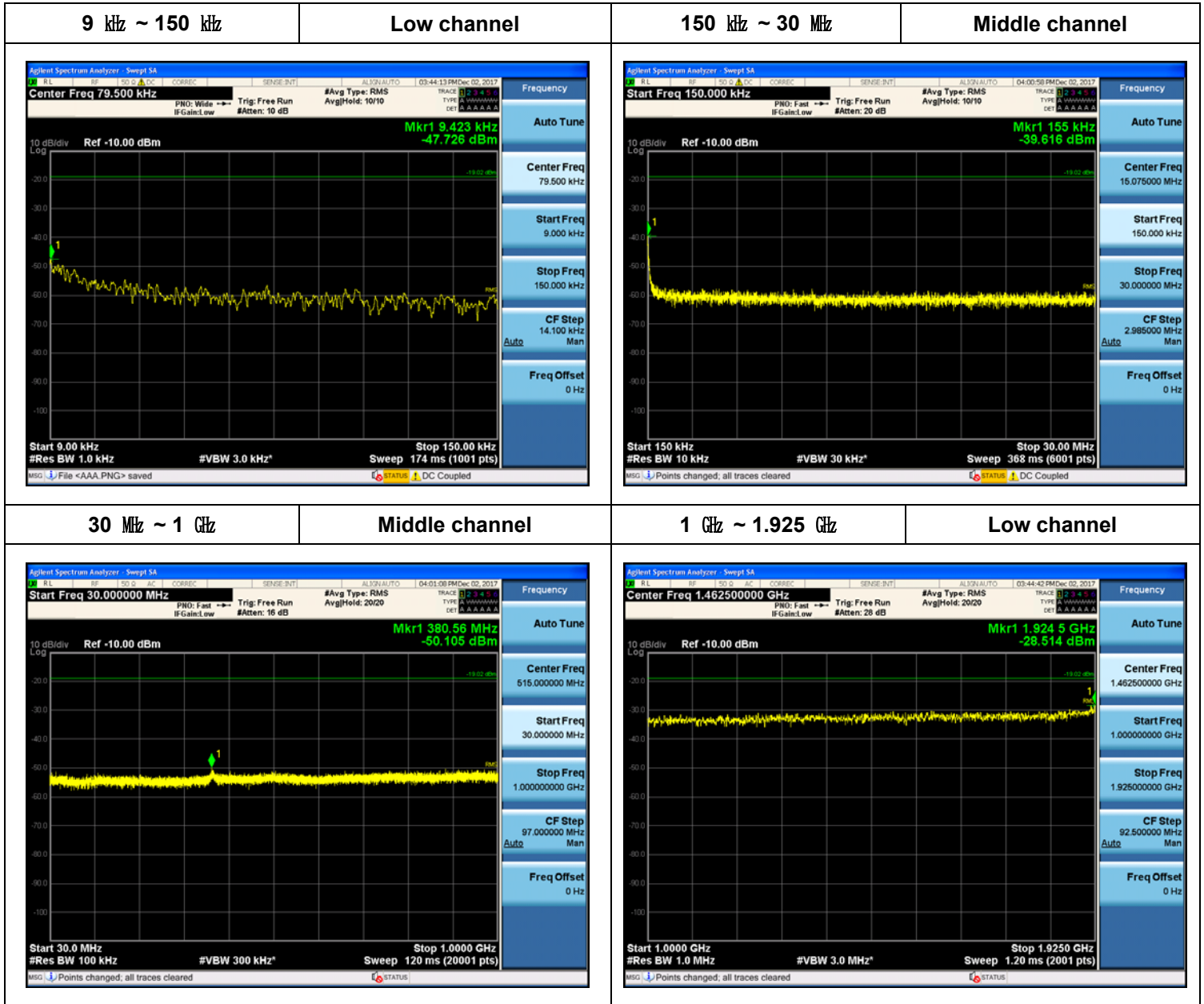


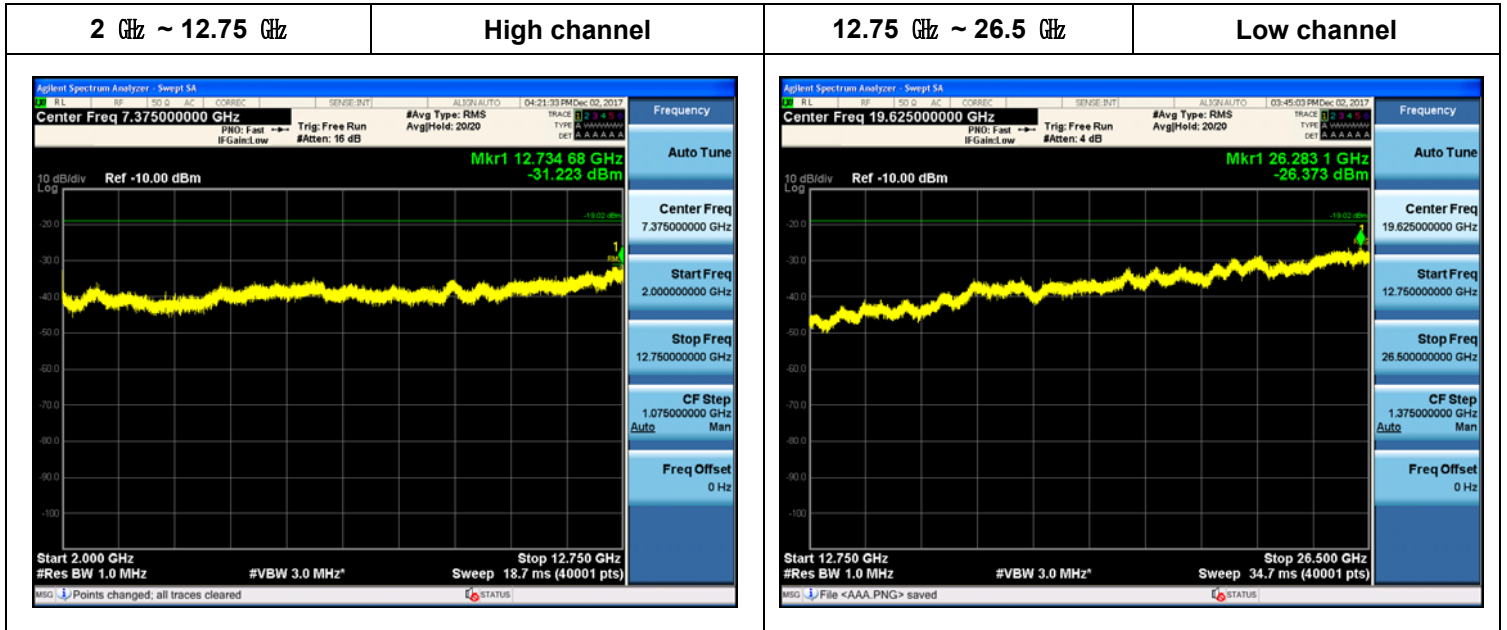
**Plot Data for Output Port 2\_QPSK**



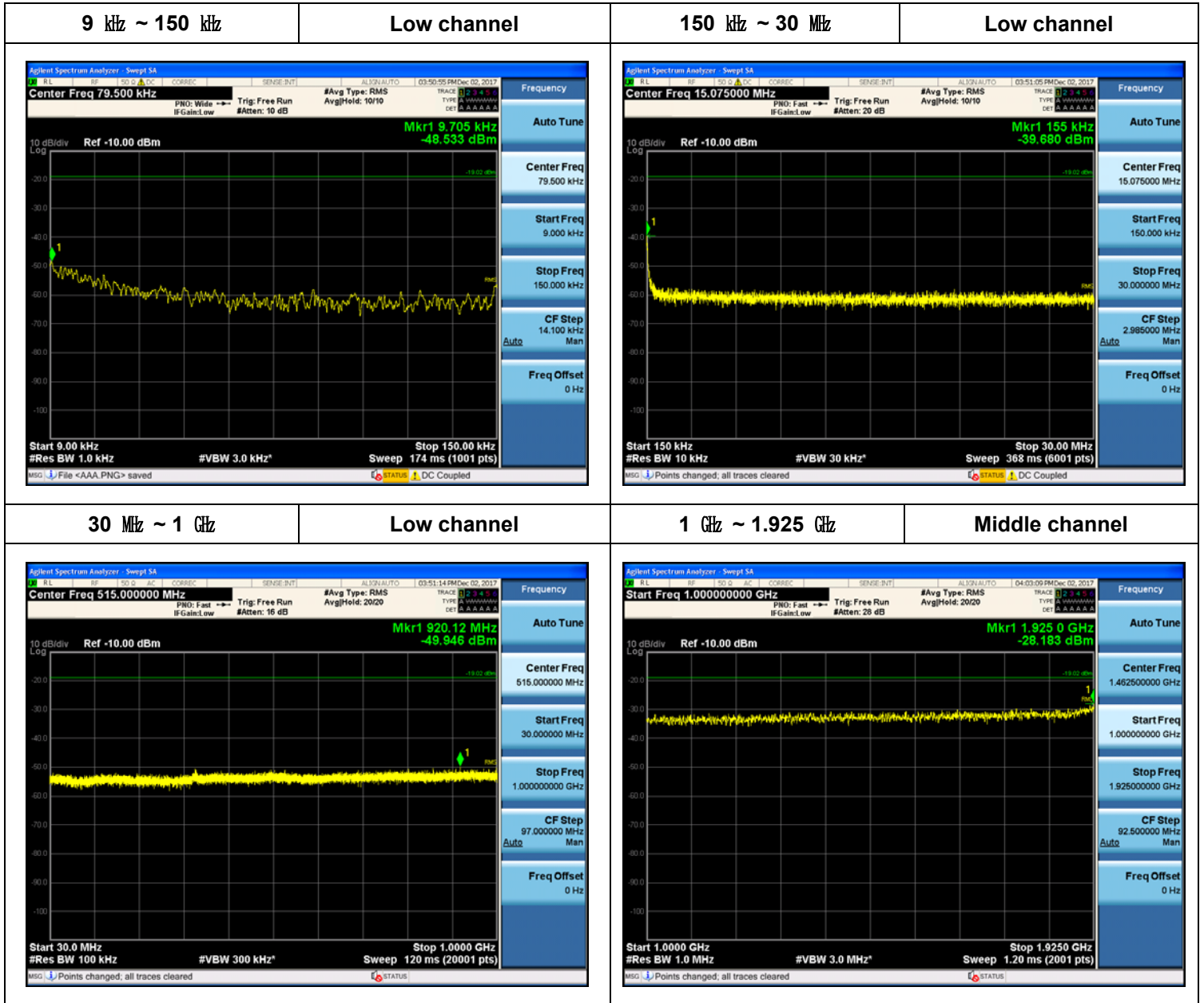


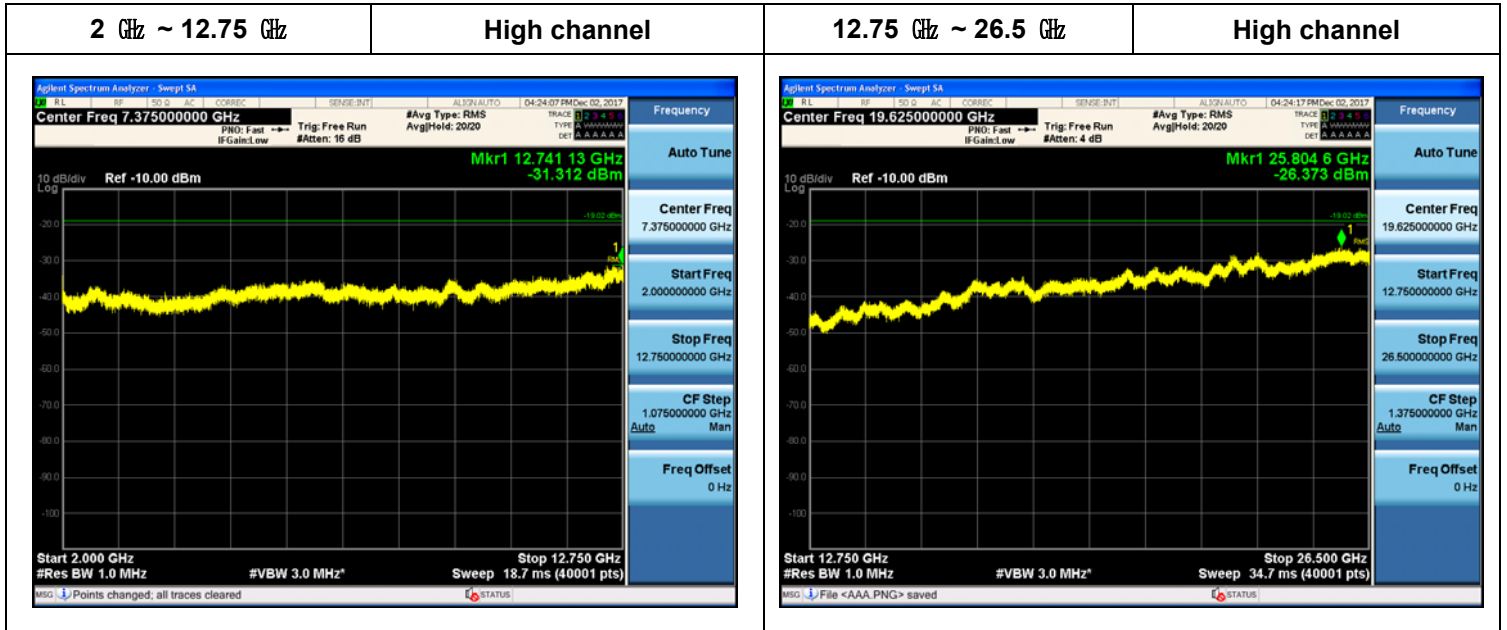
**Plot Data for Output Port 2\_16QAM**



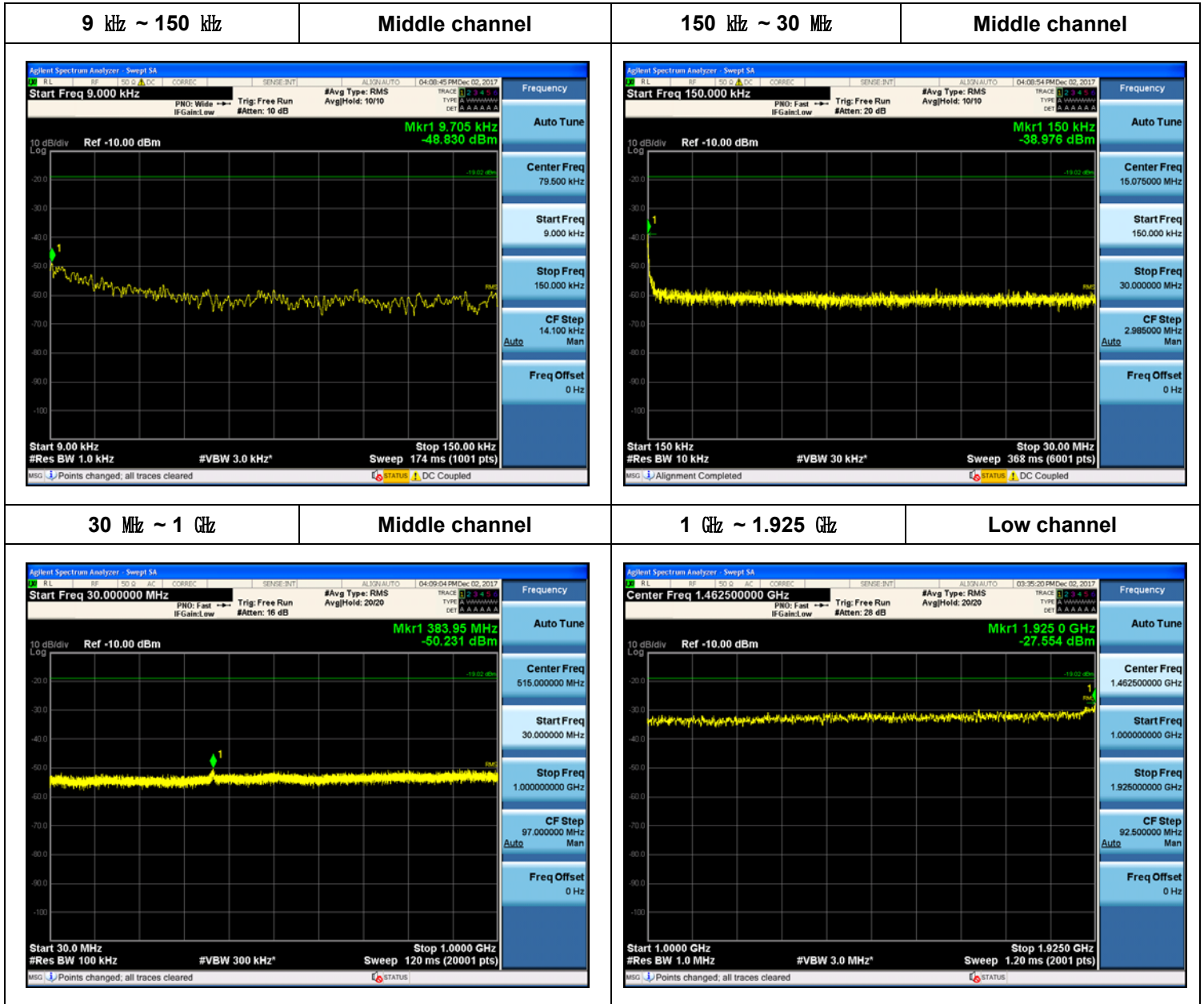


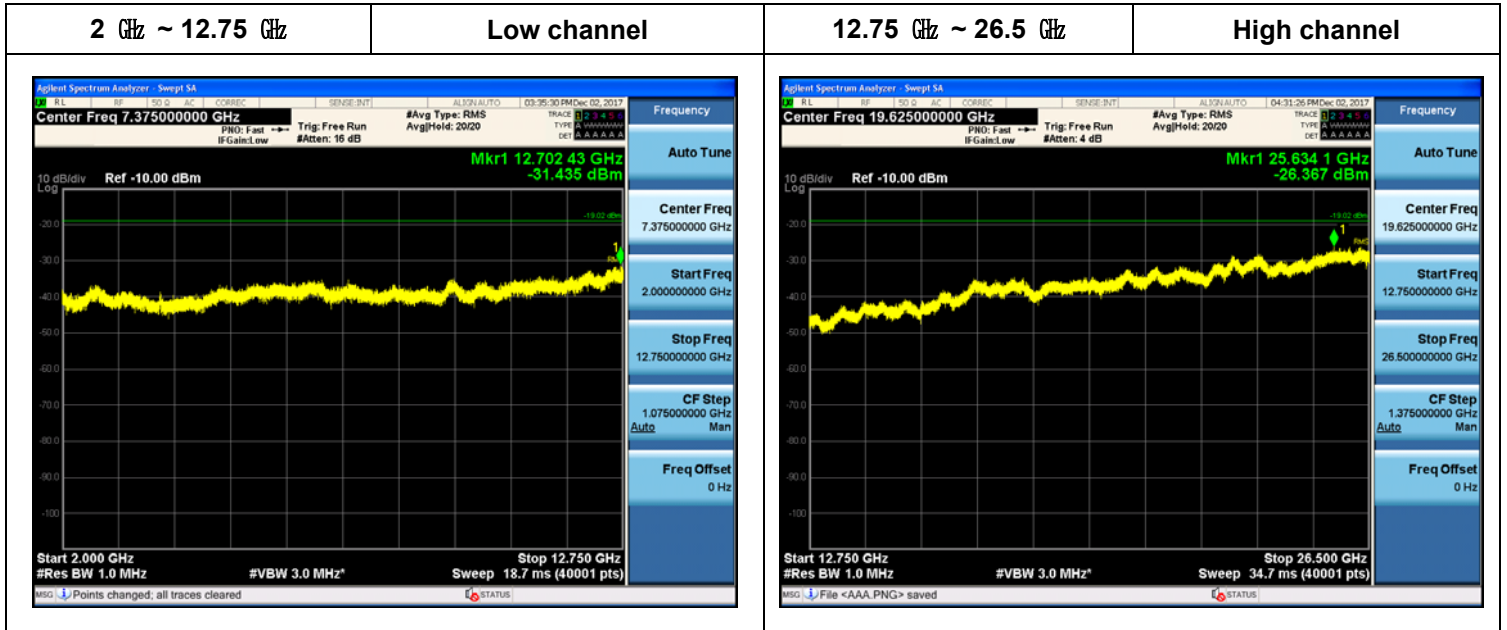
**Plot Data for Output Port 2\_64QAM**



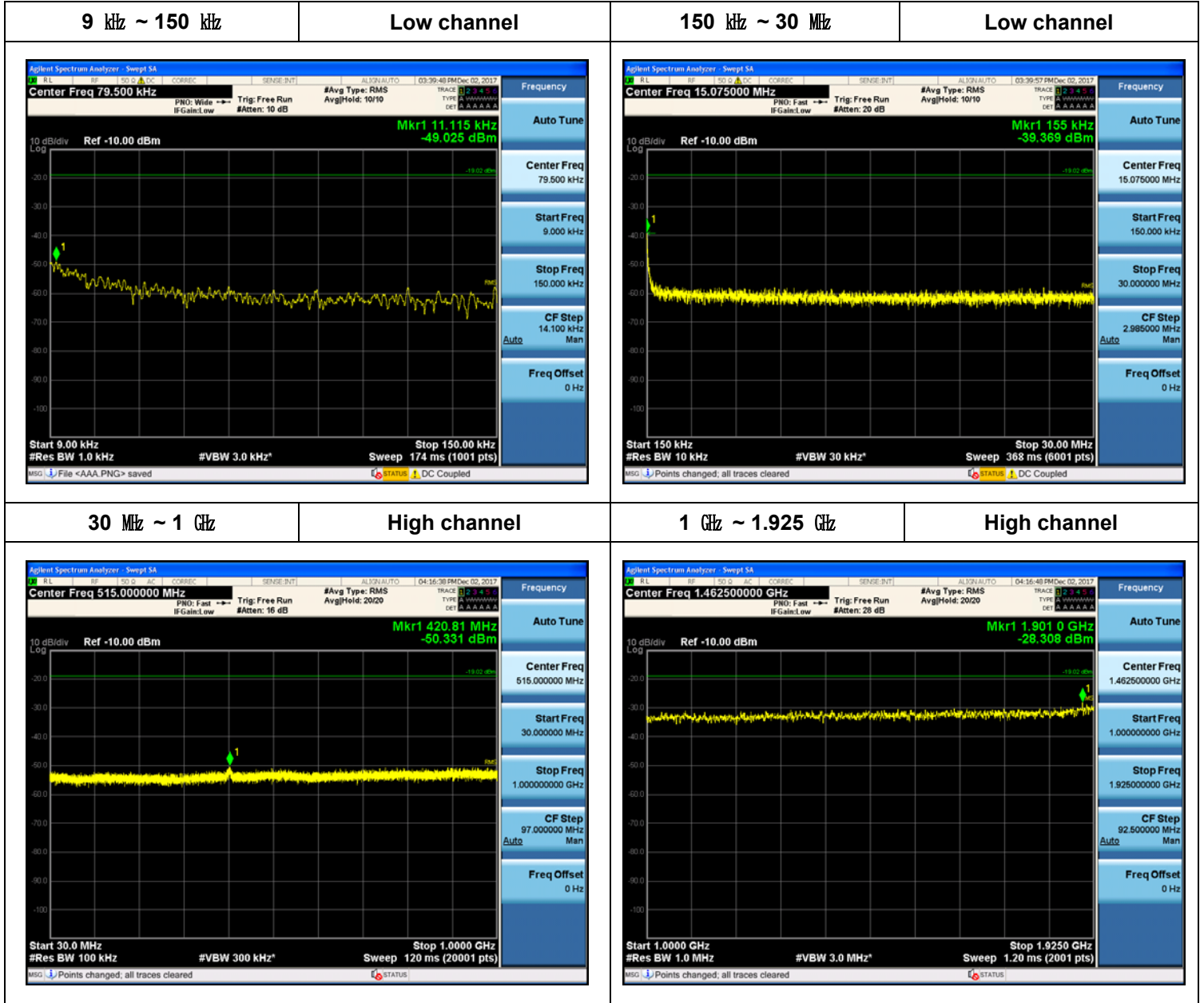


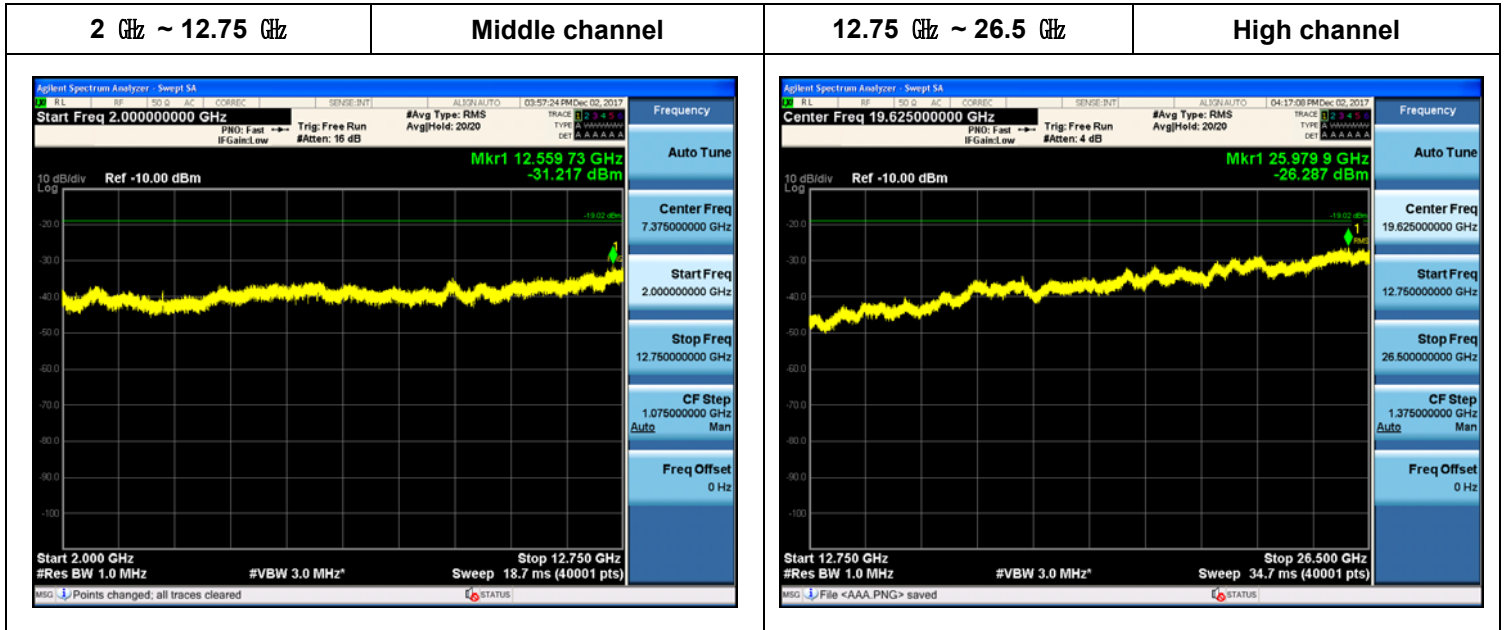
**Plot Data for Output Port 2\_256QAM**



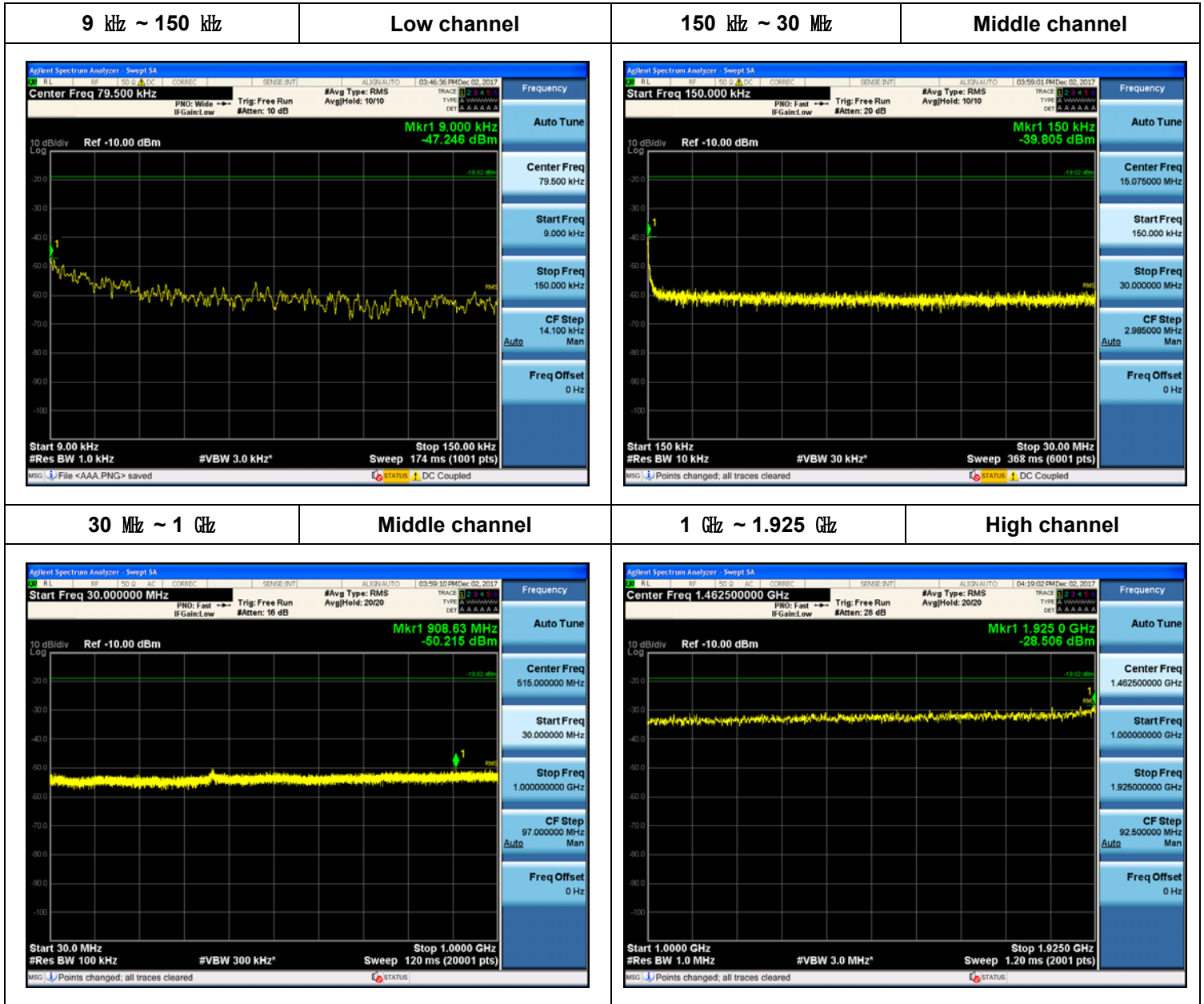


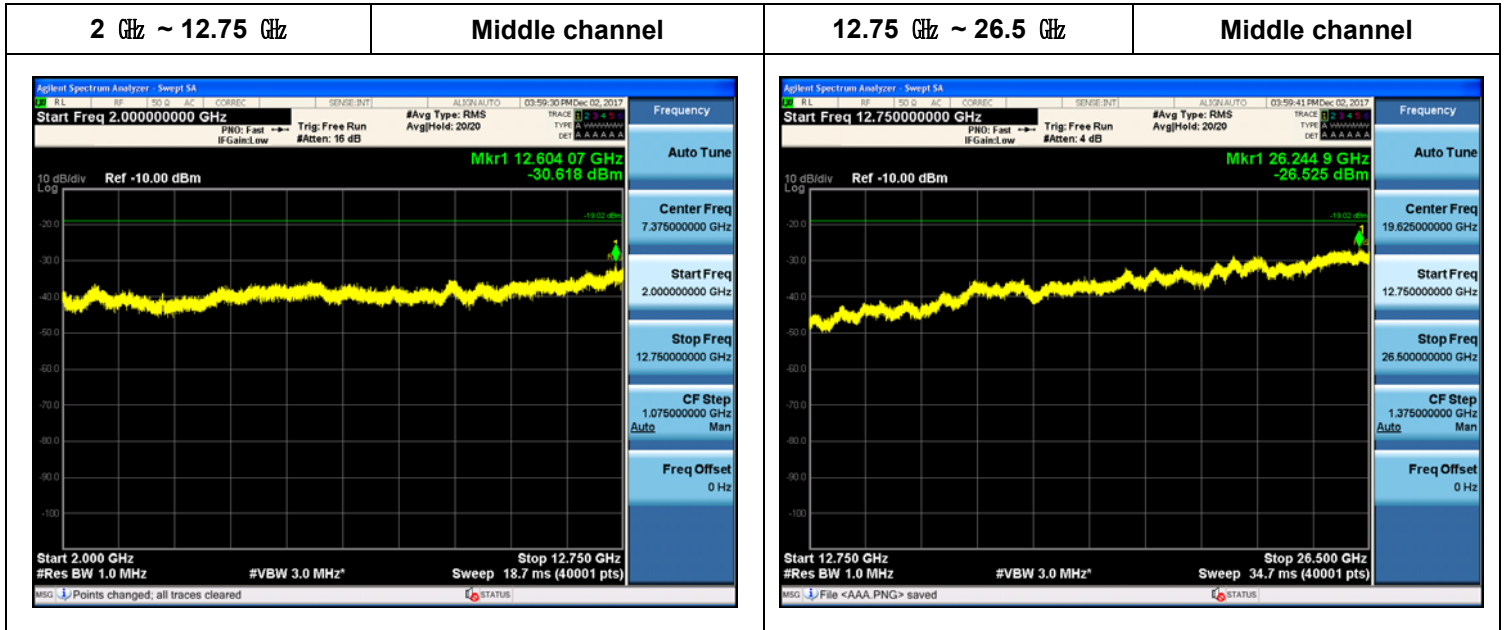
**Plot Data for Output Port 3\_QPSK**



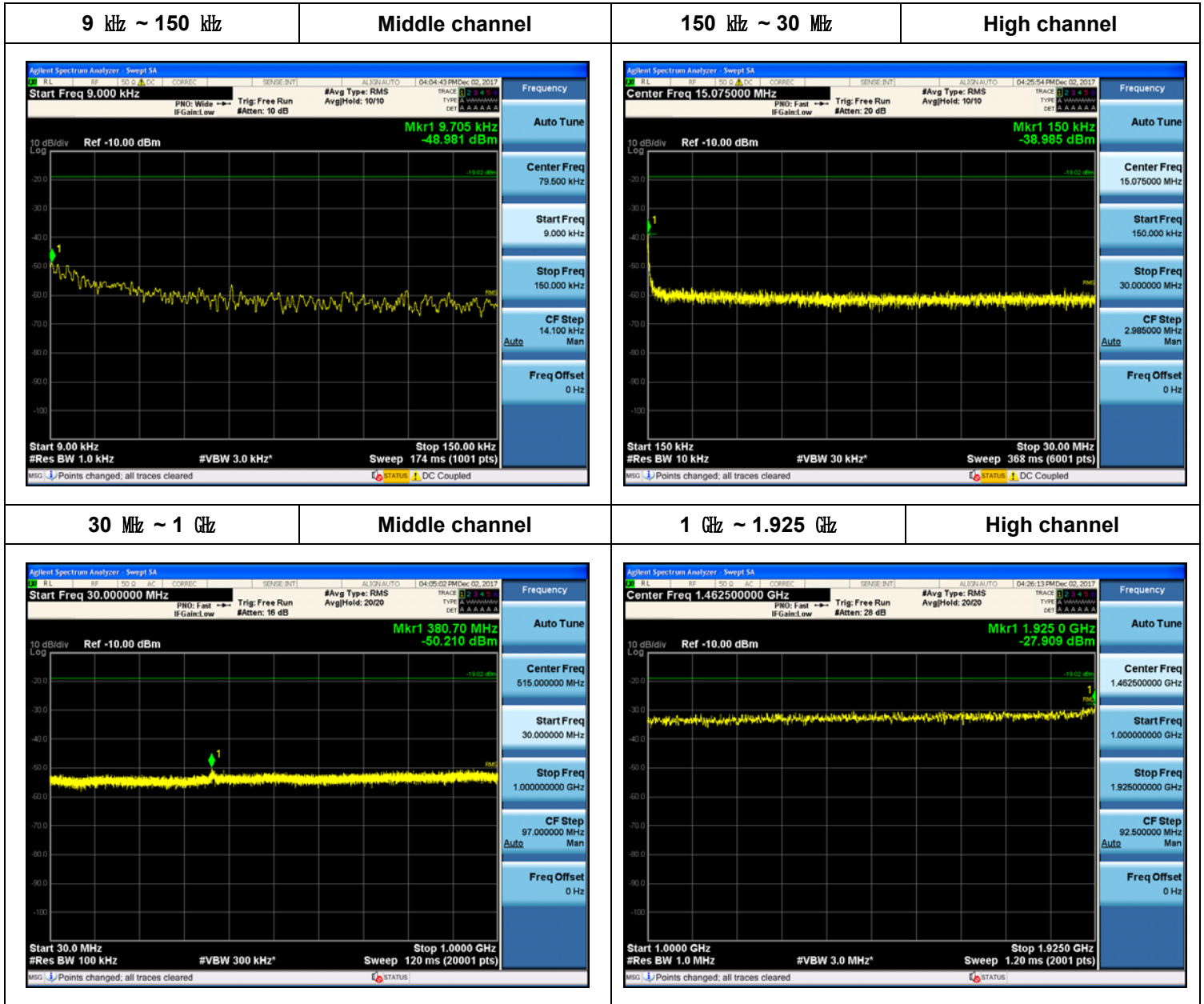


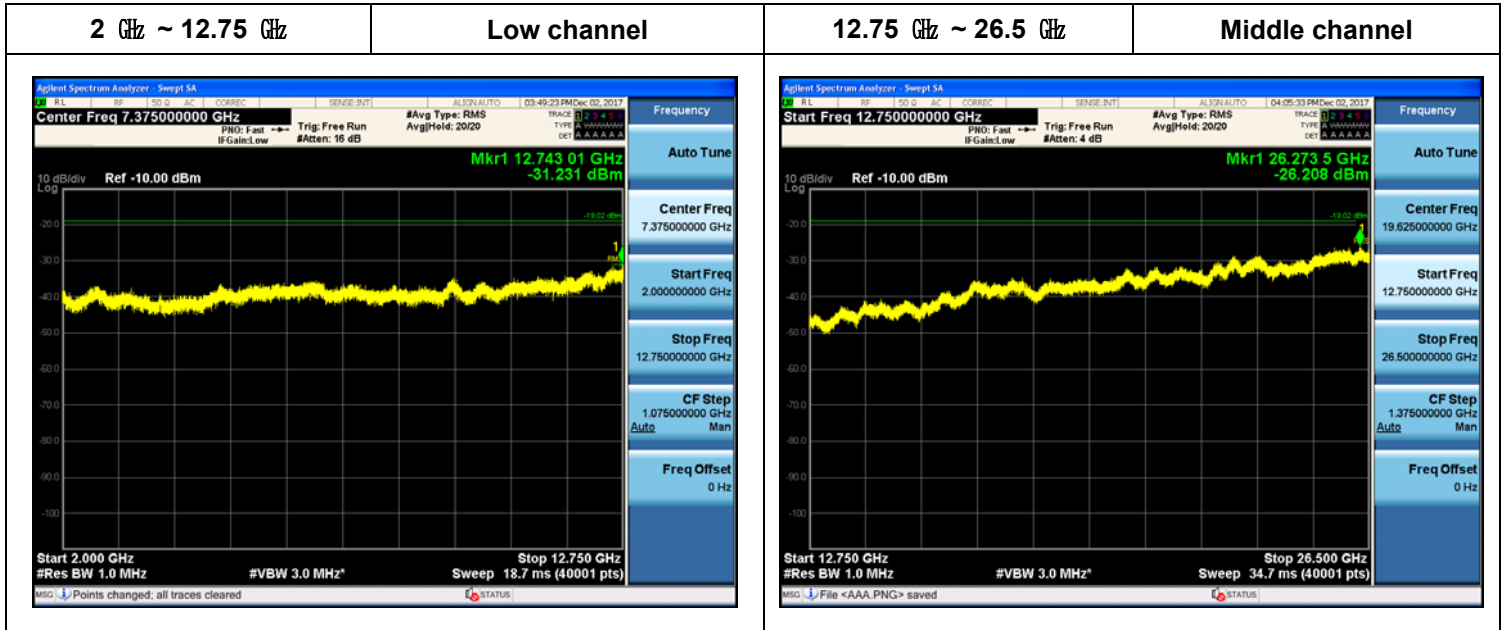
**Plot Data for Output Port 3\_16QAM**



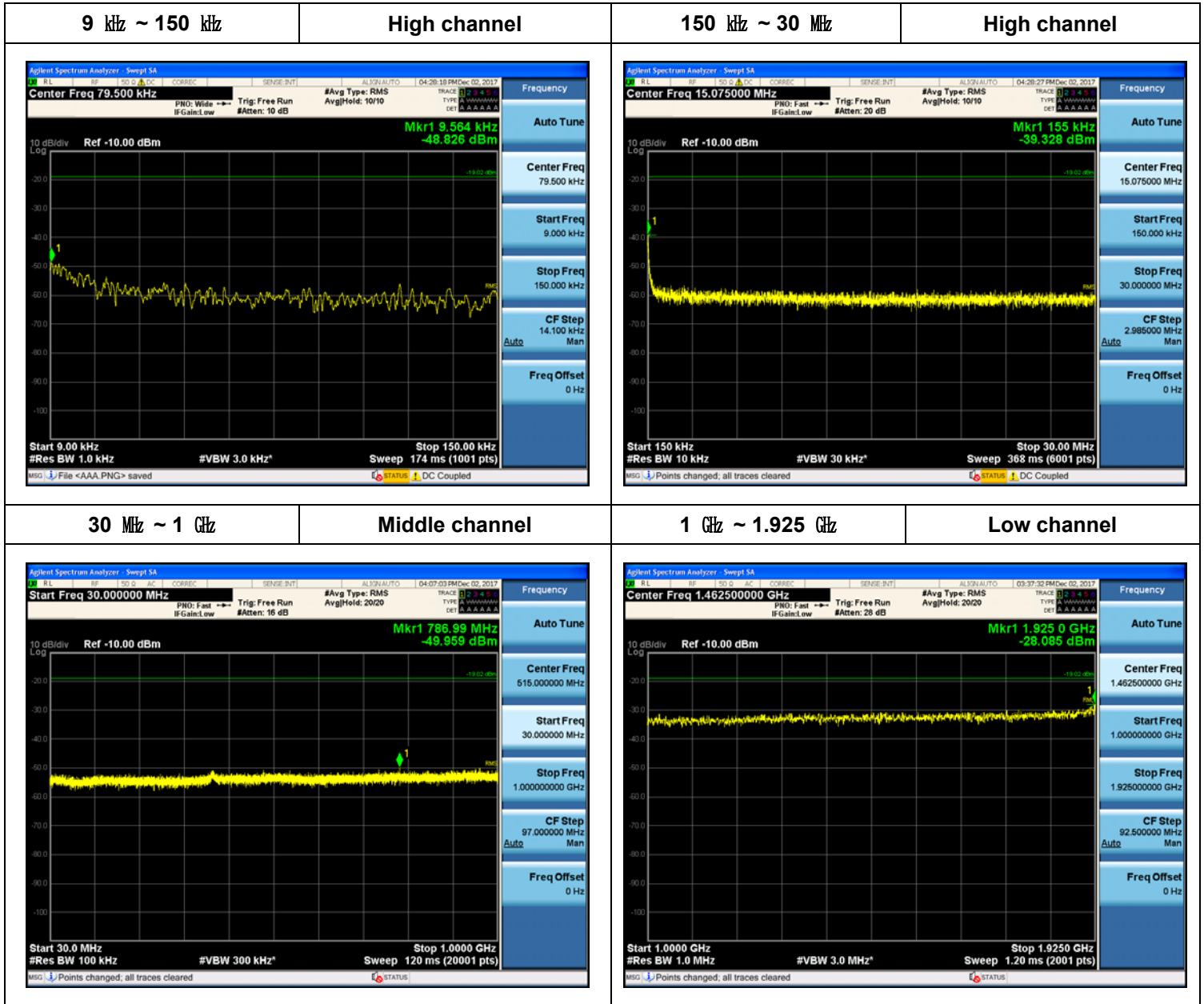


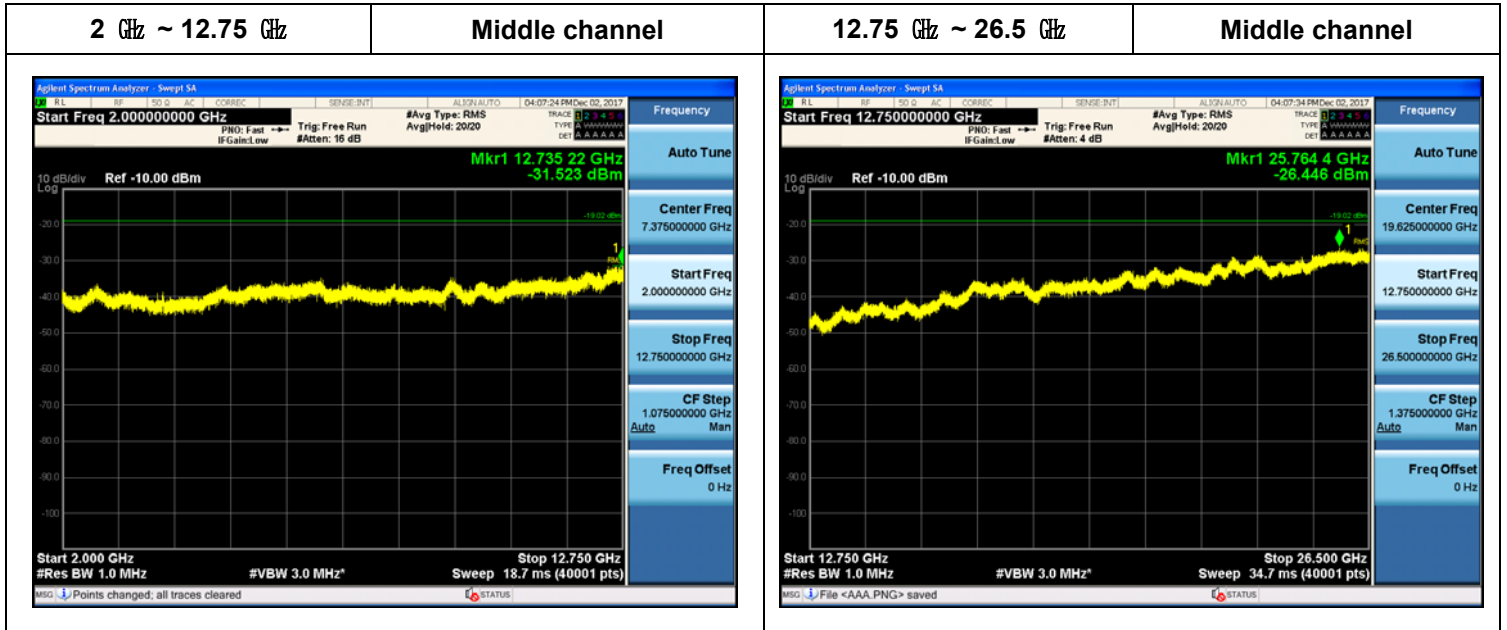
**Plot Data for Output Port 3\_64QAM**





**Plot Data for Output Port 3\_256QAM**





15 MHz + 5 MHz / 2 Carriers (30 W + 10 W)

Plot Data for Output Port 0\_QPSK

