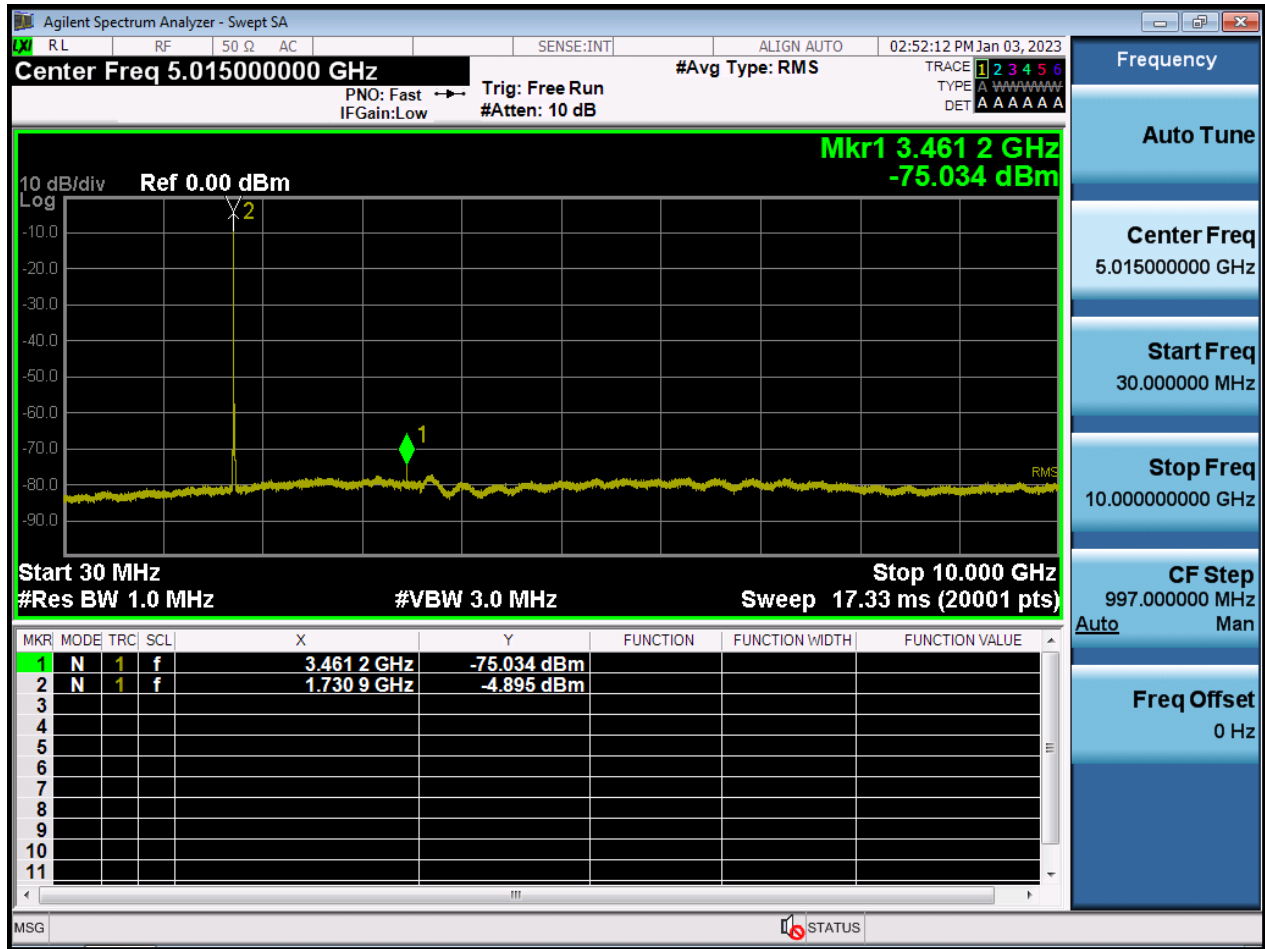
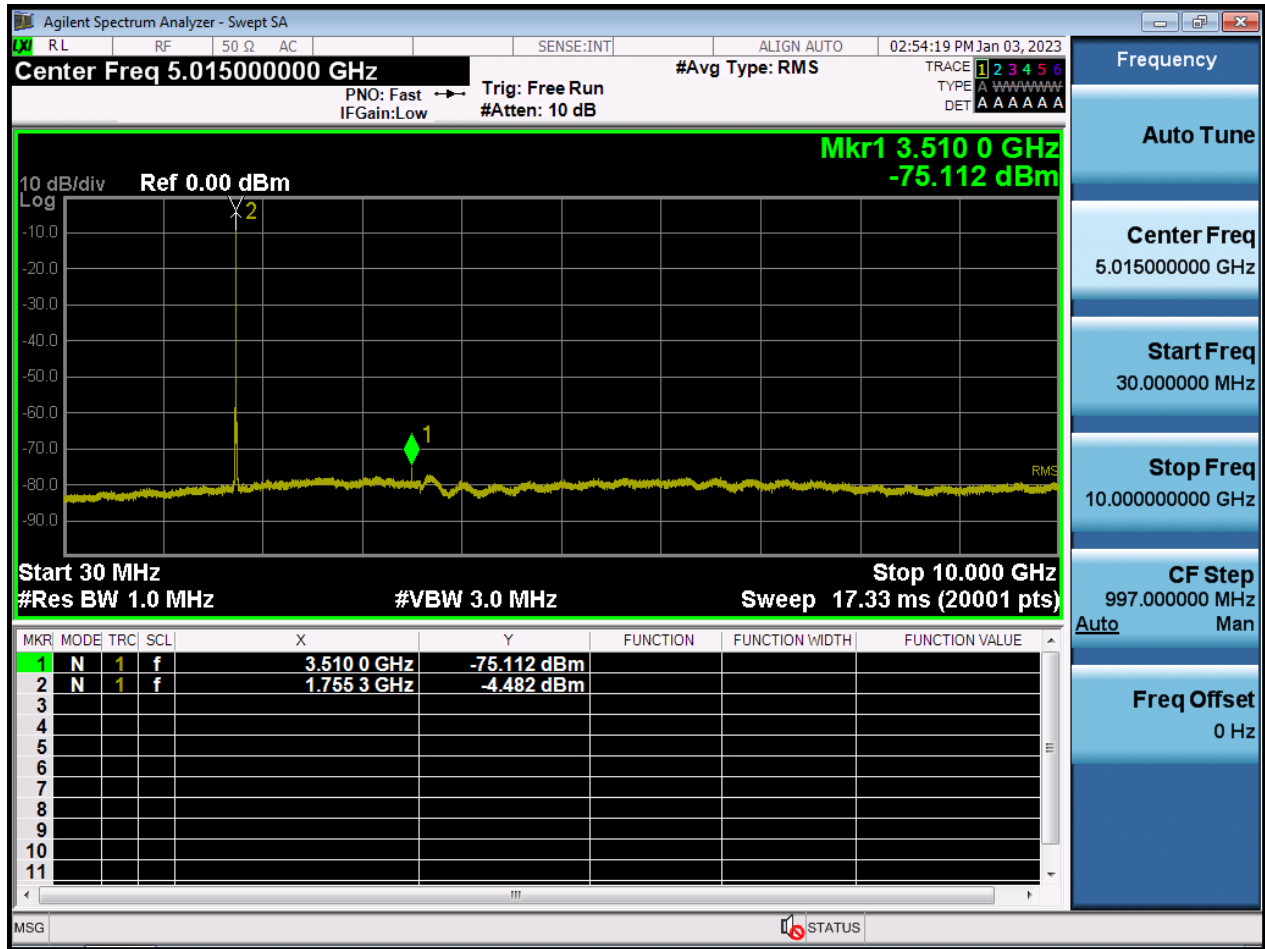


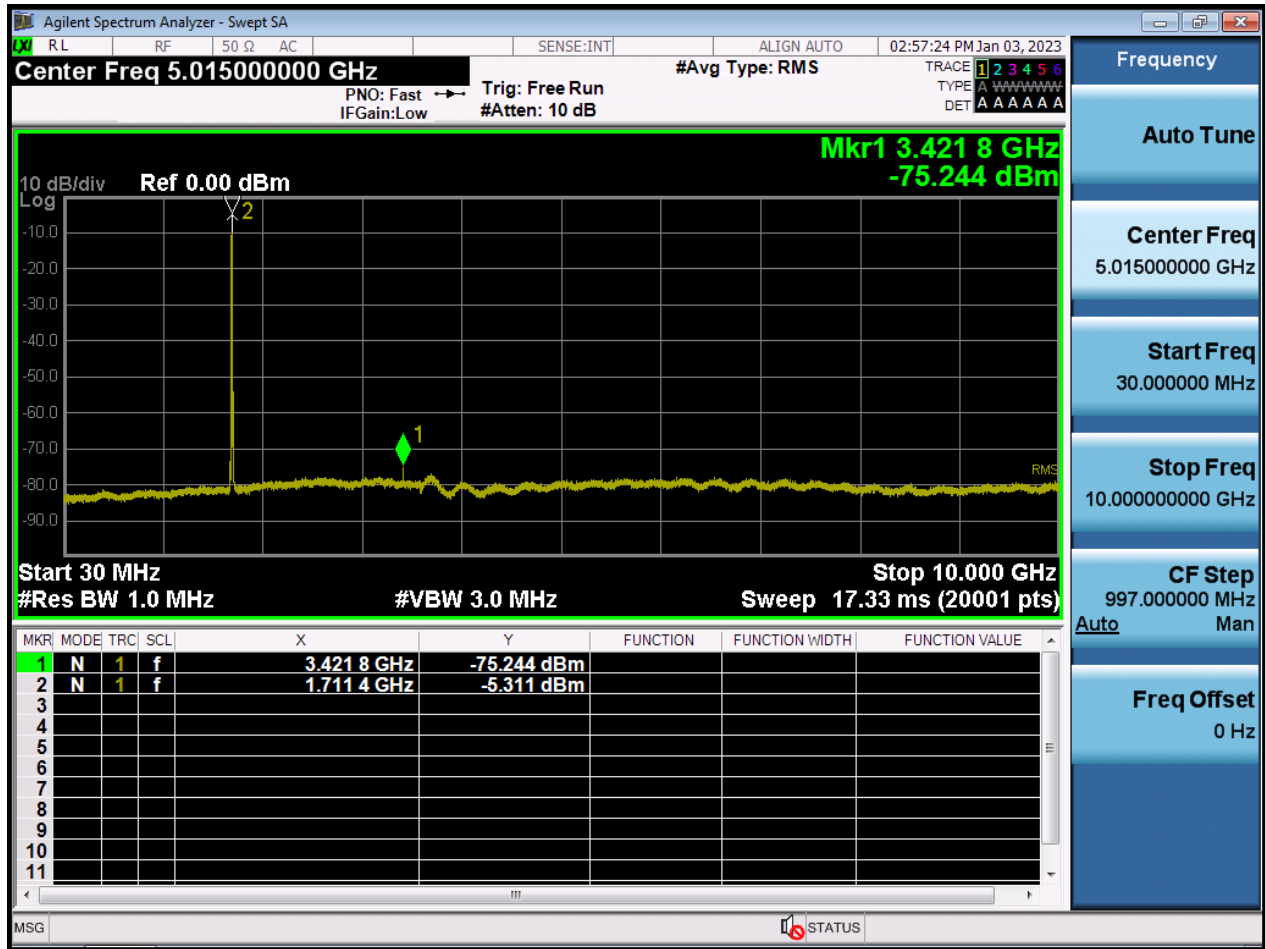
BW5 M_CSE(30 M-10 G)_Middle Channel_QPSK_1RB



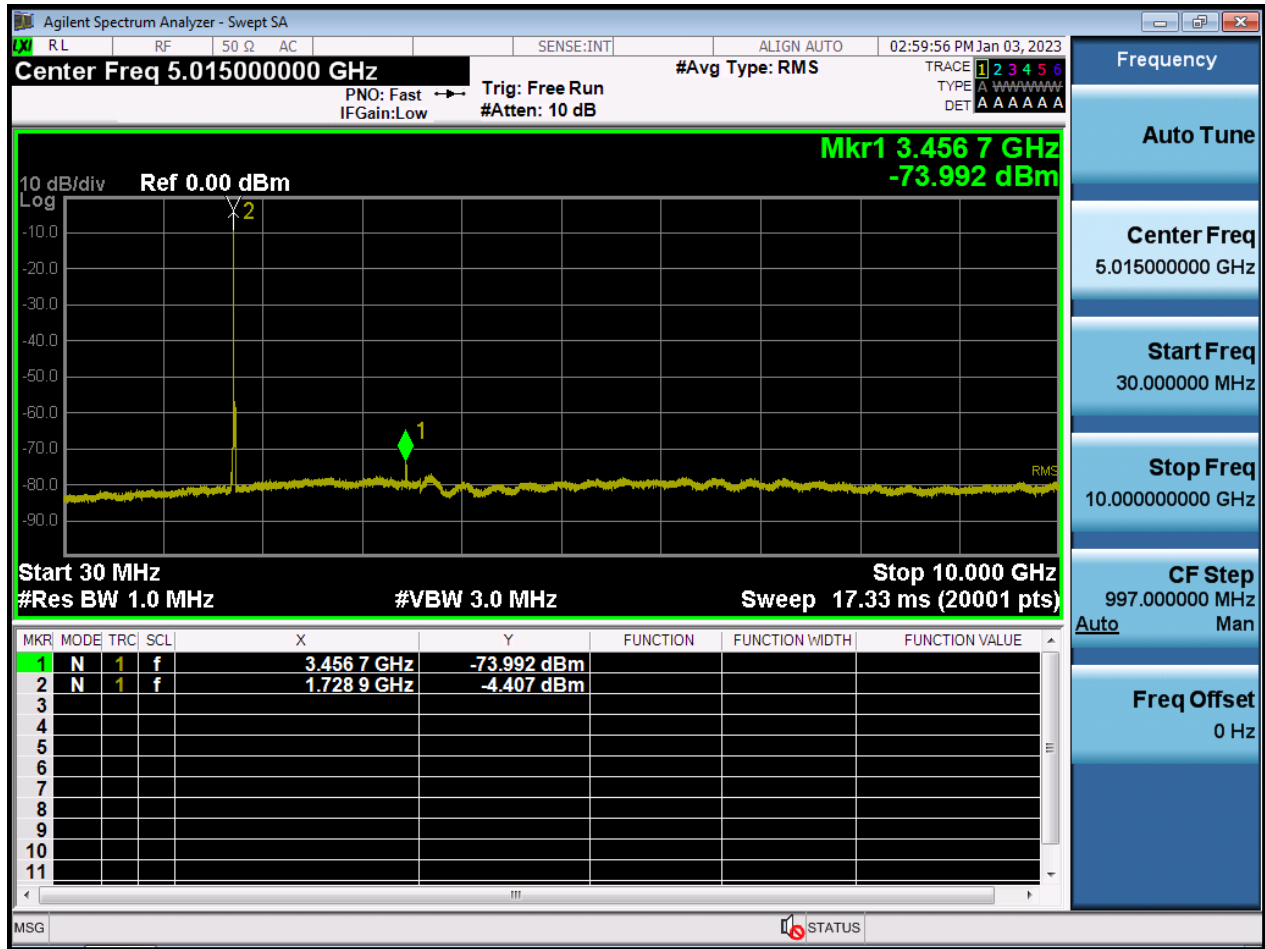
BW5 M_CSE(30 M-10 G)_Highest Channel_QPSK_1RB



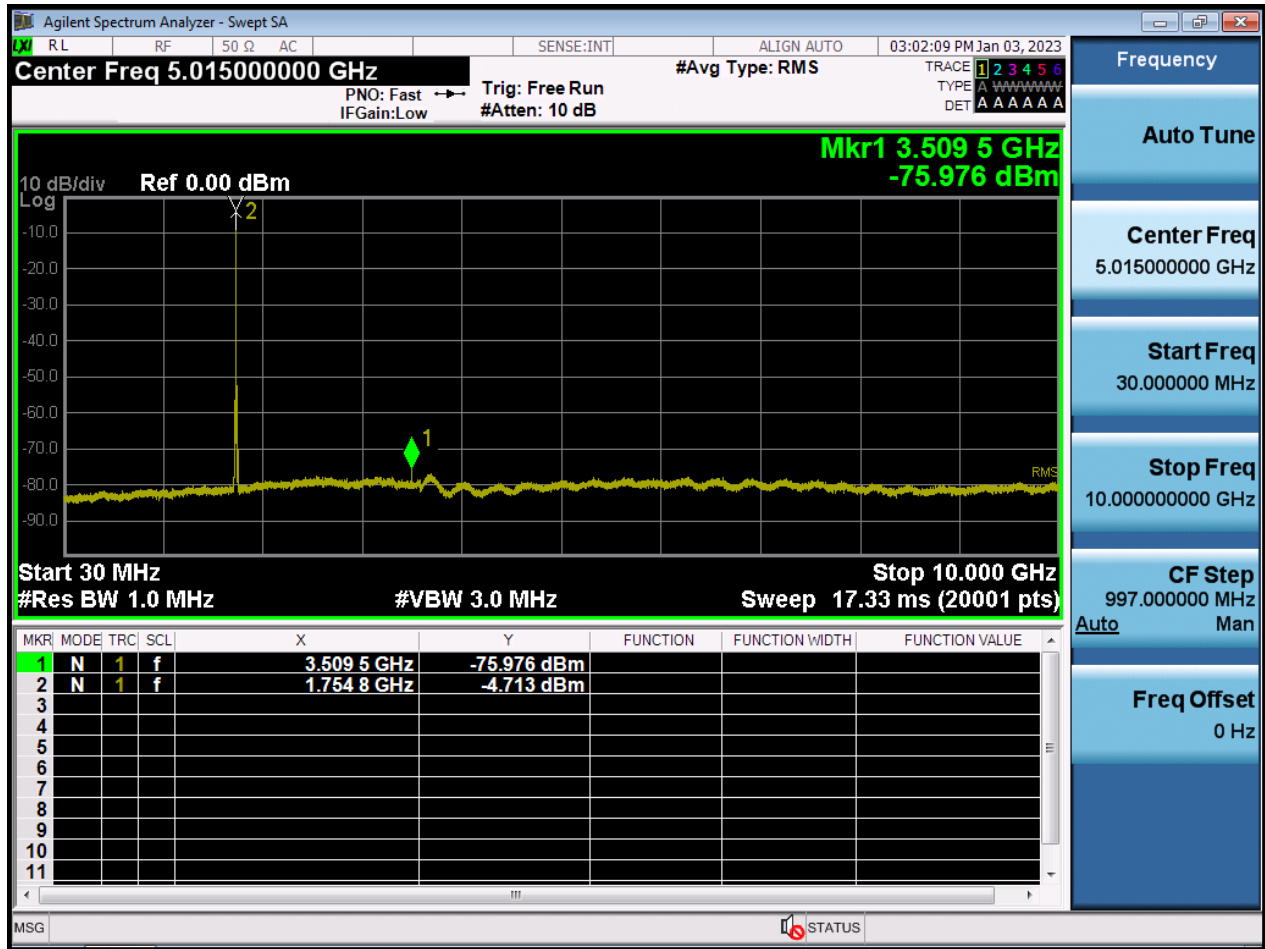
BW10 M_CSE(30 M-10 G)_Lowest Channel_QPSK_1RB



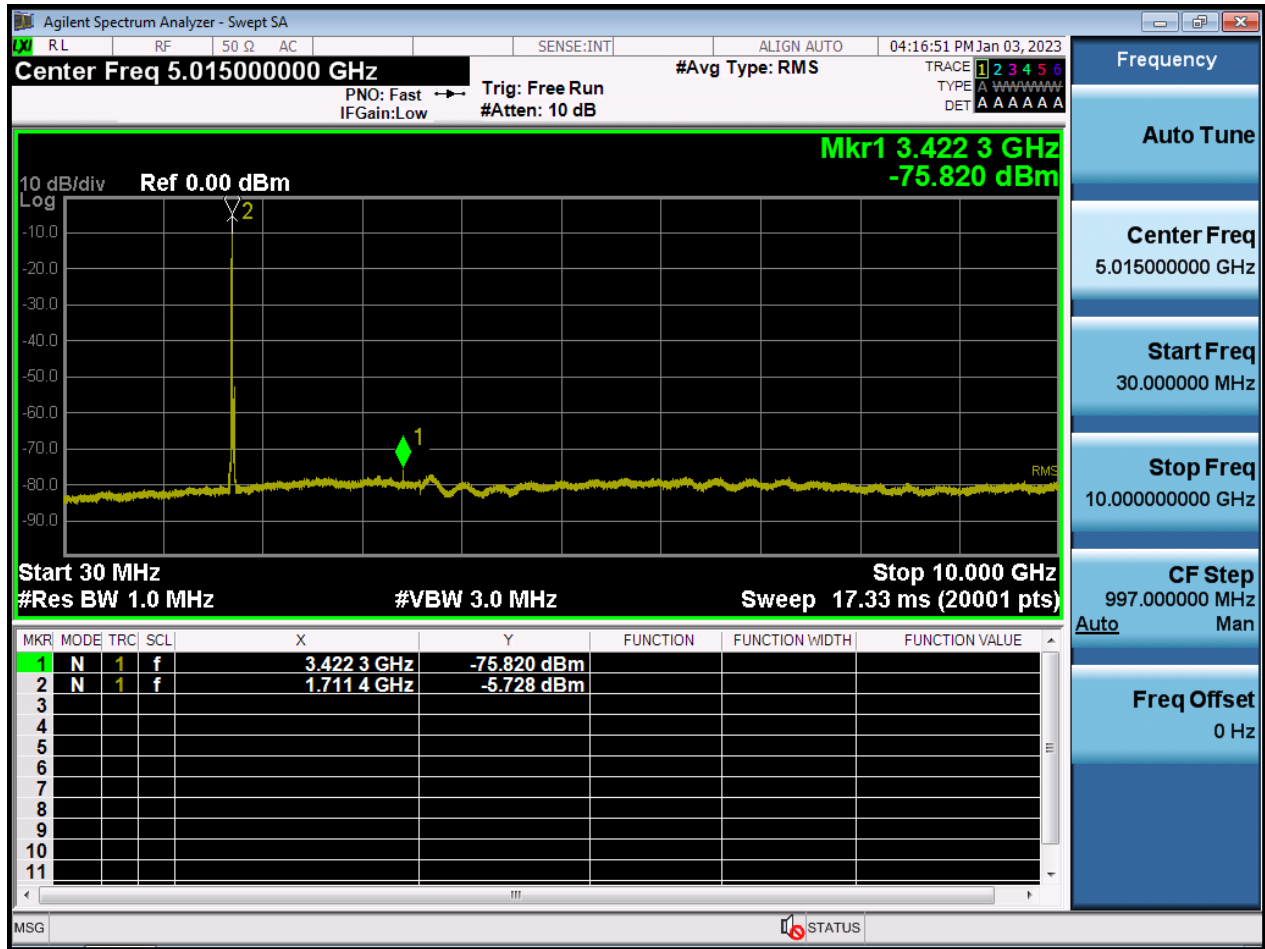
BW10 M_CSE(30 M-10 G)_Middle Channel_QPSK_1RB



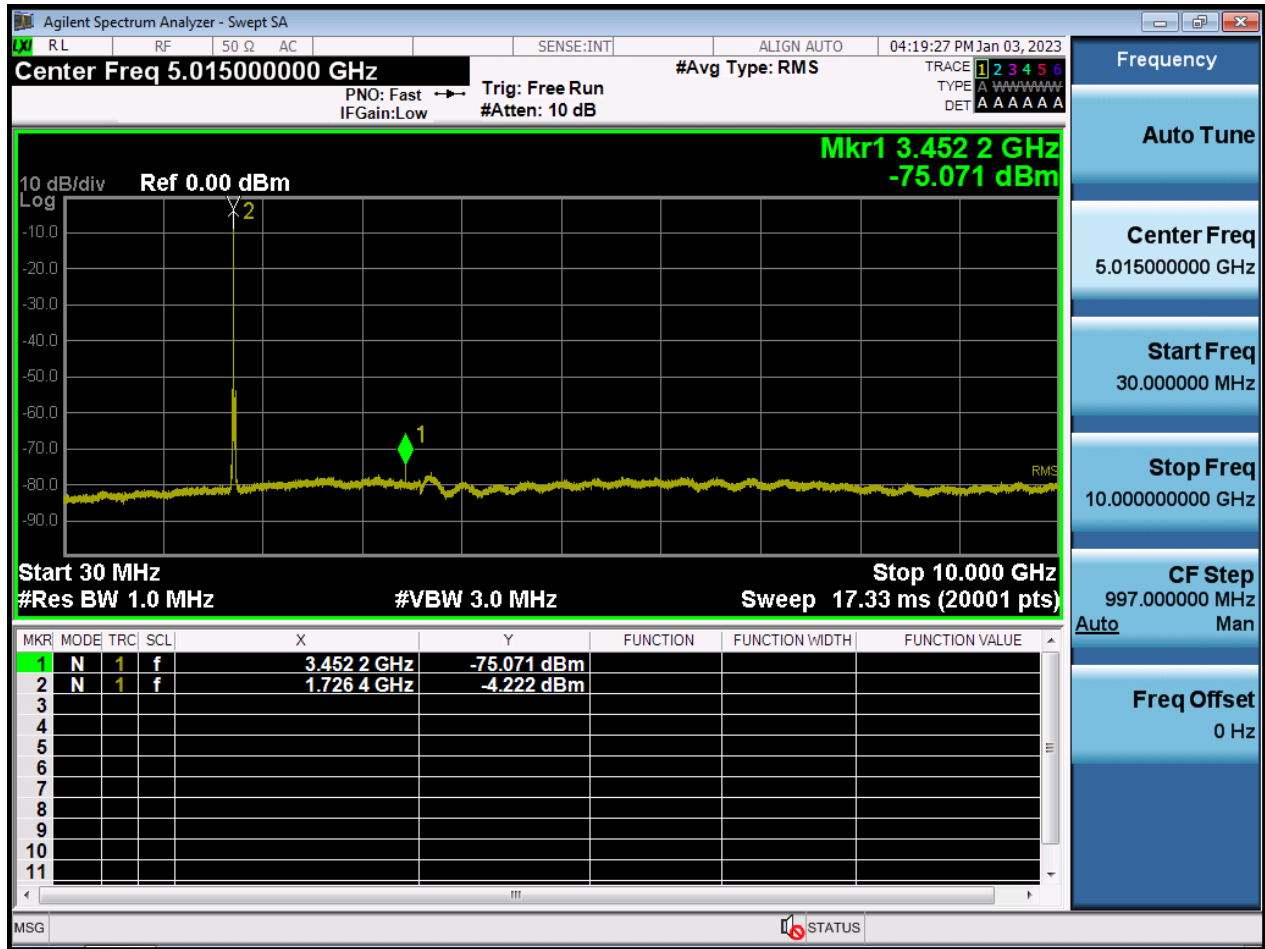
BW10 M_CSE(30 M-10 G)_Highest Channel_QPSK_1RB



BW15 M_CSE(30 M-10 G)_Lowest Channel_QPSK_1RB



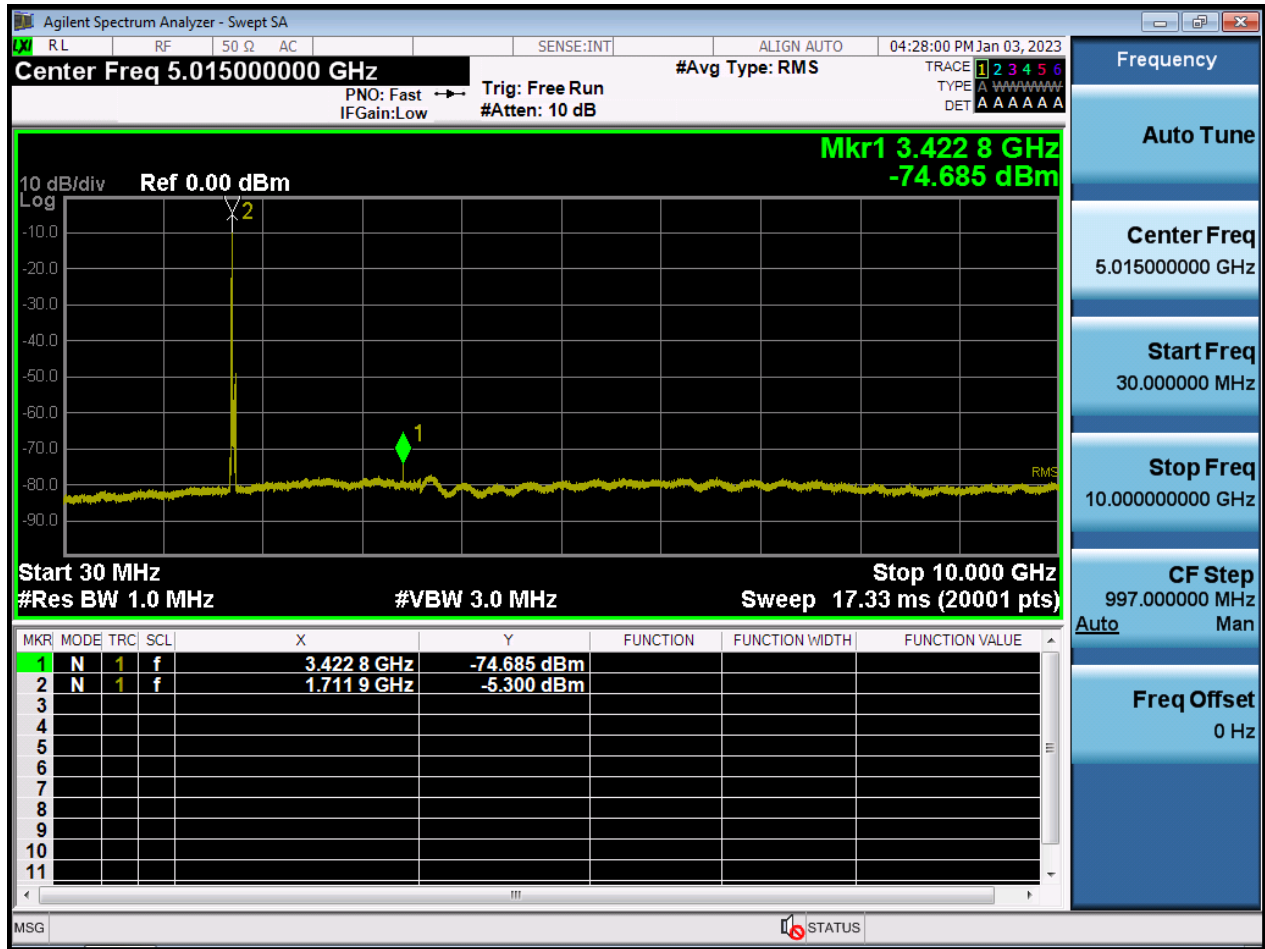
BW15 M_CSE(30 M-10 G)_Middle Channel_QPSK_1RB



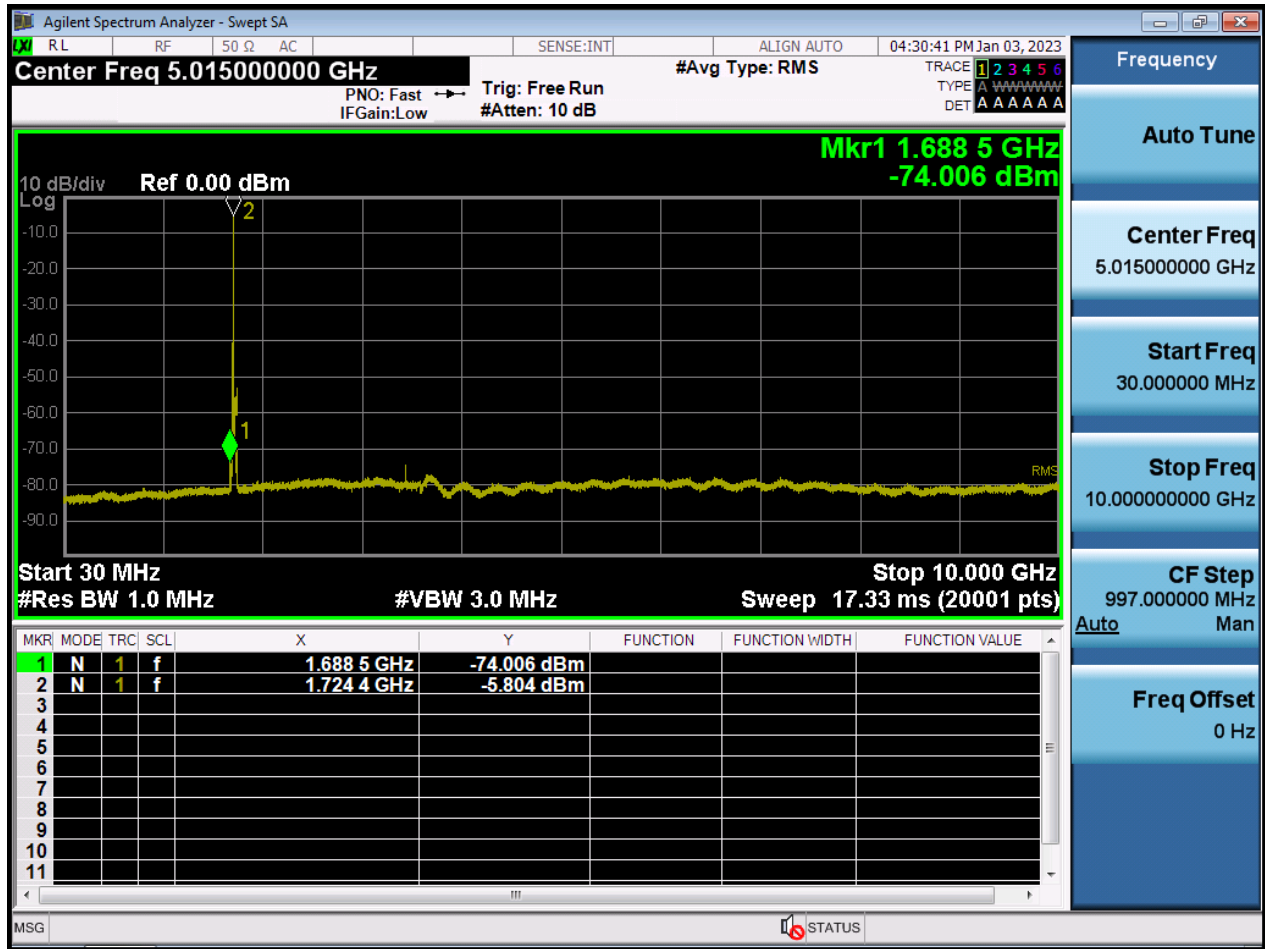
BW15 M_CSE(30 M-10 G)_Highest Channel_QPSK_1RB



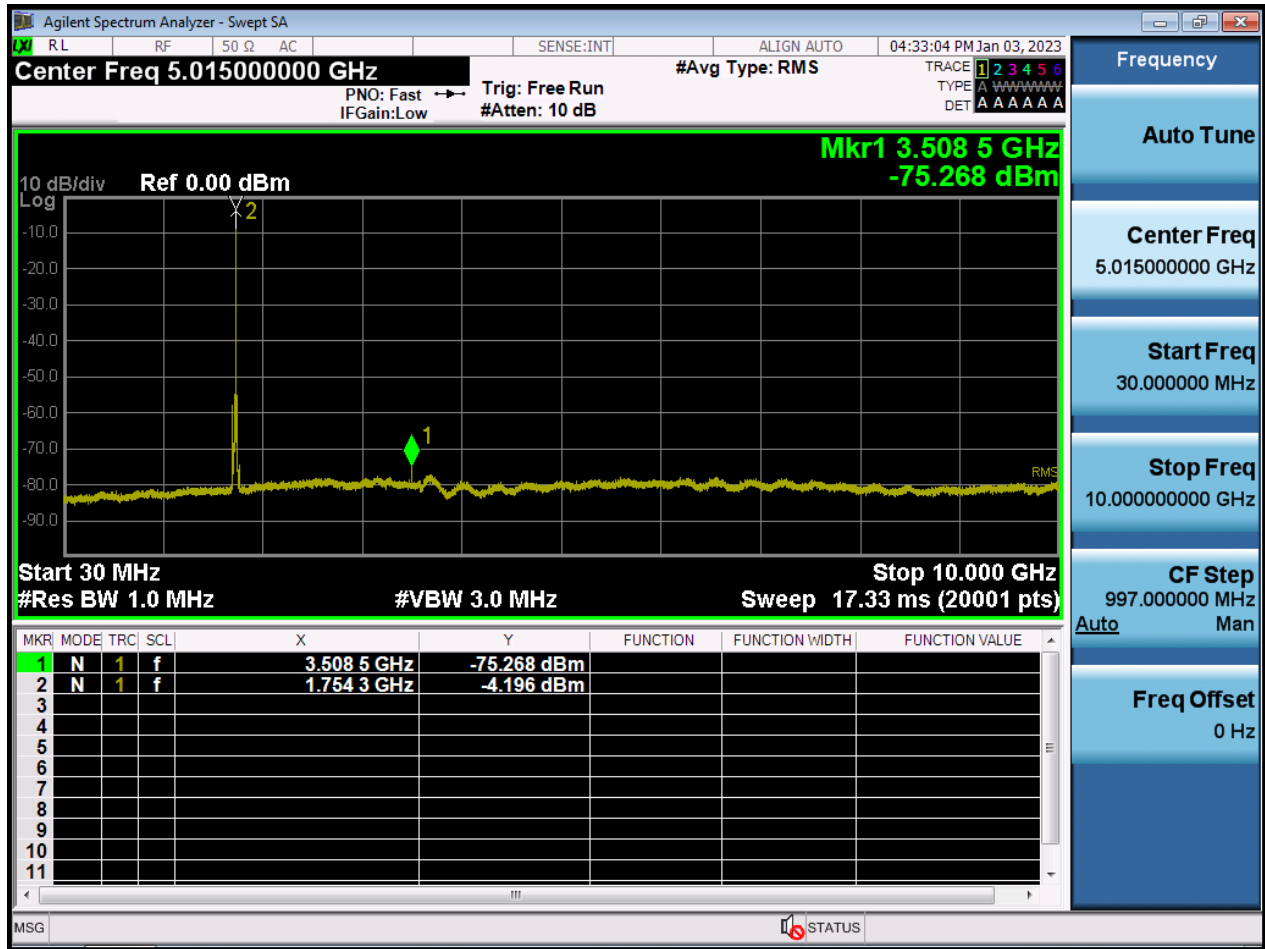
BW20 M_CSE(30 M-10 G)_Lowest Channel_QPSK_1RB



BW20 M_CSE(30 M-10 G)_Middle Channel_QPSK_1RB



BW20 M_CSE(30 M-10 G)_Highest Channel_QPSK_1RB



BW1.4 M_CSE(10 G-26.5 G)_Lowest Channel_QPSK_1RB



BW1.4 M_CSE(10 G-26.5 G)_Middle Channel_QPSK_1RB



BW1.4 M_CSE(10 G-26.5 G)_Highest Channel_QPSK_1RB



BW3 M_CSE(10 G-26.5 G)_Lowest Channel_QPSK_1RB



BW3 M_CSE(10 G-26.5 G)_Middle Channel_QPSK_1RB



BW3 M_CSE(10 G-26.5 G)_Highest Channel_QPSK_1RB



BW5 M_CSE(10 G-26.5 G)_Lowest Channel_QPSK_1RB



BW5 M_CSE(10 G-26.5 G)_Middle Channel_QPSK_1RB



BW5 M_CSE(10 G-26.5 G)_Highest Channel_QPSK_1RB



BW10 M_CSE(10 G-26.5 G)_Lowest Channel_QPSK_1RB



BW10 M_CSE(10 G-26.5 G)_Middle Channel_QPSK_1RB



BW10 M_CSE(10 G-26.5 G)_Highest Channel_QPSK_1RB



BW15 M_CSE(10 G-26.5 G)_Lowest Channel_QPSK_1RB



BW15 M_CSE(10 G-26.5 G)_Middle Channel_QPSK_1RB



BW15 M_CSE(10 G-26.5 G)_Highest Channel_QPSK_1RB



BW20 M_CSE(10 G-26.5 G)_Lowest Channel_QPSK_1RB



BW20 M_CSE(10 G-26.5 G)_Middle Channel_QPSK_1RB



BW20 M_CSE(10 G-26.5 G)_Highest Channel_QPSK_1RB

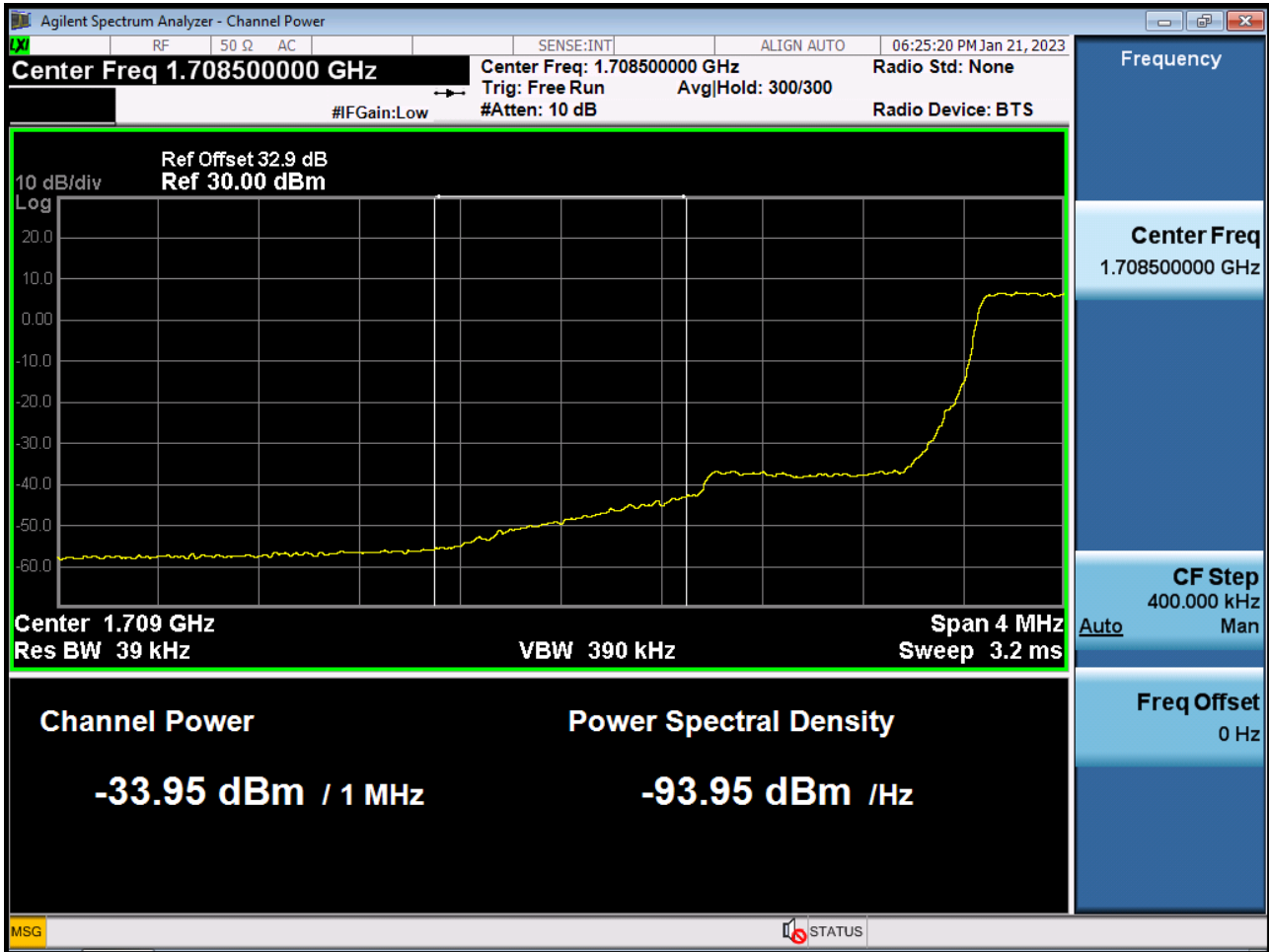


11. TEST PLOTS (Sub2 Ant)

BW1.4 M_BandEdge_Lowest Channel_QPSK_FullRB(1)



BW1.4 M_BandEdge_Lowest Channel_QPSK_FullRB(2)



BW1.4 M_BandEdge_Highest Channel_QPSK_FullIRB(1)



BW1.4 M_BandEdge_Highest Channel_QPSK_FullRB(2)



BW1.4 M_BandEdge_Lowest Channel_QPSK_1RB



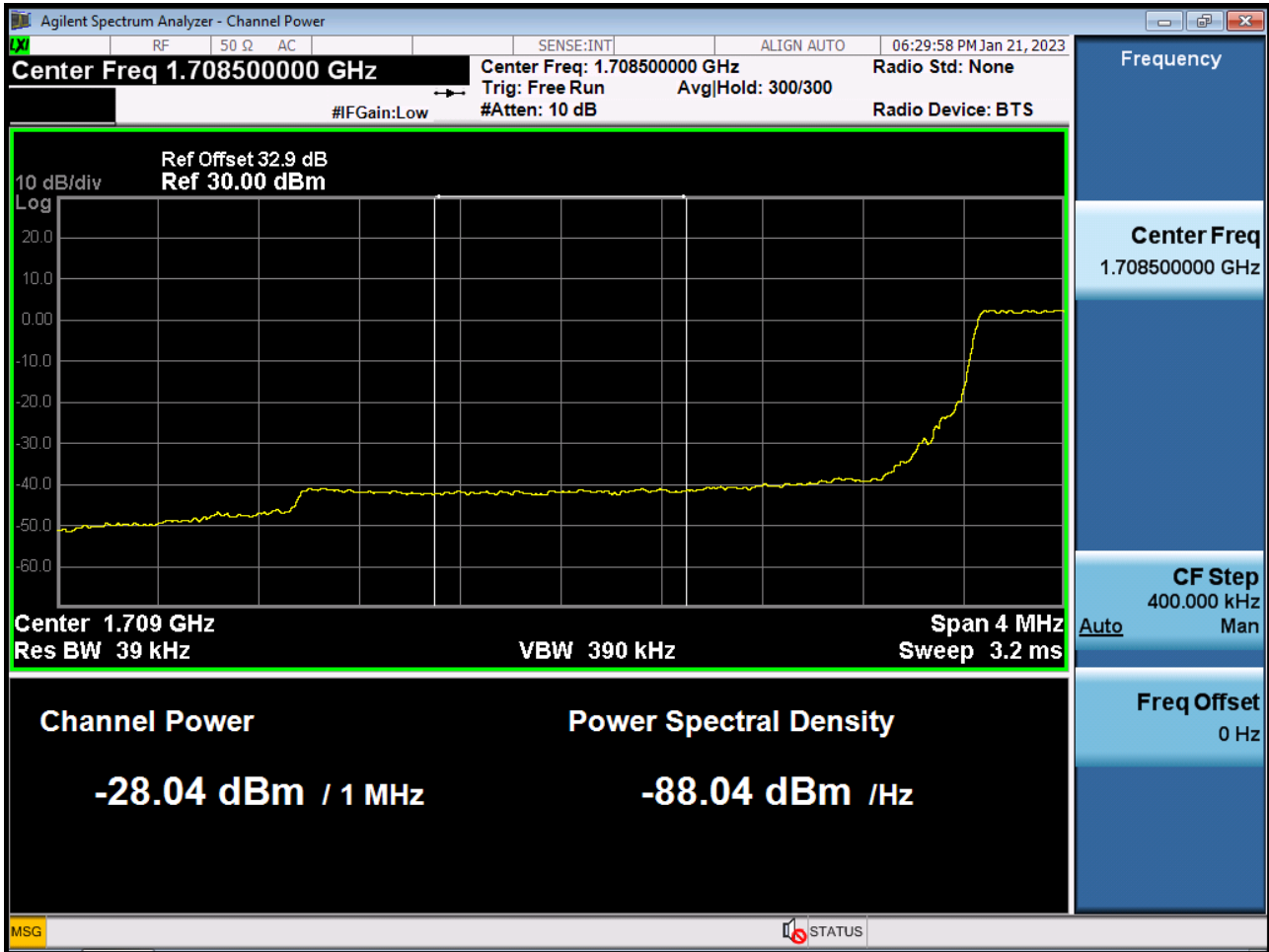
BW1.4 M_BandEdge_Highest Channel_QPSK_1RB



BW3 M_BandEdge_Lowest Channel_QPSK_FullIRB(1)



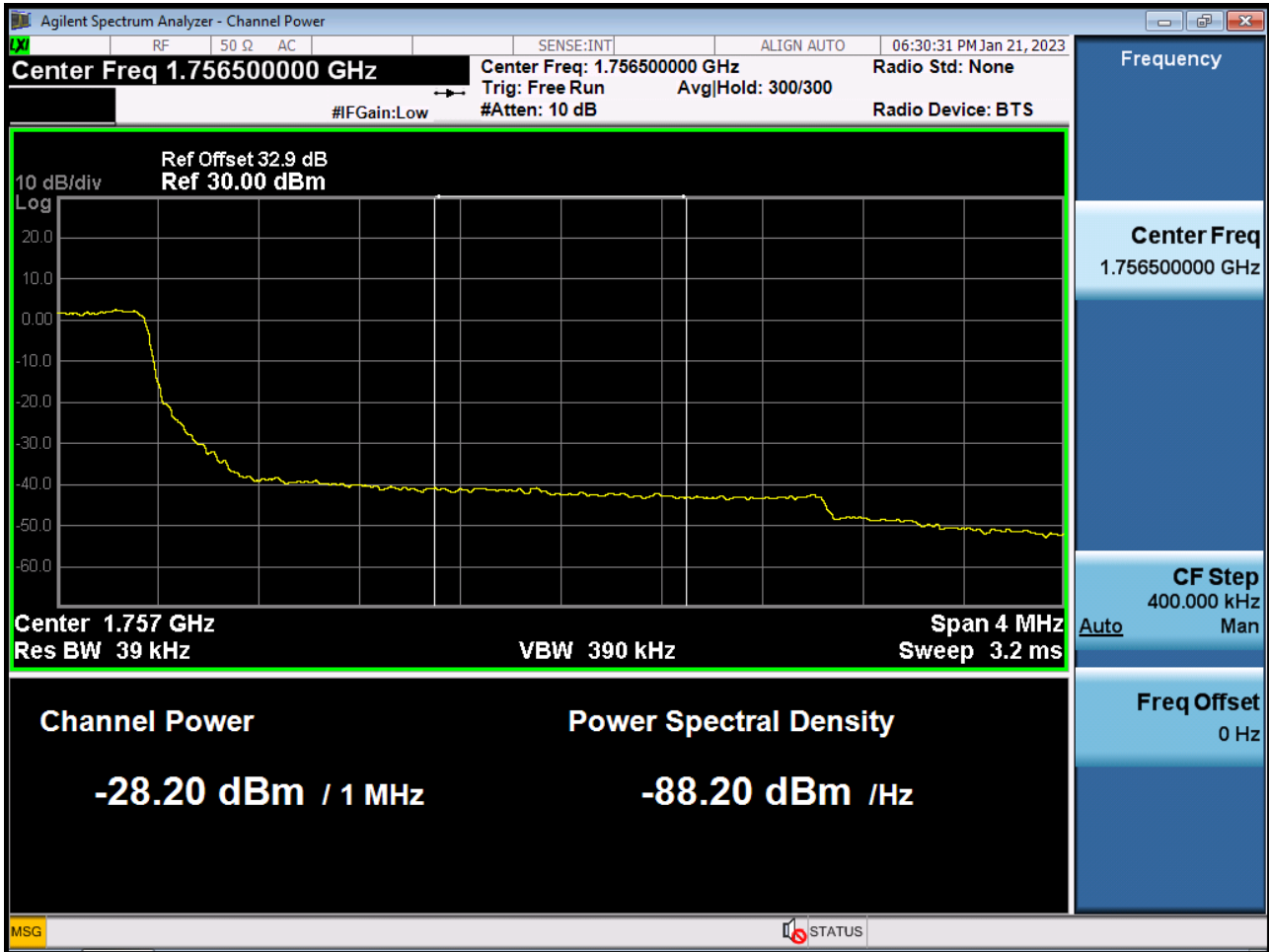
BW3 M_BandEdge_Lowest Channel_QPSK_FullIRB(2)



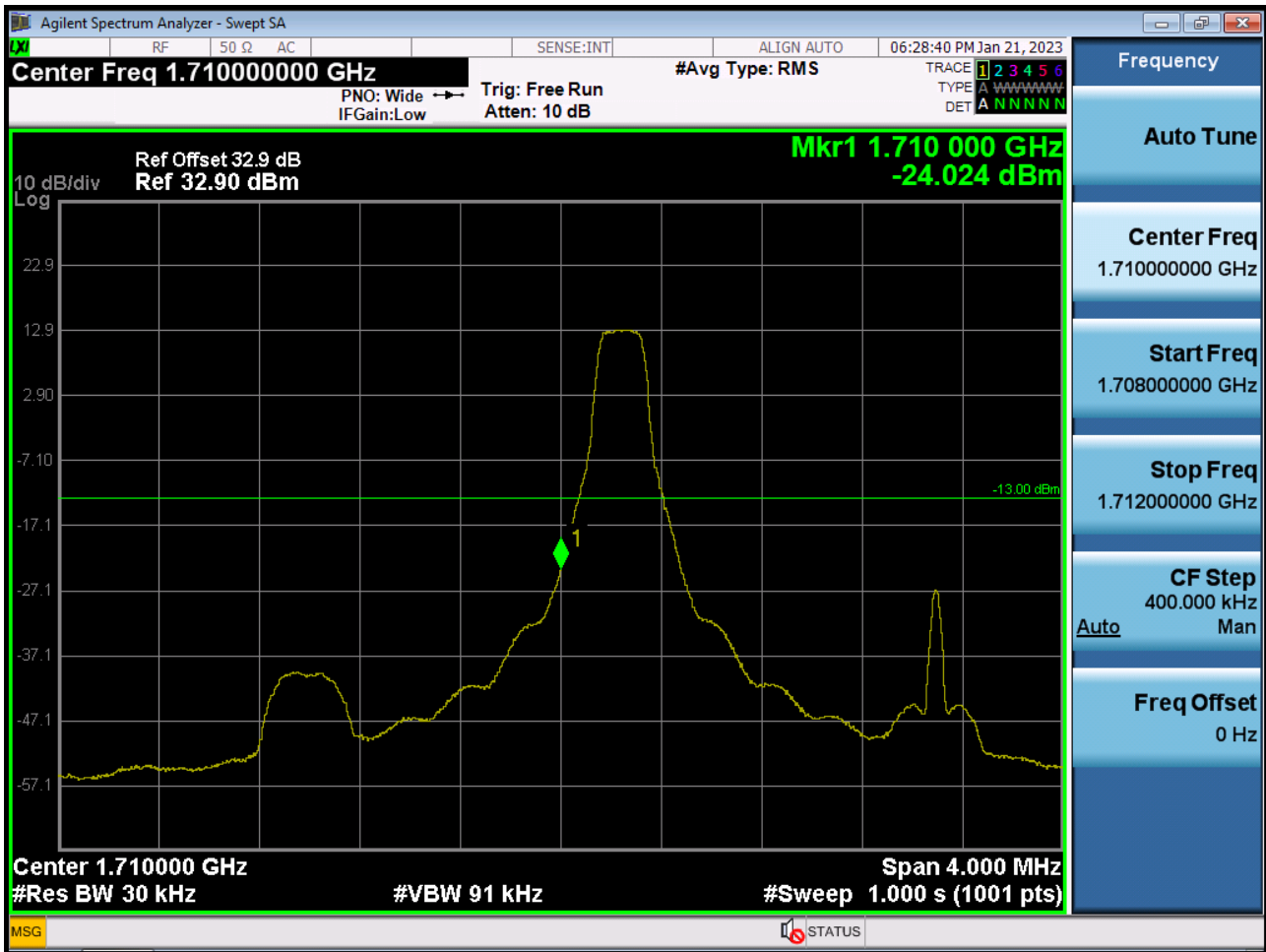
BW3 M_BandEdge_Highest Channel_QPSK_FullRB(1)



BW3 M_BandEdge_Highest Channel_QPSK_FullRB(2)



BW3 M_BandEdge_Lowest Channel_QPSK_1RB



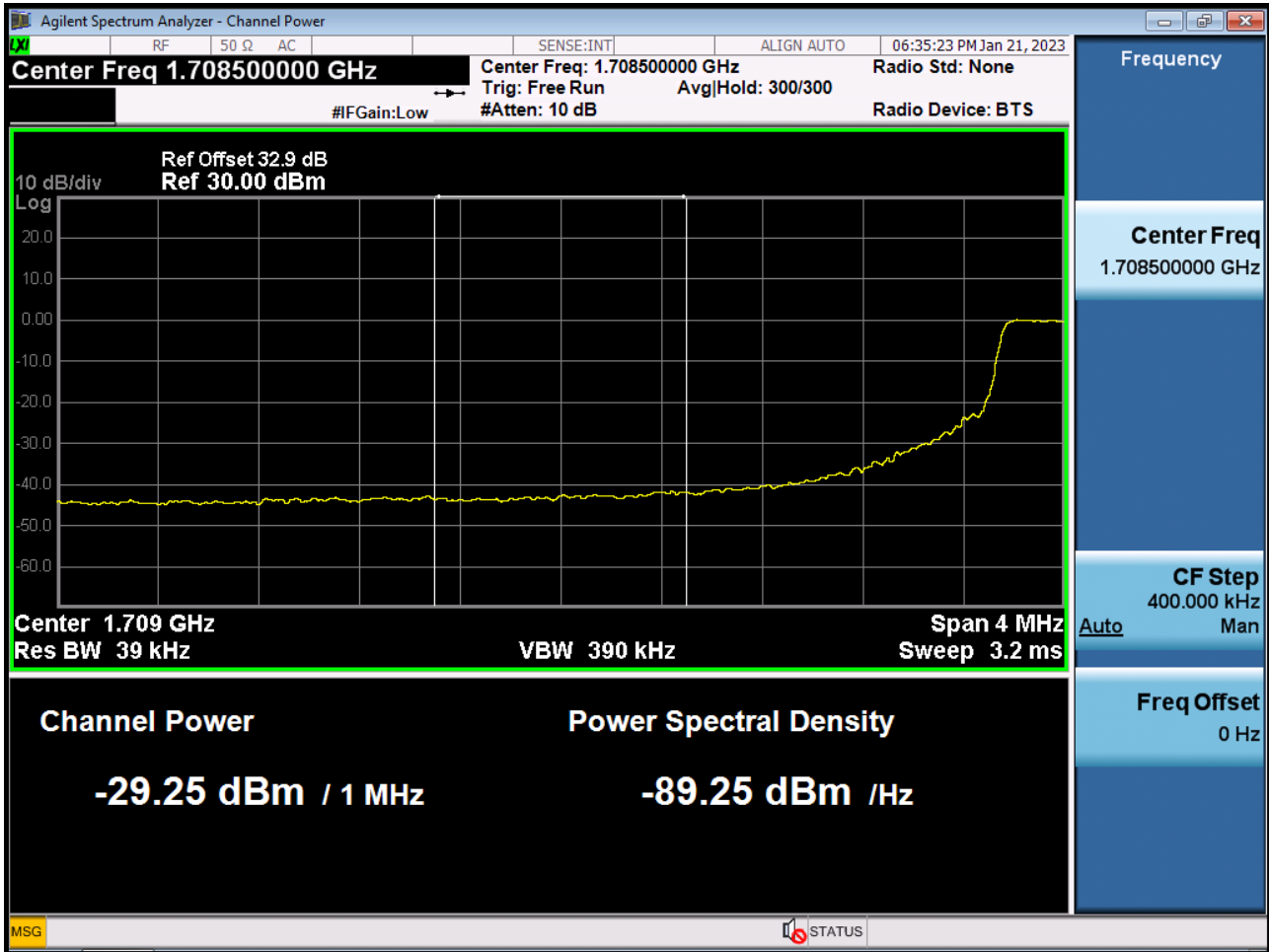
BW3 M_BandEdge_Highest Channel_QPSK_1RB



BW5 M_BandEdge_Lowest Channel_QPSK_FullIRB(1)



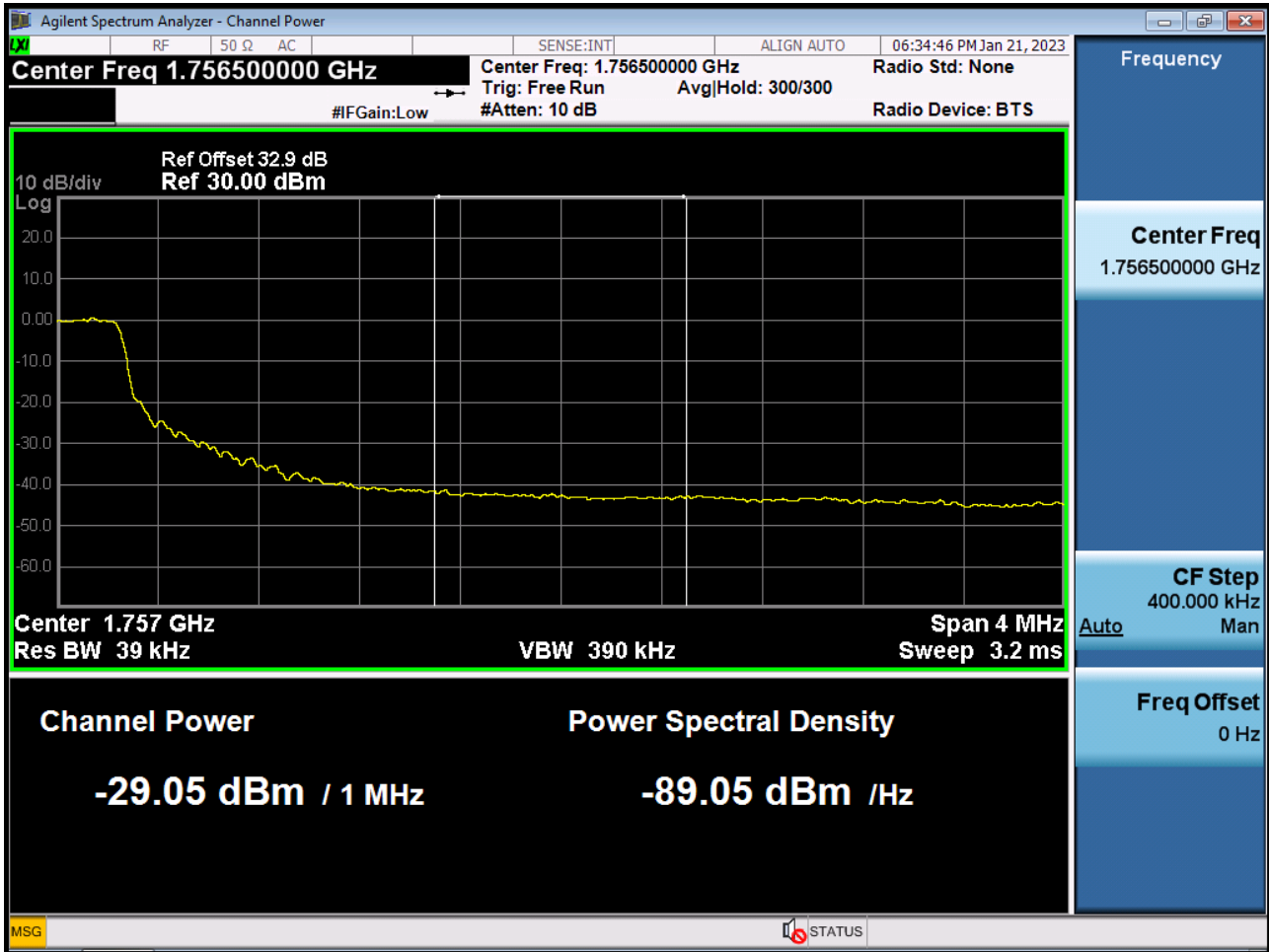
BW5 M_BandEdge_Lowest Channel_QPSK_FullIRB(2)



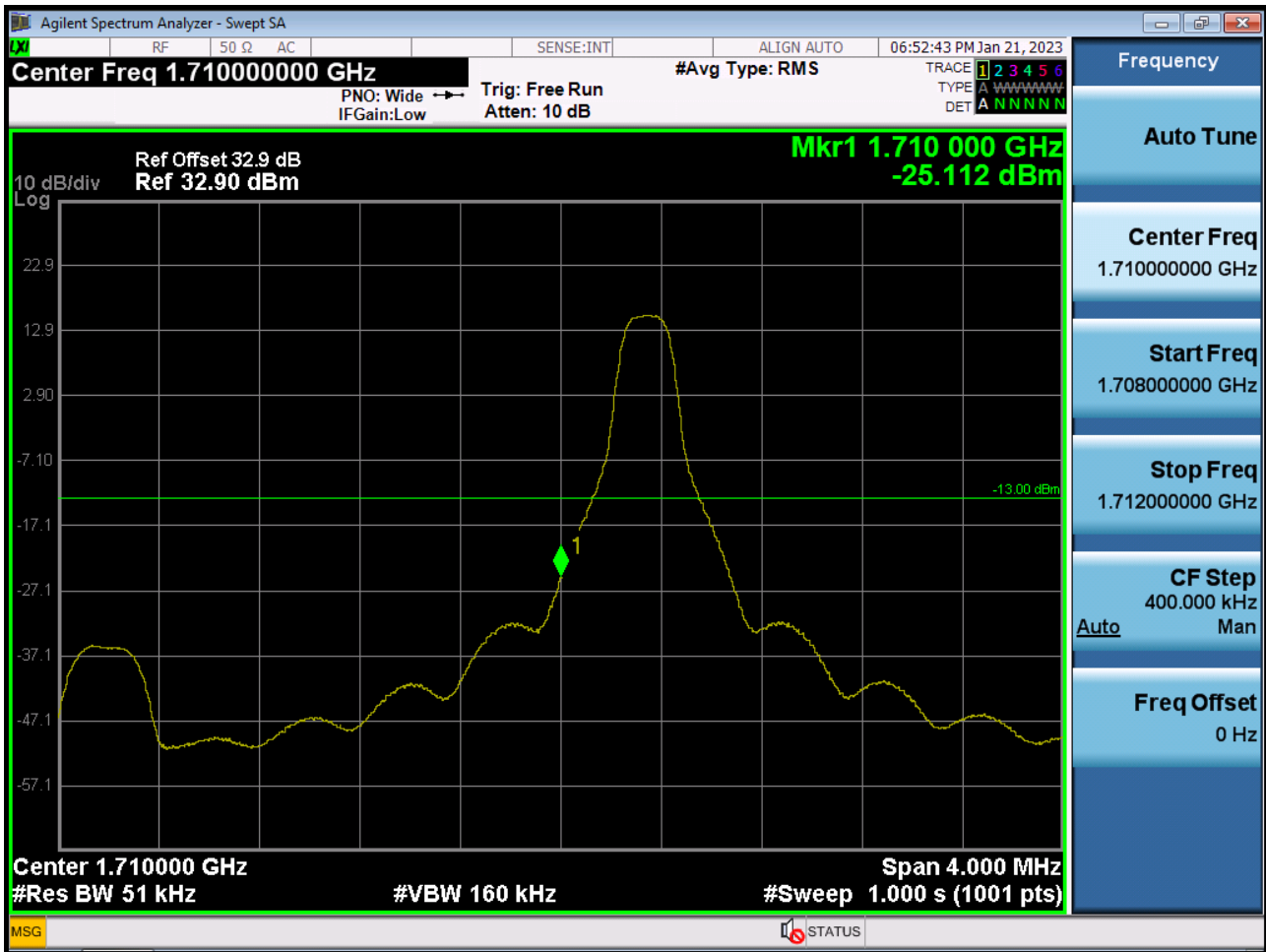
BW5 M_BandEdge_Highest Channel_QPSK_FullRB(1)



BW5 M_BandEdge_Highest Channel_QPSK_FullRB(2)



BW5 M_BandEdge_Lowest Channel_QPSK_1RB



BW5 M_BandEdge_Highest Channel_QPSK_1RB



BW10 M_BandEdge_Lowest Channel_QPSK_FullRB(1)



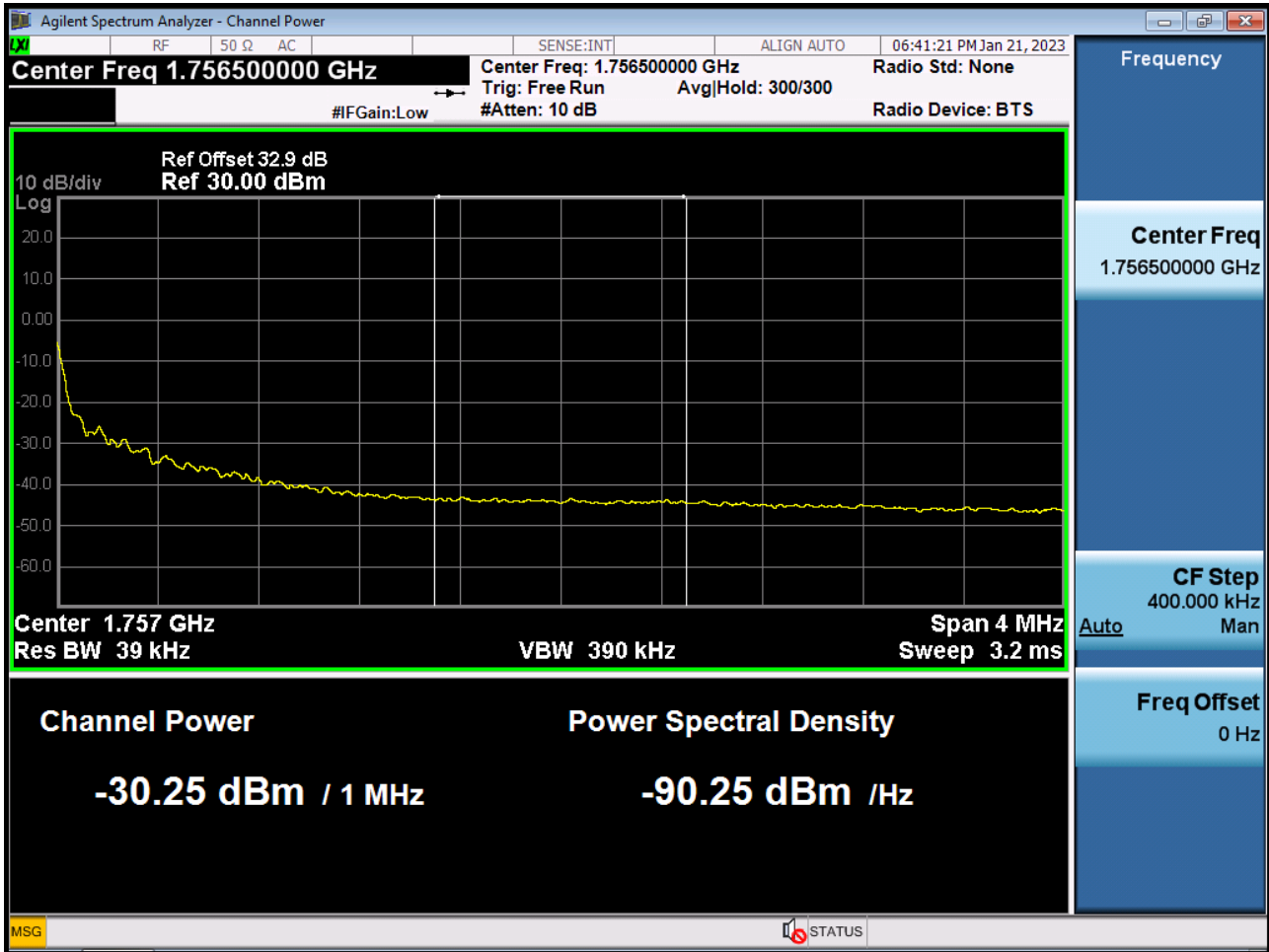
BW10 M_BandEdge_Lowest Channel_QPSK_FullRB(2)



BW10 M_BandEdge_Highest Channel_QPSK_FullRB(1)



BW10 M_BandEdge_Highest Channel_QPSK_FullRB(2)



BW10 M_BandEdge_Lowest Channel_QPSK_1RB



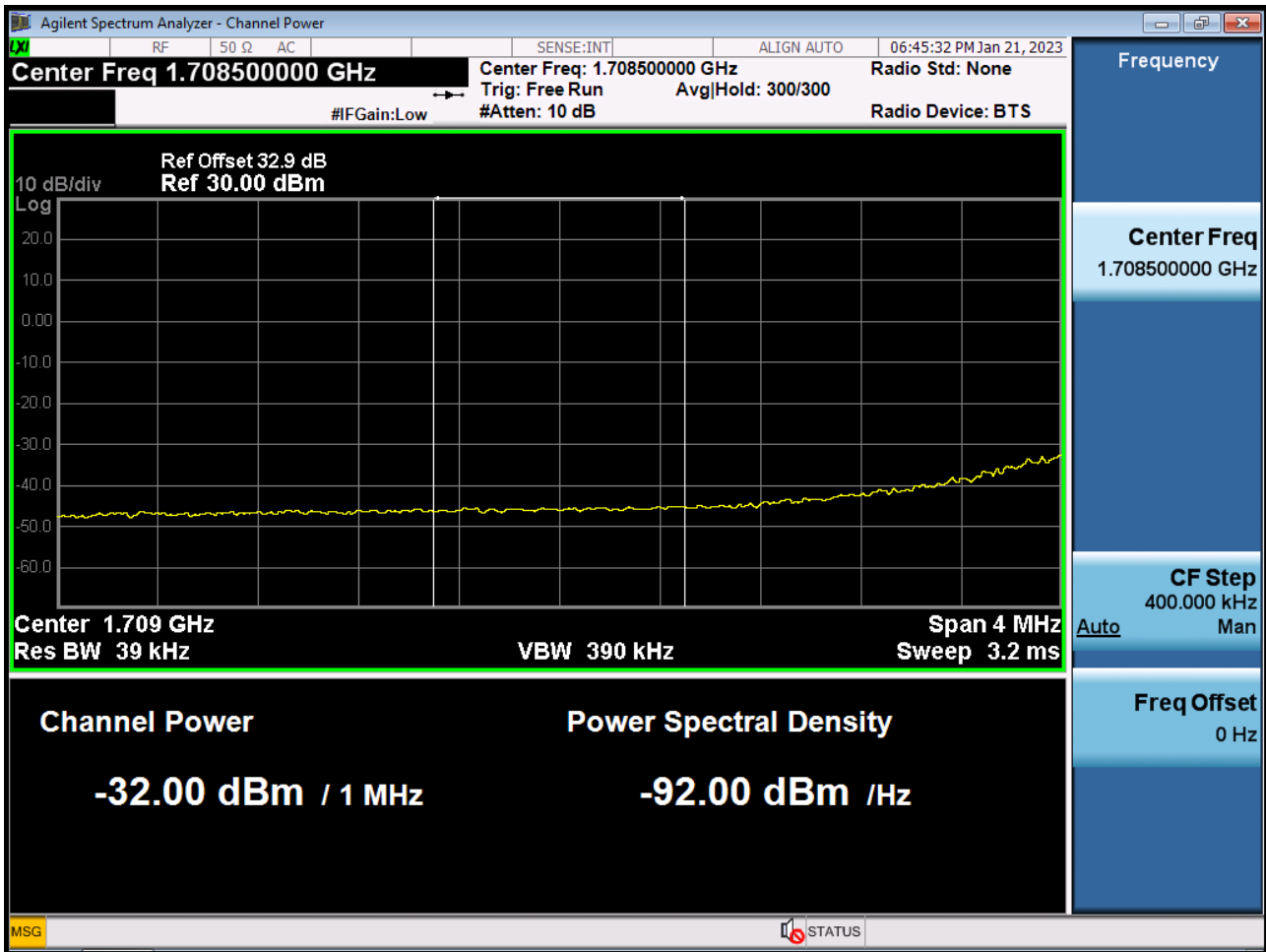
BW10 M_BandEdge_Highest Channel_QPSK_1RB



BW15 M_BandEdge_Lowest Channel_QPSK_FullRB(1)



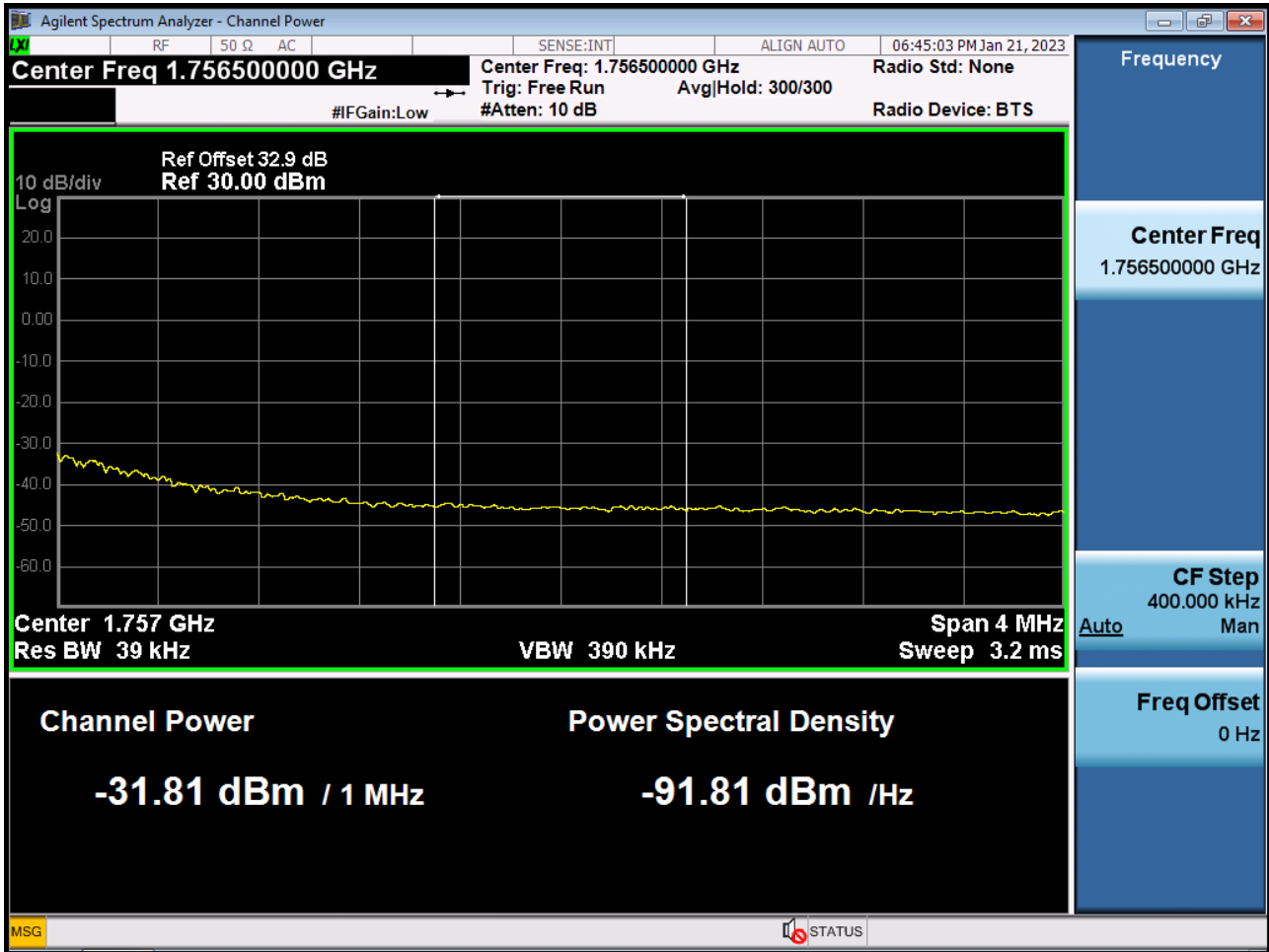
BW15 M_BandEdge_Lowest Channel_QPSK_FullRB(2)



BW15 M_BandEdge_Highest Channel_QPSK_FullRB(1)



BW15 M_BandEdge_Highest Channel_QPSK_FullRB(2)



BW15 M_BandEdge_Lowest Channel_QPSK_1RB



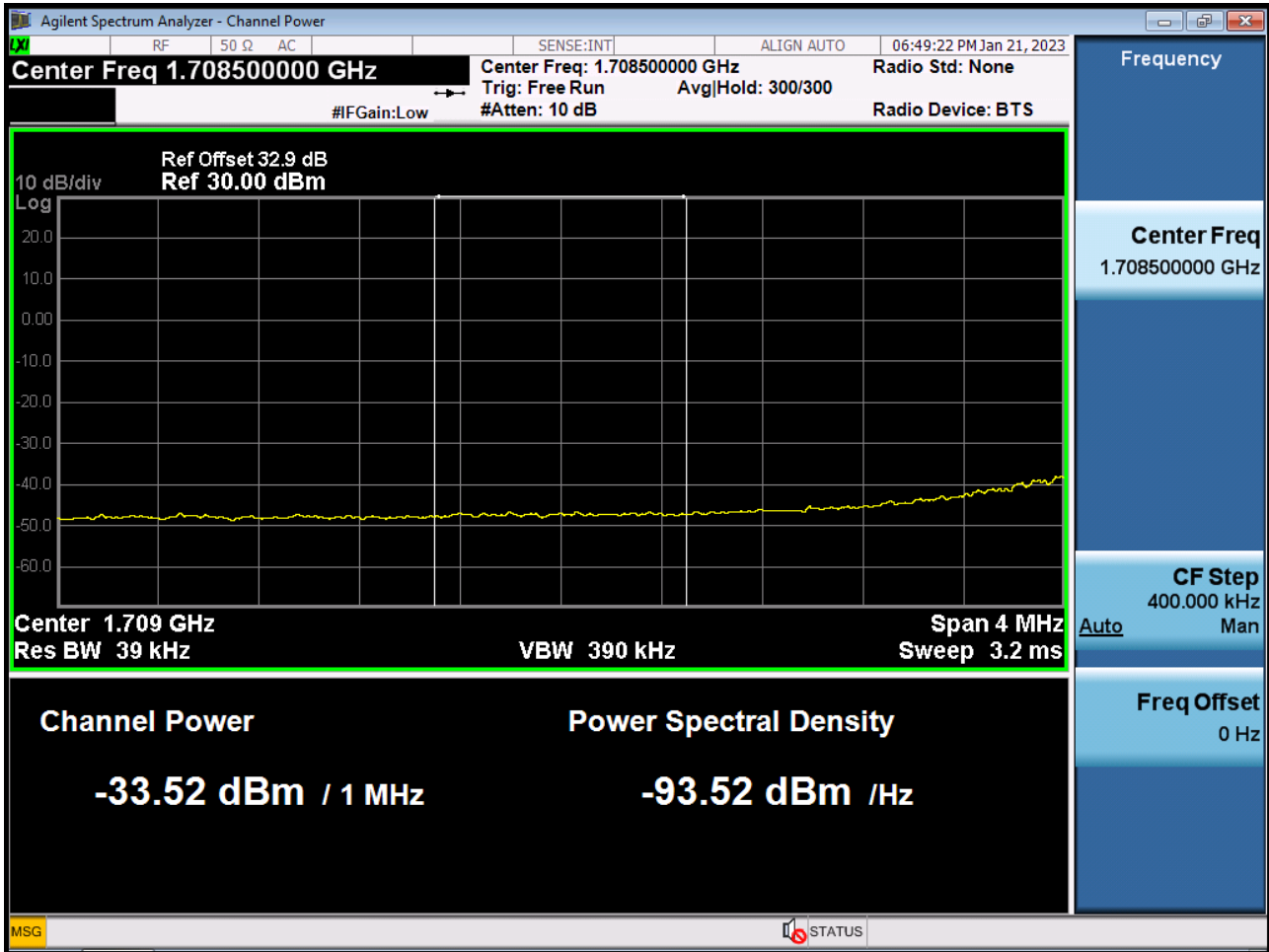
BW15 M_BandEdge_Highest Channel_QPSK_1RB



BW20 M_BandEdge_Lowest Channel_QPSK_FullRB(1)



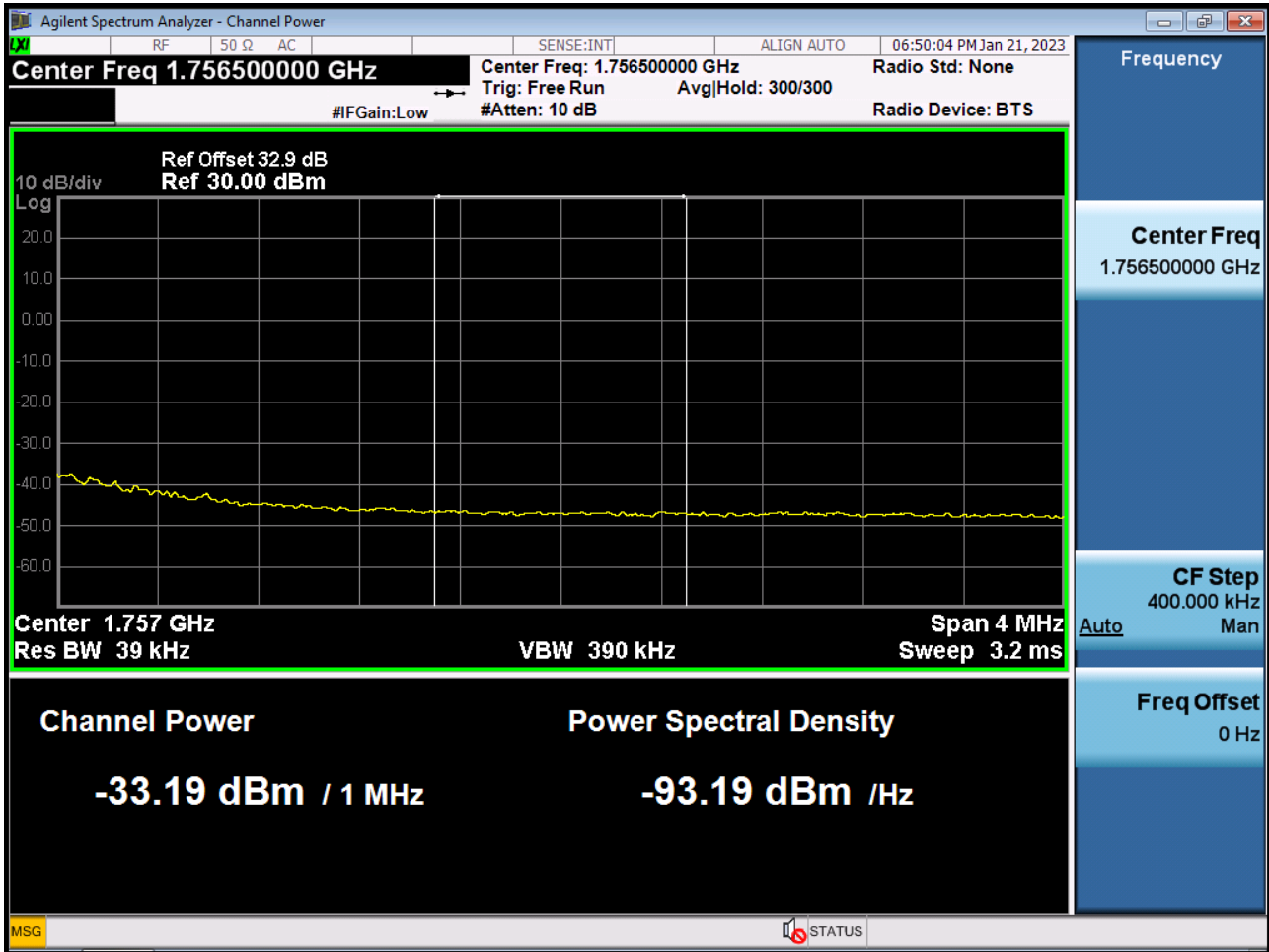
BW20 M_BandEdge_Lowest Channel_QPSK_FullIRB(2)



BW20 M_BandEdge_Highest Channel_QPSK_FullRB(1)



BW20 M_BandEdge_Highest Channel_QPSK_FullRB(2)



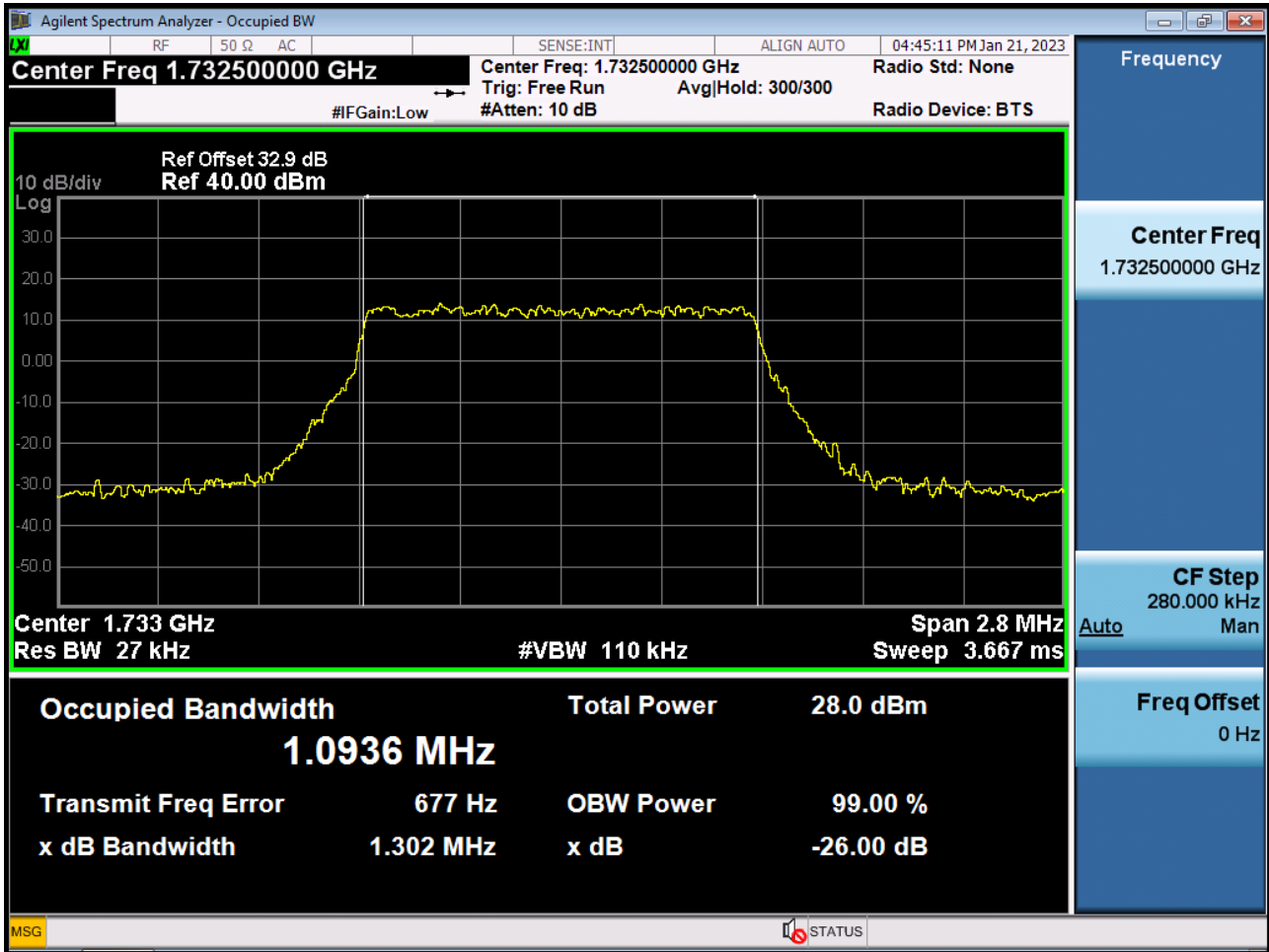
BW20 M_BandEdge_Lowest Channel_QPSK_1RB



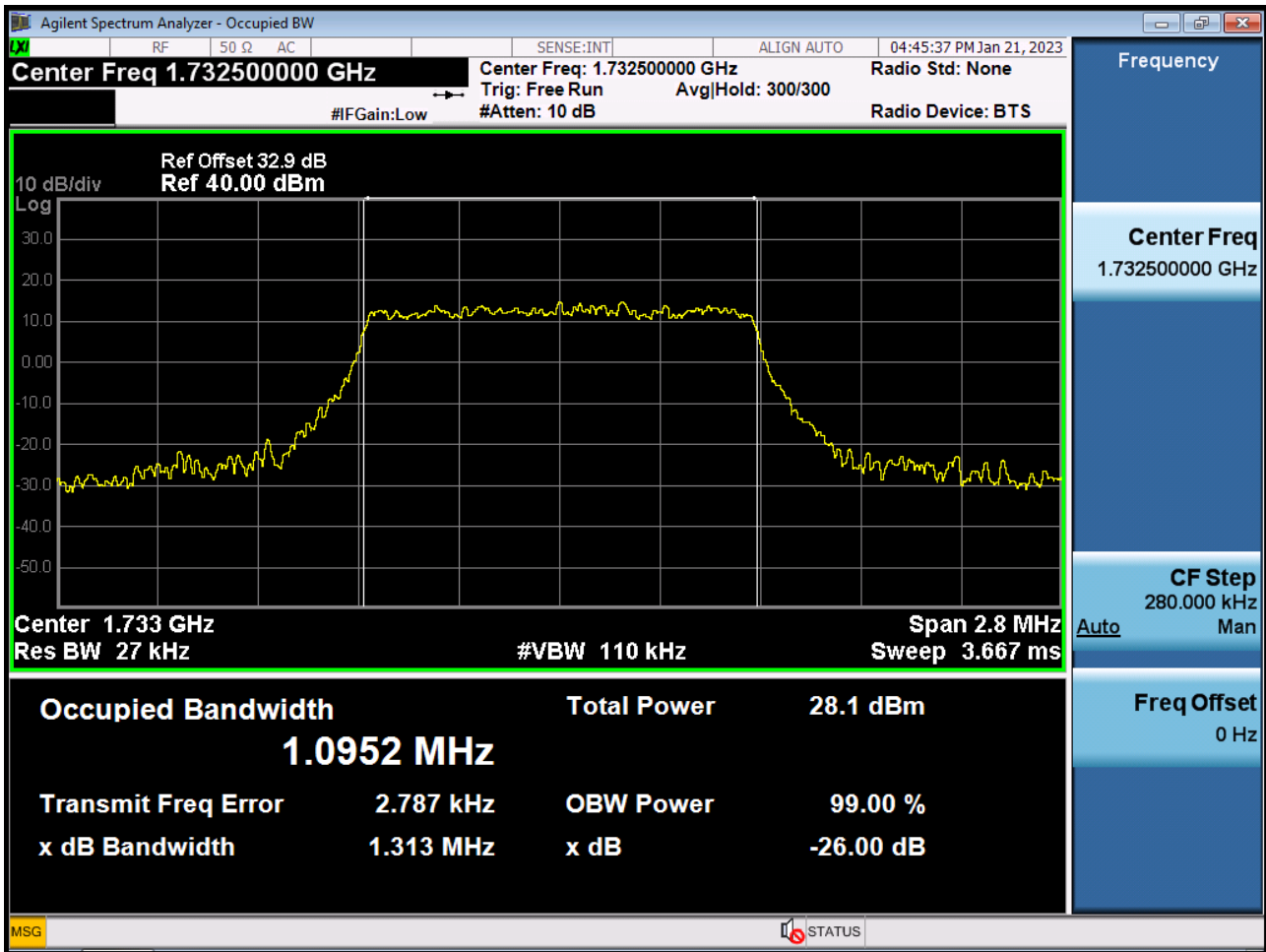
BW20 M_BandEdge_Highest Channel_QPSK_1RB.



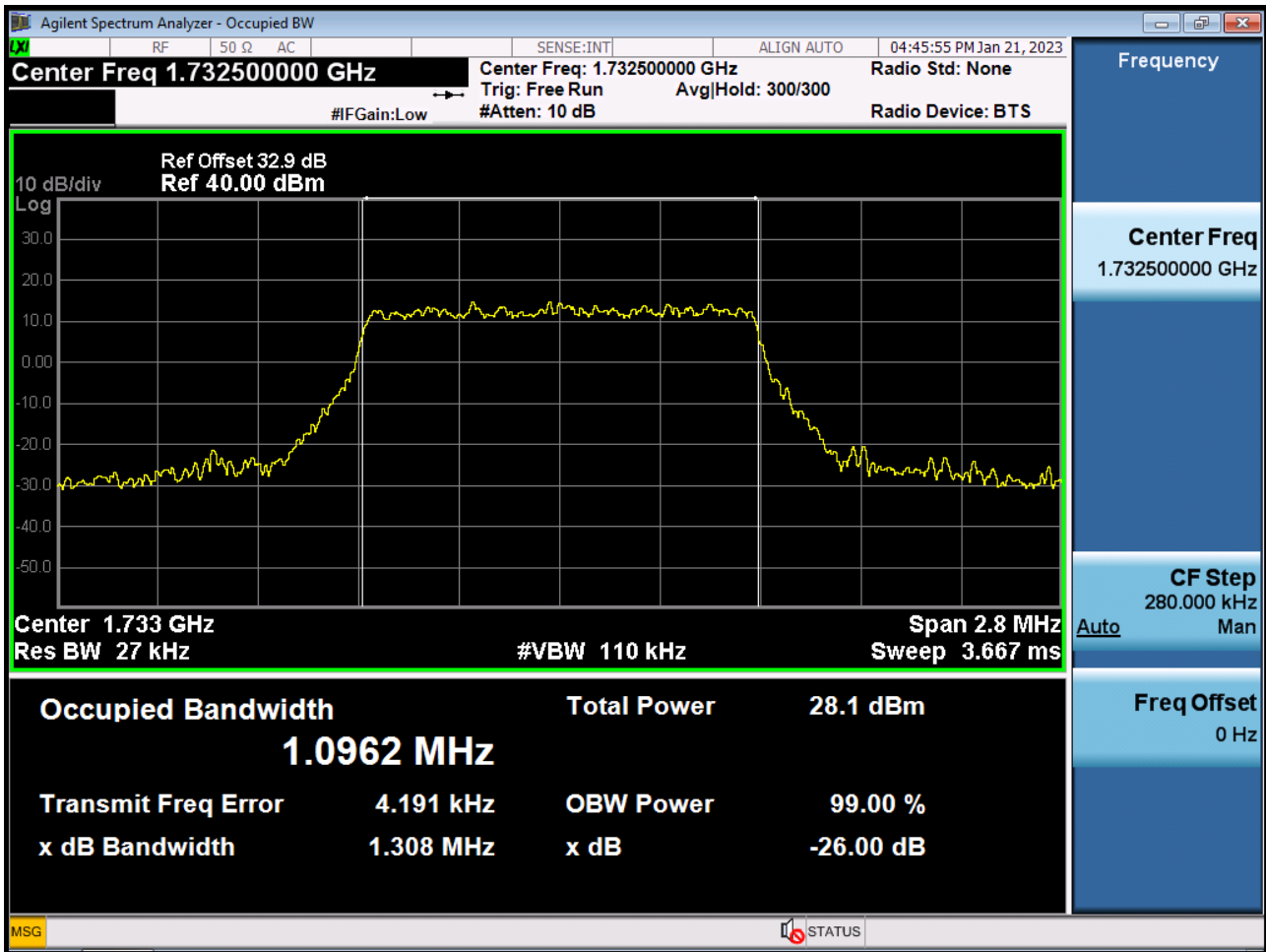
BW.1.4 M_OBW_Middle Channel_QPSK_FullIRB



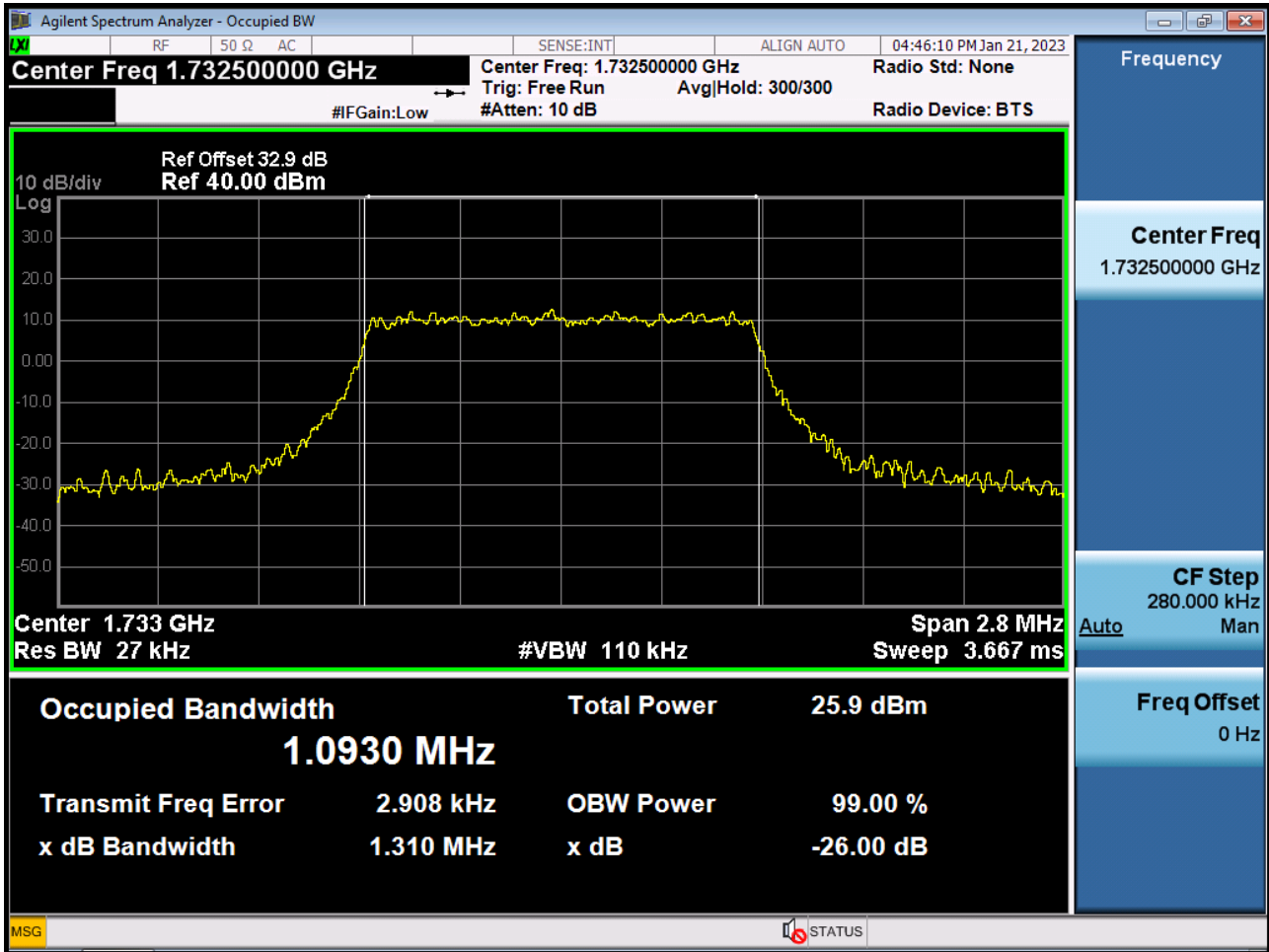
BW1.4 M_OBW_Middle Channel_16QAM_FullRB



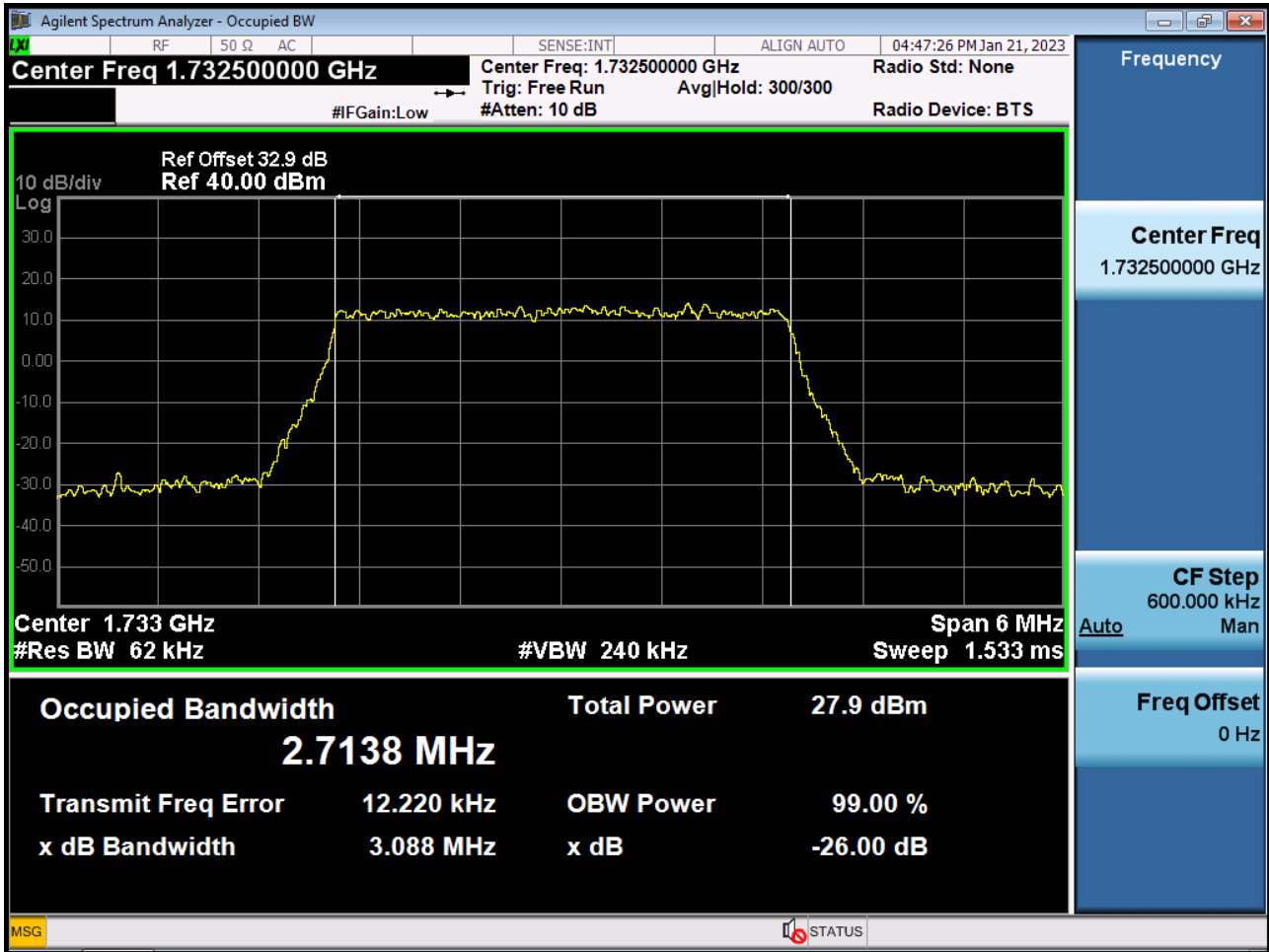
BW1.4 M_OBW_Middle Channel_64QAM_FullIRB



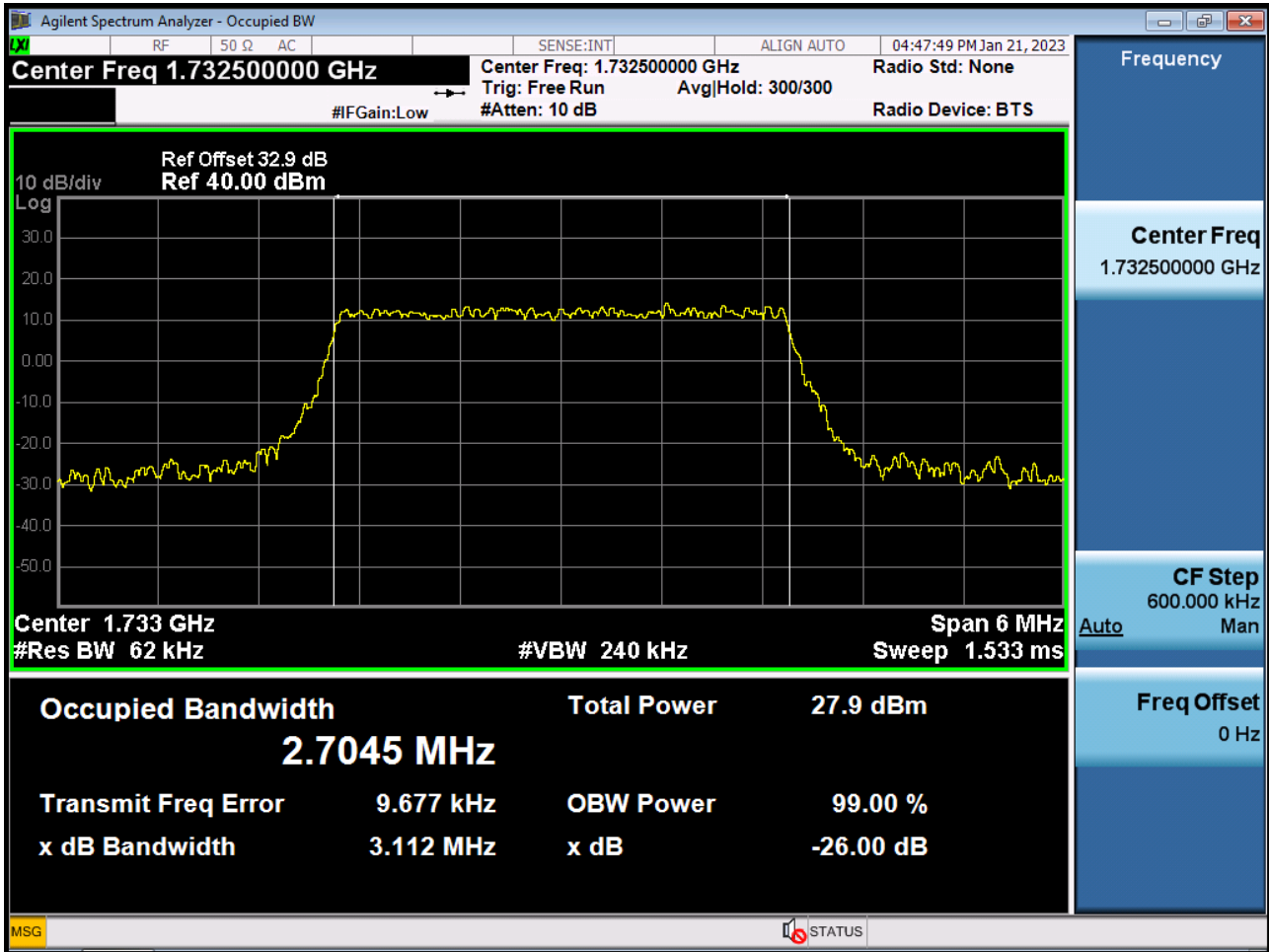
BW1.4 M_OBW_Middle Channel_256QAM_FullRB



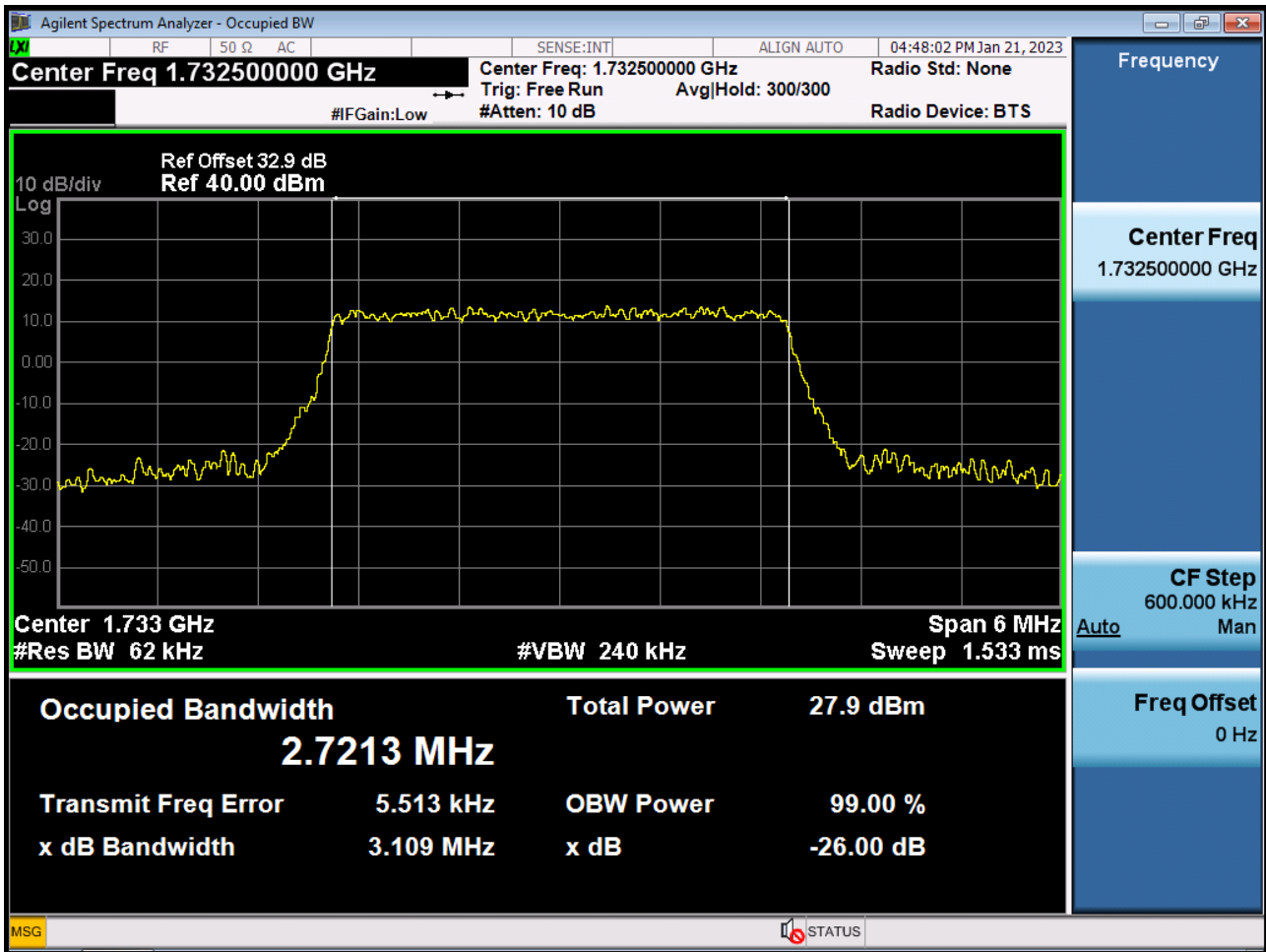
BW3 M_OBW_Middle Channel_QPSK_FullIRB



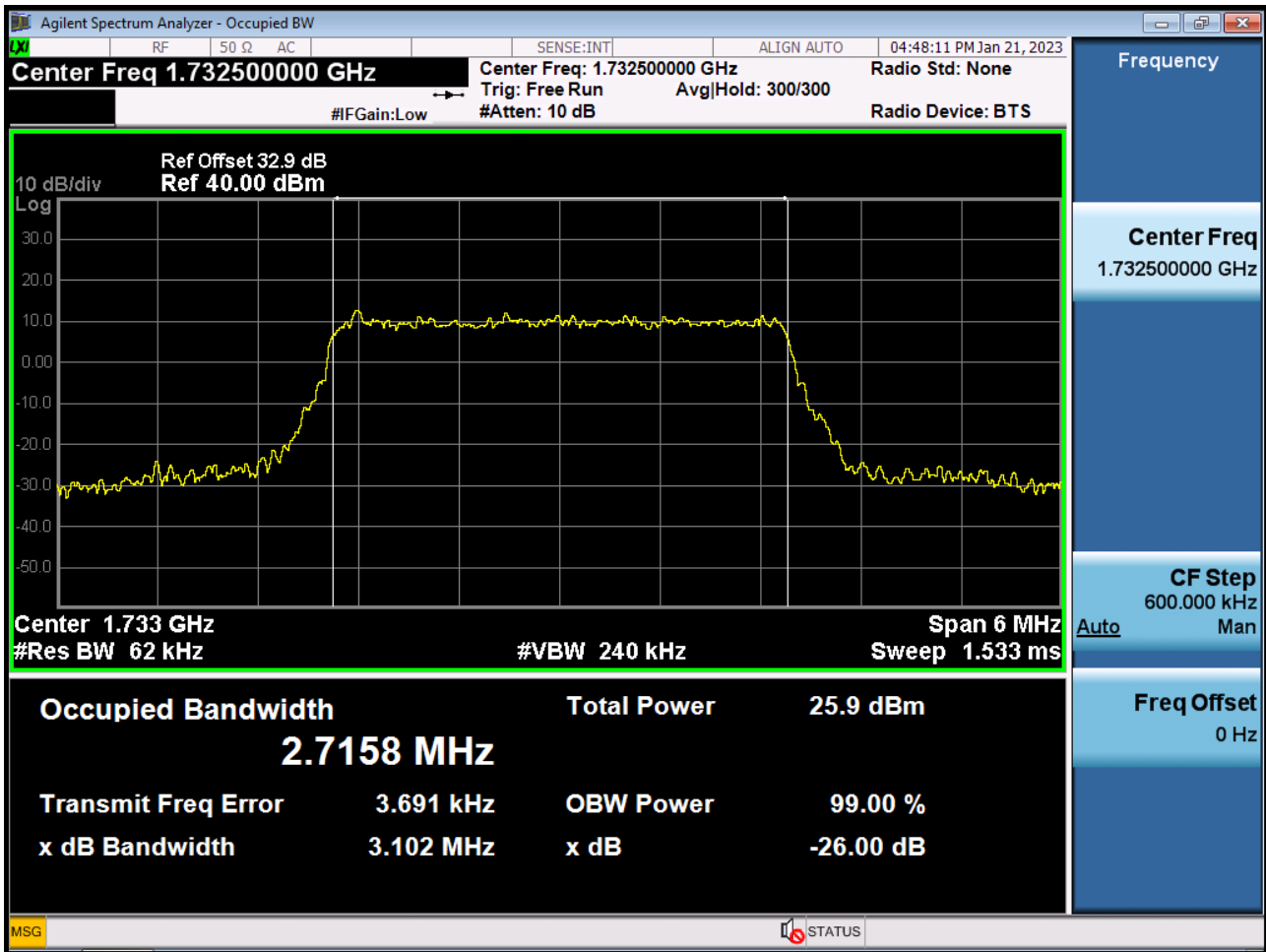
BW3 M_OBW_Middle Channel_16QAM_FullRB



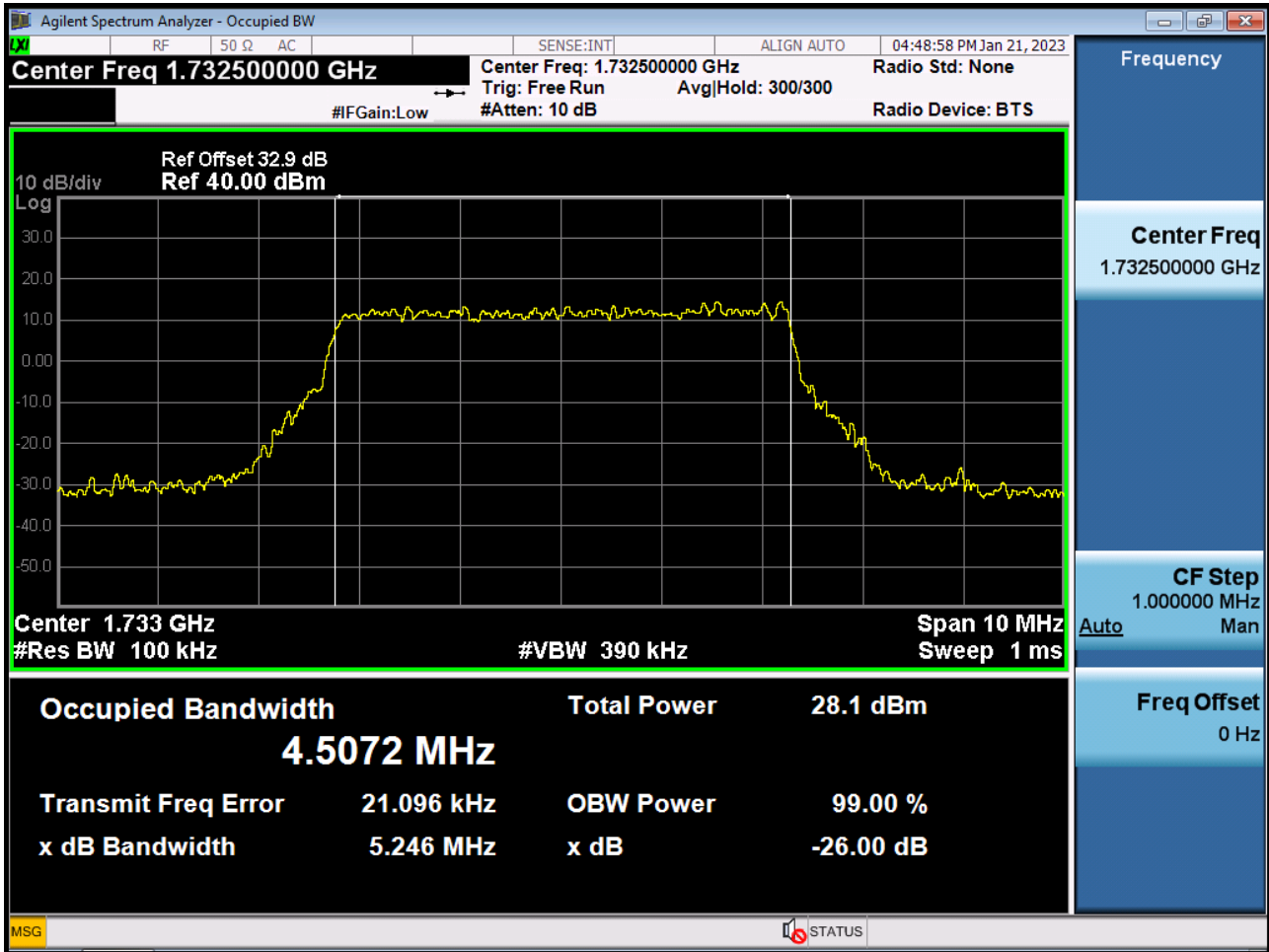
BW3 M_OBW_Middle Channel_64QAM_FullRB



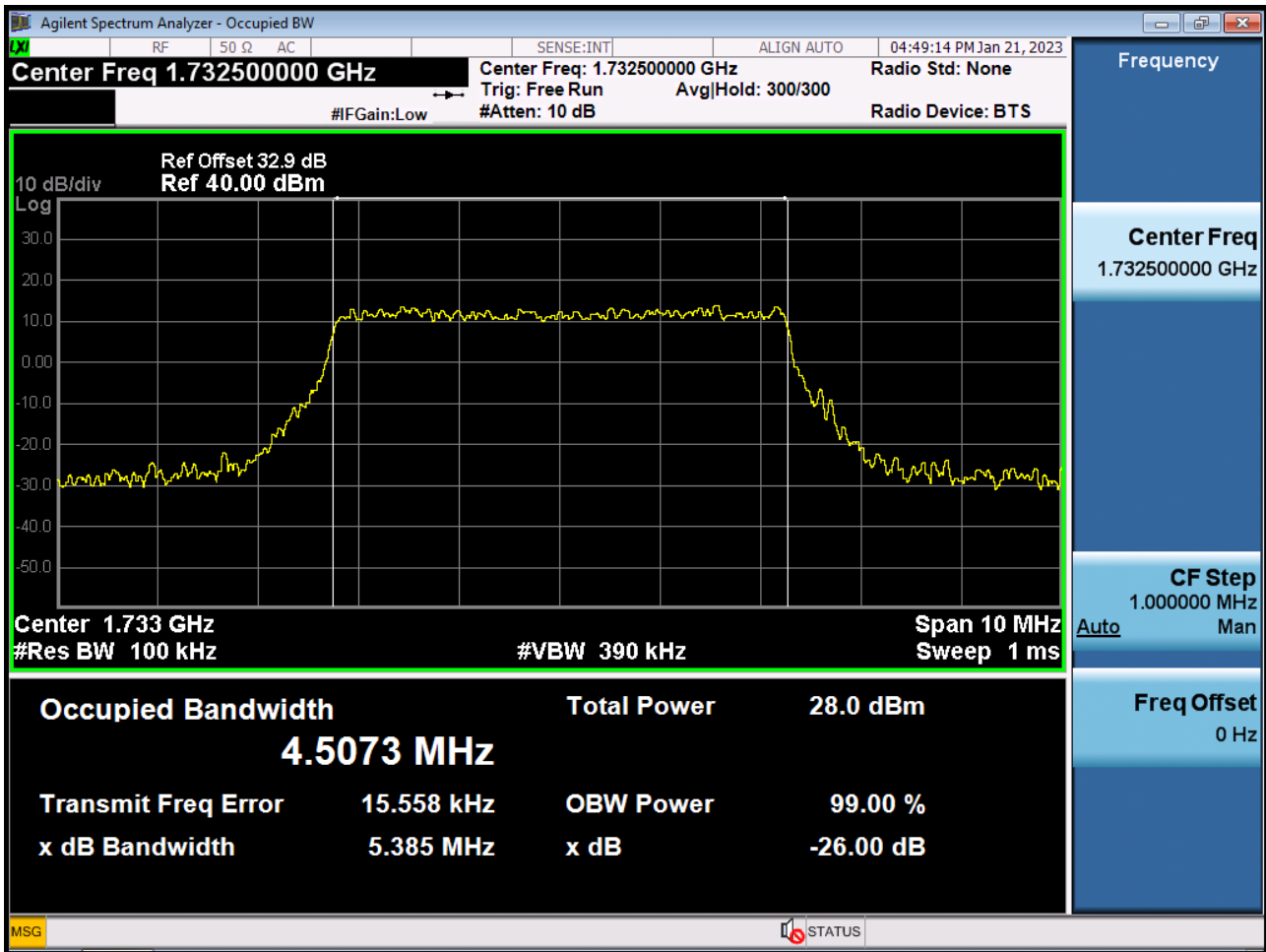
BW3 M_OBW_Middle Channel_256QAM_FullIRB



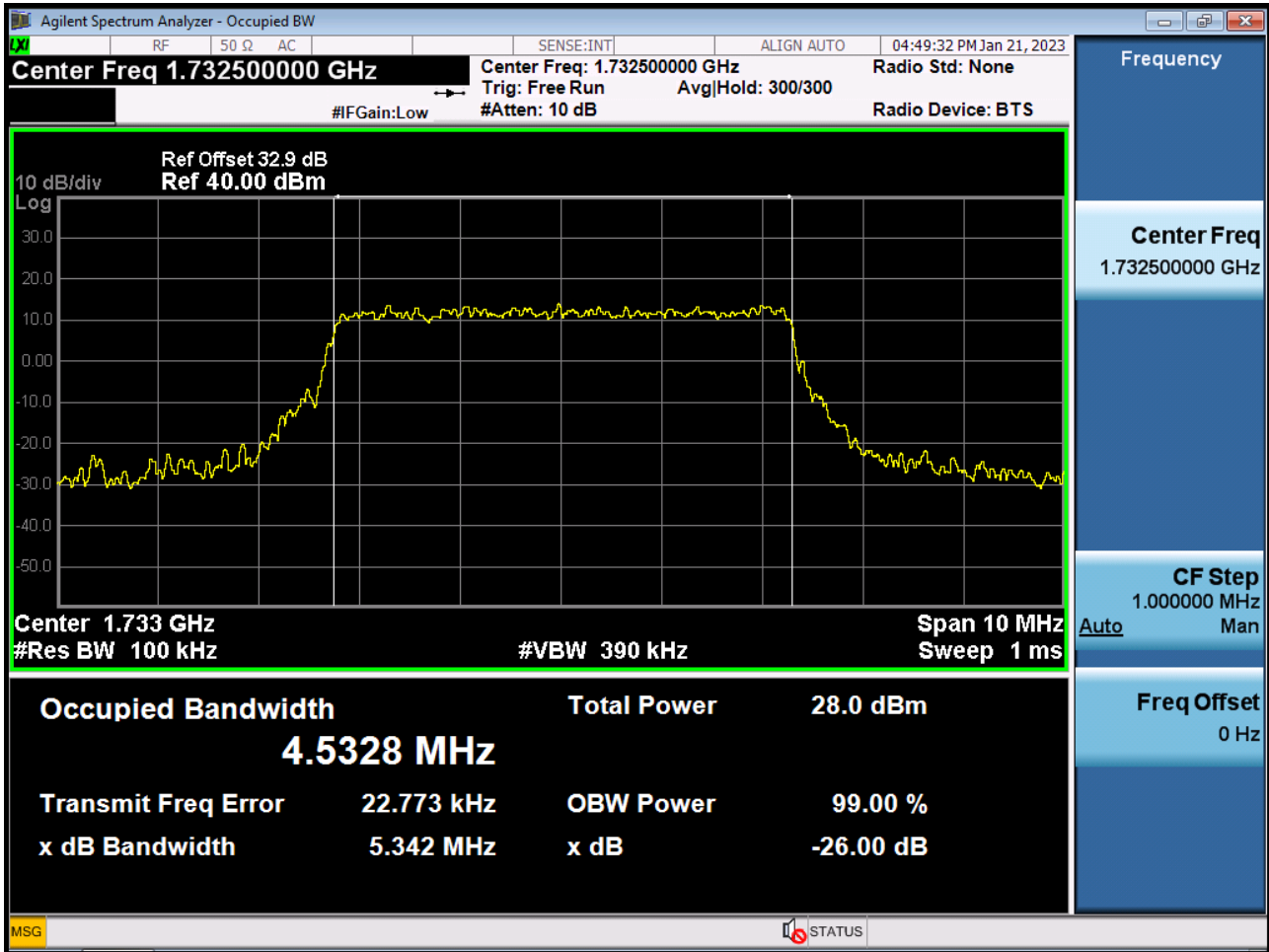
BW5 M_OBW_Middle Channel_QPSK_FullIRB



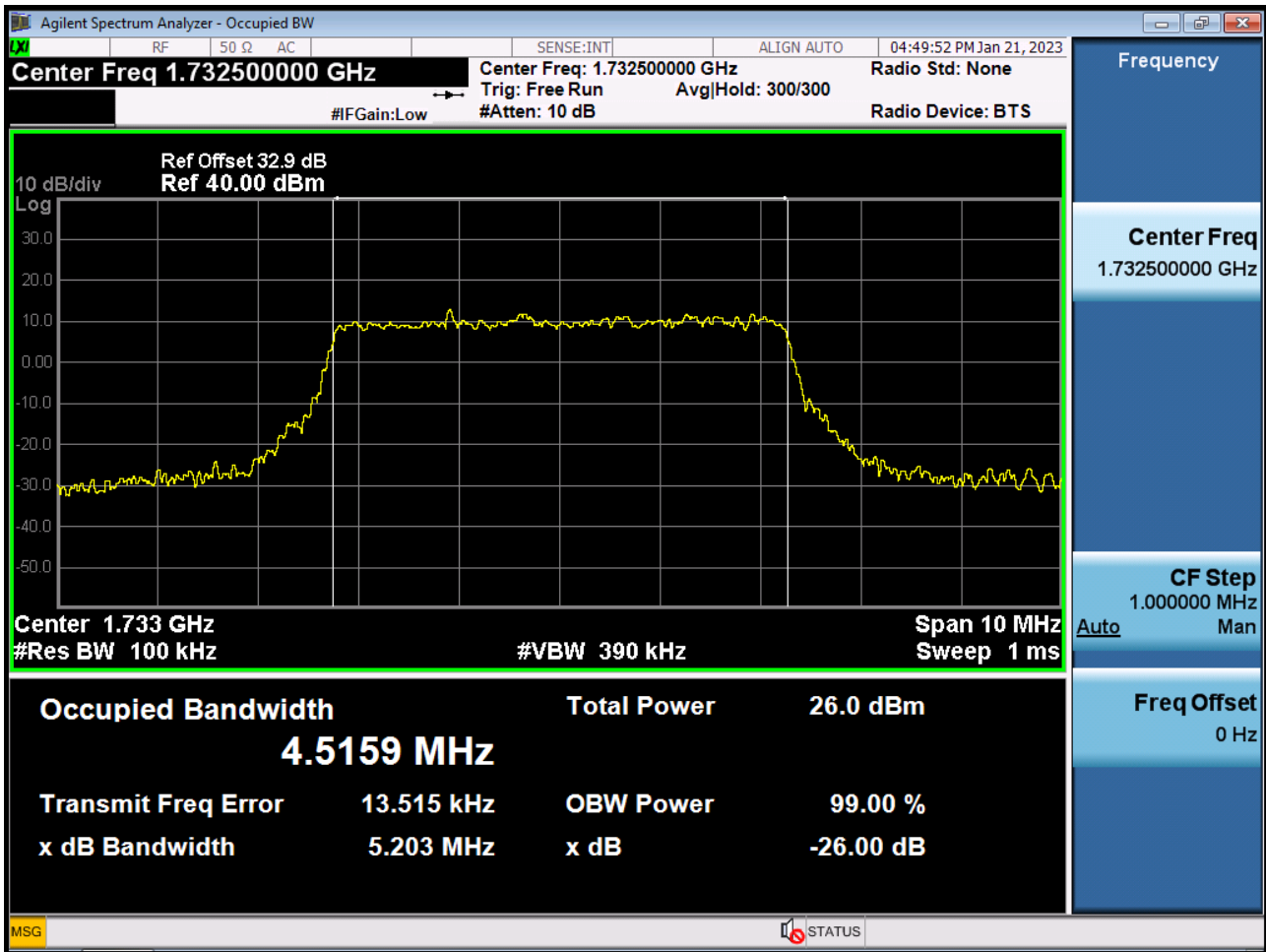
BW5 M_OBW_Middle Channel_16QAM_FullRB



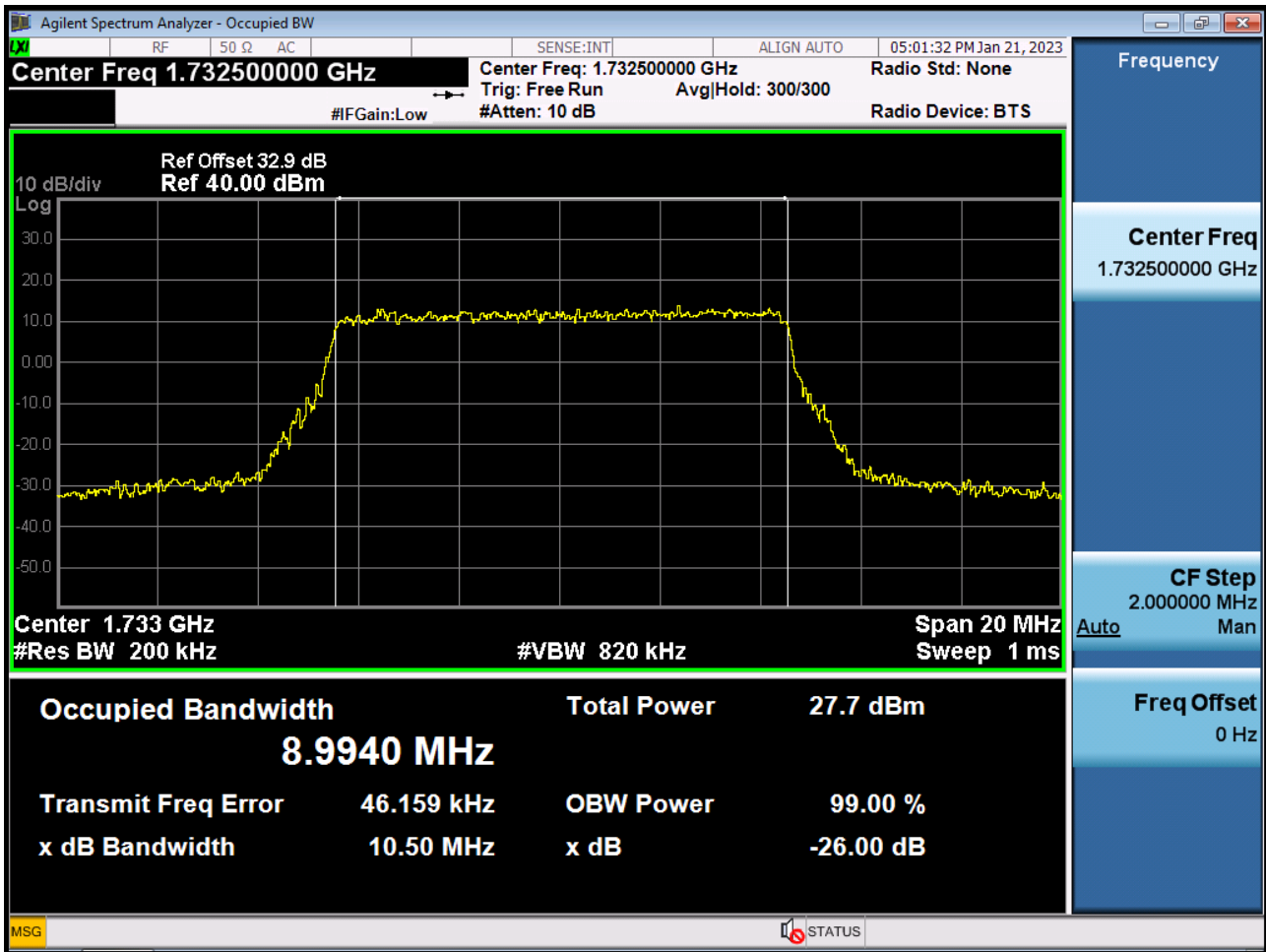
BW5 M_OBW_Middle Channel_64QAM_FullRB



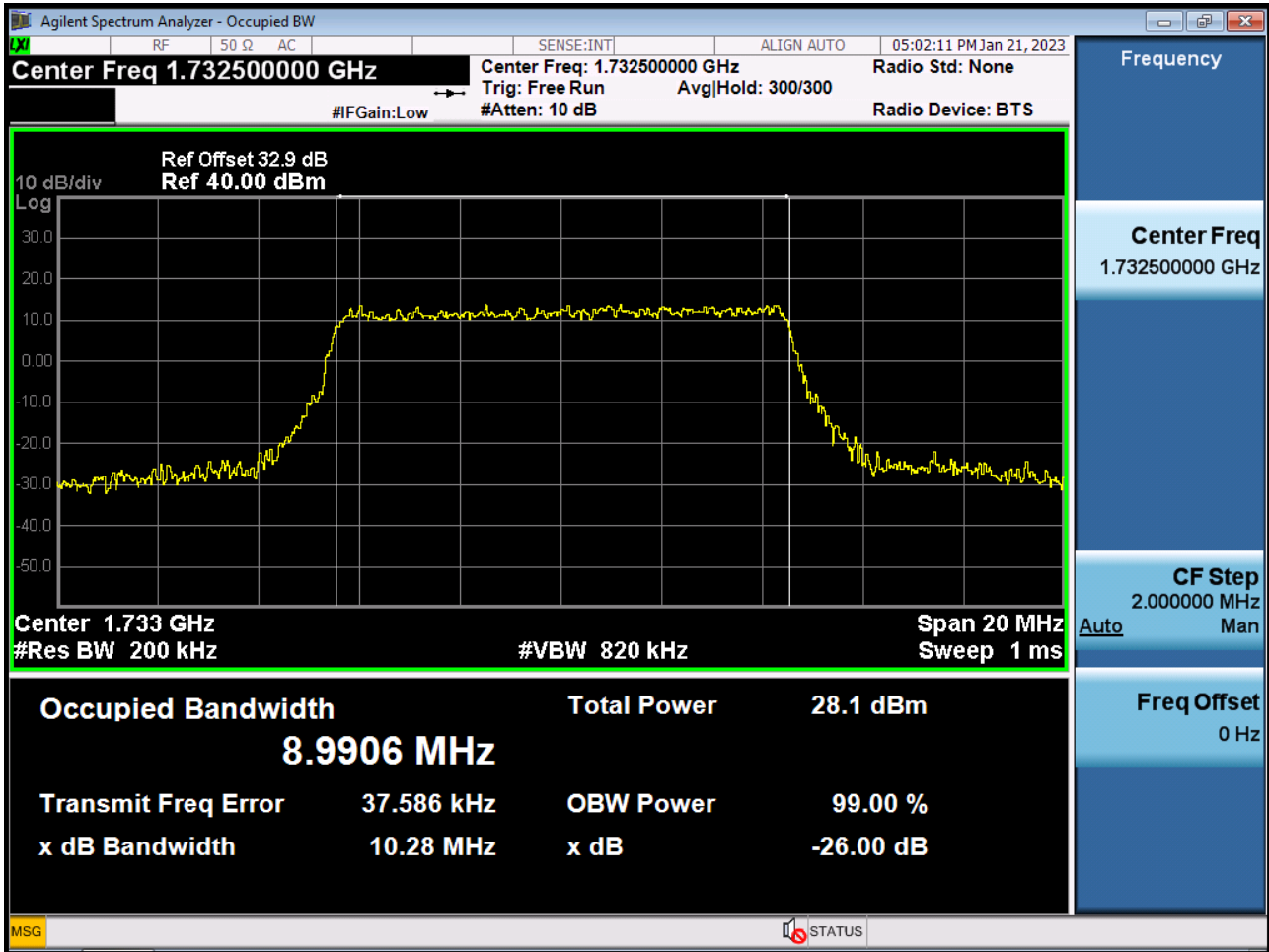
BW5 M_OBW_Middle Channel_256QAM_FullIRB



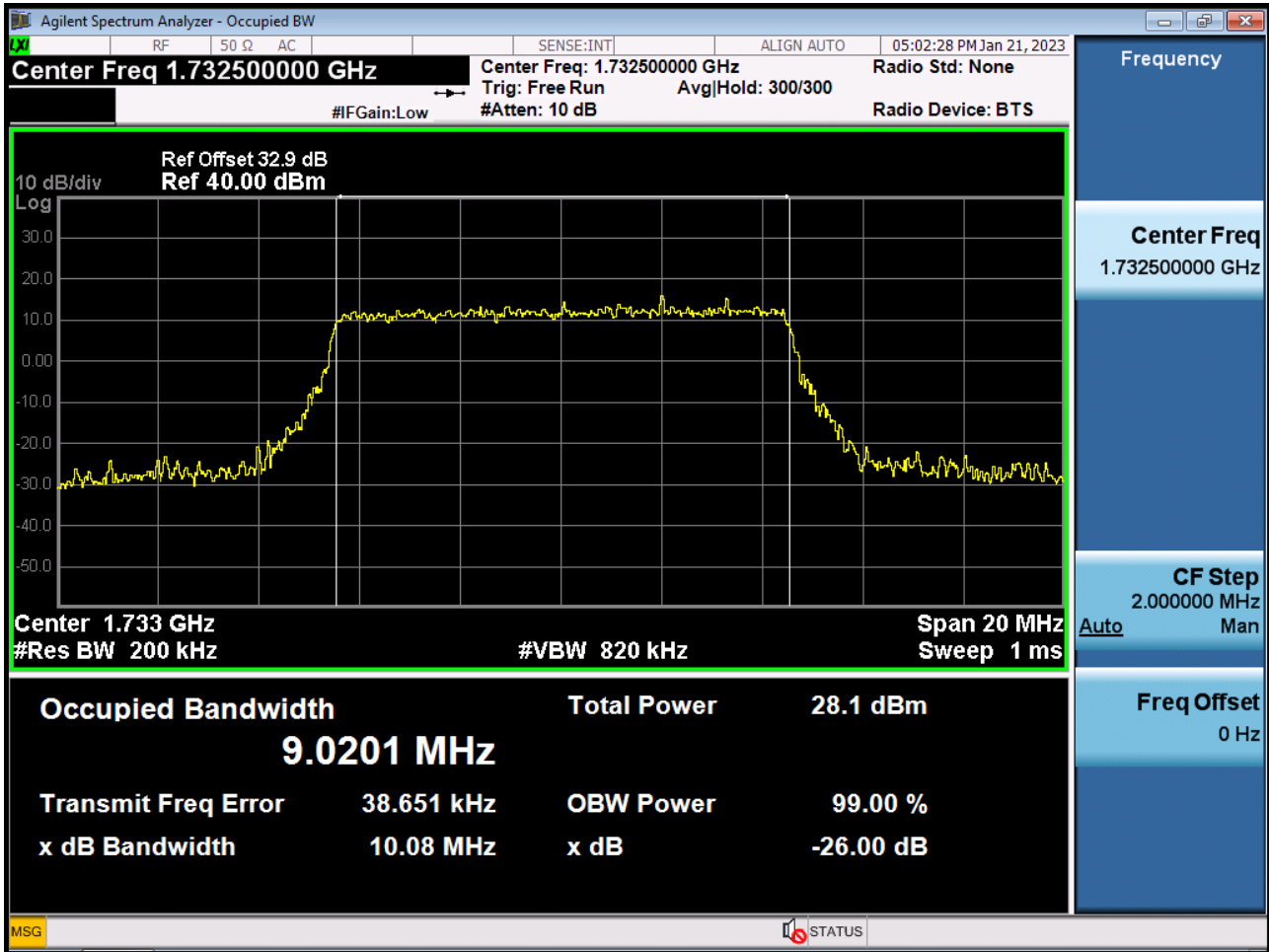
BW10 M_OBW_Middle Channel_QPSK_FullRB



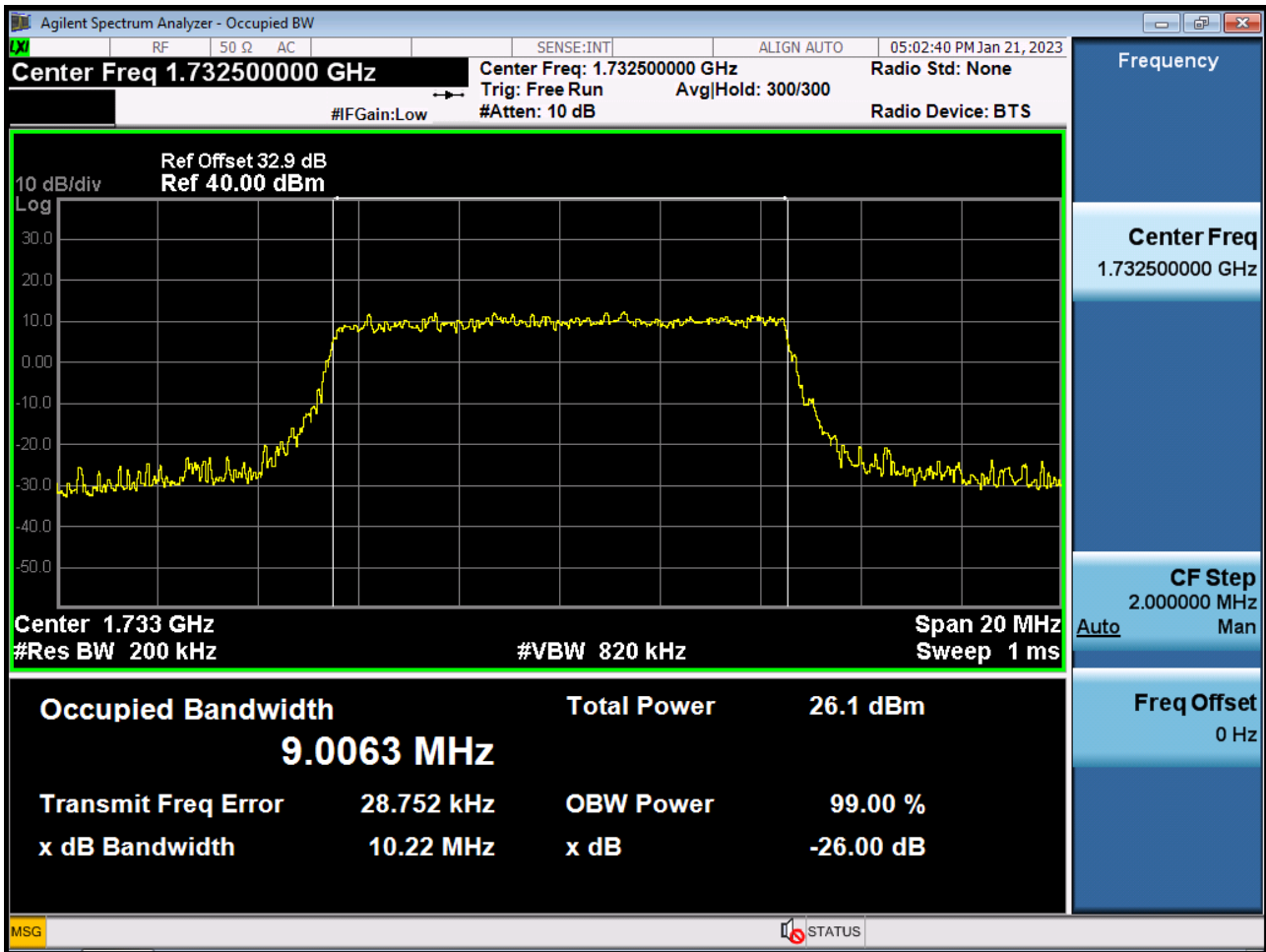
BW10 M_OBW_Middle Channel_16QAM_FullIRB



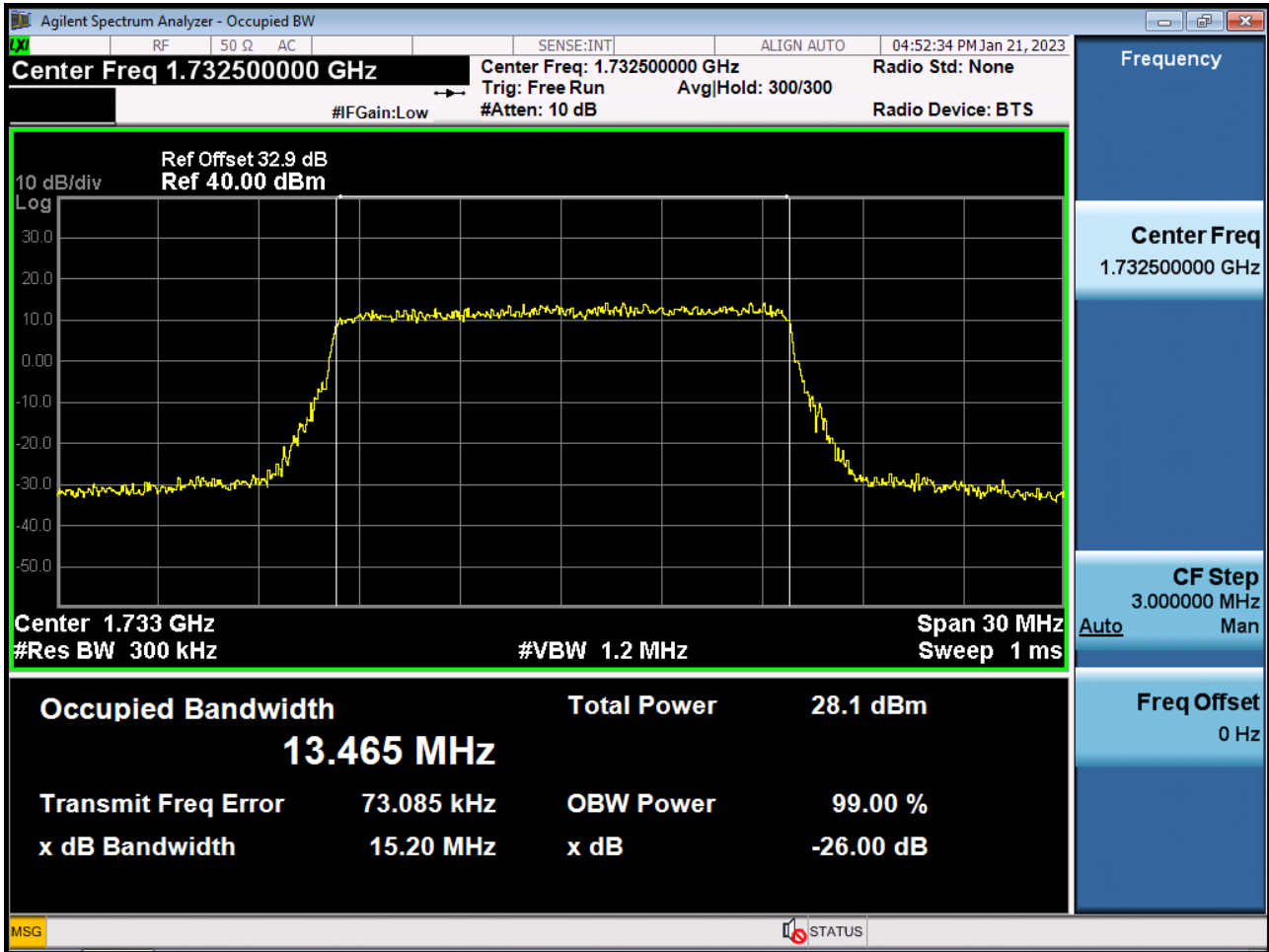
BW10 M_OBW_Middle Channel_64QAM_FullIRB



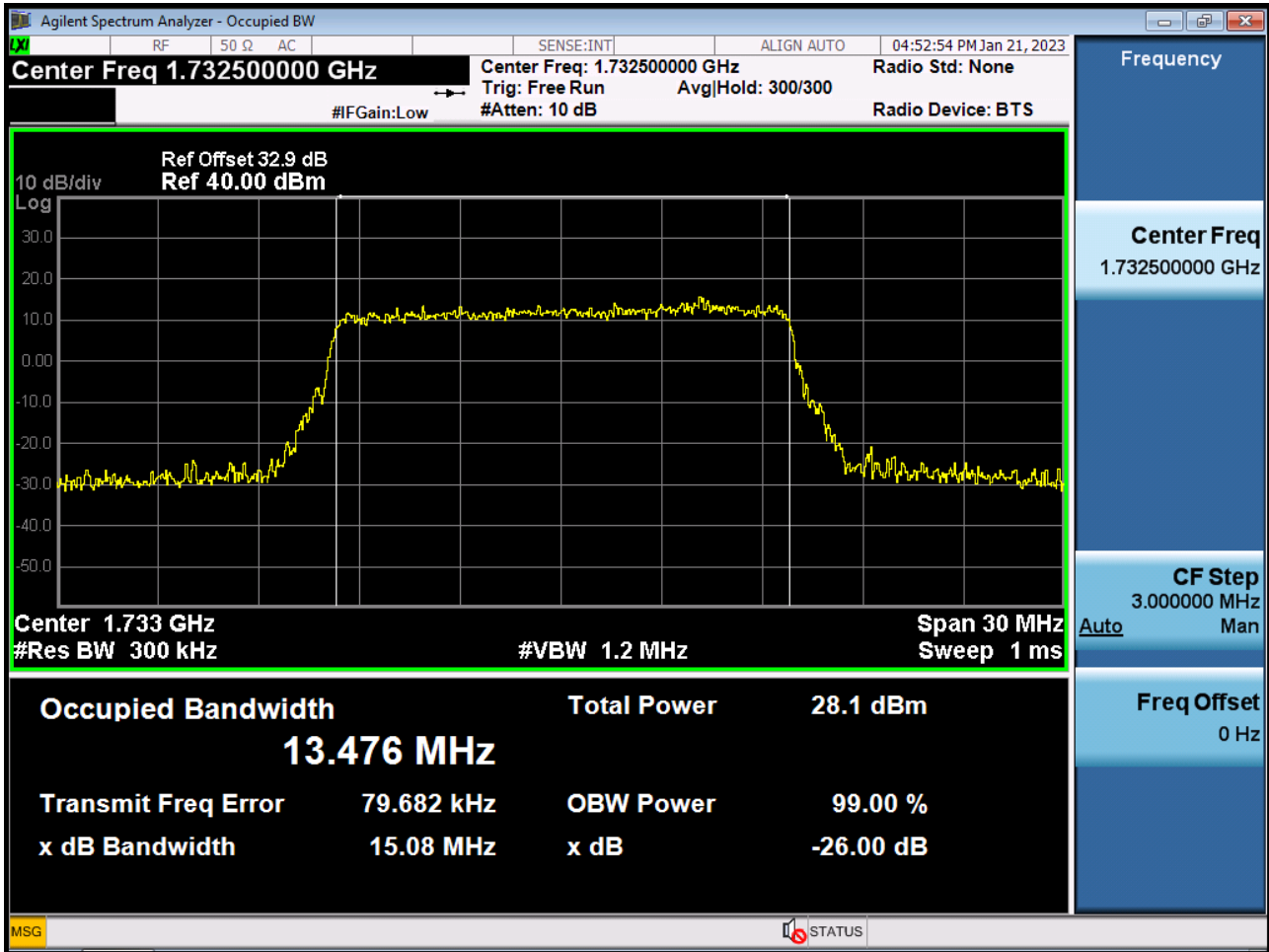
BW10 M_OBW_Middle Channel_256QAM_FullIRB



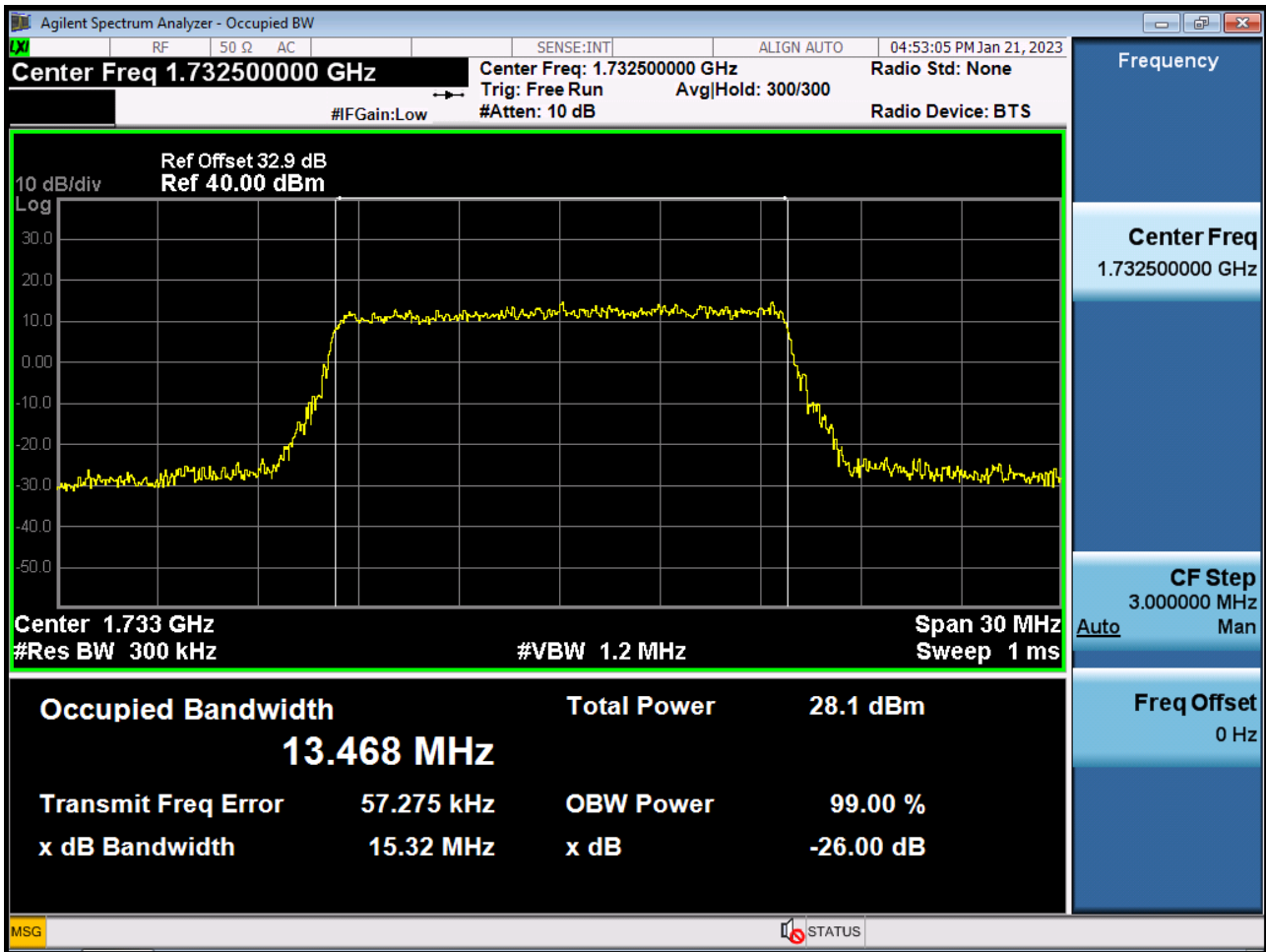
BW15 M_OBW_Middle Channel_QPSK_FullRB



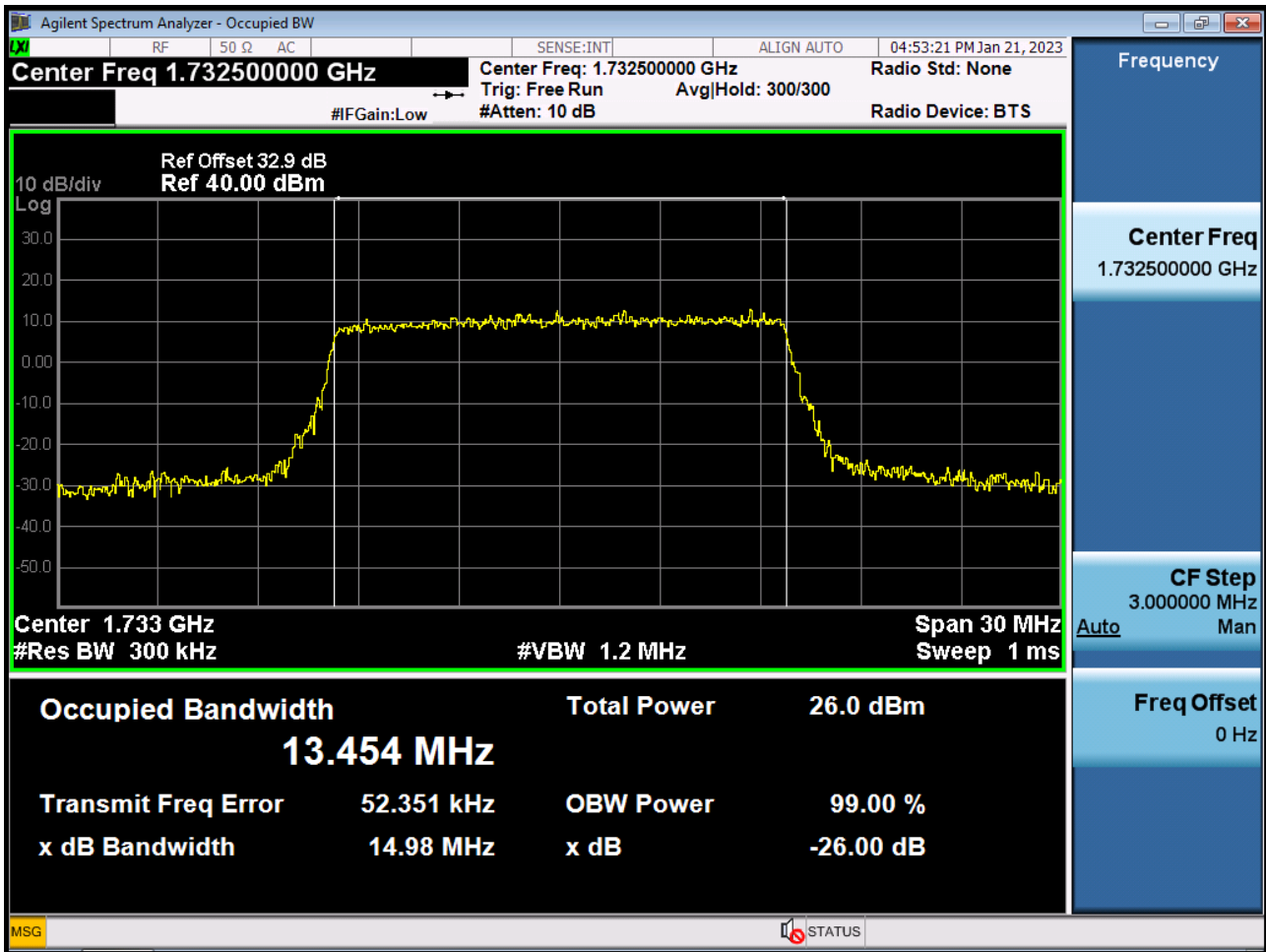
BW15 M_OBW_Middle Channel_16QAM_FullIRB



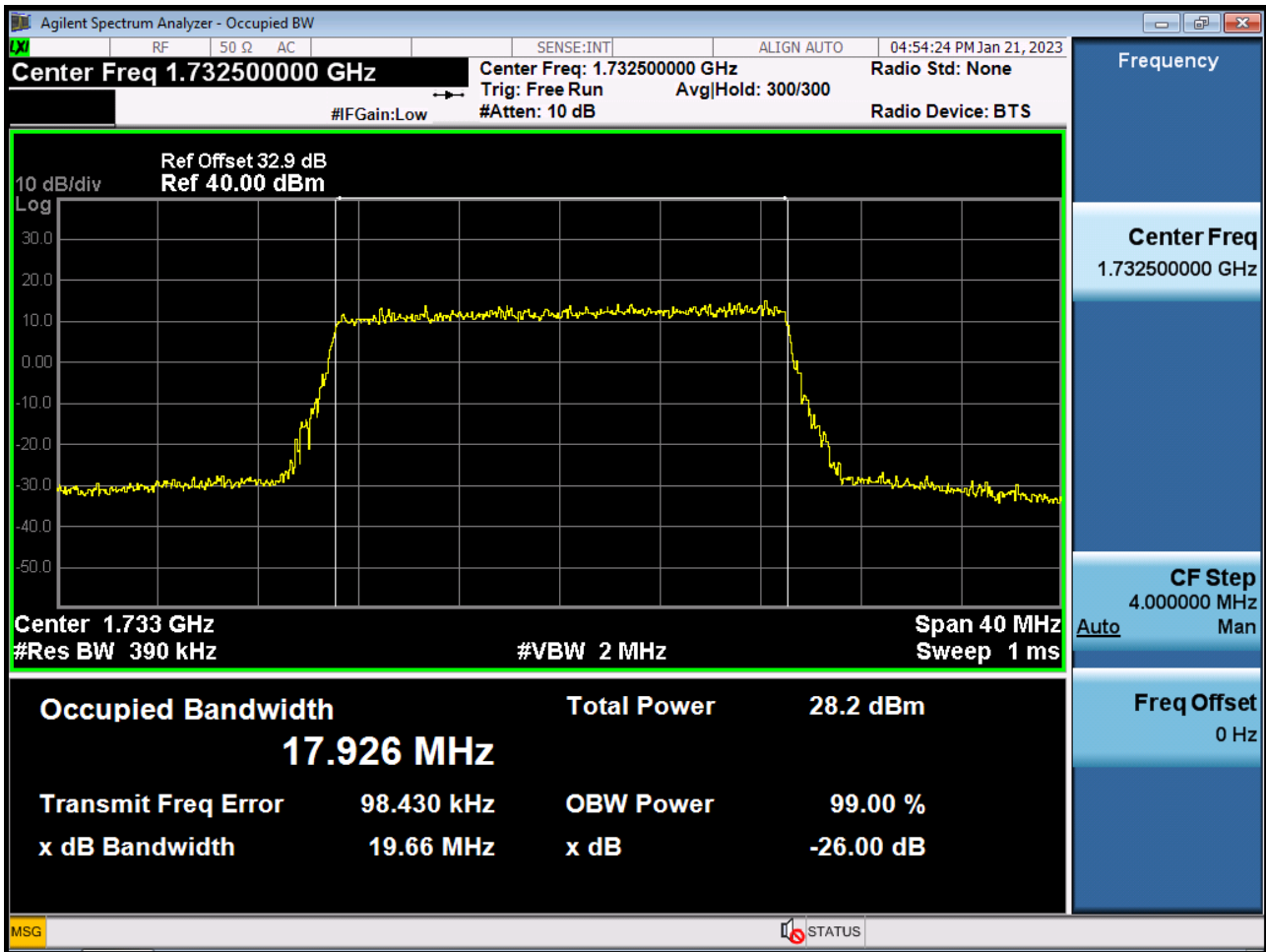
BW15 M_OBW_Middle Channel_64QAM_FullIRB



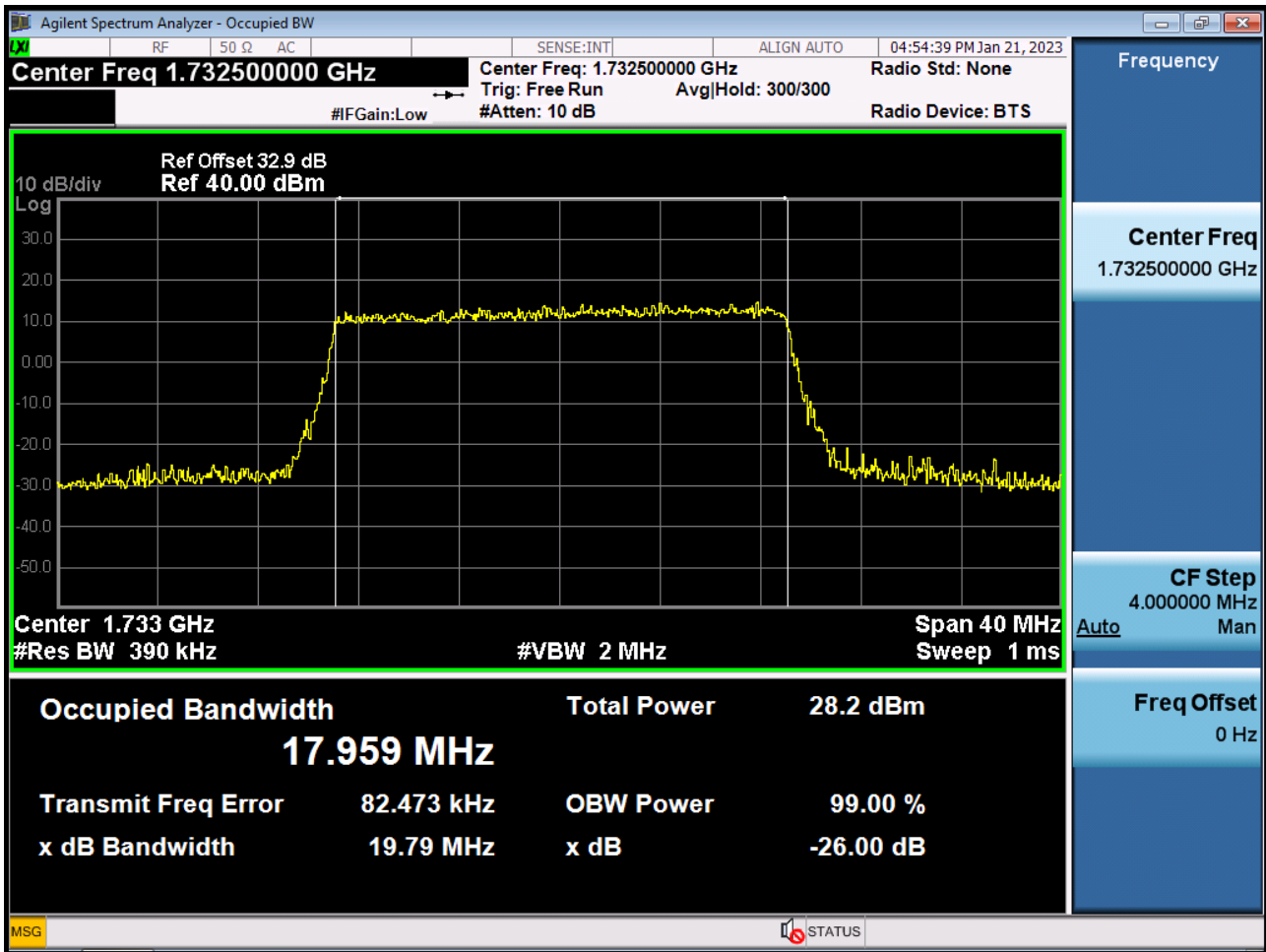
BW15 M_OBW_Middle Channel_256QAM_FullRB



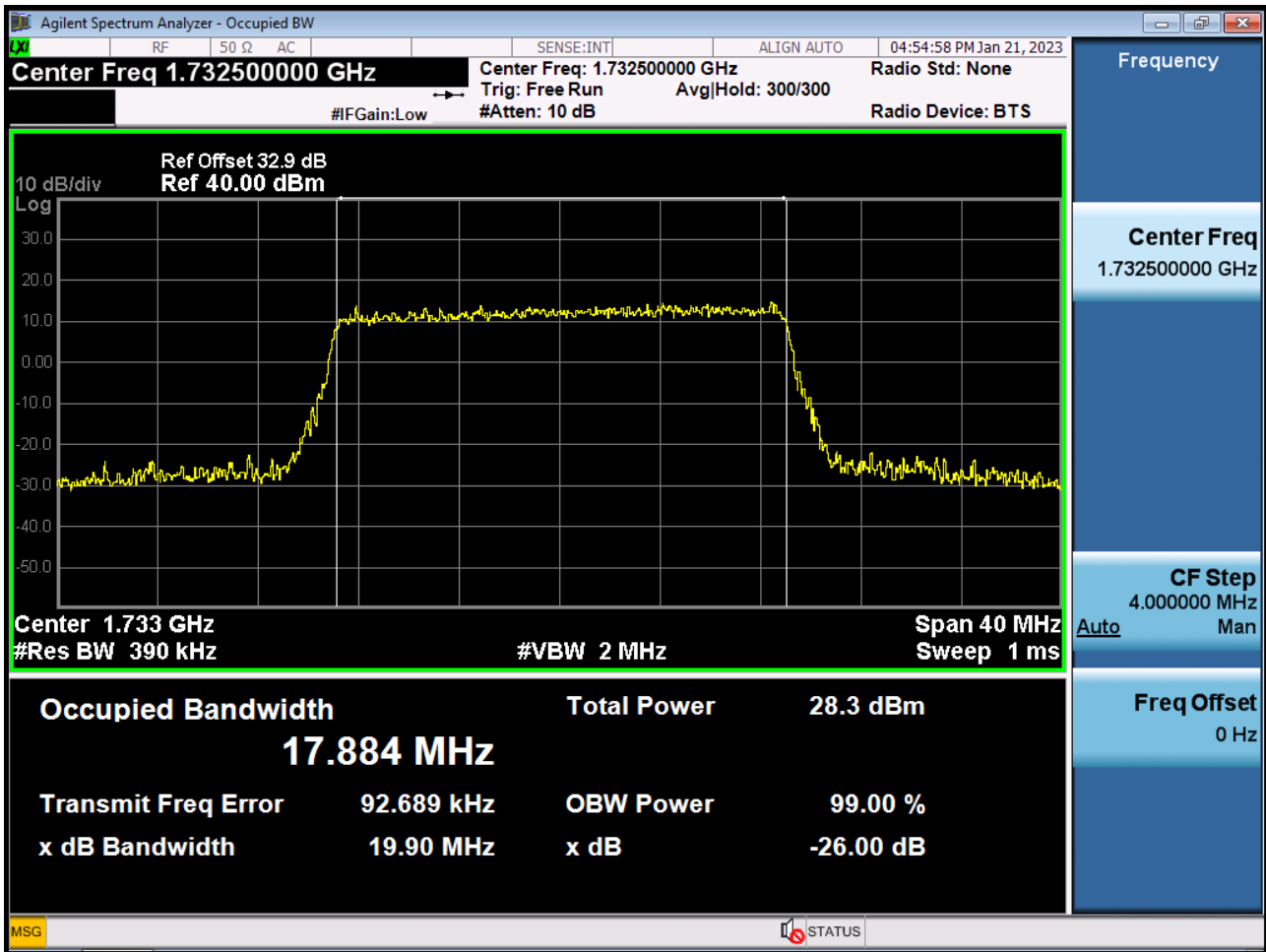
BW20 M_OBW_Middle Channel_QPSK_FullRB



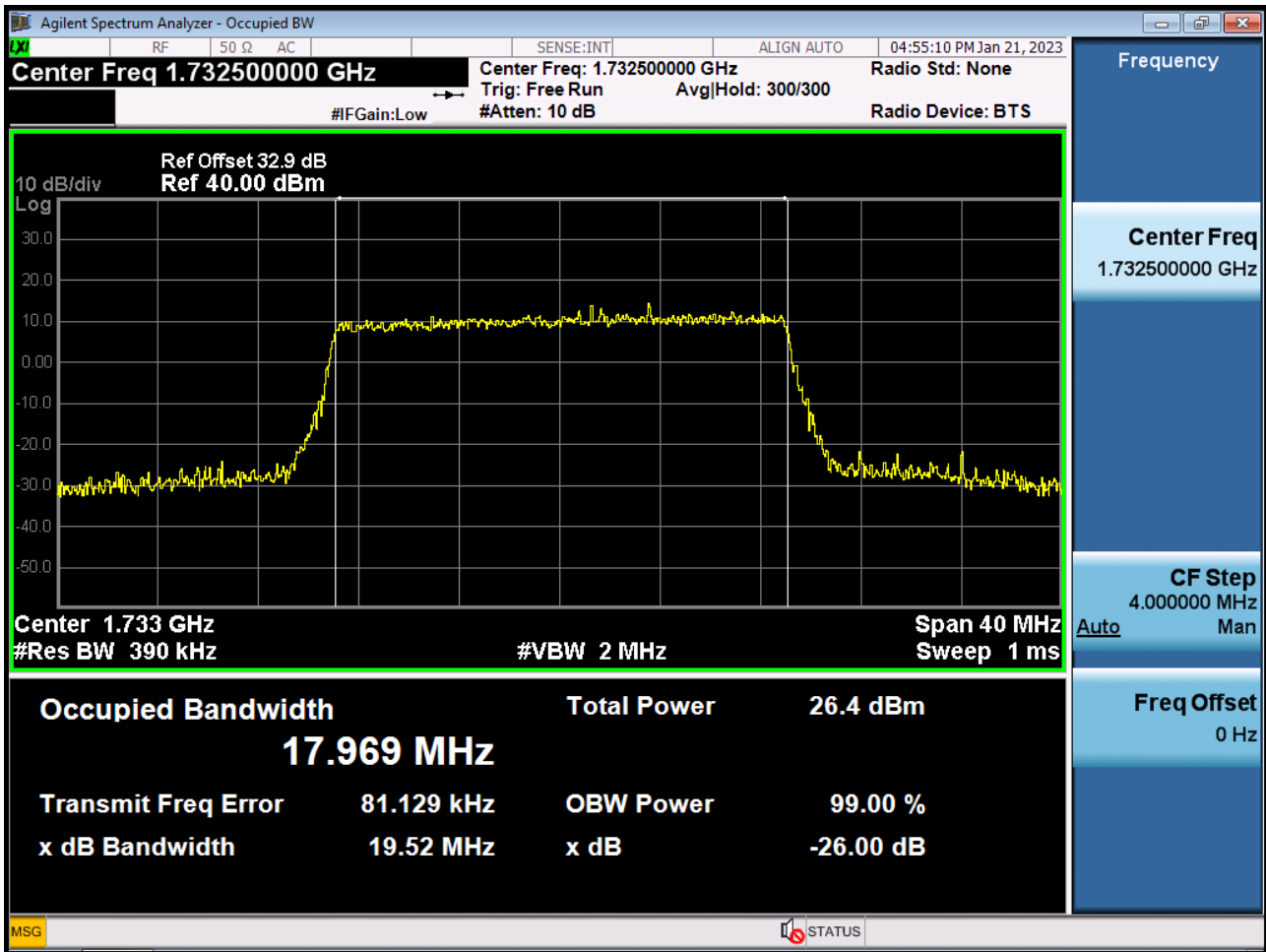
BW20 M_OBW_Middle Channel_16QAM_FullIRB



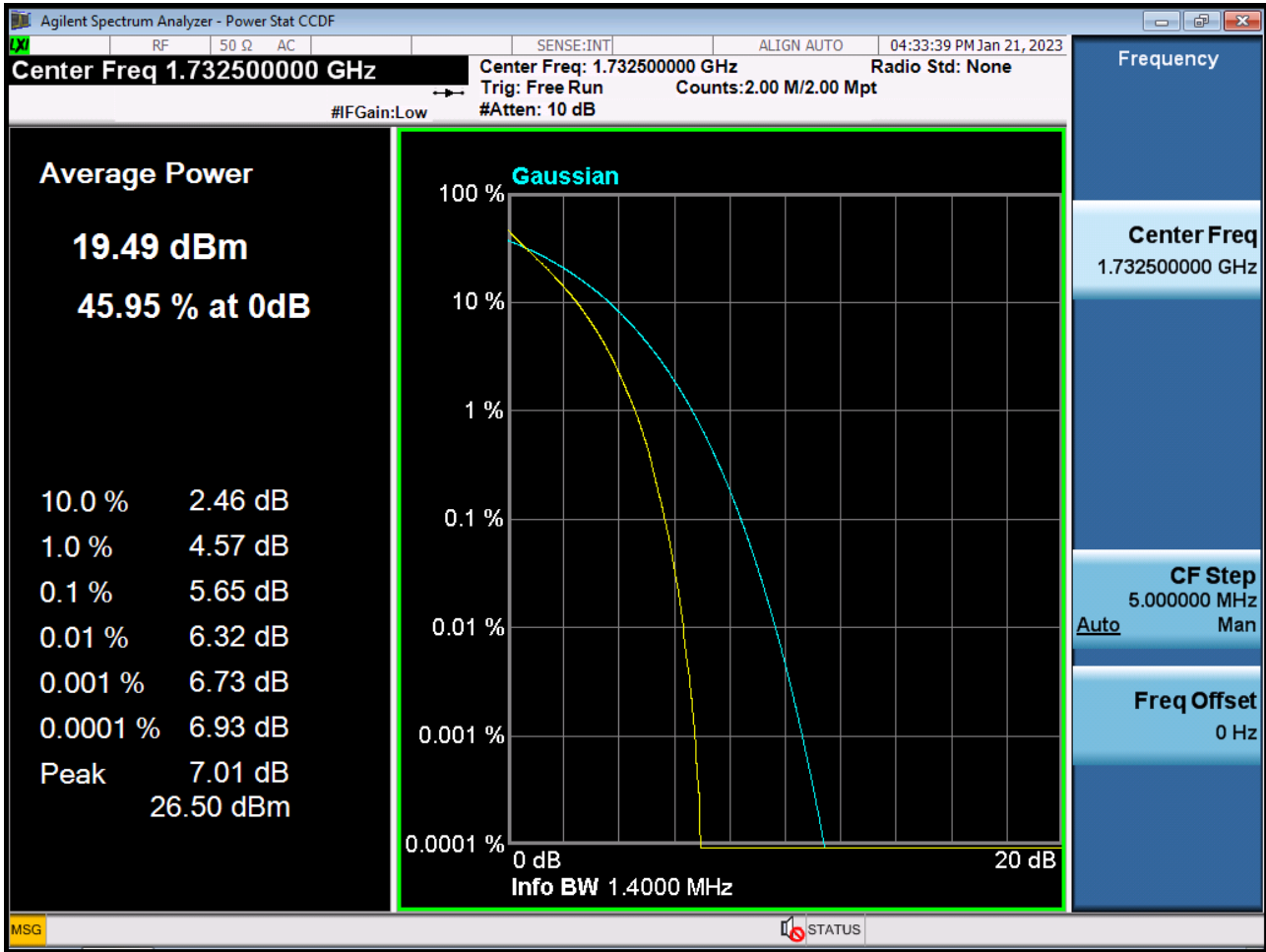
BW20 M_OBW_Middle Channel_64QAM_FullIRB



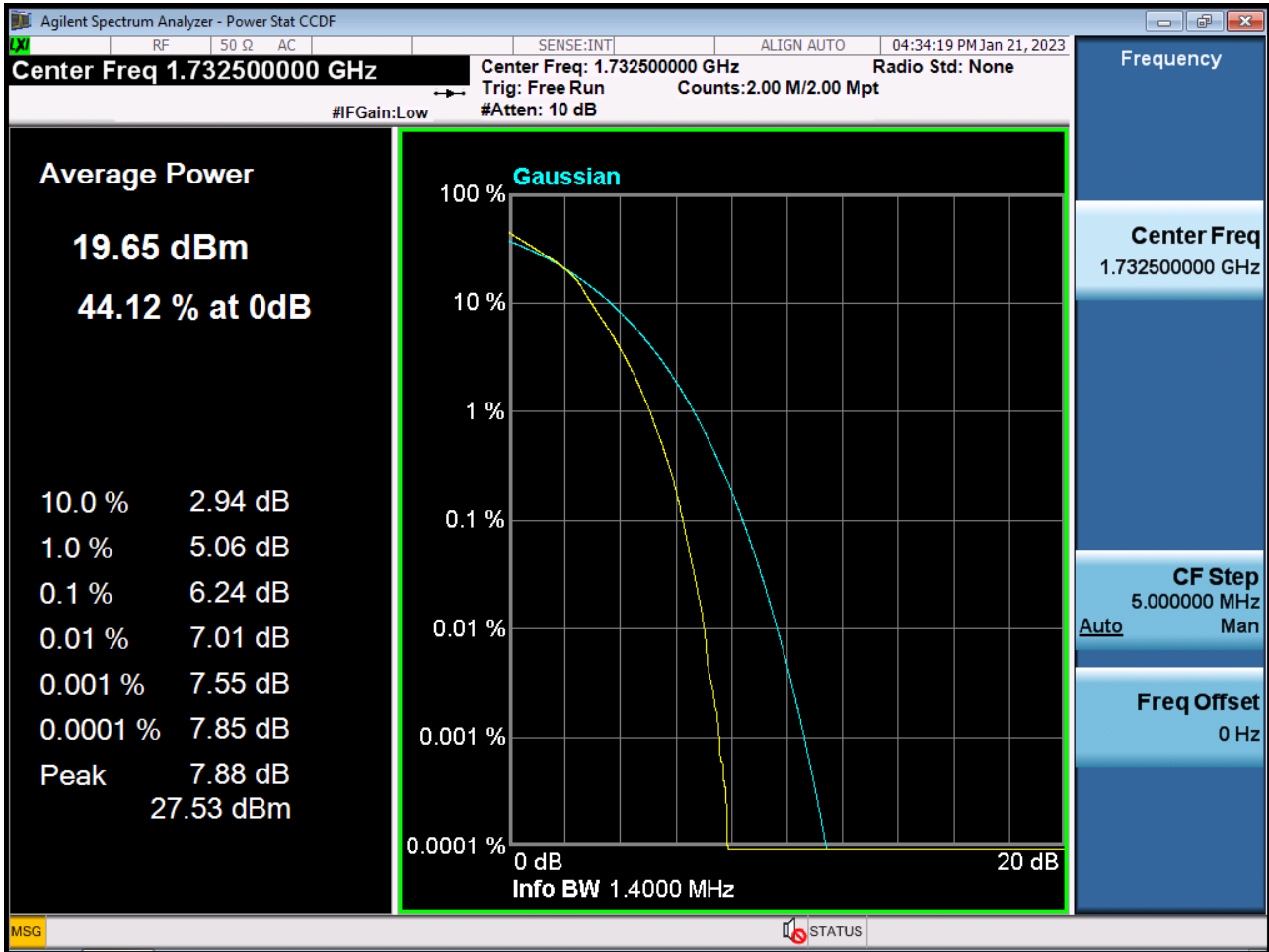
BW20 M_OBW_Middle Channel_256QAM_FullRB



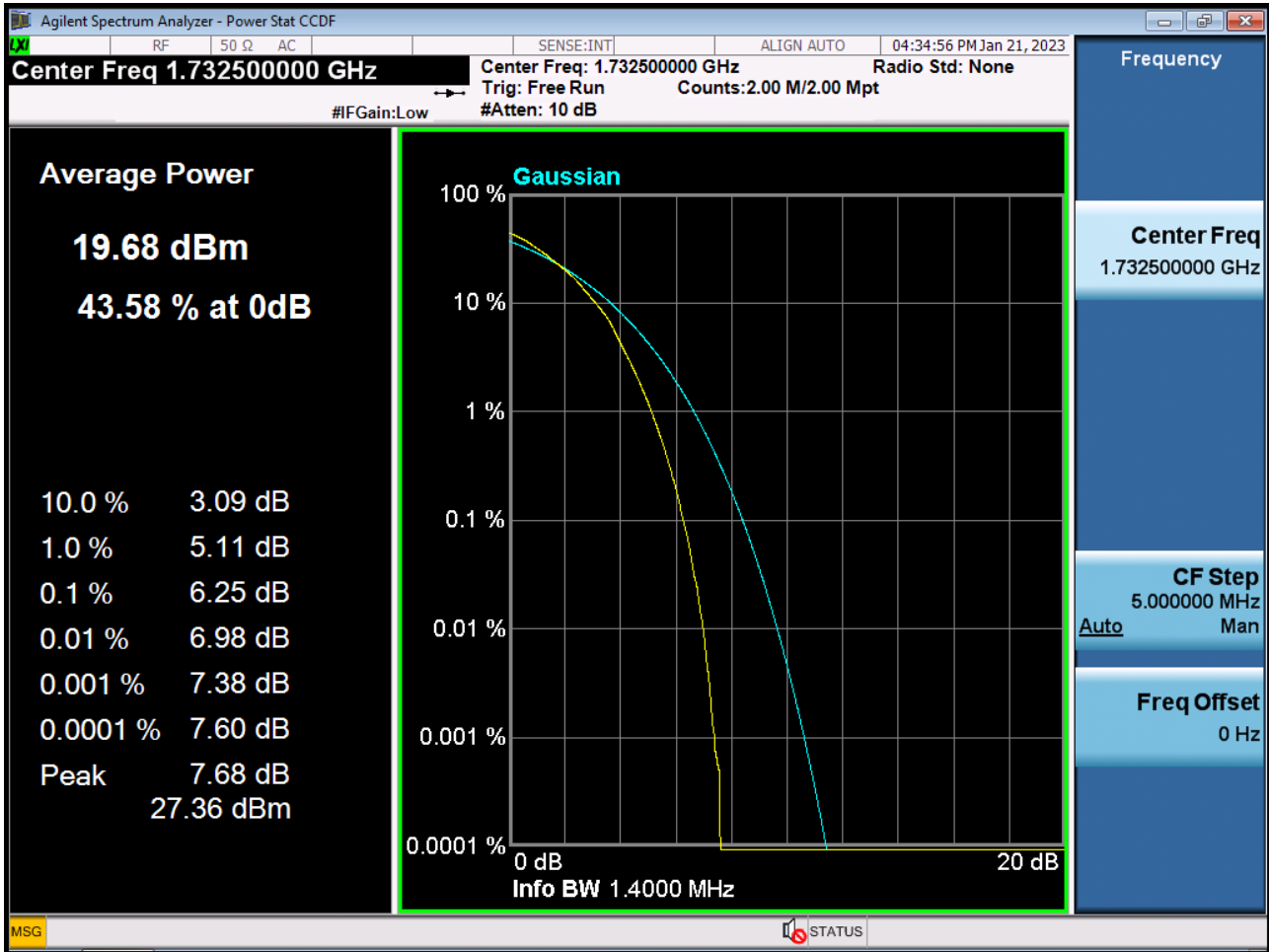
BW1.4 M_PAR_Middle Channel_QPSK_FullIRB



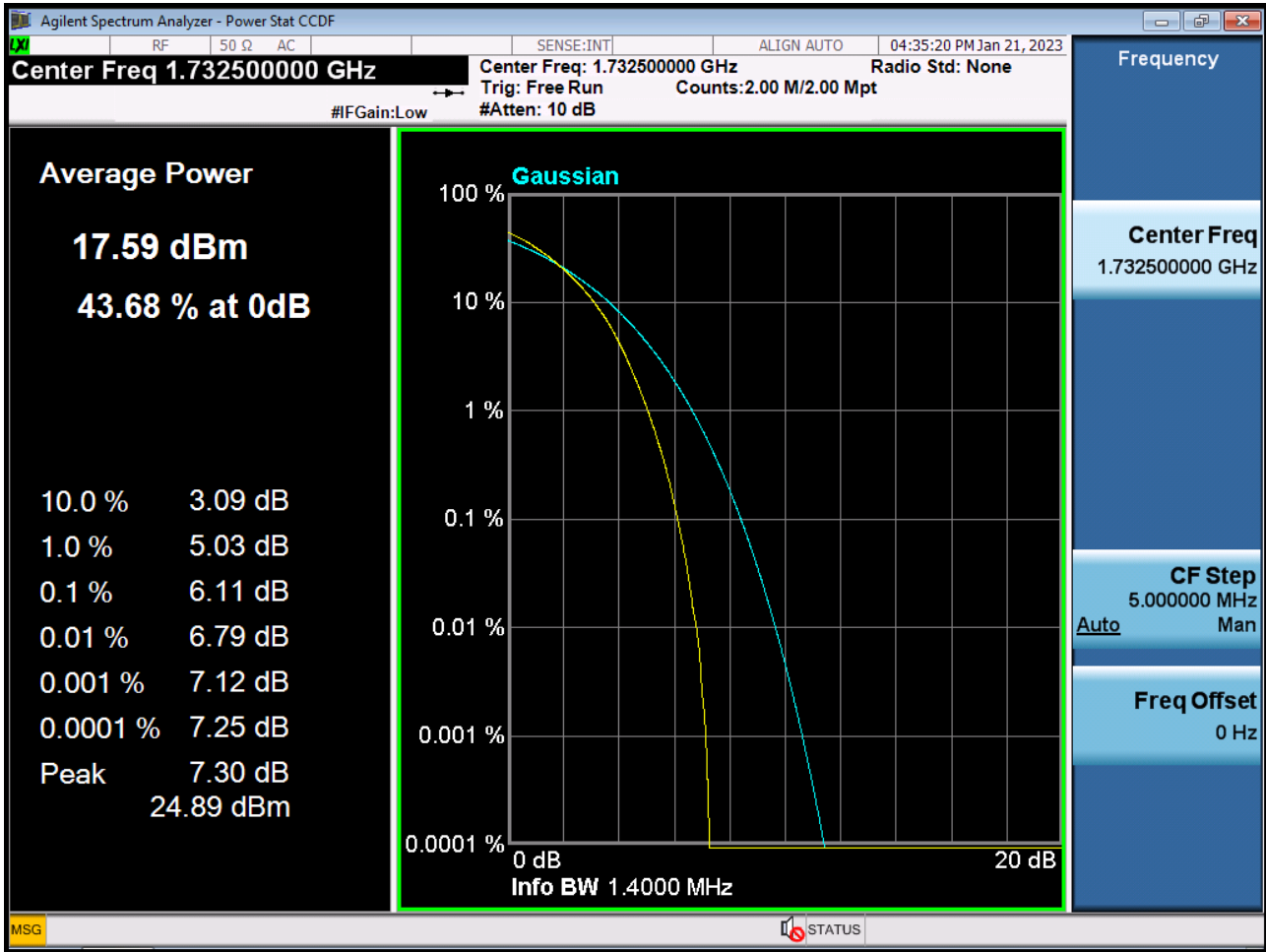
BW1.4 M_PAR_Middle Channel_16QAM_FullIRB



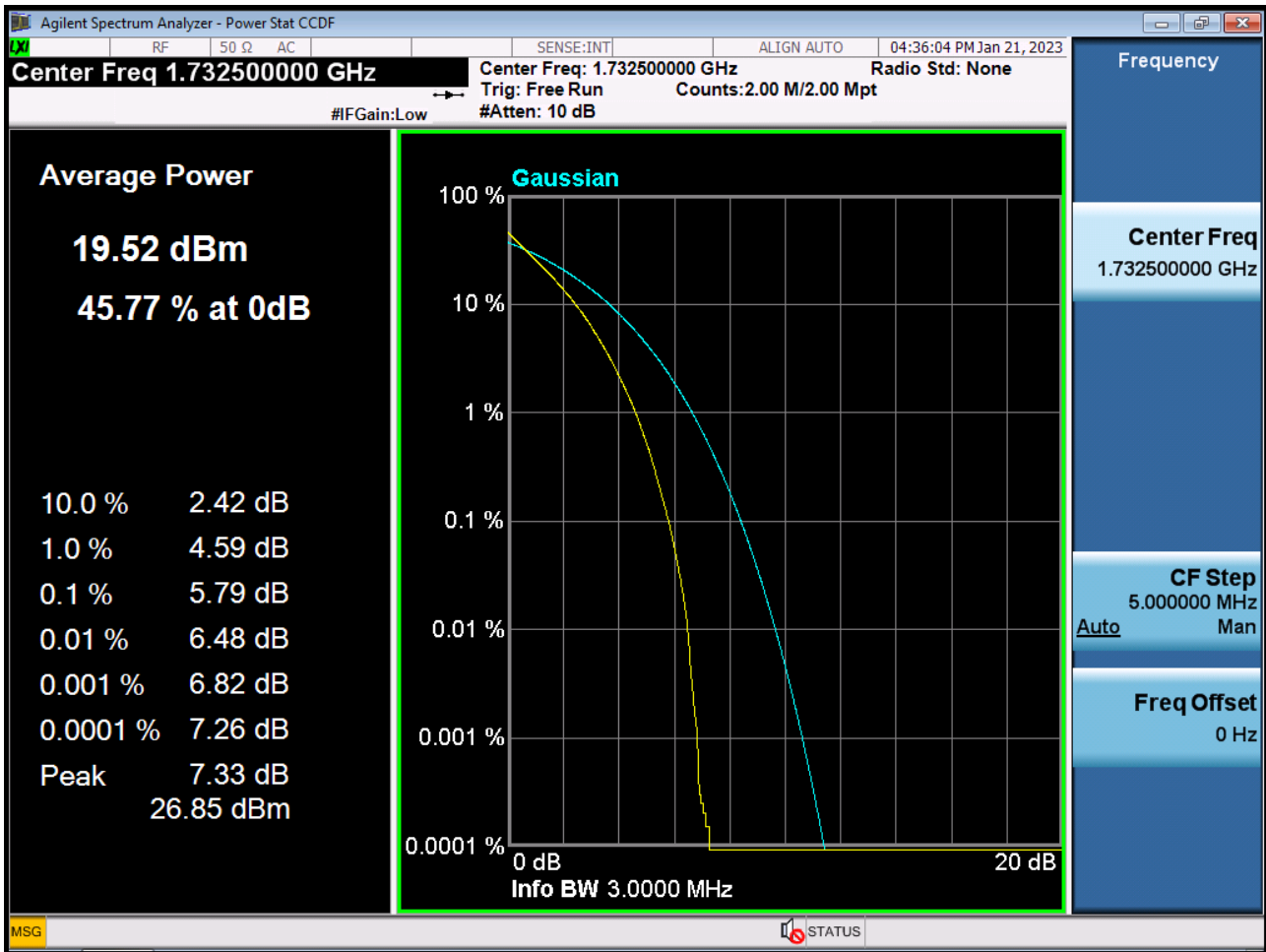
BW1.4 M_PAR_Middle Channel_64QAM_FullIRB



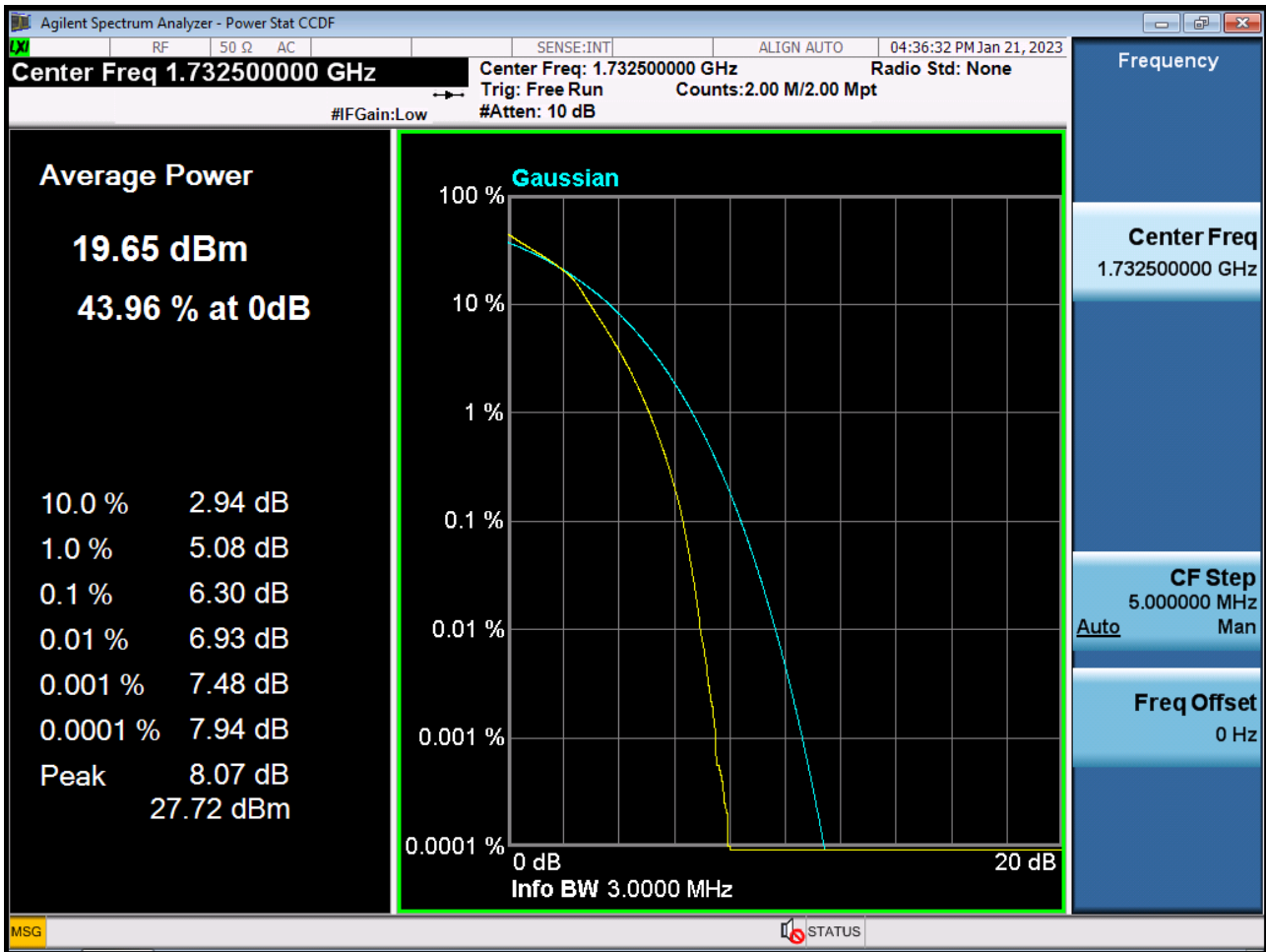
BW1.4 M_PAR_Middle Channel_256QAM_FuIRB



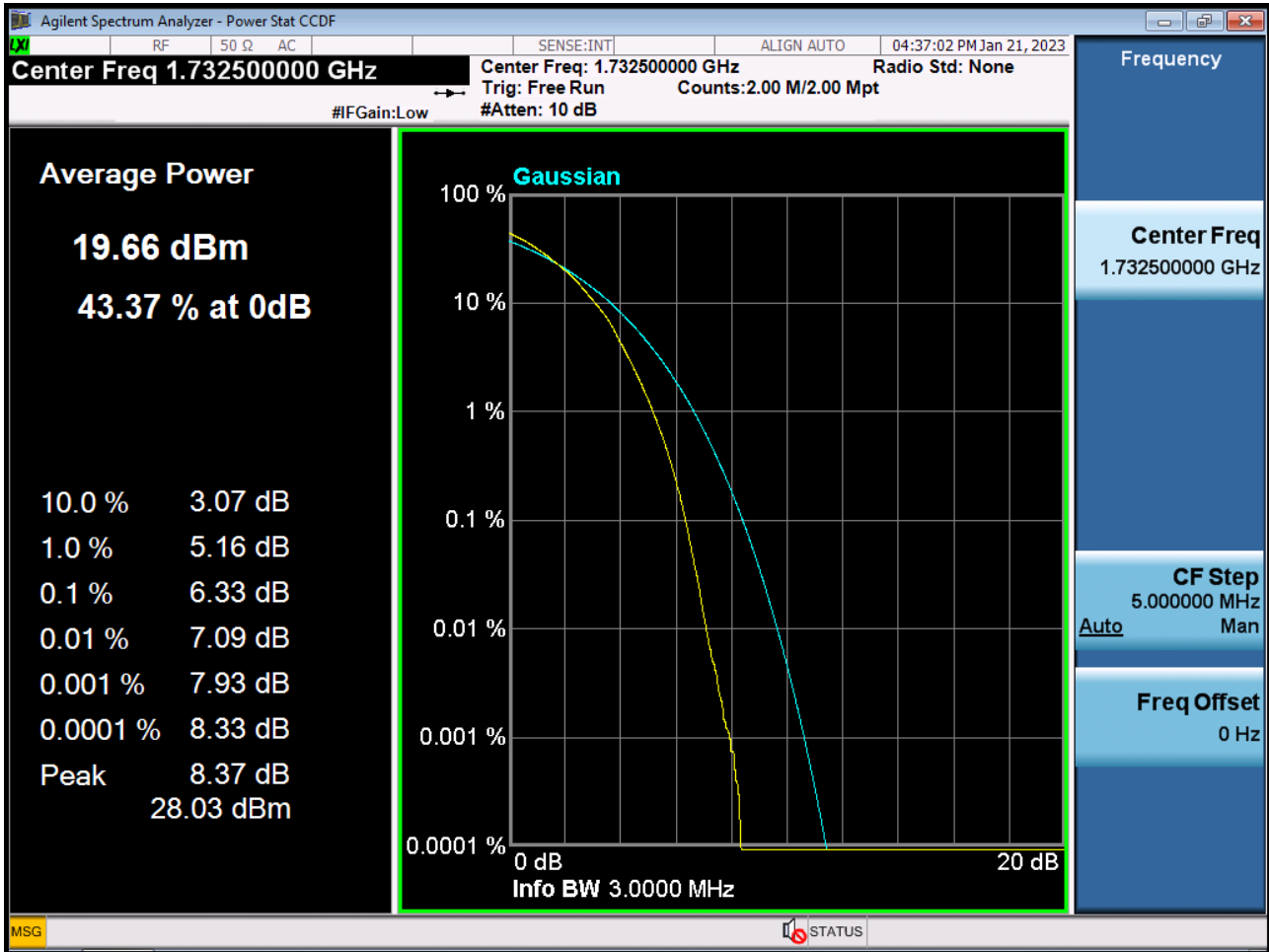
BW3 M_PAR_Middle Channel_QPSK_FullIRB



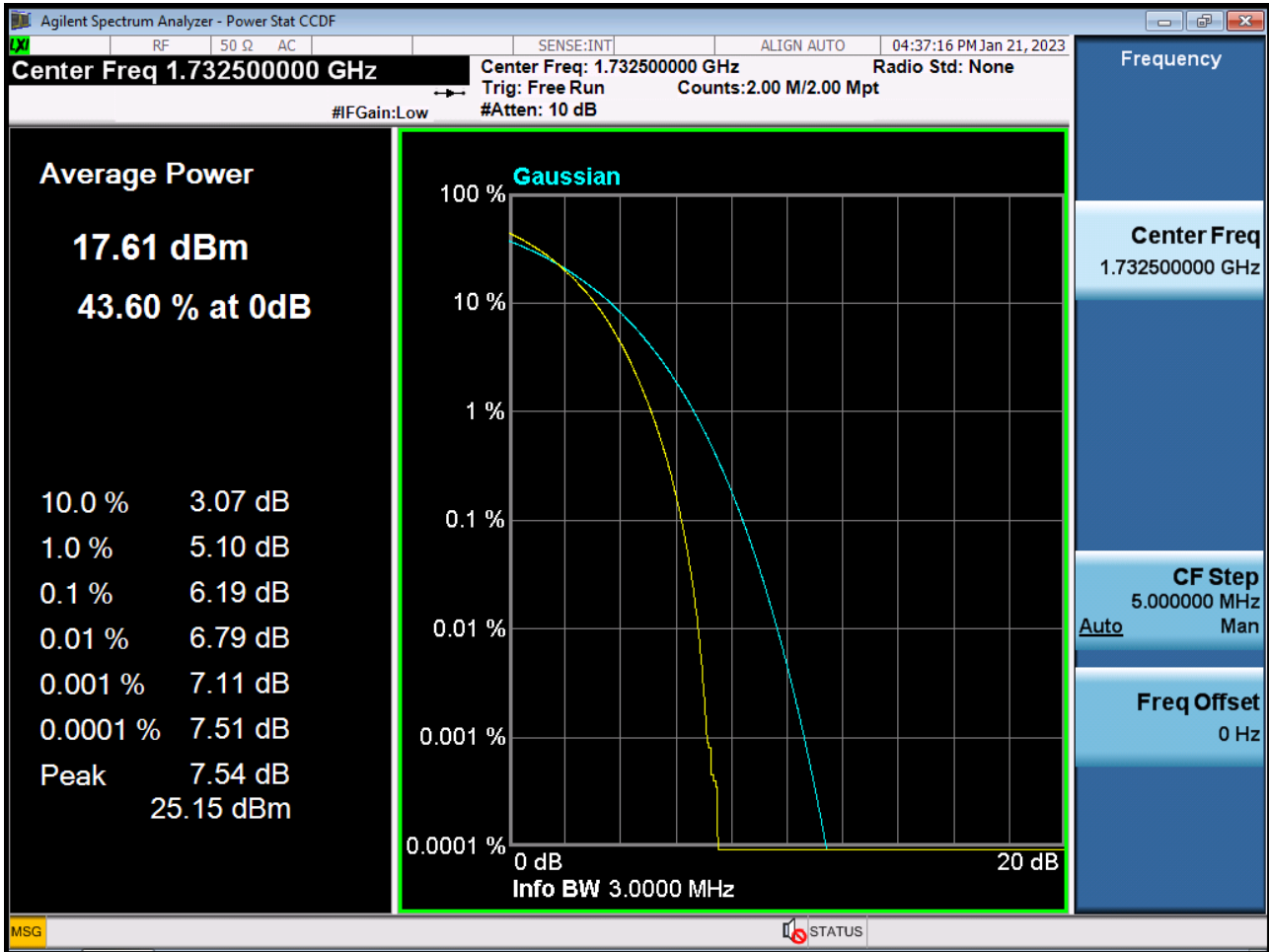
BW3 M_PAR_Middle Channel_16QAM_FullRB



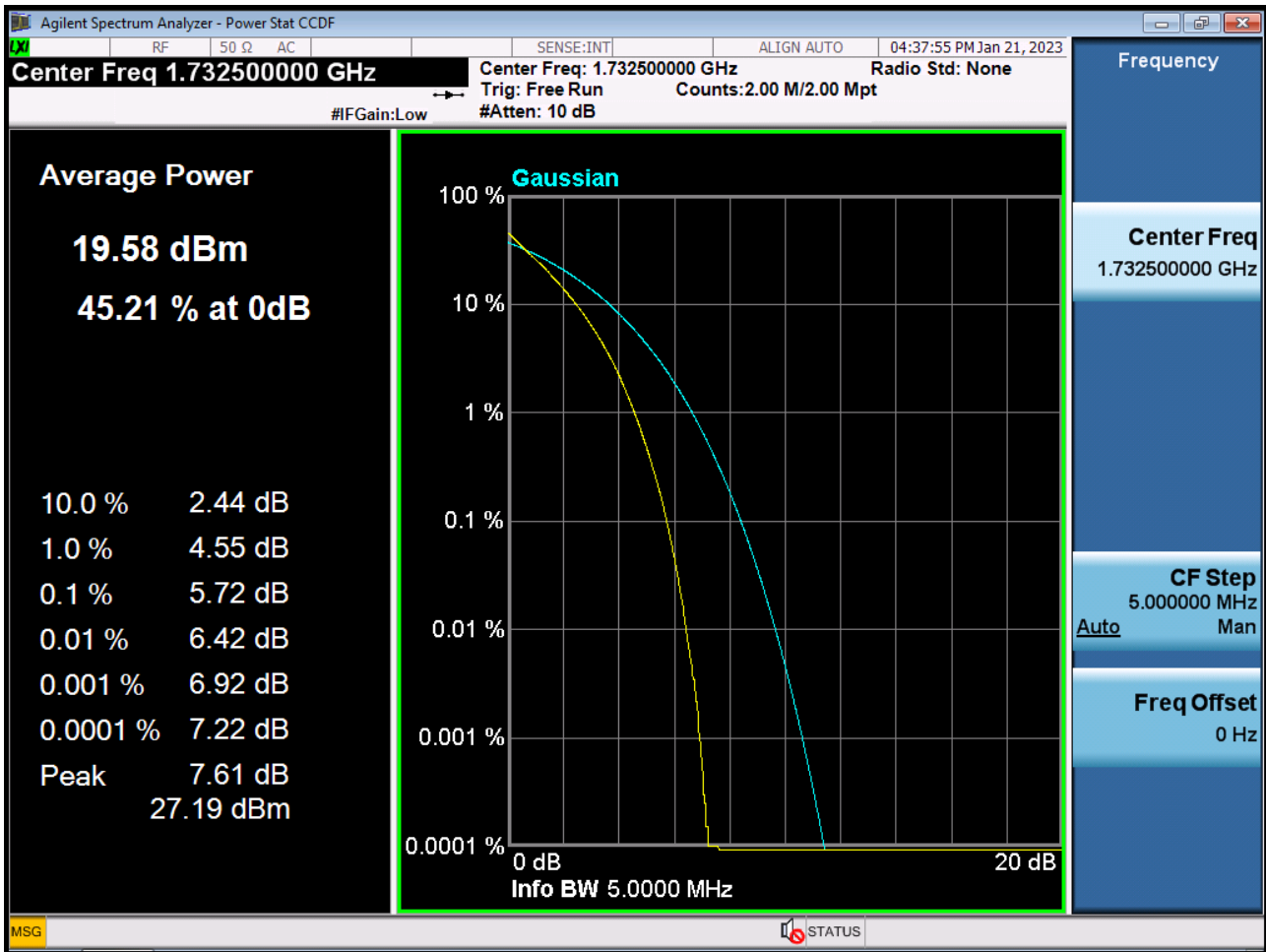
BW3 M_PAR_Middle Channel_64QAM_FullRB



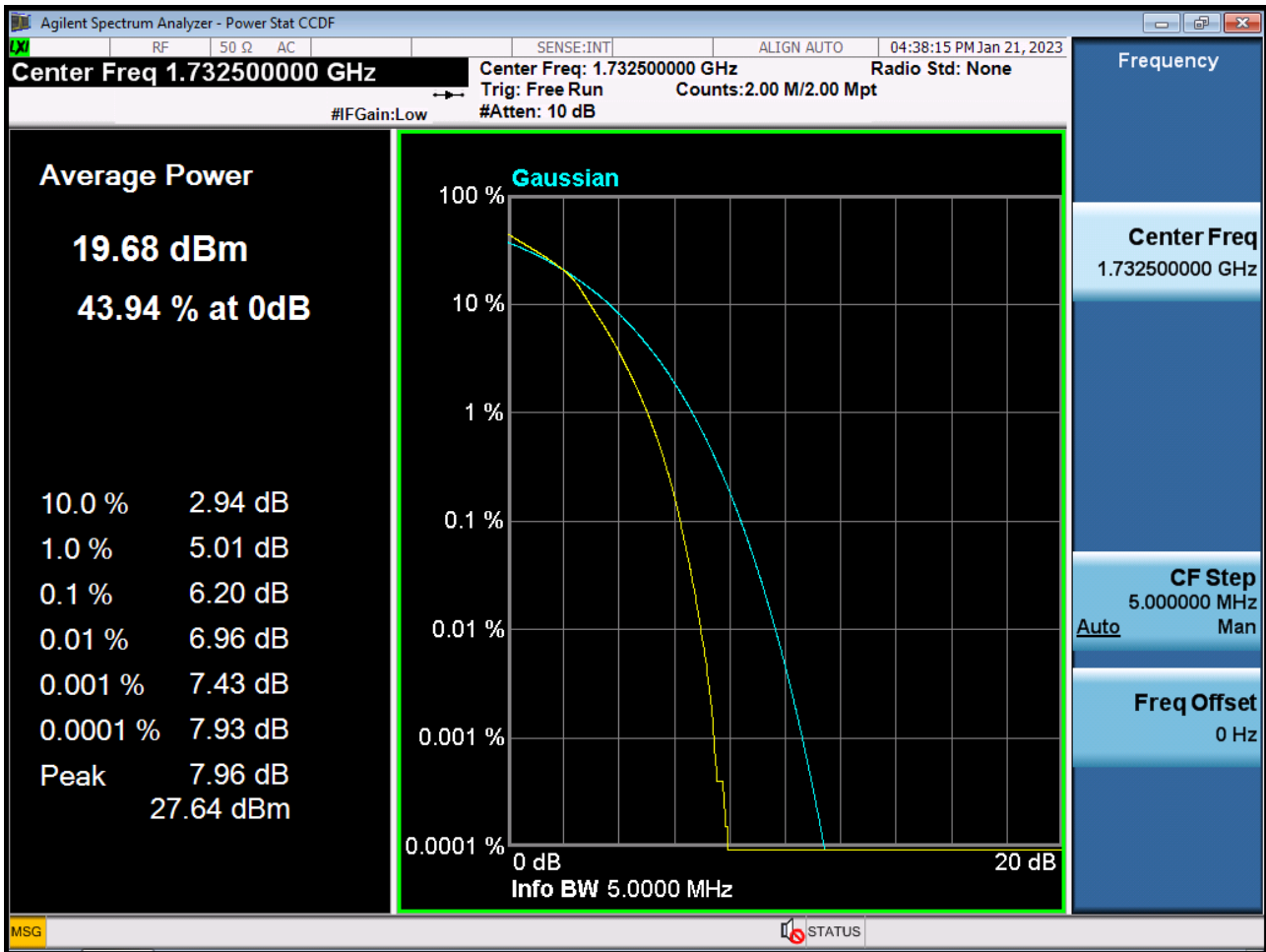
BW3 M_PAR_Middle Channel_256QAM_FullRB



BW5 M_PAR_Middle Channel_QPSK_FullIRB



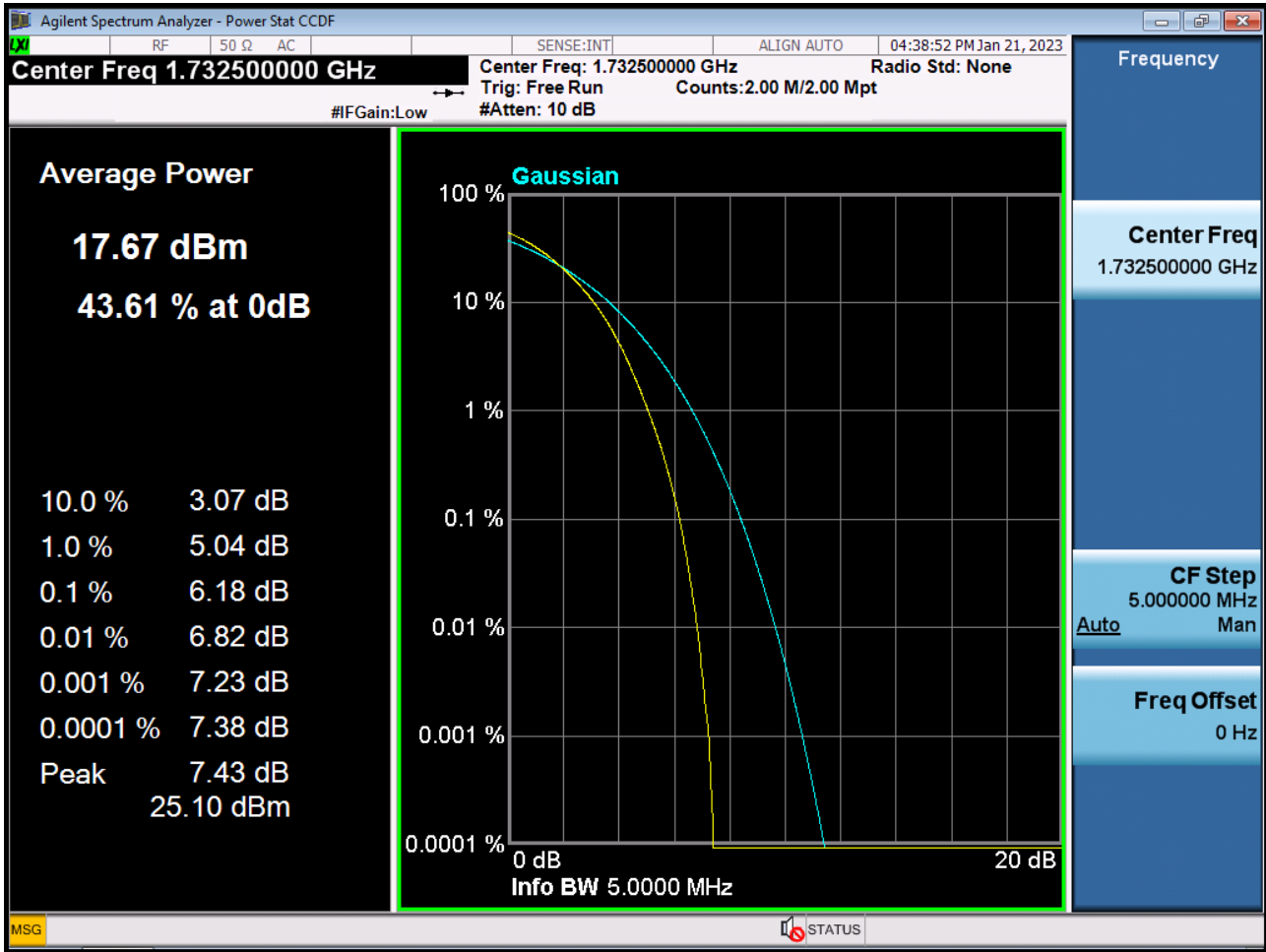
BW5 M_PAR_Middle Channel_16QAM_FullRB



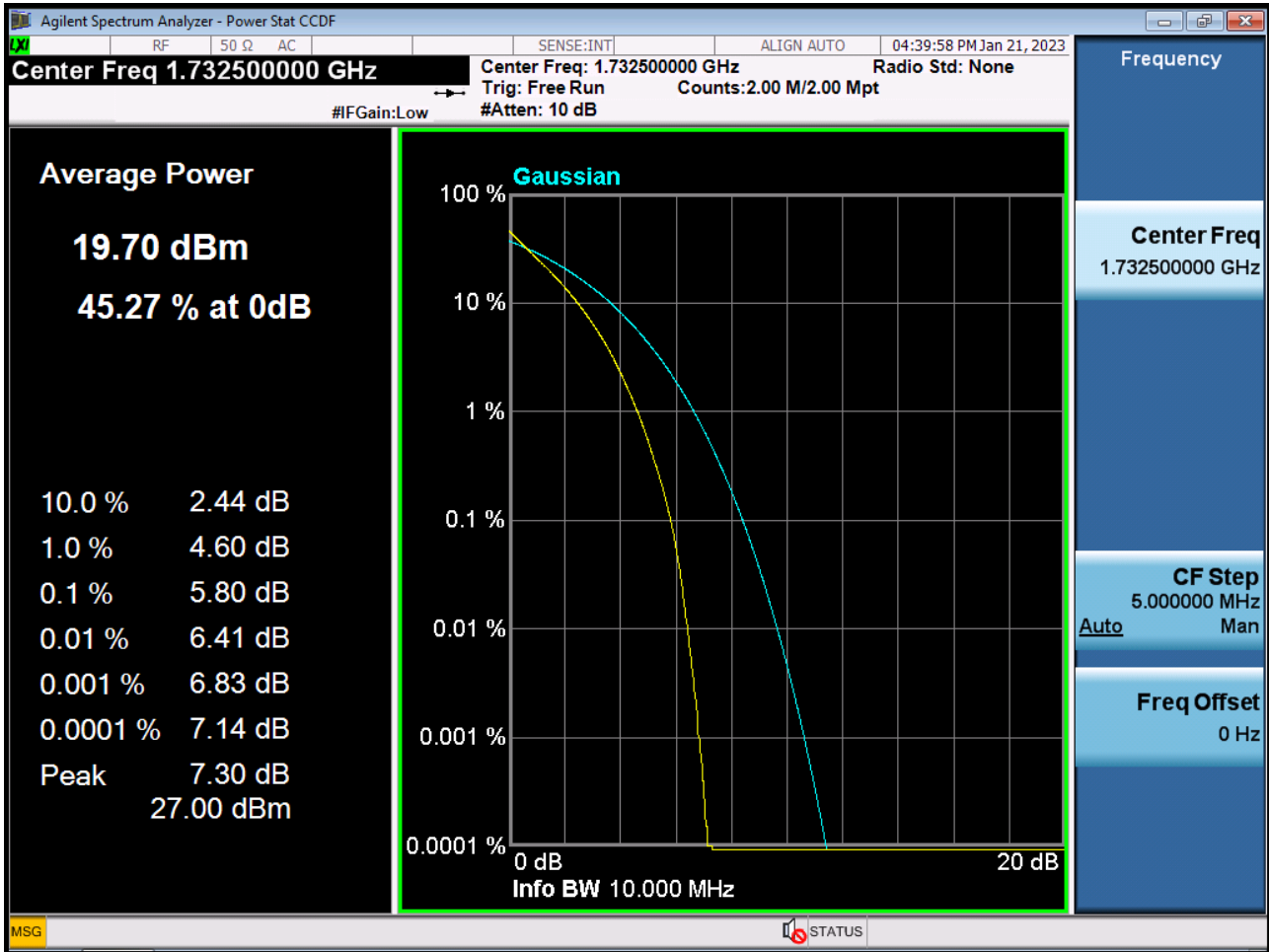
BW5 M_PAR_Middle Channel_64QAM_FullRB



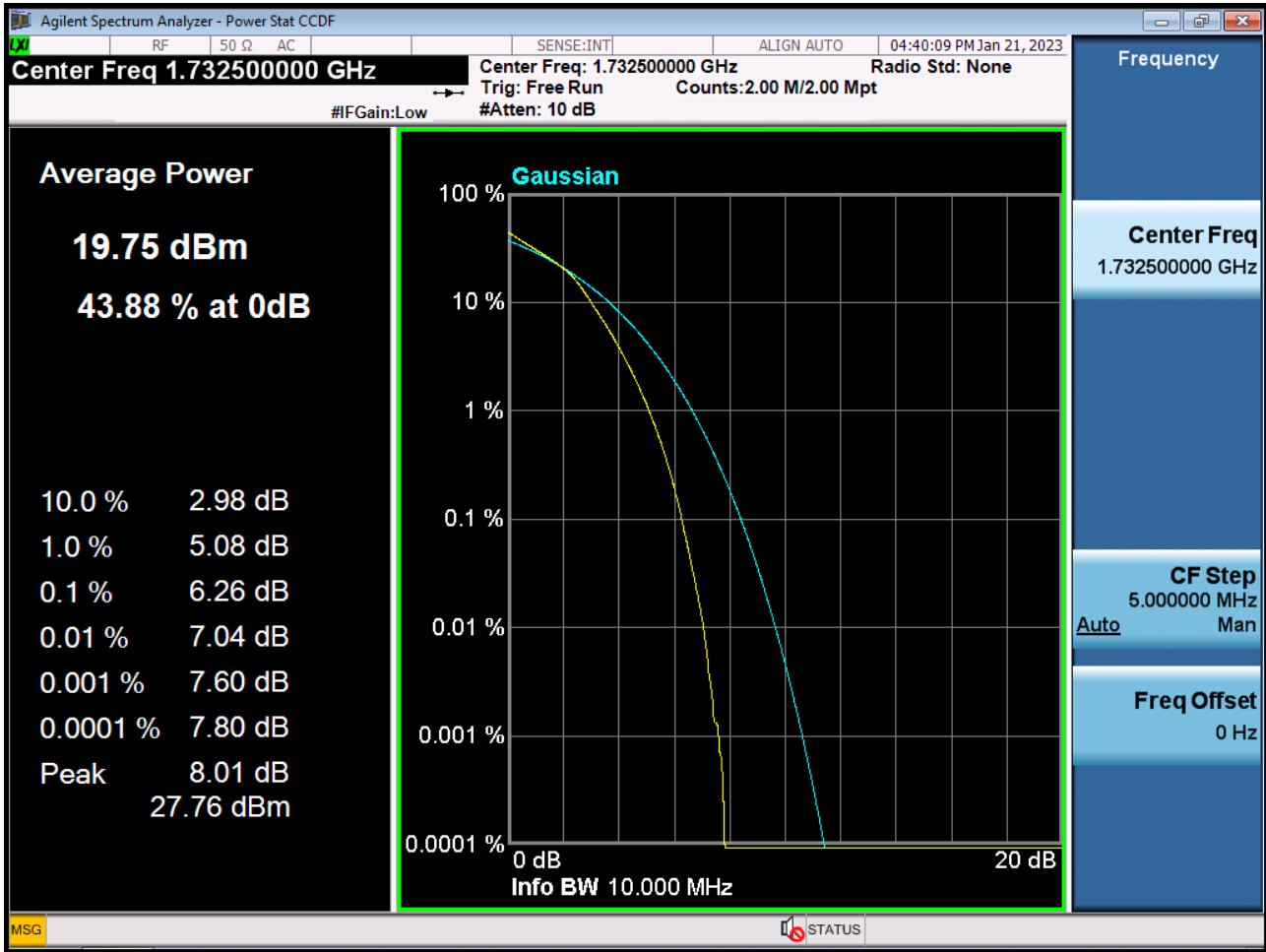
BW5 M_PAR_Middle Channel_256QAM_FullRB



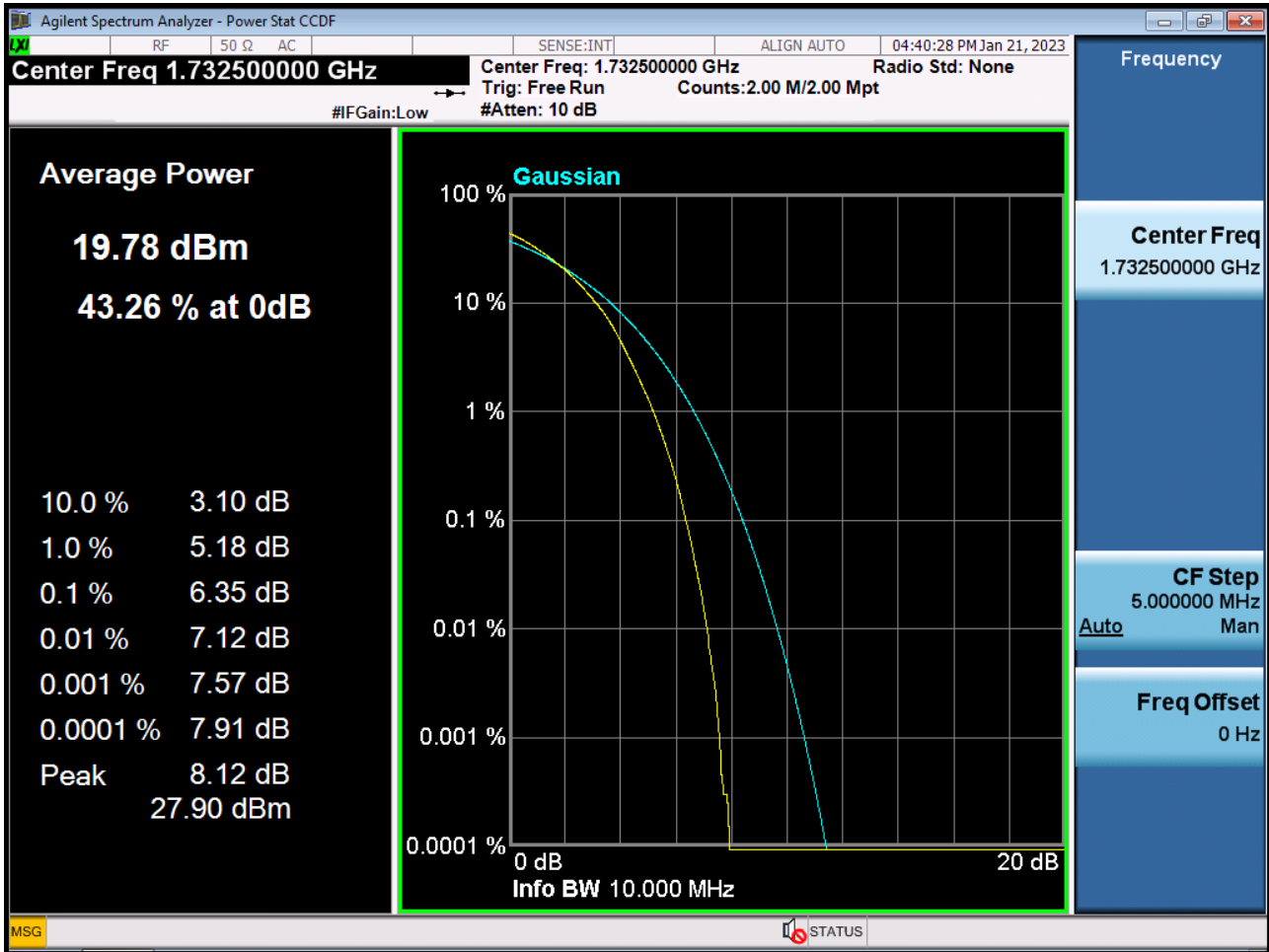
BW10 M_PAR_Middle Channel_QPSK_FullRB



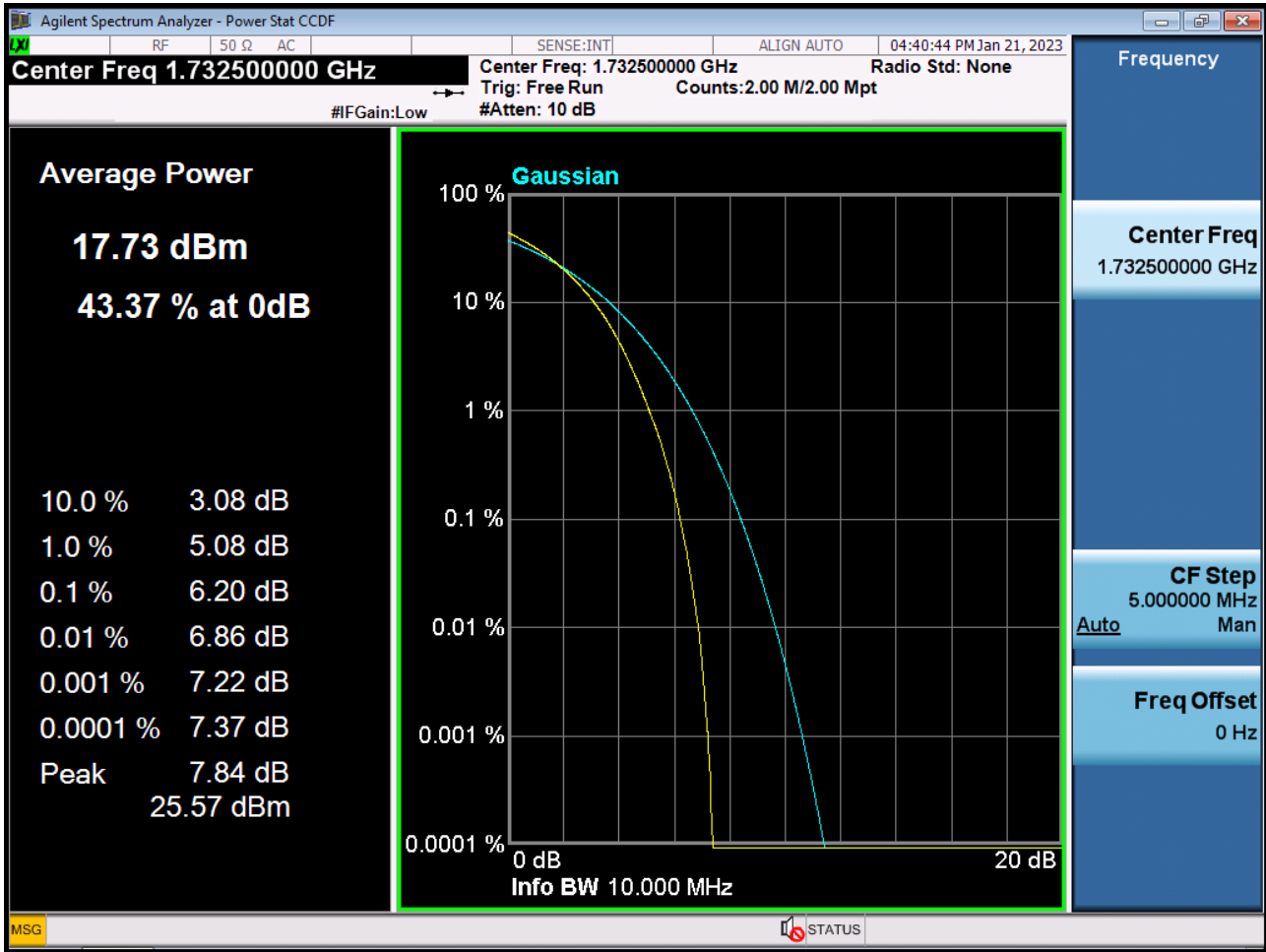
BW10 M_PAR_Middle Channel_16QAM_FullRB



BW10 M_PAR_Middle Channel_64QAM_FullRB



BW10 M_PAR_Middle Channel_256QAM_FullIRB



BW15 M_PAR_Middle Channel_QPSK_FullRB

