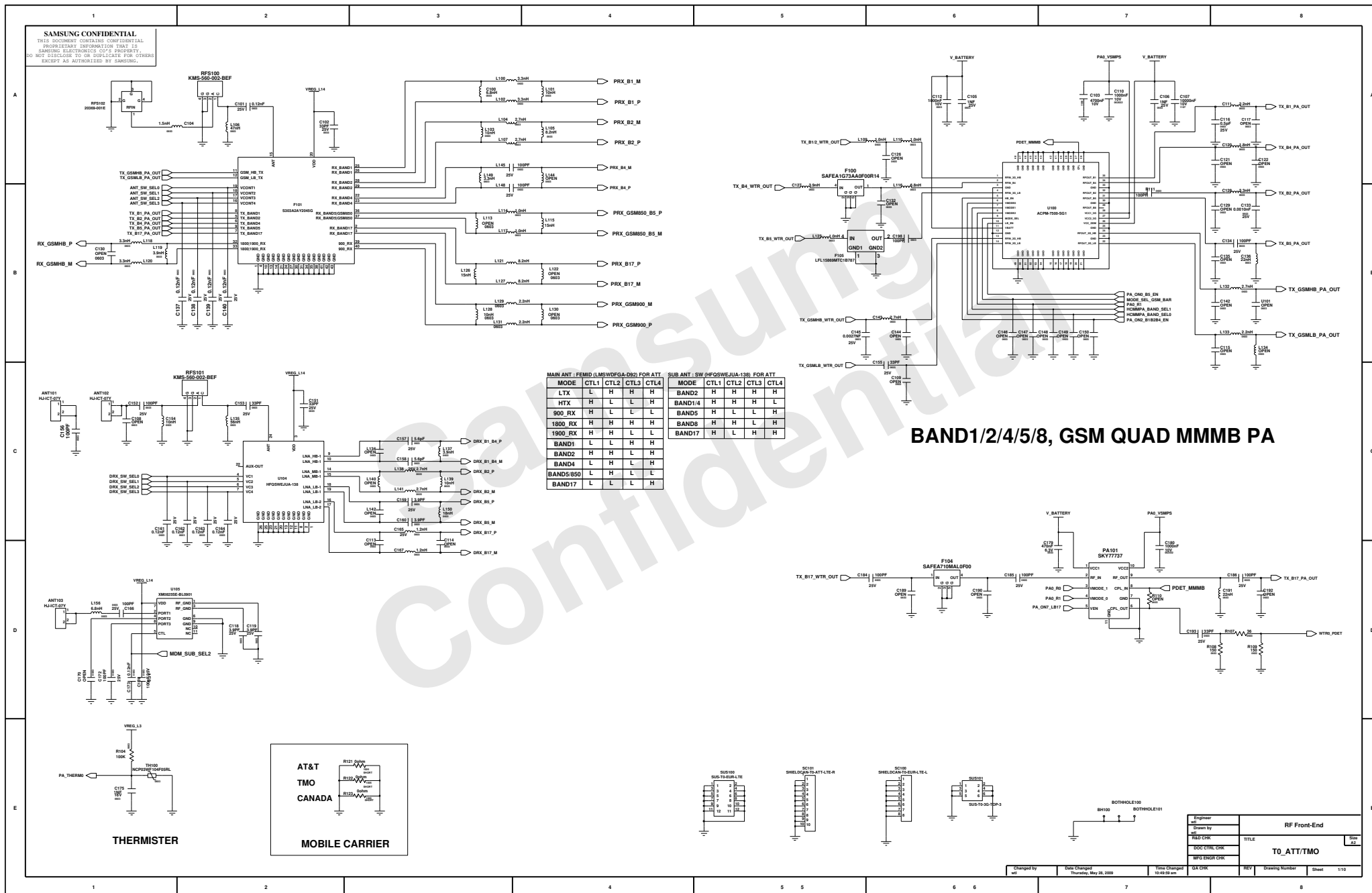
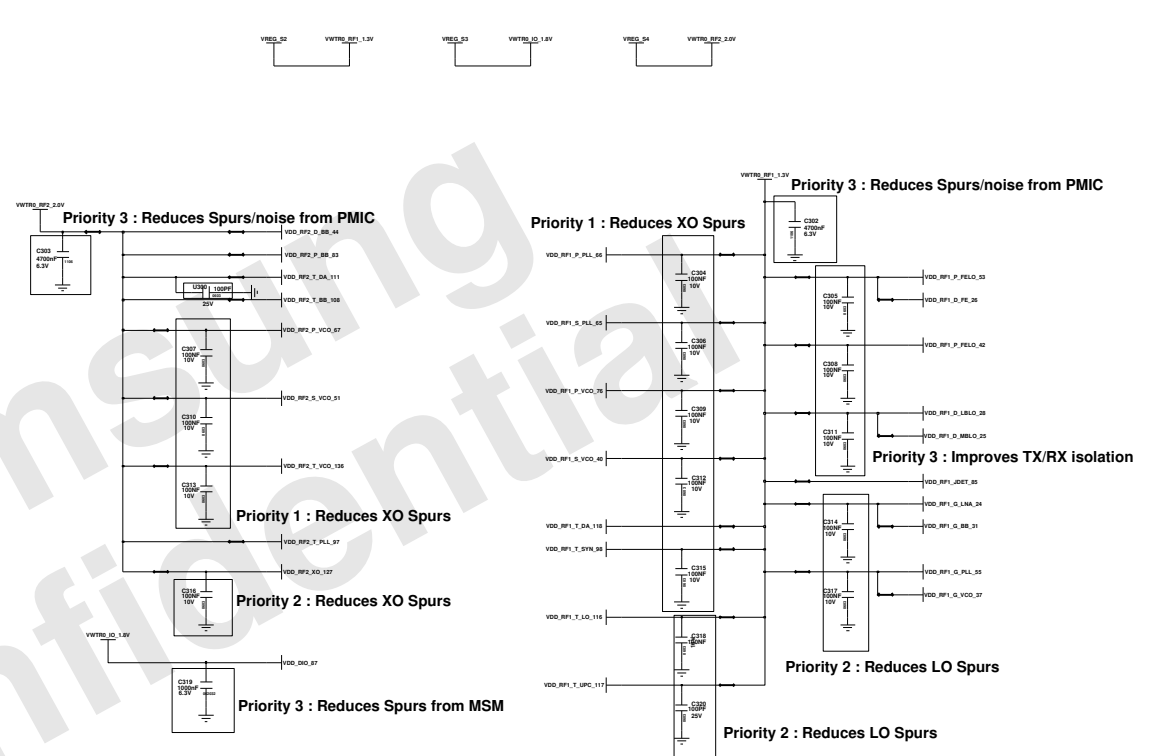
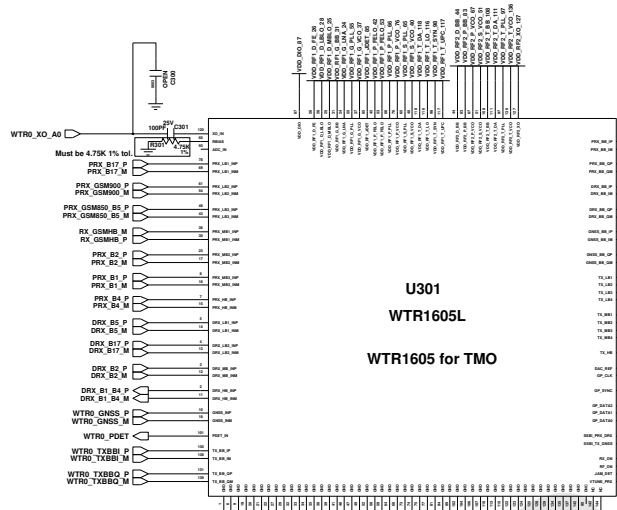
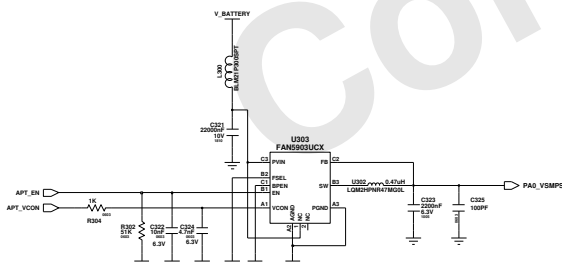


8. Schematics



[illegible]

All decaps should have star connections



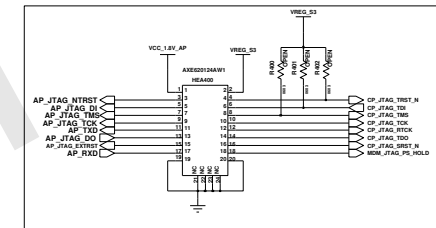
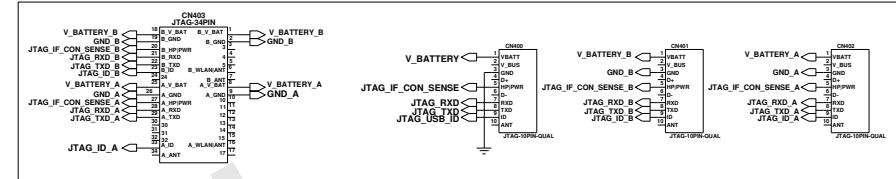
PA DCDC

Engineer wt	Tranceiver		
Drawn by wt			
R&D CHK	TITLE	Size A2	
DOC CTRL CHK	T0_ATT/TMO		
MFG ENGR CHK			
QA CHK	REV	Drawing Number	Sheet 1/10

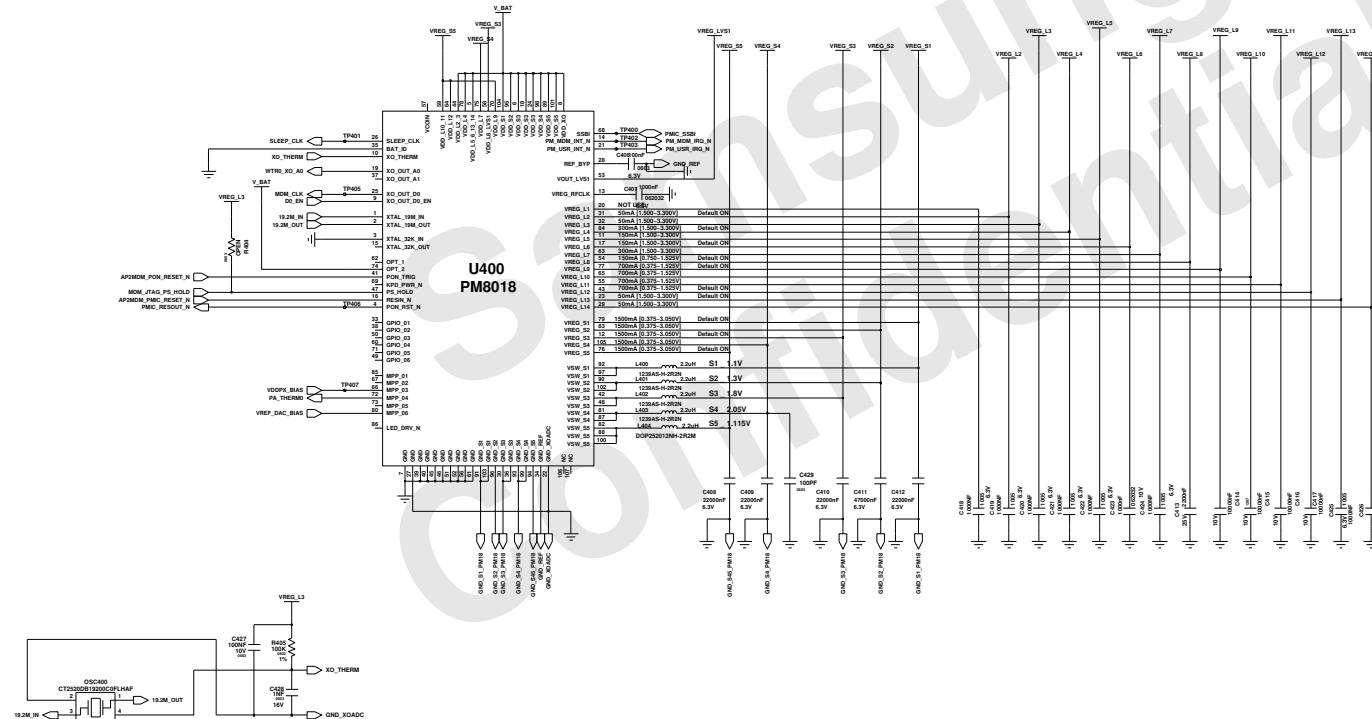
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CP PMIC

PCB JTAG



AP / CP JTAG Connector



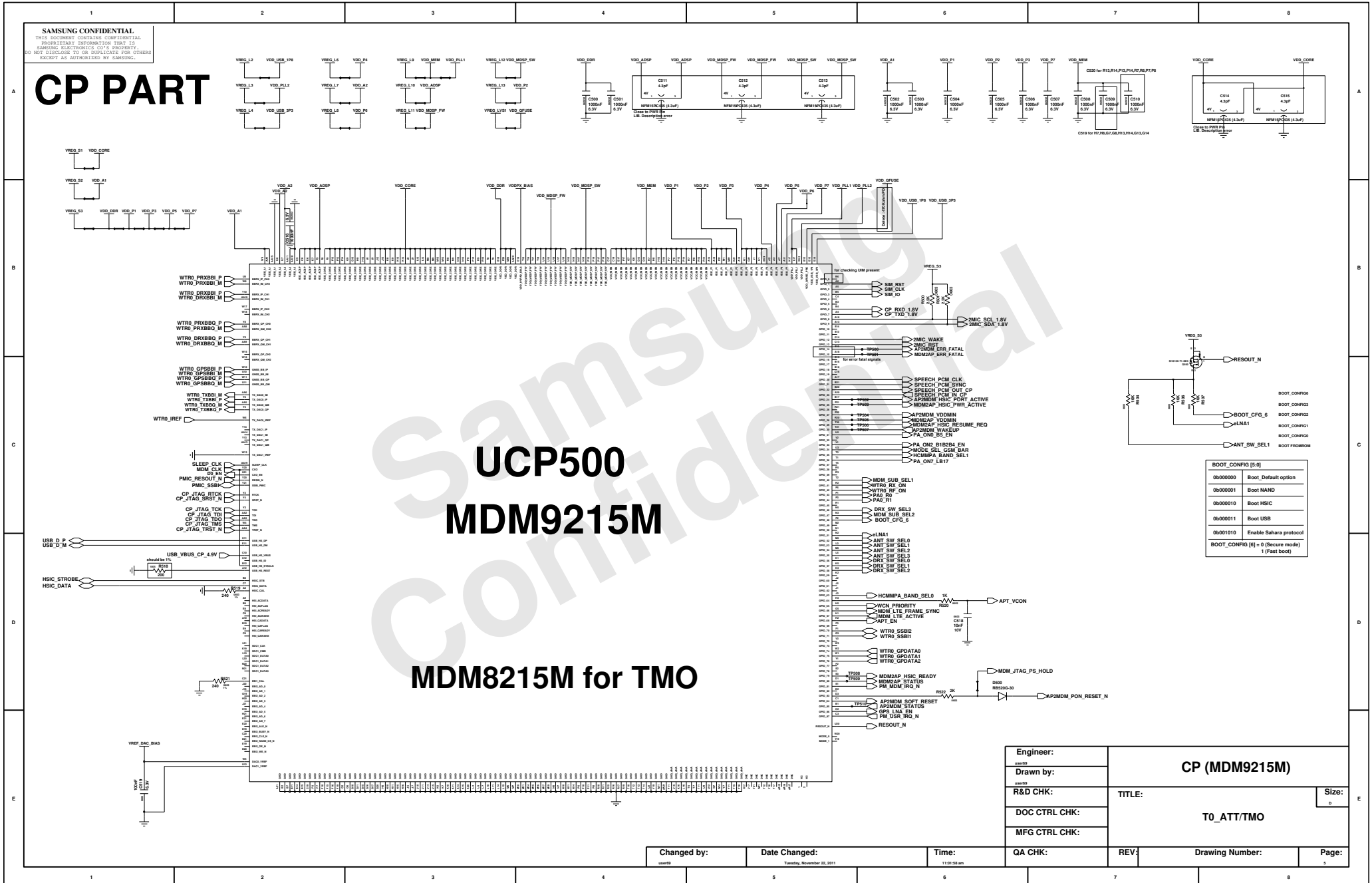
Engineer:	CP_PMIC		
Drawn by:			
QA CHK:	TITLE:	Size:	813A
DOC CTRL CHK:	TO_ATT/TMO		
MFG CTRL CHK:			
QA CHK:	REV:	Drawing Number:	Page:

Changed by:

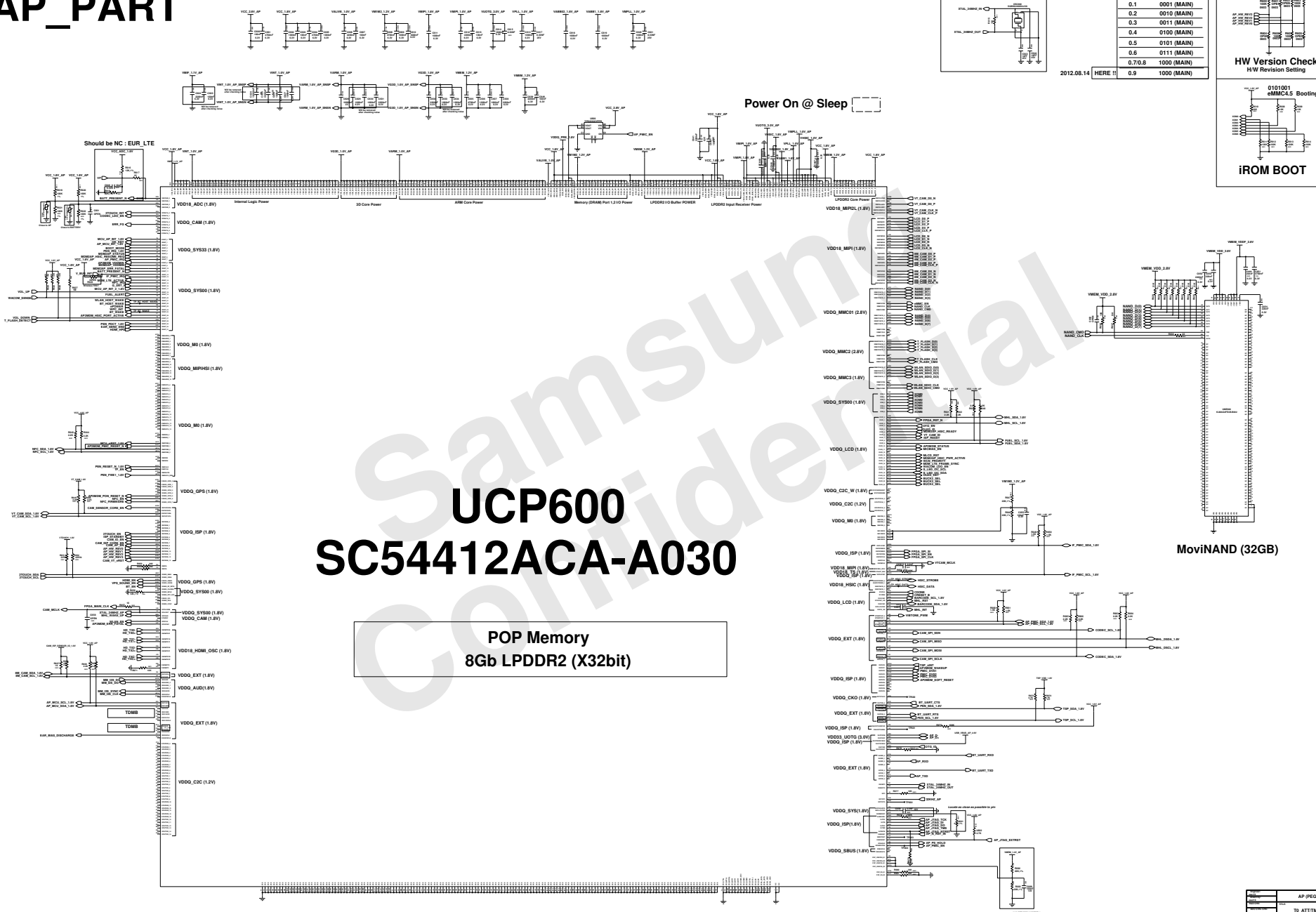
Date Changed:

Time:

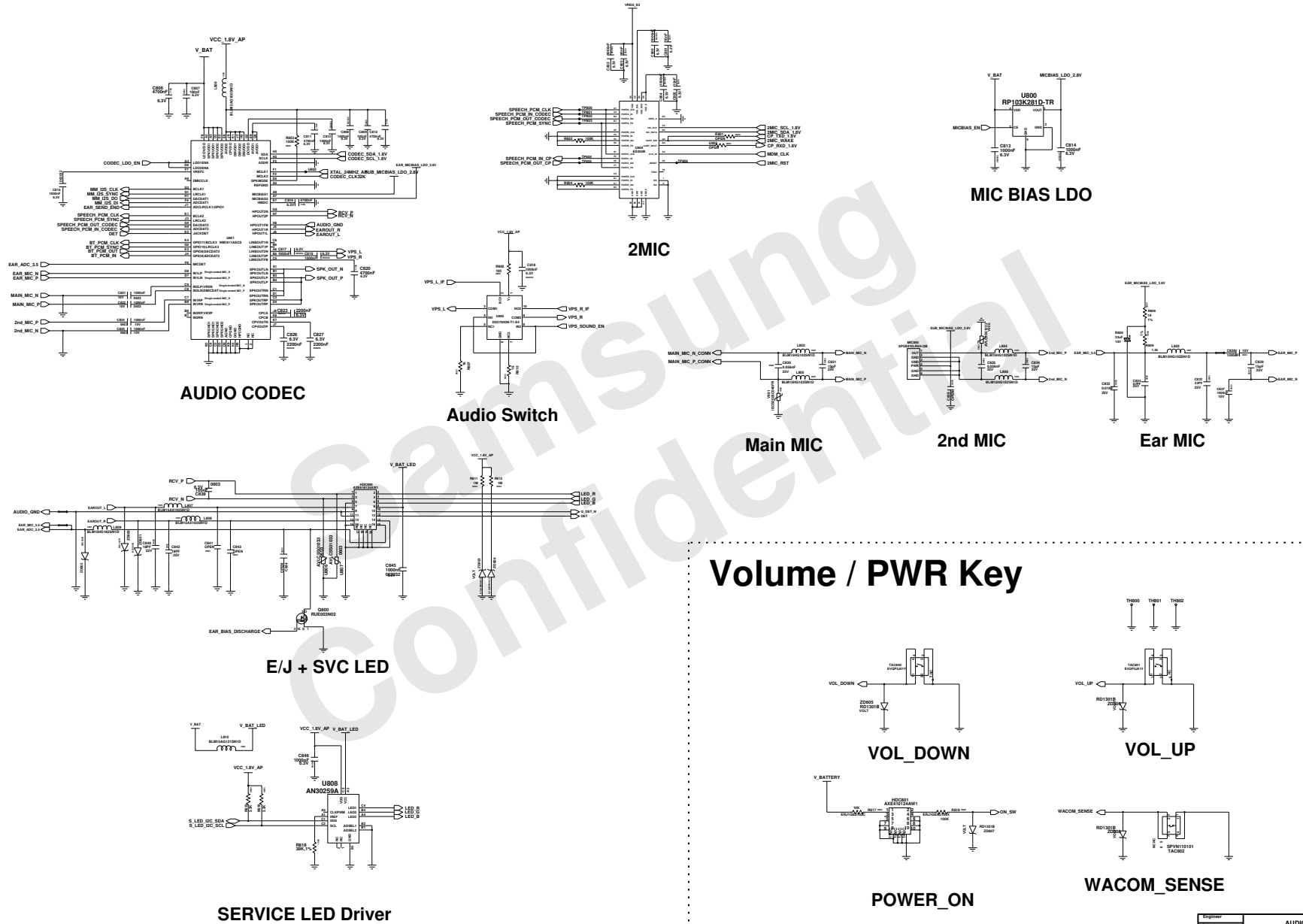
CP PART



AP_PART

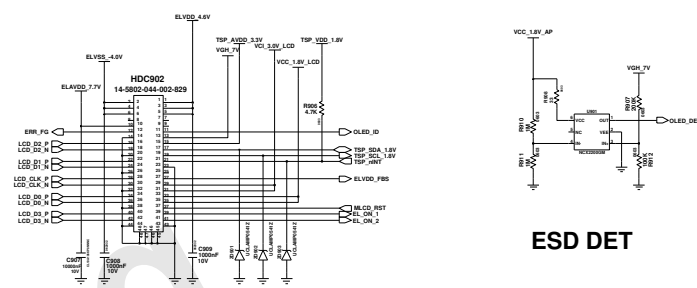


Audio PART

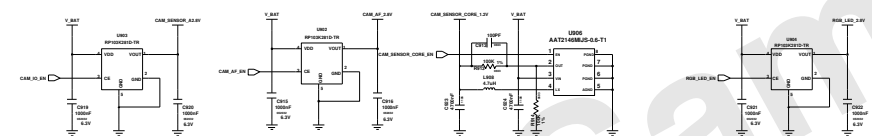


Project	AUDIO
Version	1.0
Rev. Date	2014.12.10
Rev. Date	2014.12.10
Rev. Date	2014.12.10
Rev. Date	2014.12.10
Rev. Date	2014.12.10
Rev. Date	2014.12.10
Rev. Date	2014.12.10
Rev. Date	2014.12.10
Rev. Date	2014.12.10

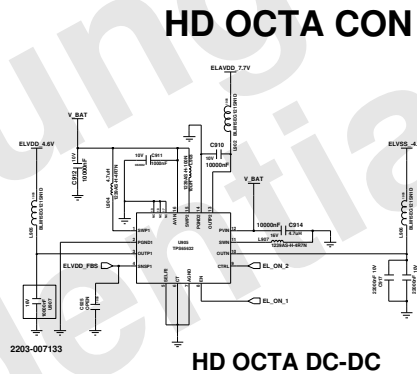
DISPLAY PART



1.9M Camera + RGB SENSOR Connector



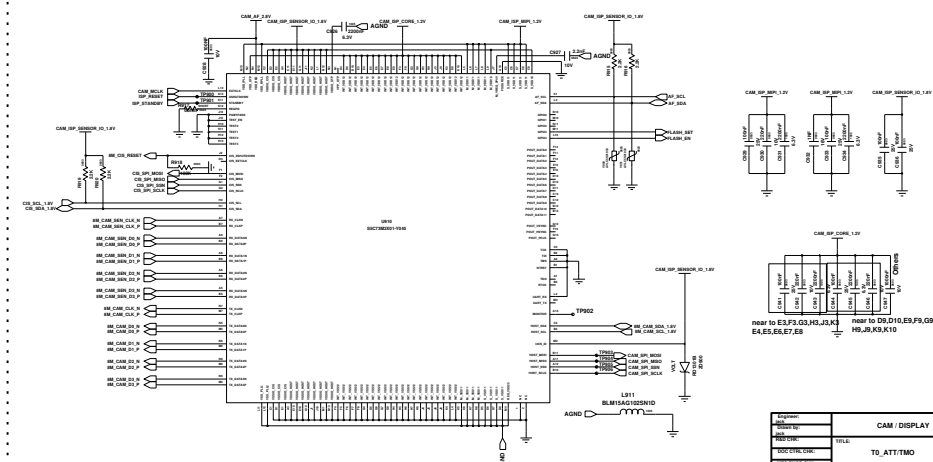
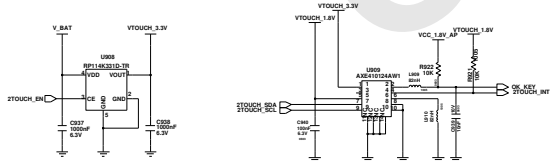
RGB Sensor LDO



HD OCTA CON

HD OCTA DC-DC

CAMERA ISP



Engineer:	
Drawn by:	CAM / DISPLAY
Rev'd CHK:	TITLE
LOG CTRL CHK:	T0_ATT/TMO
MFG ENGR CHK:	

